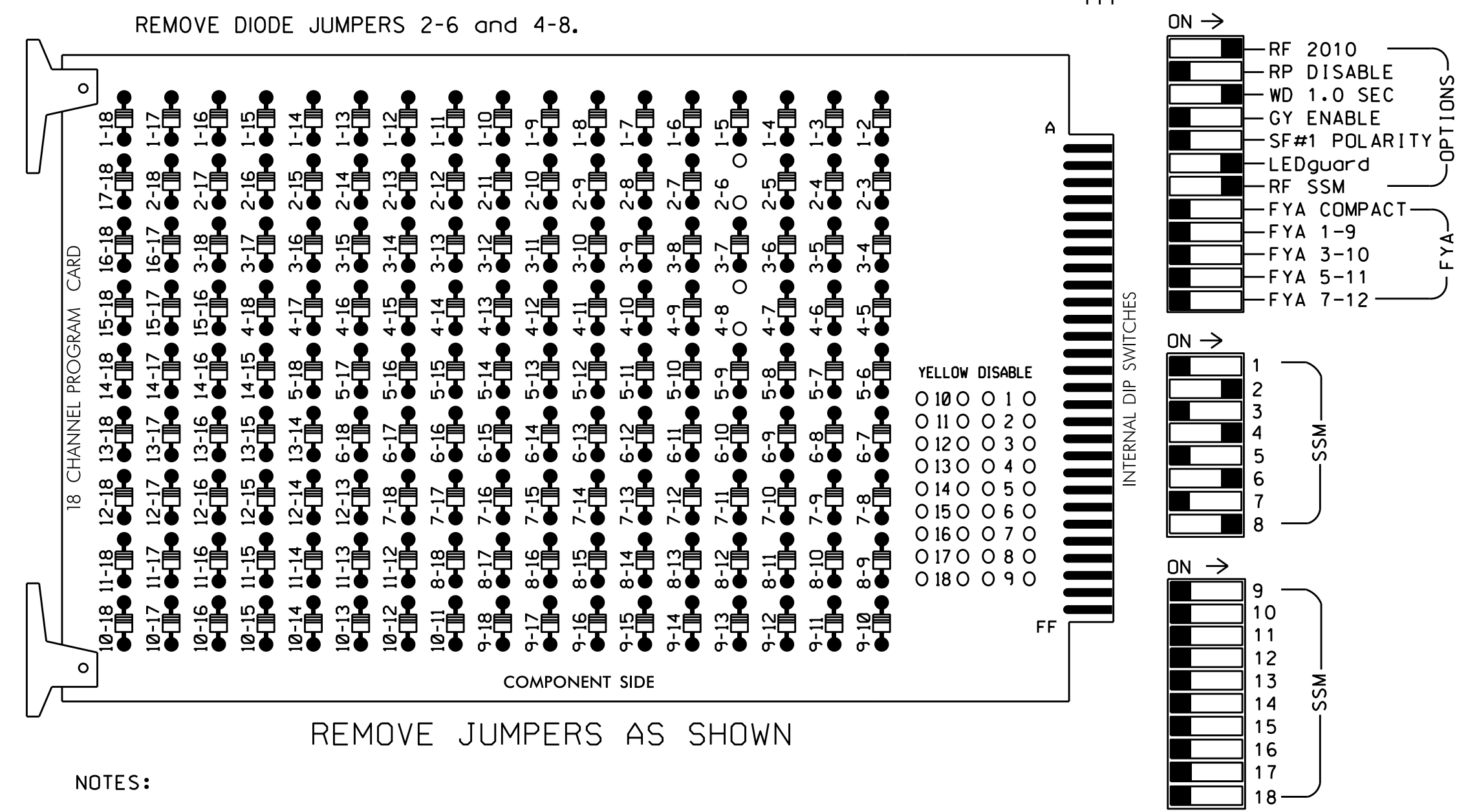


**EDI MODEL 2018ECLip-NC CONFLICT MONITOR PROGRAMMING DETAIL**

(remove jumpers and set switches as shown)



**NOTES:**

1. Card is provided with all diode jumpers in place. Removal of any jumper allows its channels to run concurrently.
2. Ensure jumpers SEL2-SEL5 and SEL9 are present on the monitor board.
3. Ensure that Red Enable is active at all times during normal operation.
4. Integrate monitor with Ethernet network in cabinet.

**NOTES**

1. To prevent "flash-conflict" problems, insert red flash program blocks for all unused vehicle load switches in the output file. The installer shall verify that signal heads flash in accordance with the Signal Plans.
2. Program phases 4 and 8 for Dual Entry.
3. Enable Simultaneous Gap-Out for all phases.
4. Program phases 2 and 6 for Variable Initial and Gap Reduction.
5. Program phases 2 and 6 for Start Up In Green.
6. Program phases 2 and 6 for Yellow Flash.
7. The cabinet and controller are part of the High Point Signal System.

**SIGNAL HEAD HOOK-UP CHART**

LOAD SWITCH NO.	S1	S2	S3	S4	S5	S6	S7	S8	S9	S10	S11	S12
CMU CHANNEL NO.	1	2	13	3	4	14	5	6	15	7	8	16
PHASE	1	2	2 PED	3	4	4 PED	5	6	6 PED	7	8	8 PED
SIGNAL HEAD NO.	NU	21,22	NU	NU	41,42 43	NU	NU	61,62	NU	NU	81,82	NU
RED		128			101			134			107	
YELLOW		129			102			135			108	
GREEN		130			103			136			109	
RED ARROW												
YELLOW ARROW												
GREEN ARROW												

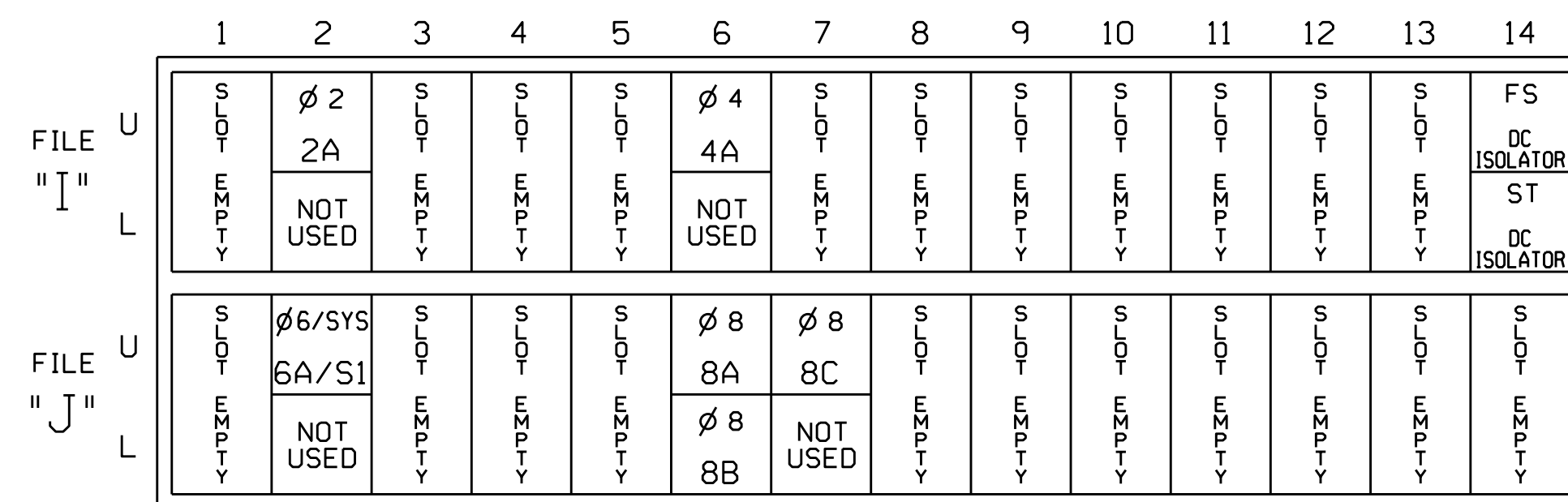
NU = Not Used

**EQUIPMENT INFORMATION**

CONTROLLER.....2070  
 CABINET.....332  
 SOFTWARE.....ECONOLITE OASIS  
 CABINET MOUNT.....BASE  
 OUTPUT FILE POSITIONS...12  
 LOAD SWITCHES USED.....S2,S5,S8,S11  
 PHASES USED.....2,4,6,8  
 OVERLAPS.....NONE

**INPUT FILE POSITION LAYOUT**

(front view)



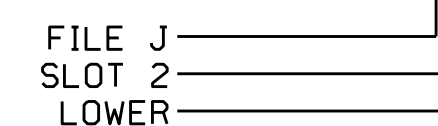
EX.: 1A, 2A, ETC. = LOOP NO.'S

FS = FLASH SENSE  
 ST = STOP TIME

**INPUT FILE CONNECTION & PROGRAMMING CHART**

LOOP NO.	LOOP TERMINAL	INPUT FILE POS.	PIN NO.	INPUT ASSIGNMENT NO.	DETECTOR NO.	NEMA PHASE	CALL	EXTEND	FULL TIME DELAY	STRETCH TIME	DELAY TIME
2A	TB2-5,6	I2U	39	1	2	2	Y	Y			
4A	TB4-9,10	I6U	41	3	4	4	Y	Y			10
6A/S1	TB3-5,6	J2U	40	2	6	6/SYS	Y	Y			
8A	TB5-9,10	J6U	42	4	8	8	Y	Y			3
8B	TB5-11,12	J6L	46	8	18	8	Y	Y			10
8C	TB7-1,2	J7U	66	28	38	8	Y	Y			15

INPUT FILE POSITION LEGEND: J2L



THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 07-1524  
 DESIGNED: March 2014  
 SEALED: 4/1/2015  
 REVISED: N/A

**Electrical Detail**

Electrical and Programming Details for: **NC 62 (Liberty Rd.) at NC 610 (Fairfield Rd.) and SR 4012 (Aldridge Rd.)**

Division 7 Guilford County High Point

PLAN DATE: July 2014 REVIEWED BY: [Signature]

PREPARED BY: S. Armstrong REVIEWED BY: [Signature]

REVISIONS: [Table with columns for REVISIONS, INIT., DATE]

Prepared in the Offices of: **Transportation Mobility and Safety Solutions**

750 N. Greenfield Pkwy, Garner, NC 27529

SEAL: **John T. Rowe, Jr.**, Professional Engineer, License No. 008453, State of North Carolina. Date: 4/8/2015

SIG. INVENTORY NO. 07-1524

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