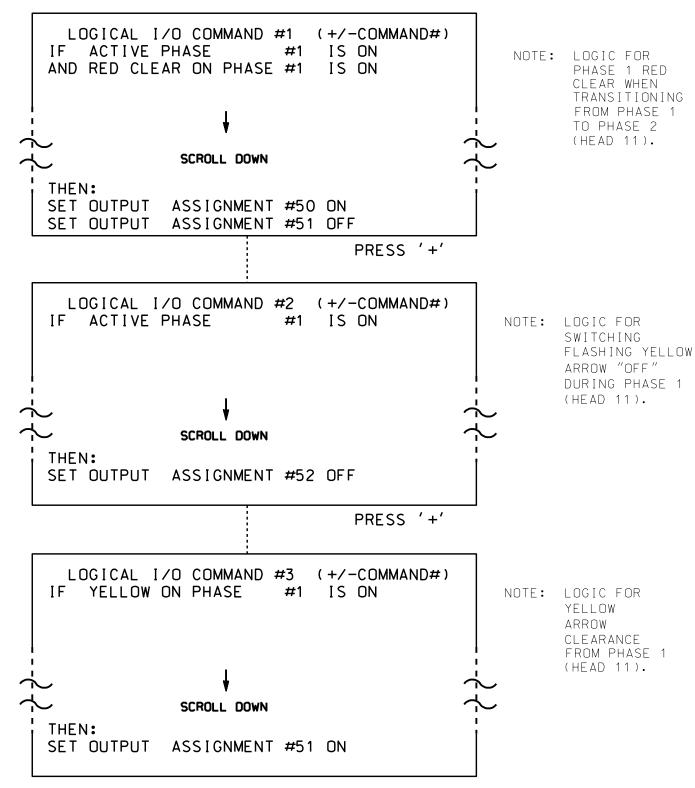
## LOGICAL I/O PROCESSOR PROGRAMMING DETAIL TO PRODUCE SPECIAL FYA-PPLT SIGNAL SEQUENCE

(program controller as shown below)

- 1. FROM MAIN MENU PRESS '2' (PHASE CONTROL), THEN '1' (PHASE CONTROL FUNCTIONS). SCROLL TO THE BOTTOM OF THE MENU AND ENABLE ACT LOGIC COMMANDS 1, 2, AND 3.
- 2. FROM MAIN MENU PRESS '6' (OUTPUTS), THEN '3' (LOGICAL I/O PROCESSOR).



LOGIC I/O PROCESSOR PROGRAMMING COMPLETE

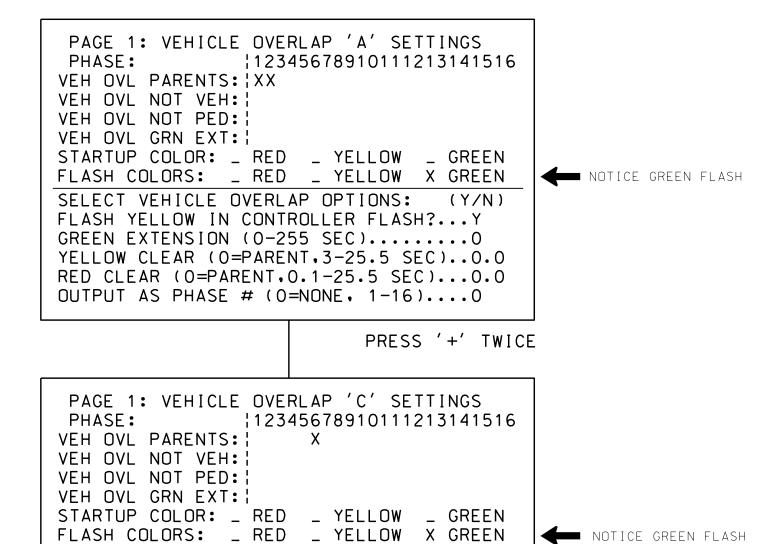
OUTPUT REFERENCE SCHEDULE

OUTPUT 50 = Overlap A Red OUTPUT 51 = Overlap A Yellow OUTPUT 52 = Overlap A Green

## OVERLAP PROGRAMMING DETAIL

(program controller as shown below)

FROM MAIN MENU PRESS '8' (OVERLAPS). THEN '1' (VEHICLE OVERLAP SETTINGS).



OVERLAP PROGRAMMING COMPLETE

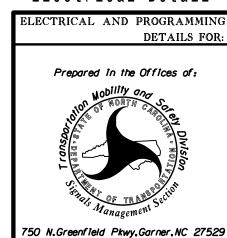
SELECT VEHICLE OVERLAP OPTIONS: (Y/N) FLASH YELLOW IN CONTROLLER FLASH?...Y

GREEN EXTENSION (0-255 SEC).....0 YELLOW CLEAR (O=PARENT,3-25.5 SEC)..0.0 RED CLEAR (0=PARENT.0.1-25.5 SEC)...0.0

OUTPUT AS PHASE # (0=NONE, 1-16)....0

THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 07-1467 DESIGNED: June 2014 SEALED: 4/1/2015 REVISED: N/A

Electrical Detail - Sheet 2 of 2



SR 1113 (E. Kivett Drive)

Pendleton Street Guilford County

PLAN DATE: August 2014 REVIEWED BY: 978 prepared by: S. Armstrong | reviewed by: REVISIONS INIT. DATE

John T. Rowe, Jr. 4/7/2015

SIG. INVENTORY NO. 07-1467