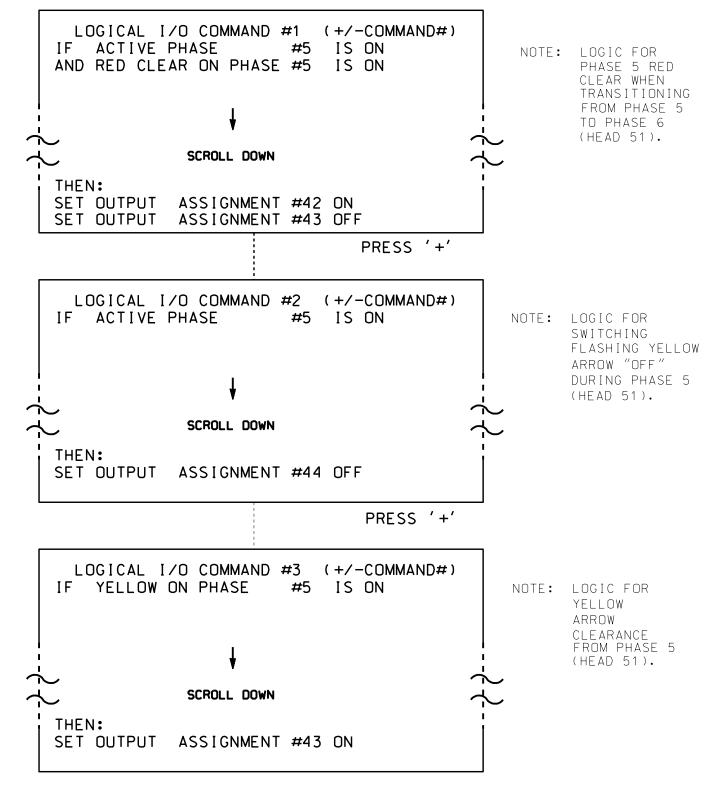
(program controller as shown below)

- 1. FROM MAIN MENU PRESS '2' (PHASE CONTROL), THEN '1' (PHASE CONTROL FUNCTIONS). SCROLL TO THE BOTTOM OF THE MENU AND ENABLE ACT LOGIC COMMANDS 1, 2, AND 3.
- 2. FROM MAIN MENU PRESS '6' (OUTPUTS), THEN '3' (LOGICAL I/O PROCESSOR).



LOGIC I/O PROCESSOR PROGRAMMING COMPLETE

OUTPUT REFERENCE SCHEDULE OUTPUT 42 = Overlap C Red

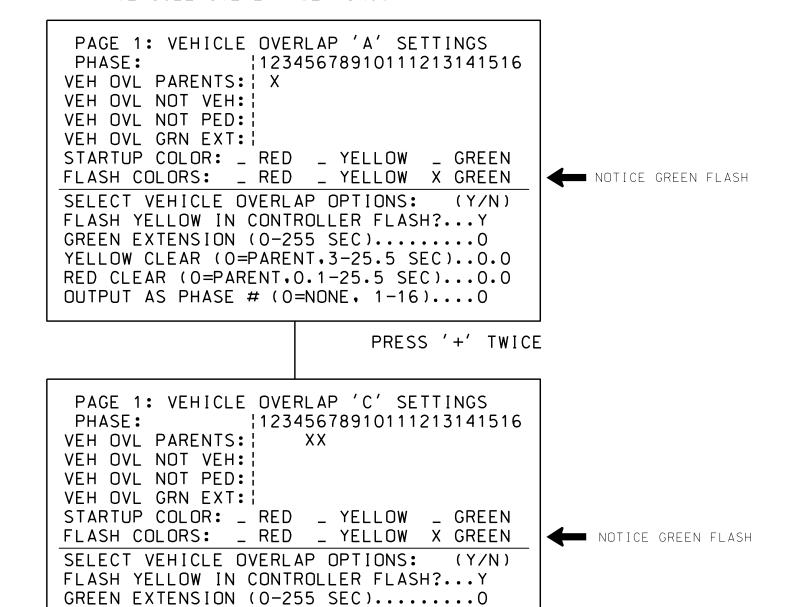
> OUTPUT 43 = Overlap C Yellow OUTPUT 44 = Overlap C Green

PROJECT REFERENCE NO. Sig. 115.2 C-5558

OVERLAP PROGRAMMING DETAIL

(program controller as shown below)

FROM MAIN MENU PRESS '8' (OVERLAPS). THEN '1' (VEHICLE OVERLAP SETTINGS).



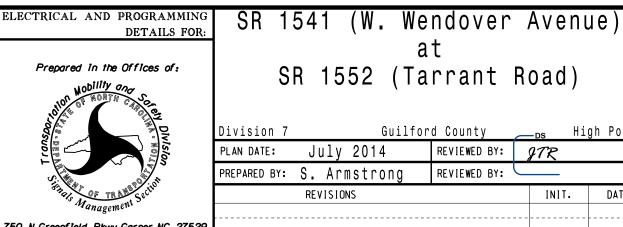
OVERLAP PROGRAMMING COMPLETE

YELLOW CLEAR (O=PARENT,3-25.5 SEC)..0.0

RED CLEAR (0=PARENT.0.1-25.5 SEC)...0.0 OUTPUT AS PHASE # (0=NONE, 1-16)....0

> THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 07-1441 DESIGNED: May 2014 SEALED: 3/11/2015 REVISED: N/A

Electrical Detail - Sheet 1 of 2



750 N.Greenfield Pkwy, Garner, NC 27529

INIT. DATE SIG. INVENTORY NO. 07-1441

SEAL