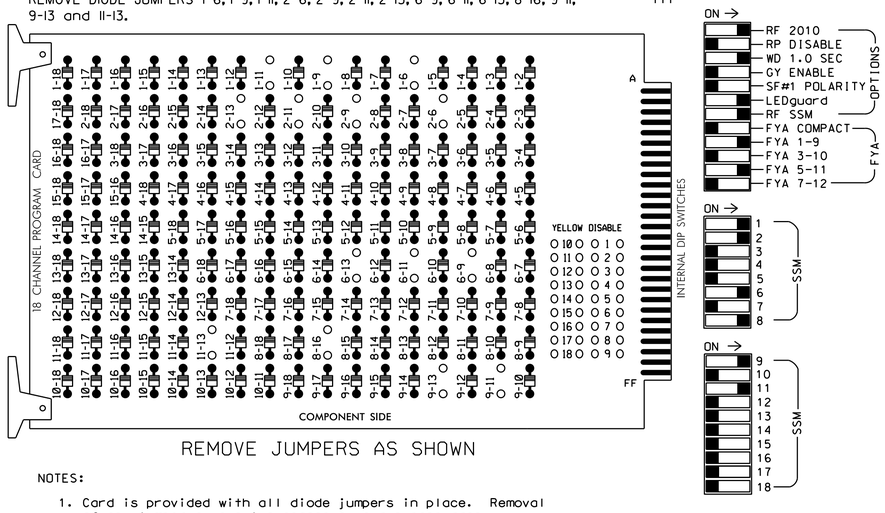
PROJECT REFERENCE NO.	SHEET NO.
C-5558	Sig.113.1

EDI MODEL 2018ECLip-NC CONFLICT MONITOR PROGRAMMING DETAIL

(remove jumpers and set switches as shown)

REMOVE DIODE JUMPERS 1-6, 1-9, 1-11, 2-6, 2-9, 2-11, 2-13, 6-9, 6-11, 6-13, 8-16, 9-11, 9-13 and 11-13.



REMOVE JUMPERS AS SHOWN

NOTES:

1. Card is provided with all diode jumpers in place. Removal of any jumper allows its channels to run concurrently.
2. Ensure jumpers SEL2-SEL5 and SEL9 are present on the monitor board.
3. Ensure that Red Enable is active at all times during normal operation.
4. Integrate monitor with Ethernet network in cabinet.

NOTES

1. To prevent "flash-conflict" problems, insert red flash program blocks for all unused vehicle load switches in the output file. The installer shall verify that signal heads flash in accordance with the Signal Plans.
2. Enable Simultaneous Gap-Out for all phases.
3. Program phases 2 and 6 for Start Up In Green.
4. Program phases 2 and 8 for 'STARTUP PED CALL'.
5. Program phases 2 and 6 for Yellow Flash, and overlap 1 as Wag Overlaps.
6. The cabinet and controller are part of the High Point Signal System.

EQUIPMENT INFORMATION

CONTROLLER.....2070
CABINET.....332 /W/ AUX
SOFTWARE.....ECONOLITE OASIS
CABINET MOUNT.....BASE
OUTPUT FILE POSITIONS...18 WITH AUX. OUTPUT FILE
LOAD SWITCHES USED.....S1,S2,S3,S8,S11,S12,AUX S1, AUX S4
PHASES USED.....1,2,2 PED,6,8,8 PED
OVERLAP "A".....1+2
OVERLAP "B".....NOT USED
OVERLAP "C".....6
OVERLAP "D".....NOT USED

SIGNAL HEAD HOOK-UP CHART

LOAD SWITCH NO.	S1		S2	S3	S4	S5	S6	S7	S8	S9	S10	S11	S12	AUX S1	AUX S2	AUX S3	AUX S4	AUX S5	AUX S6
CMU CHANNEL NO.	1		2	13	3	4	14	5	6	15	7	8	16	9	10	17	11	12	18
PHASE	1		2	2 PED	3	4	4 PED	5	6	6 PED	7	8	8 PED	OLA	OLB	SPARE	OLC	OLD	SPARE
SIGNAL HEAD NO.	11★	82	21,22	P21, P22	NU	NU	NU	NU	61, 62,63	NU	NU	81,82	P81,P82, P83,P84	11★	NU	NU	23★	NU	NU
RED		✳	128						134			107							
YELLOW			129						135			108							
GREEN			130						136			109							
RED ARROW														A121			A114		
YELLOW ARROW		126												A122			A115		
FLASHING YELLOW ARROW														A123			A116		
GREEN ARROW	127	127																	
✋				113									110						
🚶				115									112						

NU = Not Used

✳ Denotes install load resistor. See load resistor installation detail this sheet.

★ See pictorial of head wiring in detail below.

INPUT FILE POSITION LAYOUT

(front view)

		1	2	3	4	5	6	7	8	9	10	11	12	13	14
FILE "I"	U	Ø1 1A	Ø1 1B	Ø2 2A	SLOT EMPTY	SLOT EMPTY	SLOT EMPTY	SLOT EMPTY	SLOT EMPTY	SLOT EMPTY	SLOT EMPTY	SLOT EMPTY	Ø2 PED DC ISOLATOR	NOT USED	FS DC ISOLATOR
	L	NOT USED	Ø1 1C	NOT USED									NOT USED	Ø8 PED DC ISOLATOR	ST DC ISOLATOR
FILE "J"	U	SLOT EMPTY	Ø6 6A	SLOT EMPTY	WIRED INPUT ⊗	SLOT EMPTY	Ø8 8A	SLOT EMPTY	SLOT EMPTY	SLOT EMPTY	SLOT EMPTY	SLOT EMPTY	SLOT EMPTY	SLOT EMPTY	SLOT EMPTY
	L		NOT USED				NOT USED								

EX.: 1A, 2A, ETC. = LOOP NO.'S

FS = FLASH SENSE
ST = STOP TIME

⊗ Wired Input - Do not populate slot with detector card

INPUT FILE CONNECTION & PROGRAMMING CHART

LOOP NO.	LOOP TERMINAL	INPUT FILE POS.	PIN NO.	INPUT ASSIGNMENT NO.	DETECTOR NO.	NEMA PHASE	CALL	EXTEND	FULL TIME DELAY	STRETCH TIME	DELAY TIME
1A[1]	TB2-1,2	I1U	56	18	1	1	Y	Y			15
	-	J4U	48	10	26	6	Y	Y			
1B	TB2-5,6	I2U	39	1	2	1	Y	Y			15
1C	TB2-7,8	I2L	43	5	12	1	Y	Y			15
2A	TB2-9,10	I3U	63	25	32	2	Y	Y			
6A	TB3-5,6	J2U	40	2	6	6	Y	Y			
8A	TB5-9,10	J6U	42	4	8	8	Y	Y			3
PED PUSH BUTTONS											
P21,P22	TB8-4,6	I12U	67	29	PED 2	2 PED					
P81,P82, P83,P84	TB8-8,9	I13L	70	32	PED 8	8 PED					

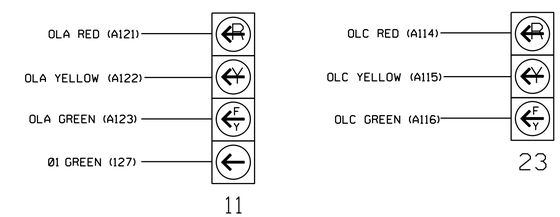
NOTE:
INSTALL DC ISOLATORS IN INPUT FILE SLOTS I12 AND I13.

[1] Add jumper from I1-W to J4-W, on rear of input file.

INPUT FILE POSITION LEGEND: J2L
FILE J
SLOT 2
LOWER

FYA SIGNAL WIRING DETAIL

(wire signal heads as shown)



NOTE

1. The sequence display for signal head 11 requires special logic programming. See sheet 2 of 2 for programming instructions.

COUNTDOWN PEDESTRIAN SIGNAL OPERATION

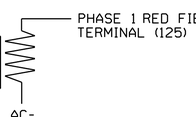
Countdown Ped Signals are required to display timing only during Ped Clearance Interval. Consult Ped Signal Module user's manual for instructions on selecting this feature.

LOAD RESISTOR INSTALLATION DETAIL

(install resistor as shown below)

ACCEPTABLE VALUES

VALUE (ohms)	WATTAGE
1.5K - 1.9K	25W (min)
2.0K - 3.0K	10W (min)



NOTE: The purpose of this resistor is to load the channel red monitor input in order for the Signal Sequence Monitor to use the full signal sequence monitoring capability on channels that do not use the red display in the field.

THIS ELECTRICAL DETAIL IS FOR
THE SIGNAL DESIGN: 07-1437
DESIGNED: May 2014
SEALED: 4/7/2015
REVISED: N/A

Electrical Detail - Sheet 1 of 2

ELECTRICAL AND PROGRAMMING DETAILS FOR:

Prepared in the Offices of:
Transportation Mobility and Safety Division
STATE OF NORTH CAROLINA DEPARTMENT OF TRANSPORTATION
Signal Management Section
750 N.Greenfield Pkwy,Garner,NC 27529

SR 1486/4121 (Main Street)
at
SR 1352 (Oakdale Road)

Division 7 Guilford County Jamestown

PLAN DATE: September 2014	REVIEWED BY: T. Joyce
PREPARED BY: C. Strickland	REVIEWED BY:

REVISIONS	INIT.	DATE

SEAL
NORTH CAROLINA PROFESSIONAL SEAL 022013 ENGINEER GEORGE C. BROWN

DocuSigned by: George C. Brown 4/10/2015
F12601ED0BED434 DATE

SIG. INVENTORY NO. 07-1437

10-APR-2015 13:24
S:*ITS&SM\ITS Signals\Workgroups\Sig Mon\Strickland\071437_sm_ele_xxx.dgn
castrickland