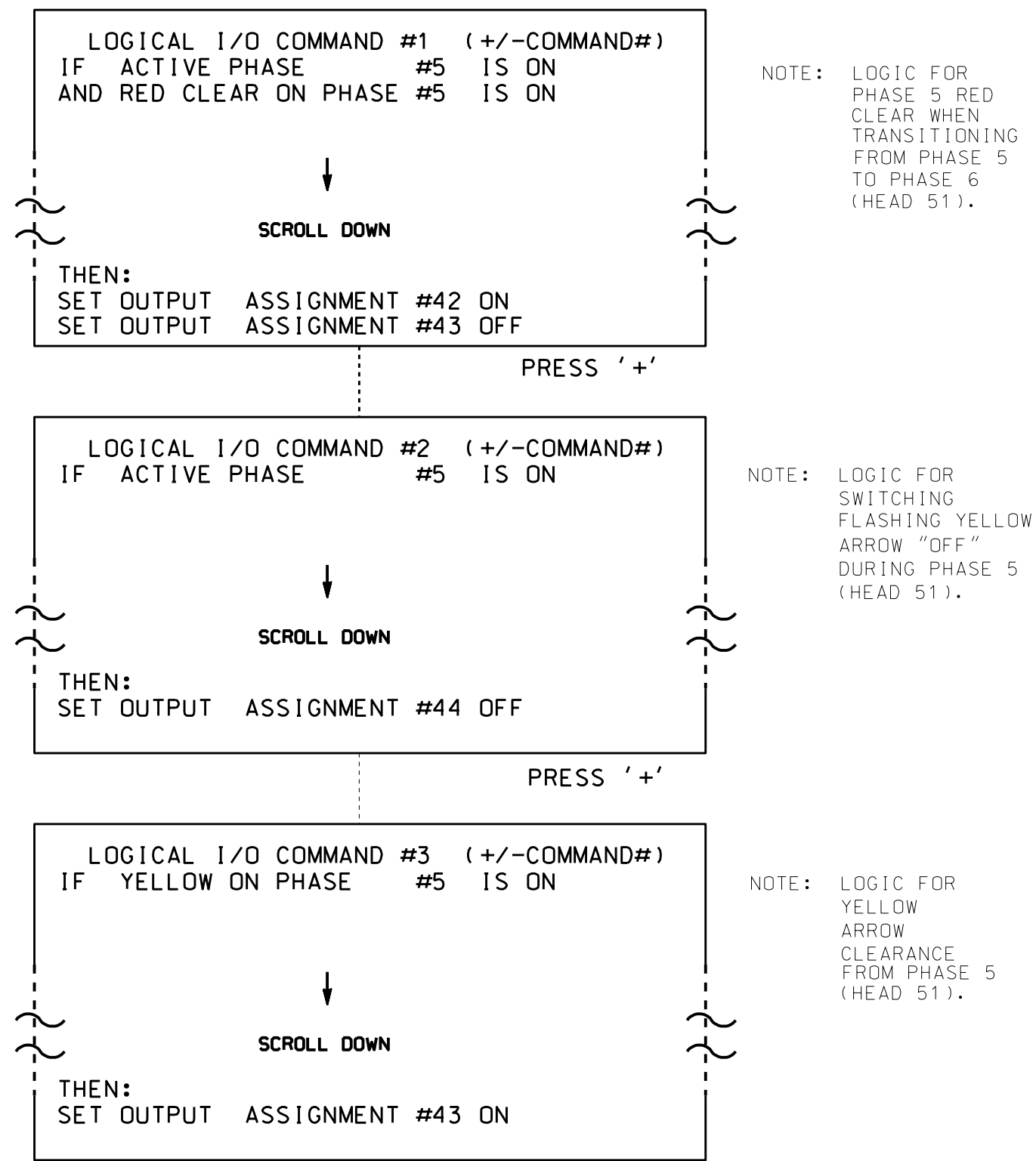


LOGICAL I/O PROCESSOR PROGRAMMING DETAIL TO PRODUCE SPECIAL FYA-PPLT SIGNAL SEQUENCE

(program controller as shown below)

1. FROM MAIN MENU PRESS '2' (PHASE CONTROL), THEN '1' (PHASE CONTROL FUNCTIONS). SCROLL TO THE BOTTOM OF THE MENU AND ENABLE ACT LOGIC COMMANDS 1, 2 AND 3.
2. FROM MAIN MENU PRESS '6' (OUTPUTS), THEN '3' (LOGICAL I/O PROCESSOR).



NOTE: LOGIC FOR PHASE 5 RED CLEAR WHEN TRANSITIONING FROM PHASE 5 TO PHASE 6 (HEAD 51).

NOTE: LOGIC FOR SWITCHING FLASHING YELLOW ARROW "OFF" DURING PHASE 5 (HEAD 51).

NOTE: LOGIC FOR YELLOW ARROW CLEARANCE FROM PHASE 5 (HEAD 51).

LOGIC I/O PROCESSOR PROGRAMMING COMPLETE

OUTPUT REFERENCE SCHEDULE

OUTPUT 42 = Overlap C Red
OUTPUT 43 = Overlap C Yellow
OUTPUT 44 = Overlap C Green

OVERLAP PROGRAMMING DETAIL

(program controller as shown below)

FROM MAIN MENU PRESS '8' (OVERLAPS), THEN '1' (VEHICLE OVERLAP SETTINGS).

PRESS '+' TWICE

```

    PAGE 1: VEHICLE OVERLAP 'C' SETTINGS
    PHASE:           12345678910111213141516
    VEH OVL PARENTS: XX
    VEH OVL NOT VEH:
    VEH OVL NOT PED:
    VEH OVL GRN EXT:
    STARTUP COLOR:  _ RED _ YELLOW _ GREEN
    FLASH COLORS:   _ RED _ YELLOW X GREEN
    SELECT VEHICLE OVERLAP OPTIONS: (Y/N)
    FLASH YELLOW IN CONTROLLER FLASH?...Y
    GREEN EXTENSION (0-255 SEC)...0.0
    YELLOW CLEAR (0=PARENT,3-25.5 SEC)...0.0
    RED CLEAR (0=PARENT,0.1-25.5 SEC)...0.0
    OUTPUT AS PHASE # (0=NONE, 1-16)...0
  
```

← NOTICE GREEN FLASH

PRESS '+'

```

    PAGE 1: VEHICLE OVERLAP 'D' SETTINGS
    PHASE:           12345678910111213141516
    VEH OVL PARENTS: XX
    VEH OVL NOT VEH:
    VEH OVL NOT PED:
    VEH OVL GRN EXT:
    STARTUP COLOR:  _ RED _ YELLOW _ GREEN
    FLASH COLORS:   _ RED _ YELLOW _ GREEN
    SELECT VEHICLE OVERLAP OPTIONS: (Y/N)
    FLASH YELLOW IN CONTROLLER FLASH?...N
    GREEN EXTENSION (0-255 SEC)...0.0
    YELLOW CLEAR (0=PARENT,3-25.5 SEC)...0.0
    RED CLEAR (0=PARENT,0.1-25.5 SEC)...0.0
    OUTPUT AS PHASE # (0=NONE, 1-16)...0
  
```

OVERLAP PROGRAMMING COMPLETE

THIS ELECTRICAL DETAIL IS FOR
THE SIGNAL DESIGN: 07-1323
DESIGNED: May 2014
SEALED: 4-23-15
REVISED: NA

ELECTRICAL DETAIL SHEET 2 OF 2

<p style="font-size: small;">ELECTRICAL AND PROGRAMMING DETAILS FOR:</p> <p style="font-size: x-small;">Prepared in the Offices of: TRANSPO TRANSPORTATION MOBILITY AND SAFETY INSTITUTE OF NORTH CAROLINA 750 N. Greenfield Pkwy, Garner, NC 27529</p>	<p>SR 1486 (Greensboro Rd./W. Main St.) at SR 1536 (Penny Rd.)</p> <p style="font-size: x-small;">Division 7 Guilford County Jamestown</p> <p style="font-size: x-small;">PLAN DATE: September 2014 REVIEWED BY: <i>JTP</i> PREPARED BY: James Peterson REVIEWED BY:</p> <table border="1" style="width: 100%; border-collapse: collapse;"> <thead> <tr> <th style="font-size: x-small;">REVISIONS</th> <th style="font-size: x-small;">INIT.</th> <th style="font-size: x-small;">DATE</th> </tr> </thead> <tbody> <tr> <td> </td> <td> </td> <td> </td> </tr> </tbody> </table>	REVISIONS	INIT.	DATE				<p>SEAL</p> <p style="font-size: x-small;">NORTH CAROLINA PROFESSIONAL SEAL 008453 ENGINEER JOHN T. ROWE, JR.</p> <p style="font-size: x-small;">DocuSigned by: John T. Rowe, Jr. 4/30/2015 DATE</p> <p style="font-size: x-small;">SIG. INVENTORY NO. 07-1323</p>
REVISIONS	INIT.	DATE						

30-APR-2015 10:18
S:\IT\5501\T5\Sig\m\work\gpc\sig\m\peter\son\071323_sml.ele...xxx.dgn
J. Peterson