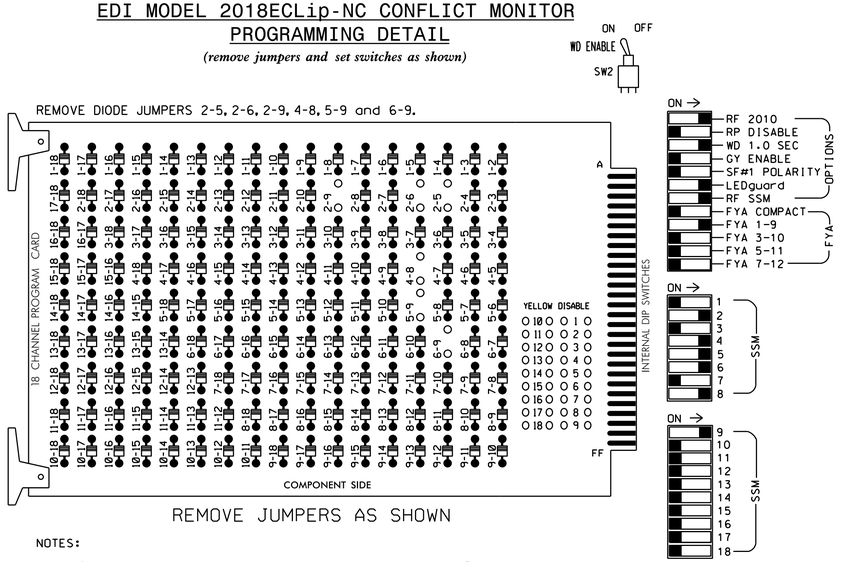
PROJECT REFERENCE NO.	SHEET NO.
C-5558	Sig. 107.1

EDI MODEL 2018ECLip-NC CONFLICT MONITOR PROGRAMMING DETAIL

(remove jumpers and set switches as shown)



REMOVE JUMPERS AS SHOWN

NOTES:

1. Card is provided with all diode jumpers in place. Removal of any jumper allows its channels to run concurrently.
2. Ensure jumpers SEL2-SEL5 and SEL9 are present on the monitor board.
3. Ensure that Red Enable is active at all times during normal operation.
4. Integrate monitor with Ethernet network in cabinet.

■ = DENOTES POSITION OF SWITCH

NOTES

1. To prevent "flash-conflict" problems, insert red flash program blocks for all unused vehicle load switches in the output file. The installer shall verify that signal heads flash in accordance with the Signal Plans.
2. Program phases 4 and 8 for Dual Entry.
3. Enable Simultaneous Gap-Out for all phases.
4. Program phases 2 and 6 for Start Up In Green.
5. Program phases 2 and 6 for Yellow Flash, and overlap 1 as Wag Overlaps.
6. The cabinet and controller are part of the High Point Signal System.

SIGNAL HEAD HOOK-UP CHART

LOAD SWITCH NO.	S1	S2	S3	S4	S5	S6	S7	S8	S9	S10	S11	S12	AUX S1	AUX S2	AUX S3	AUX S4	AUX S5	AUX S6
CMU CHANNEL NO.	1	2	13	3	4	14	5	6	15	7	8	16	9	10	17	11	12	18
PHASE	1	2	2 PED	3	4	4 PED	5	6	6 PED	7	8	8 PED	OLA	OLB	SPARE	OLC	OLD	SPARE
SIGNAL HEAD NO.	NU	21,22 23	NU	NU	41,42	NU	51	62,63	NU	NU	81,82	NU	61★	NU	NU	NU	NU	NU
RED		128			101			134			107							
YELLOW		129			102			135			108							
GREEN		130			103			136			109							
RED ARROW							131						A121					
YELLOW ARROW							132						A122					
FLASHING YELLOW ARROW													A123					
GREEN ARROW							133											

NU = Not Used

✱ Denotes install load resistor. See load resistor installation detail this sheet.

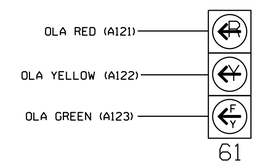
★ See pictorial of head wiring in detail below.

EQUIPMENT INFORMATION

CONTROLLER.....2070L
CABINET.....332 /W/ AUX
SOFTWARE.....ECONOLITE OASIS
CABINET MOUNT.....BASE
OUTPUT FILE POSITIONS...18 WITH AUX. OUTPUT FILE
LOAD SWITCHES USED.....S2,S5,S7,S8,S11,AUX S1.
PHASES USED.....2,4,5,6,8.
OVERLAP "A".....2
OVERLAP "B".....NOT USED
OVERLAP "C".....NOT USED
OVERLAP "D".....NOT USED

3 SECTION FYA PPLT SIGNAL WIRING DETAIL

(wire signal heads as shown)



INPUT FILE POSITION LAYOUT

(front view)

		1	2	3	4	5	6	7	8	9	10	11	12	13	14
FILE "I"	U	SLOT	Ø2 2A,2B	SLOT	SLOT	SLOT	Ø4 4A	SLOT	SLOT	SYS. DET. S1	SLOT	SLOT	SLOT	SLOT	FS DC ISOLATOR
	L	EMPTY	NOT USED	EMPTY	EMPTY	EMPTY	Ø4 4B	EMPTY	EMPTY	SYS. DET. S2	EMPTY	EMPTY	EMPTY	EMPTY	ST DC ISOLATOR
FILE "J"	U	Ø5 5A	Ø6 6A,6B	Ø6 6E	SLOT	SLOT	Ø8 8A	SLOT	SLOT	SYS. DET. S3	SLOT	SLOT	SLOT	SLOT	SLOT
	L	NOT USED	NOT USED	NOT USED	EMPTY	EMPTY	Ø8 8B	EMPTY	EMPTY	SYS. DET. S4	EMPTY	EMPTY	EMPTY	EMPTY	EMPTY

EX.: 1A, 2A, ETC. = LOOP NO.'S

FS = FLASH SENSE
ST = STOP TIME

INPUT FILE CONNECTION & PROGRAMMING CHART

LOOP NO.	LOOP TERMINAL	INPUT FILE POS.	PIN NO.	INPUT ASSIGNMENT NO.	DETECTOR NO.	NEMA PHASE	CALL	EXTEND	FULL TIME DELAY	STRETCH TIME	DELAY TIME
2A,2B	TB2-5,6	I2U	39	1	2	2	Y	Y			
4A	TB4-9,10	I6U	41	3	4	4	Y	Y			3
4B	TB4-11,12	I6L	45	7	14	4	Y	Y			10
✱ S1	TB6-9,10	I9U	60	22	11	SYS					
✱ S2	TB6-11,12	I9L	62	24	13	SYS					
5A	TB3-1,2	J1U	55	17	5	5	Y	Y			3
6A,6B	TB3-5,6	J2U	40	2	6	6	Y	Y			
6E	TB3-9,10	J3U	64	26	36	6	Y	Y			
8A	TB5-9,10	J6U	42	4	8	8	Y	Y			3
8B	TB5-11,12	J6L	46	8	18	8	Y	Y			10
✱ S3	TB7-9,10	J9U	59	21	15	SYS					
✱ S4	TB7-11,12	J9L	61	23	17	SYS					

✱ SYSTEM DETECTOR ONLY. REMOVE THE VEHICLE PHASE ASSIGNED TO THIS DETECTOR IN THE DEFAULT PROGRAMMING.

INPUT FILE POSITION LEGEND: J2L
FILE J
SLOT 2
LOWER

OVERLAP PROGRAMMING DETAIL

(program controller as shown below)

FROM MAIN MENU PRESS '8' (OVERLAPS), THEN '1' (VEHICLE OVERLAP SETTINGS).

```
 PAGE 1: VEHICLE OVERLAP 'A' SETTINGS
 PHASE:            ¦123456789101112131415 16
VEH OVL PARENTS:¦ X
VEH OVL NOT VEH:¦
VEH OVL NOT PED:¦
VEH OVL GRN EXT:¦
STARTUP COLOR: _ RED  _ YELLOW  _ GREEN
FLASH COLORS:  _ RED  _ YELLOW  X GREEN
SELECT VEHICLE OVERLAP OPTIONS:   (Y/N)
FLASH YELLOW IN CONTROLLER FLASH?...Y
GREEN EXTENSION (0-255 SEC).........0
YELLOW CLEAR (0=PARENT,3-25.5 SEC)..0.0
RED CLEAR (0=PARENT,0.1-25.5 SEC)...0.0
OUTPUT AS PHASE # (0=NONE, 1-16)....0
```

NOTICE GREEN FLASH

OVERLAP PROGRAMMING COMPLETE

THIS ELECTRICAL DETAIL IS FOR
THE SIGNAL DESIGN: 07-1291
DESIGNED: September 2014
SEALED: 4-23-15
REVISED: N/A

ELECTRICAL DETAIL

ELECTRICAL AND PROGRAMMING DETAILS FOR:

Prepared in the Offices of:
Transportation Mobility and Safety Division
STATE OF NORTH CAROLINA DEPARTMENT OF TRANSPORTATION
Signal Management Section
750 N.Greenfield Pkwy,Garner,NC 27529

SR 1300 (West Fairfield Road)
at
Plaza Lane

Division 7 Guilford County DS High Point

PLAN DATE: November 2014	REVIEWED BY:	JTR	
PREPARED BY: James Peterson	REVIEWED BY:		

REVISIONS	INIT.	DATE

SEAL
NORTH CAROLINA PROFESSIONAL SEAL 008453 ENGINEER JOHN T. ROWE, JR.

DocuSigned by:
John T. Rowe, Jr. 4/27/2015
DATE
SIG. INVENTORY NO. 07-1291

24-APR-2015 13:37
S:*TIS\SIG*TIS Signals\Workgroups\Sig Mon\Petersonm071291_sm_ele_xxx.dgn
jtpeterson