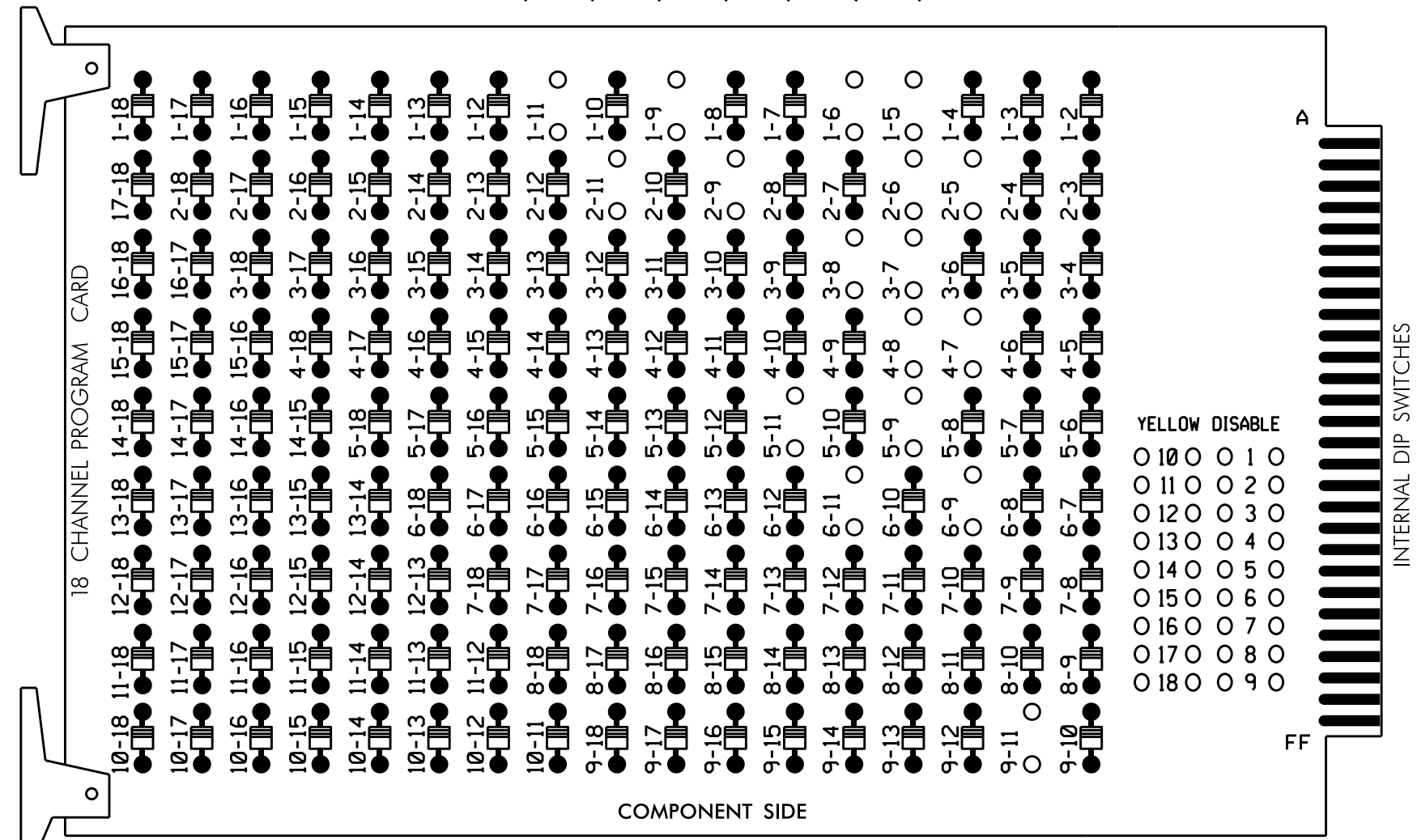


EDI MODEL 2018ECLIP-NC CONFLICT MONITOR

PROGRAMMING DETAIL

(remove jumpers and set switches as shown)

REMOVE DIODE JUMPERS 1-5, 1-6, 1-9, 1-11, 2-5, 2-6, 2-9, 2-11, 3-7, 3-8, 4-7, 4-8, 5-9, 5-11, 6-9, 6-11, and 9-11.



REMOVE JUMPERS AS SHOWN

NOTES:

- Card is provided with all diode jumpers in place. Removal of any jumper allows its channels to run concurrently.
- Ensure jumpers SEL2-SEL5 and SEL9 are present on the monitor board.
- Ensure that Red Enable is active at all times during normal operation.
- Integrate monitor with Ethernet network in cabinet.

■ = DENOTES POSITION OF SWITCH

NOTES

- To prevent "flash-conflict" problems, insert red flash program blocks for all unused vehicle load switches in the output file. The installer shall verify that signal heads flash in accordance with the Signal Plans.
- Enable Simultaneous Gap-Out for all phases.
- Program phases 2 and 6 for Variable Initial and Gap Reduction.
- Program phases 2 and 6 for Start Up In Green.
- Program phases 2 and 6 for Yellow Flash, and overlap 1 as Wag Overlaps.
- The cabinet and controller are part of the High Point Signal System.

EQUIPMENT INFORMATION

CONTROLLER.....2070  
 CABINET.....332 W/ AUX  
 SOFTWARE.....ECONOLITE OASIS  
 CABINET MOUNT.....BASE  
 OUTPUT FILE POSITIONS...18 WITH AUX. OUTPUT FILE  
 LOAD SWITCHES USED.....S1.S2.S4.S5.S7.S8.S10.S11.  
 AUX S1,AUX S4  
 PHASES USED.....1,2,3,4,5,6,7,8  
 OVERLAP "A".....1+2  
 OVERLAP "B".....NOT USED  
 OVERLAP "C".....5+6  
 OVERLAP "D".....NOT USED

SIGNAL HEAD HOOK-UP CHART

LOAD SWITCH NO.	S1	S2	S3	S4	S5	S6	S7	S8	S9	S10	S11	S12	AUX S1	AUX S2	AUX S3	AUX S4	AUX S5	AUX S6
CMU CHANNEL NO.	1	2	13	3	4	14	5	6	15	7	8	16	9	10	17	11	12	18
PHASE	1	2	2 PED	3	4	4 PED	5	6	6 PED	7	8	8 PED	OLA	OLB	SPARE	OLC	OLD	SPARE
SIGNAL HEAD NO.	11*	83	21,22	NU	31	41,42 43	NU	43	51*	61,62	NU	71	81,82 83	NU	11*	NU	51*	NU
RED		*	128			101		*		134								
YELLOW			129			102				135								
GREEN			130			103				136								
RED ARROW						116										A121		A114
YELLOW ARROW						117				132						A122		A115
FLASHING YELLOW ARROW																A123		A116
GREEN ARROW	127	127				118				133	133							124

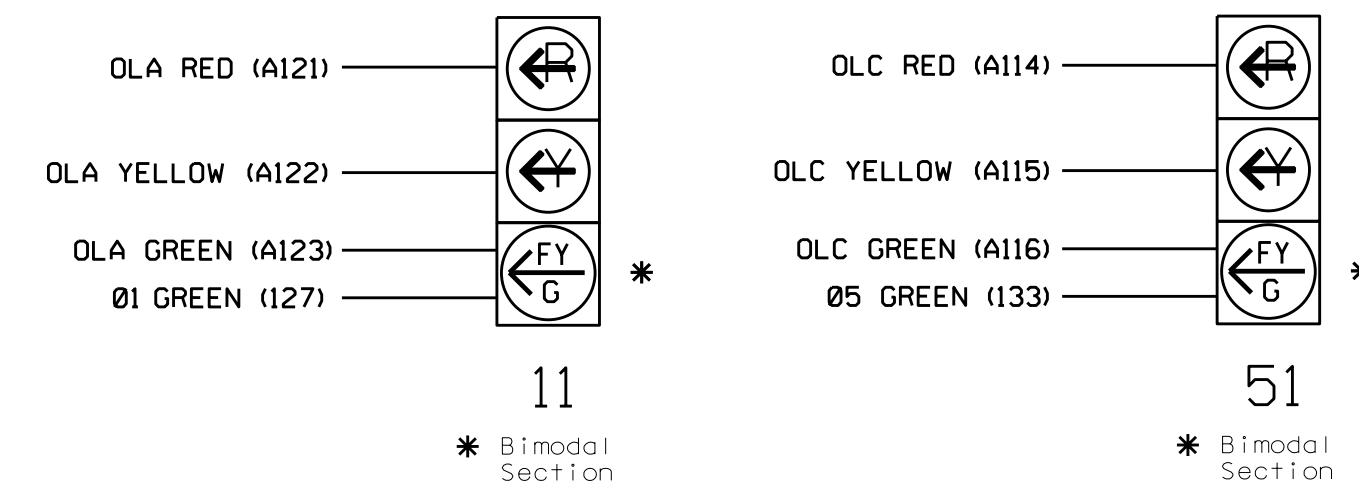
NU = Not Used

\* Denotes install load resistor. See load resistor installation detail this sheet.

\* See pictorial of head wiring in detail below.

FYA SIGNAL WIRING DETAIL

(wire signal heads as shown)



NOTE

The sequence display for signal heads 11 and 51 requires special logic programming. See sheet 2 for programming instructions.

INPUT FILE POSITION LAYOUT

(front view)

FILE U	1	2	3	4	5	6	7	8	9	10	11	12	13	14
FILE U	∅ 1	∅ 1	∅ 2	∅ 3	∅ 4	SYS. DET. S1	S	SYS. DET. S3	S	S	S	S	S	FS
FILE U	1A	1B	2A	3A	4A	SYS. DET. S2	S	SYS. DET. S4	S	S	S	S	S	DC ISOLATOR
FILE U	NOT USED	NOT USED	∅ 2	NOT USED	∅ 4	SYS. DET. S5	S	SYS. DET. S7	S	S	S	S	S	DC ISOLATOR
FILE U	∅ 5	∅ 5	∅ 6	∅ 7	∅ 8	SYS. DET. S6	S	SYS. DET. S8	S	S	S	S	S	S
FILE U	5A	5B	6A	7A	8A	SYS. DET. S8	S	SYS. DET. S8	S	S	S	S	S	S
FILE U	NOT USED	NOT USED	∅ 6	NOT USED	∅ 8	SYS. DET. S6	S	SYS. DET. S8	S	S	S	S	S	S

EX.: 1A, 2A, ETC. = LOOP NO.'S

FS = FLASH SENSE  
 ST = STOP TIME

⊗ Wired Input - Do not populate slot with detector card

INPUT FILE CONNECTION & PROGRAMMING CHART

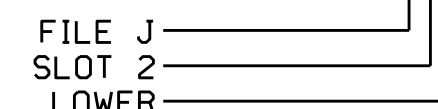
LOOP NO.	LOOP TERMINAL	INPUT FILE POS.	PIN NO.	INPUT ASSIGNMENT NO.	DETECTOR NO.	NEMA PHASE	CALL	EXTEND	FULL TIME DELAY	STRETCH TIME	DELAY TIME
1A <sup>1</sup>	TB2-1,2	I1U	56	18	1	1	Y	Y			15
	-	J4U	48	10	26	6	Y	Y	Y		3
1B	TB2-5,6	I2U	39	1	2	1	Y	Y	Y		15
2A	TB2-9,10	I3U	63	25	32	2	Y	Y			
2B	TB2-11,12	I3L	76	38	42	2	Y	Y			
3A	TB4-5,6	I5U	58	20	3	3	Y	Y			3
4A	TB4-9,10	I6U	41	3	4	4	Y	Y			
4B	TB4-11,12	I6L	45	7	14	4	Y	Y			
5A <sup>2</sup>	TB3-1,2	J1U	55	17	5	5	Y	Y			15
	-	I4U	47	9	22	2	Y	Y	Y		3
5B	TB3-5,6	J2U	40	2	6	5	Y	Y	Y		15
6A	TB3-9,10	J3U	64	26	36	6	Y	Y			
6B	TB3-11,12	J3L	77	39	46	6	Y	Y			
7A	TB5-5,6	J5U	57	19	7	7	Y	Y			3
8A	TB5-9,10	J6U	42	4	8	8	Y	Y			
8B	TB5-11,12	J6L	46	8	18	8	Y	Y			
* S1	TB6-1,2	I7U	65	27	34	SYS					
* S2	TB6-3,4	I7L	78	40	44	SYS					
* S3	TB6-9,10	I9U	60	22	11	SYS					
* S4	TB6-11,12	I9L	62	24	13	SYS					
* S5	TB7-1,2	J7U	66	28	38	SYS					
* S6	TB7-3,4	J7L	79	41	48	SYS					
* S7	TB7-9,10	J9U	59	21	15	SYS					
* S8	TB7-11,12	J9L	61	23	17	SYS					

<sup>1</sup>Add jumper from I1-W to J4-W, on rear of input file.

<sup>2</sup>Add jumper from J1-W to I4-W, on rear of input file.

\* System detector only. Remove the vehicle phase assigned to this detector in the default programming.

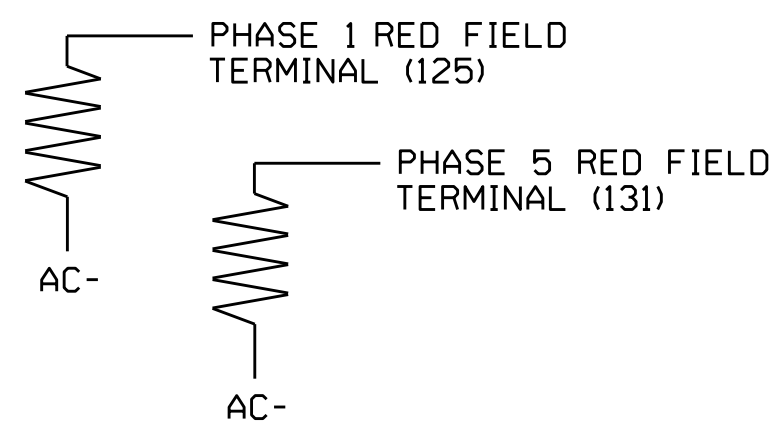
INPUT FILE POSITION LEGEND: J2L



LOAD RESISTOR INSTALLATION DETAIL

(install resistors as shown below)

VALUE (ohms)	WATTAGE
1.5K - 1.9K	25W (min)
2.0K - 3.0K	10W (min)



Electrical Detail - Sheet 1 of 2

Electrical and Programming Details for: SR 1961 (W. Market Center Dr.) at SR 1300 (West Green Drive)

Prepared In the Offices of: **Transpacific Mobility and Safety Solutions**

Division 7 Guilford County High Point

PLAN DATE: December 2014 REVIEWED BY: *[Signature]*

PREPARED BY: S. Armstrong REVIEWED BY: *[Signature]*

750 N. Greenfield Pkwy, Garner, NC 27529

SEAL: JOHN T. ROWE, Jr., ENGINEER, No. 008453

DocuSigned by: John T. Rowe, Jr., 4/8/2015

SIG. INVENTORY NO. 07-0949