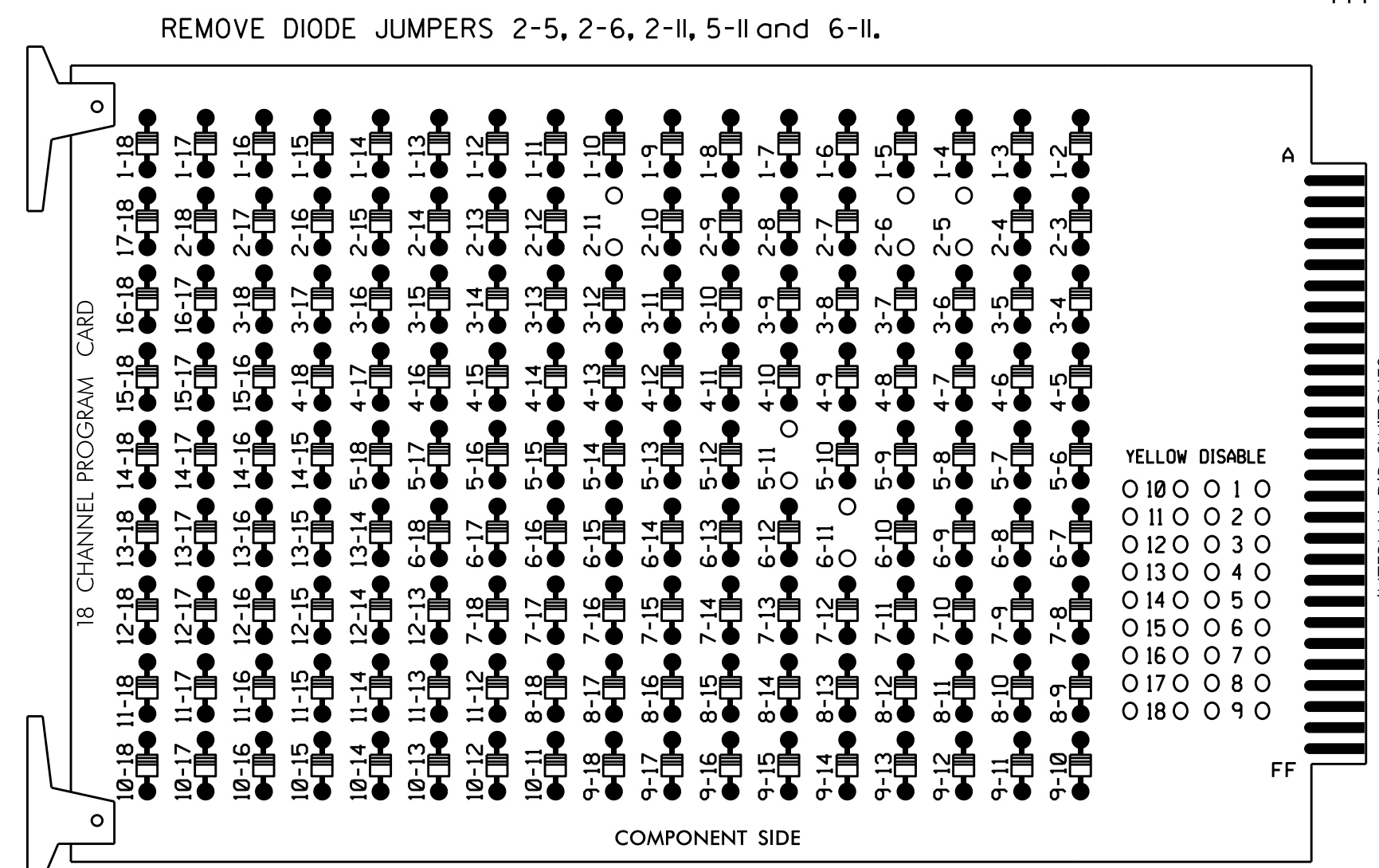


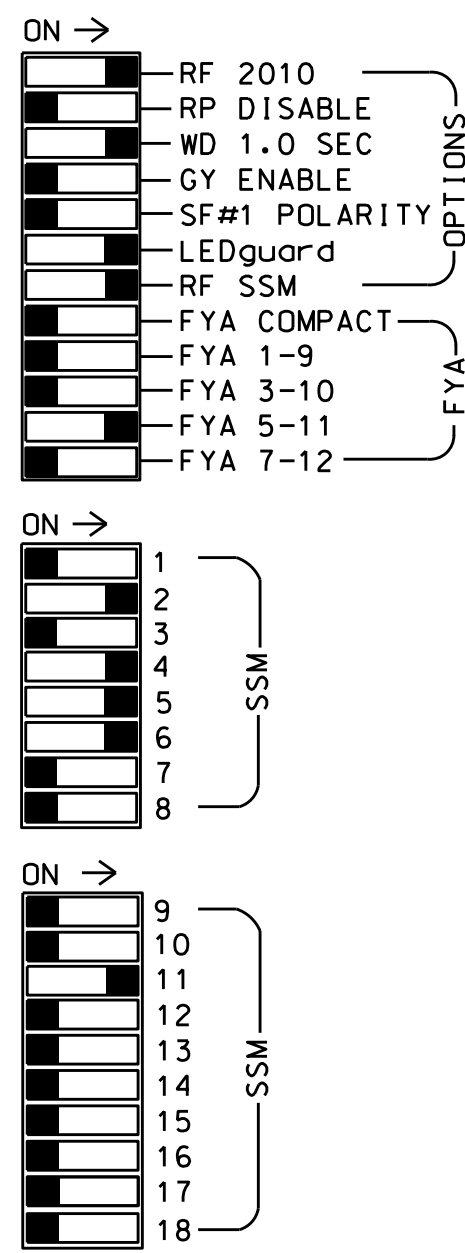
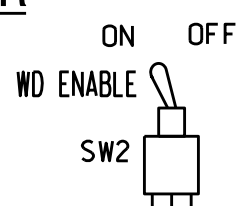
**EDI MODEL 2018ECLip-NC CONFLICT MONITOR  
PROGRAMMING DETAIL**

(remove jumpers and set switches as shown)



**NOTES:**

1. Card is provided with all diode jumpers in place. Removal of any jumper allows its channels to run concurrently.
2. Ensure jumpers SEL2-SEL5 and SEL9 are present on the monitor board.
3. Ensure that Red Enable is active at all times during normal operation.
4. Integrate monitor with Ethernet network in cabinet.



■ = DENOTES POSITION OF SWITCH

**NOTES**

1. To prevent "flash-conflict" problems, insert red flash program blocks for all unused vehicle load switches in the output file. The installer shall verify that signal heads flash in accordance with the Signal Plans.
2. Enable Simultaneous Gap-Out for all phases.
3. Program phases 2 and 6 for Start Up In Green.
4. Program phases 2 and 6 for Yellow Flash.
5. The cabinet and controller are part of the High Point Signal System.

**EQUIPMENT INFORMATION**

CONTROLLER.....2070L  
 CABINET.....332 /W/ AUX  
 SOFTWARE.....ECONOLITE OASIS  
 CABINET MOUNT.....BASE  
 OUTPUT FILE POSITIONS...18 WITH AUX. OUTPUT FILE  
 LOAD SWITCHES USED.....S2,S5,S7,S8,AUX S4.  
 PHASES USED.....2,4,5,6.  
 OVERLAP "A".....NOT USED  
 OVERLAP "B".....NOT USED  
 OVERLAP "C".....5+6  
 OVERLAP "D".....NOT USED

**SIGNAL HEAD HOOK-UP CHART**

LOAD SWITCH NO.	S1	S2	S3	S4	S5	S6	S7	S8	S9	S10	S11	S12	AUX S1	AUX S2	AUX S3	AUX S4	AUX S5	AUX S6
CMU CHANNEL NO.	1	2	13	3	4	14	5	6	15	7	8	16	9	10	17	11	12	18
PHASE	1	2	2 PED	3	4	4 PED	5	6	6 PED	7	8	8 PED	OLA	OLB	SPARE	OLC	OLD	SPARE
SIGNAL HEAD NO.	NU	21,22	NU	NU	41,42	NU	42	51	61,62	NU	NU	NU	NU	NU	NU	51	NU	NU
RED		128			101		*		134									
YELLOW		129			102				135									
GREEN		130			103				136									
RED ARROW																A114		
YELLOW ARROW								132								A115		
FLASHING YELLOW ARROW																A116		
GREEN ARROW							133	133										

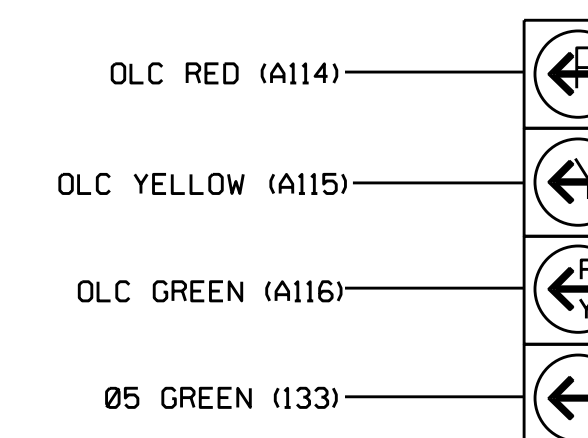
NU = Not Used

\* Denotes install load resistor. See load resistor installation detail this sheet.

\* See pictorial of head wiring in detail below.

**4 SECTION FYA PPLT SIGNAL WIRING DETAIL**

(wire signal heads as shown)



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**NOTE**

1. The sequence display for this signal requires special logic programming. See sheet 2 for programming instructions.

**BACKUP PROTECTION NOTE**

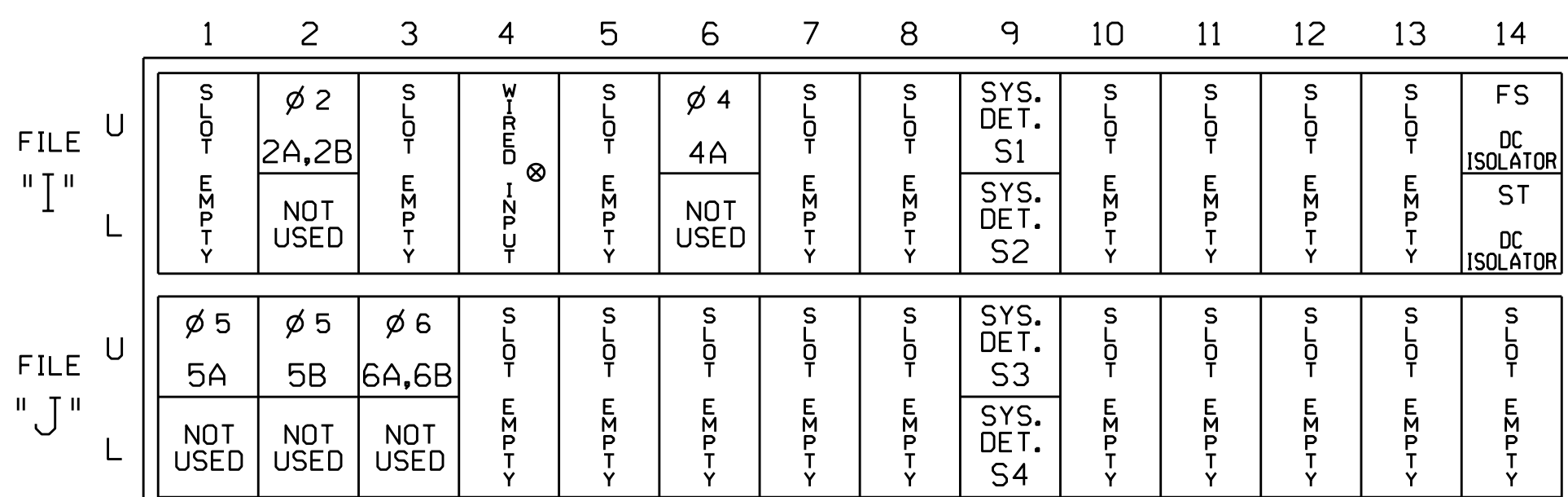
(program controller as shown below)

From Main Menu press '2' (Phase Control), then '1' (Phase Control Functions). Program phase 2 for 'Backup Protect'. Make sure the Red Revert times shown on the Signal Design Plans are programmed in the 'Phase Timing' menu.

THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 07-0869  
 DESIGNED: July 2014  
 SEALED: 4-23-15  
 REVISED: N/A

**INPUT FILE POSITION LAYOUT**

(front view)



EX.: 1A, 2A, ETC. = LOOP NO.'S

FS = FLASH SENSE  
 ST = STOP TIME

⊗ Wired Input - Do not populate slot with detector card

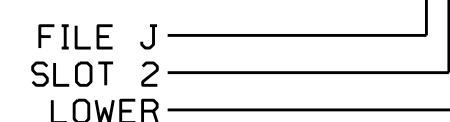
**INPUT FILE CONNECTION & PROGRAMMING CHART**

LOOP NO.	LOOP TERMINAL	INPUT FILE POS.	PIN NO.	INPUT ASSIGNMENT NO.	DETECTOR NO.	NEMA PHASE	CALL	EXTEND	FULL TIME DELAY	STRETCH TIME	DELAY TIME
2A,2B	TB2-5,6	I2U	39	1	2	2	Y	Y			
4A	TB4-9,10	I6U	41	3	4	4	Y	Y			3
* S1	TB6-9,10	I9U	60	22	11	SYS					
* S2	TB6-11,12	I9L	62	24	13	SYS					
5A <sup>1</sup>	TB3-1,2	J1U	55	17	5	5	Y	Y			15
	-	I4U	47	9	22	2	Y	Y			
5B	TB3-5,6	J2U	40	2	6	5	Y	Y			15
6A,6B	TB3-9,10	J3U	64	26	36	6	Y	Y			
* S3	TB7-9,10	J9U	59	21	15	SYS					
* S4	TB7-11,12	J9L	61	23	17	SYS					

<sup>1</sup>Add jumper from J1-W to I4-W, on rear of input file.

\* System detector only. Remove the vehicle phase assigned to this detector in the default programming.

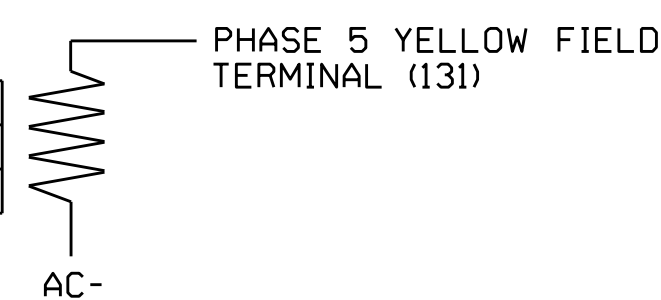
**INPUT FILE POSITION LEGEND: J2L**



**LOAD RESISTOR INSTALLATION DETAIL**

(install resistors as shown below)

VALUE (ohms)	WATTAGE
1.5K - 1.9K	25W (min)
2.0K - 3.0K	10W (min)



**ELECTRICAL DETAIL SHEET 1 OF 2**

ELECTRICAL AND PROGRAMMING DETAILS FOR:

Prepared in the Offices of:  
  
 750 N. Greenfield Pkwy, Garner, NC 27529

NC 610 (E. Fairfield Road) at Brentwood Street

Division 7 Guilford County High Point

PLAN DATE: October 2014 REVIEWED BY: JTR  
 PREPARED BY: James Peterson REVIEWED BY:

REVISIONS INIT. DATE

DocuSigned by: John T. Rowe, Jr. 4/27/2015  
 SEAL 008453  
 JOHN T. ROWE, JR. ENGINEER  
 SIG. INVENTORY NO. 07-0869