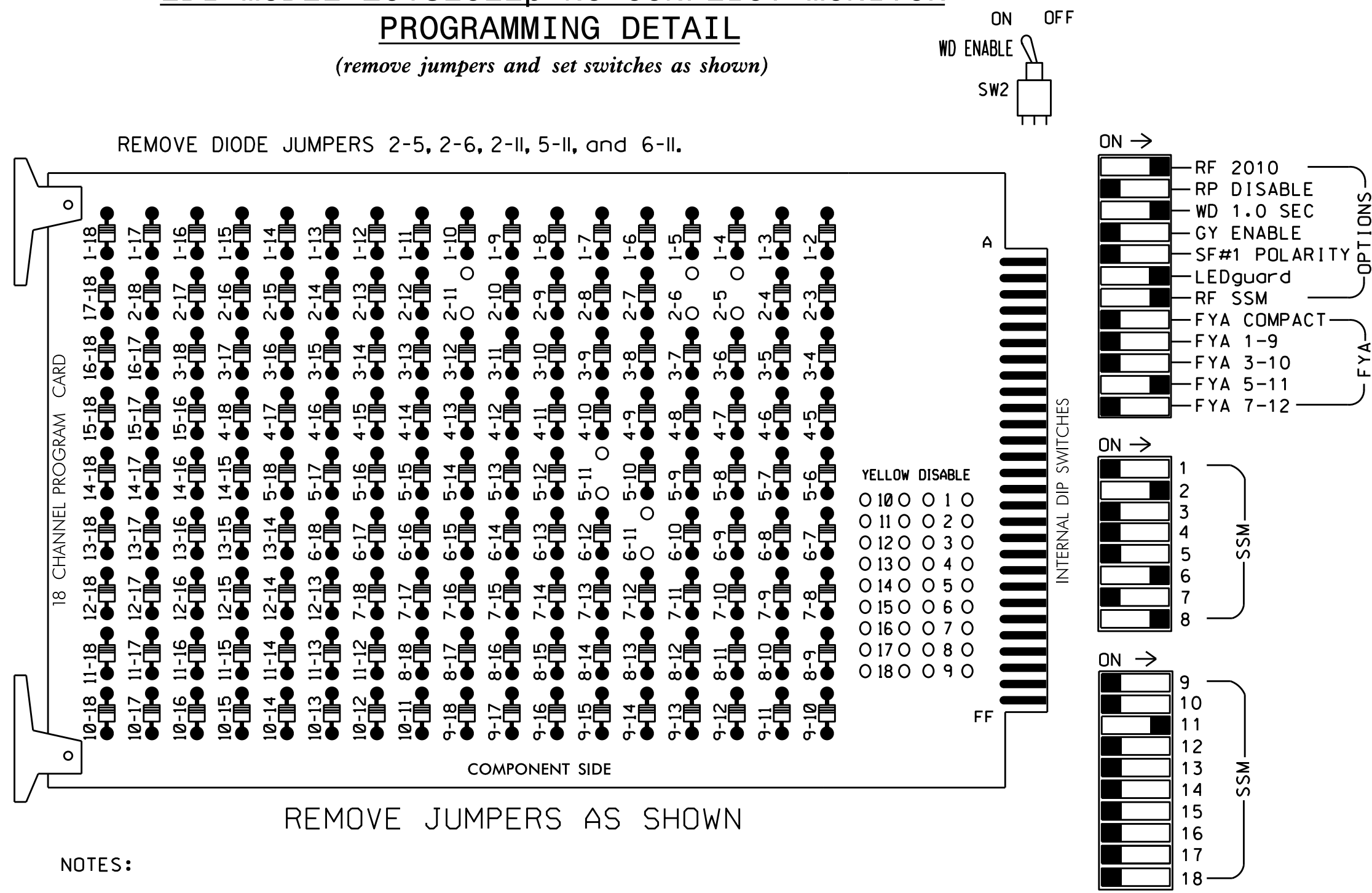


**EDI MODEL 2018ECLip-NC CONFLICT MONITOR
PROGRAMMING DETAIL**

(remove jumpers and set switches as shown)



NOTES:

- Card is provided with all diode jumpers in place. Removal of any jumper allows its channels to run concurrently.
- Ensure jumpers SEL2-SEL5 and SEL9 are present on the monitor board.
- Ensure that Red Enable is active at all times during normal operation.
- Integrate monitor with Ethernet network in cabinet.

NOTES

- To prevent "flash-conflict" problems, insert red flash program blocks for all unused vehicle load switches in the output file. The installer shall verify that signal heads flash in accordance with the Signal Plans.
- Enable Simultaneous Gap-Out for all phases.
- Program phases 2 and 6 for Start Up In Green.
- Program phases 2 and 6 for Yellow Flash.
- The cabinet and controller are part of the High Point Signal System.

EQUIPMENT INFORMATION

CONTROLLER.....2070
CABINET.....332 W/ AUX
SOFTWARE.....ECONOLITE OASIS
CABINET MOUNT.....BASE
OUTPUT FILE POSITIONS...18 WITH AUX. OUTPUT FILE
LOAD SWITCHES USED.....S2,S7,S8,S11,AUX S4
PHASES USED.....2,5,6,8
OVERLAP "A".....NOT USED
OVERLAP "B".....NOT USED
OVERLAP "C".....5+6
OVERLAP "D".....NOT USED

SIGNAL HEAD HOOK-UP CHART

LOAD SWITCH NO.	S1	S2	S3	S4	S5	S6	S7	S8	S9	S10	S11	S12	AUX S1	AUX S2	AUX S3	AUX S4	AUX S5	AUX S6	
CMU CHANNEL NO.	1	2	13	3	4	14	5	6	15	7	8	16	9	10	17	11	12	18	
PHASE	1	2	2 PED	3	4	4 PED	5	6	6 PED	7	8	8 PED	OLA	OLB	SPARE	OLC	OLD	SPARE	
SIGNAL HEAD NO.	NU	22,23	NU	NU	NU	NU	51	61,62	NU	NU	81	82	83	NU	NU	NU	51	NU	
RED		128						134			107	107							
YELLOW		129					*	135			108	108							
GREEN		130						136			109	109							
RED ARROW											107							A114	
YELLOW ARROW											108								A115
FLASHING YELLOW ARROW																			A116
GREEN ARROW								133			109	109							

NU = Not Used

* Denotes install load resistor. See load resistor installation detail this sheet.

* See pictorial of head wiring in detail below.

INPUT FILE POSITION LAYOUT

(front view)

FILE	1	2	3	4	5	6	7	8	9	10	11	12	13	14
U	S	S	S	S	S	S	S	S	S	S	S	S	S	FS
L	←-PZFE	←-PZFE	←-PZFE	←-PZFE	←-PZFE	←-PZFE	←-PZFE	←-PZFE	←-PZFE	←-PZFE	←-PZFE	←-PZFE	←-PZFE	DC ISOLATOR
U	∅ 5	S	S	S	S	∅ 8	∅ 8	S	SYS. DET. S4	S	S	S	S	S
L	NOT USED	←-PZFE	←-PZFE	←-PZFE	←-PZFE	←-PZFE	←-PZFE	←-PZFE	←-PZFE	←-PZFE	←-PZFE	←-PZFE	←-PZFE	DC ISOLATOR

EX.: 1A, 2A, ETC. = LOOP NO.'S

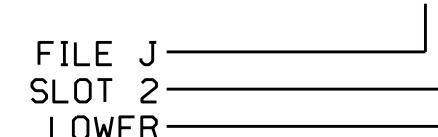
FS = FLASH SENSE
ST = STOP TIME

INPUT FILE CONNECTION & PROGRAMMING CHART

LOOP NO.	LOOP TERMINAL	INPUT FILE POS.	PIN NO.	INPUT ASSIGNMENT NO.	DETECTOR NO.	NEMA PHASE	CALL	EXTEND	FULL TIME DELAY	STRETCH TIME	DELAY TIME
5A	TB3-1,2	J1U	55	17	5	5	Y	Y			15
8A	TB5-9,10	J6U	42	4	8	8	Y	Y			
8B	TB5-11,12	J6L	46	8	18	8	Y	Y			
8C	TB7-1,2	J7U	66	28	38	8	Y	Y			10
* S1	TB6-9,10	J9U	60	22	11	SYS					
* S2	TB6-11,12	J9L	62	24	13	SYS					
* S3	TB7-3,4	J7L	79	41	48	SYS					
* S4	TB7-9,10	J9U	59	21	15	SYS					
* S5	TB7-11,12	J9L	61	23	17	SYS					

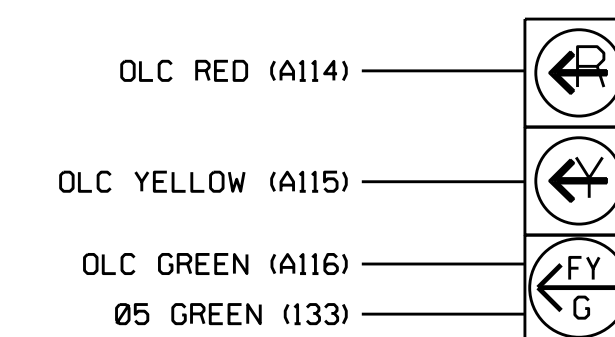
* SYSTEM DETECTOR ONLY. REMOVE THE VEHICLE PHASE ASSIGNED TO THIS DETECTOR IN THE DEFAULT PROGRAMMING.

INPUT FILE POSITION LEGEND: J2L



FYA SIGNAL WIRING DETAIL

(wire signal head as shown)



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* Bimodal Section

NOTE

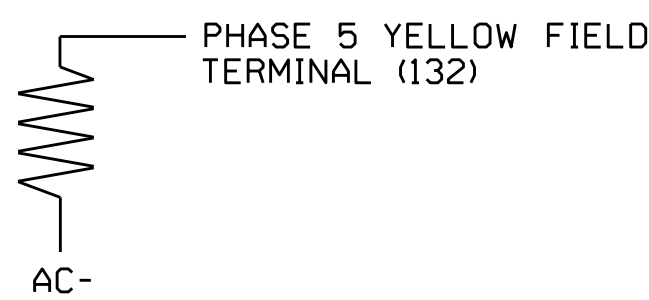
The sequence display for signal head 51 requires special logic programming. See sheet 2 for programming instructions.

LOAD RESISTOR INSTALLATION DETAIL

(install resistor as shown below)

ACCEPTABLE VALUES

VALUE (ohms)	WATTAGE
1.5K - 1.9K	25W (min)
2.0K - 3.0K	10W (min)



Electrical Detail - Sheet 1 of 2

ELECTRICAL AND PROGRAMMING DETAILS FOR: Prepared in the Offices of: 750 N. Greenfield Pkwy, Garner, NC 27529	SR 1009 (South Main Street) at SR 1961 (Market Center Drive)	SEAL
	Division 7 PLAN DATE: November 2014 PREPARED BY: S. Armstrong	GUILFORD COUNTY REVIEWED BY: JTR REVIEWED BY: