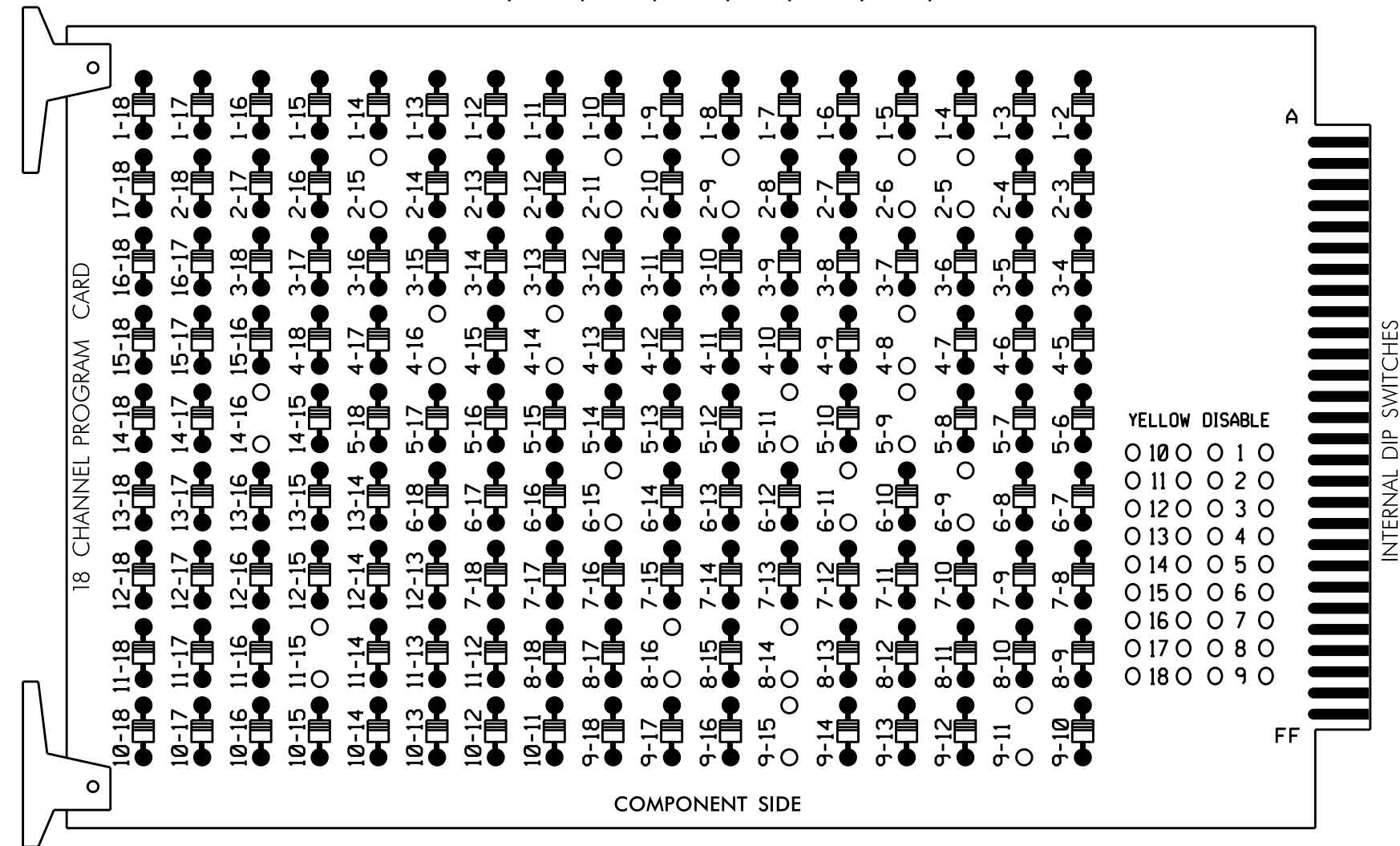


EDI MODEL 2018ECLIP-NC CONFLICT MONITOR

PROGRAMMING DETAIL

(remove jumpers and set switches as shown)

REMOVE DIODE JUMPERS 2-5, 2-6, 2-9, 2-11, 2-15, 4-8, 4-14, 4-16, 5-9, 5-11, 6-9, 6-11, 6-15, 8-14, 8-16, 9-11, 9-15, 11-15, and 14-16.



REMOVE JUMPERS AS SHOWN

NOTES:

1. Card is provided with all diode jumpers in place. Removal of any jumper allows its channels to run concurrently.
2. Ensure jumpers SEL2-SEL5 and SEL9 are present on the monitor board.
3. Ensure that Red Enable is active at all times during normal operation.
4. Integrate monitor with Ethernet network in cabinet.

INPUT FILE POSITION LAYOUT

(front view)

| | | | | | | | | | | | | | | |
|----------|----------|----------|----------|----------|----------|----------|----------|----------|----------|----------|----------|----------|----------|----------|
| FILE "I" | 1 | 2 | 3 | 4 | 5 | 6 | 7 | 8 | 9 | 10 | 11 | 12 | 13 | 14 |
| | ∅4 | ∅4 | ∅4 | ∅4 | ∅4 | ∅4 | ∅4 | ∅4 | ∅4 | ∅4 | ∅4 | ∅4 | ∅4 | ∅4 |
| | 4A | 4A | 4A | 4A | 4A | 4A | 4A | 4A | 4A | 4A | 4A | 4A | 4A | 4A |
| | NOT USED | NOT USED | NOT USED | NOT USED | NOT USED | NOT USED | NOT USED | NOT USED | NOT USED | NOT USED | NOT USED | NOT USED | NOT USED | NOT USED |
| | ∅8 | ∅8 | ∅8 | ∅8 | ∅8 | ∅8 | ∅8 | ∅8 | ∅8 | ∅8 | ∅8 | ∅8 | ∅8 | ∅8 |
| | 8A | 8A | 8A | 8A | 8A | 8A | 8A | 8A | 8A | 8A | 8A | 8A | 8A | 8A |
| | NOT USED | NOT USED | NOT USED | NOT USED | NOT USED | NOT USED | NOT USED | NOT USED | NOT USED | NOT USED | NOT USED | NOT USED | NOT USED | NOT USED |
| FILE "J" | 5A | 5A | 5A | 5A | 5A | 5A | 5A | 5A | 5A | 5A | 5A | 5A | 5A | 5A |
| | 5B | 5B | 5B | 5B | 5B | 5B | 5B | 5B | 5B | 5B | 5B | 5B | 5B | 5B |

EX.: 1A, 2A, ETC. = LOOP NO.'S

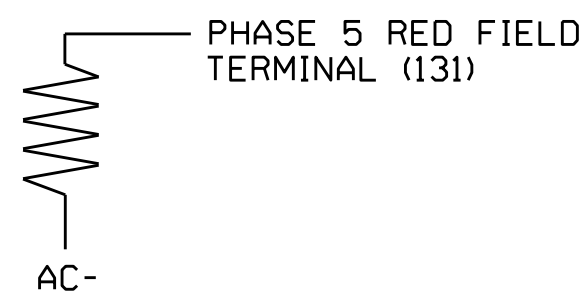
FS = FLASH SENSE
ST = STOP TIME

NOTE: Zone 8A uses microwave detection. See Special Detector Note this sheet.

LOAD RESISTOR INSTALLATION DETAIL

(install resistor as shown below)

| ACCEPTABLE VALUES | |
|-------------------|-----------|
| VALUE (ohms) | WATTAGE |
| 1.5K - 1.9K | 25W (min) |
| 2.0K - 3.0K | 10W (min) |



NOTES

1. To prevent "flash-conflict" problems, insert red flash program blocks for all unused vehicle load switches in the output file. The installer shall verify that signal heads flash in accordance with the Signal Plans.
2. Program phases 4 and 8 for Dual Entry.
3. Enable Simultaneous Gap-Out for all phases.
4. Program phases 2 and 6 for Start Up In Green.
5. Program phases 4, 6 and 8 for 'STARTUP PED CALL'.
6. Program phases 2 and 6 for Yellow Flash, and overlap 1 as Wag Overlaps.
7. The cabinet and controller are part of the High Point Signal System.

EQUIPMENT INFORMATION

CONTROLLER.....2070
 CABINET.....332 W/ AUX
 SOFTWARE.....ECONOLITE OASIS
 CABINET MOUNT.....BASE
 OUTPUT FILE POSITIONS...18 WITH AUX. OUTPUT FILE
 LOAD SWITCHES USED.....S2,S5,S6,S7,S8,S9,S11,S12,
 AUX S1,AUX S4
 PHASES USED.....2,4,4PED,5,6,6PED,8,8PED
 OVERLAP "A".....2
 OVERLAP "B".....NOT USED
 OVERLAP "C".....5+6
 OVERLAP "D".....NOT USED

COUNTDOWN PEDESTRIAN SIGNAL OPERATION

Countdown Ped Signals are required to display timing only during Ped Clearance Interval. Consult Ped Signal Module user's manual for instructions on selecting this feature.

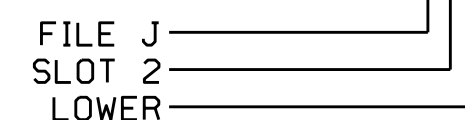
INPUT FILE CONNECTION & PROGRAMMING CHART

| LOOP NO. | LOOP TERMINAL | INPUT FILE POS. | PIN NO. | INPUT ASSIGNMENT NO. | DETECTOR NO. | NEMA PHASE | CALL | EXTEND | FULL TIME DELAY | STRETCH TIME | DELAY TIME |
|------------------|---------------|-----------------|---------|----------------------|--------------|------------|------|--------|-----------------|--------------|------------|
| 4A | TB4-9,10 | I6U | 41 | 3 | 4 | 4 | Y | Y | | | 3 |
| 5A | TB3-5,6 | J2U | 40 | 2 | 6 | 5 | Y | Y | | | 15 |
| 5B | TB3-7,8 | J2L | 44 | 6 | 16 | 5 | Y | Y | | | 15 |
| ZONE 8A | - | J6U | 42 | 4 | 8 | 8 | Y | Y | | | 5 |
| * S1 | TB6-9,10 | I9U | 60 | 22 | 11 | SYS | | | | | |
| * S2 | TB6-11,12 | I9L | 62 | 24 | 13 | SYS | | | | | |
| PED PUSH BUTTONS | | | | | | | | | | | |
| P41,P42 | TB8-5,6 | I12L | 69 | 31 | PED 4 | 4 PED | | | | | |
| P81,P82 | TB8-8,9 | I13L | 70 | 32 | PED 8 | 8 PED | | | | | |

NOTE:
INSTALL DC ISOLATORS IN INPUT FILE SLOTS 112 AND 113.

* SYSTEM DETECTOR ONLY. REMOVE THE VEHICLE PHASE ASSIGNED TO THIS DETECTOR IN THE DEFAULT PROGRAMMING.

INPUT FILE POSITION LEGEND: J2L



SPECIAL DETECTOR NOTE

For detection zone 8A, install a microwave detection system for vehicle detection. Perform installation according to manufacturer's directions and NCDOT engineer-approved mounting locations to accomplish the detection schemes shown on the Signal Design Plans.

SIGNAL HEAD HOOK-UP CHART

| LOAD SWITCH NO. | S1 | S2 | S3 | S4 | S5 | S6 | S7 | S8 | S9 | S10 | S11 | S12 | AUX S1 | AUX S2 | AUX S3 | AUX S4 | AUX S5 | AUX S6 |
|-----------------------|----|-------|-------|----|-------|----------|-----|-----|-------|----------|-----|-------|----------|--------|--------|--------|--------|--------|
| CMU CHANNEL NO. | 1 | 2 | 13 | 3 | 4 | 14 | 5 | 6 | 15 | 7 | 8 | 16 | 9 | 10 | 17 | 11 | 12 | 18 |
| PHASE | 1 | 2 | 2 PED | 3 | 4 | 4 PED | 5 | 6 | 6 PED | 7 | 8 | 8 PED | OLA | OLB | SPARE | OLC | OLD | SPARE |
| SIGNAL HEAD NO. | NU | 21,22 | NU | NU | 41,42 | P41, P42 | 42 | 51 | 61,62 | P61, P62 | NU | 81,82 | P81, P82 | 63 | NU | 51 | NU | NU |
| RED | | 128 | | | 101 | | * | | 134 | | | 107 | | | | | | |
| YELLOW | | 129 | | | 102 | | | | 135 | | | 108 | | | | | | |
| GREEN | | 130 | | | 103 | | | | 136 | | | 109 | | | | | | |
| RED ARROW | | | | | | | | | | | | | | A121 | | | A114 | |
| YELLOW ARROW | | | | | | | 132 | | | | | | | A122 | | | A115 | |
| FLASHING YELLOW ARROW | | | | | | | | | | | | | | A123 | | | A116 | |
| GREEN ARROW | | | | | | | 133 | 133 | | | | | | | | | | |
| Hand icon | | | | | | | 104 | | | | | 119 | | | | | 110 | |
| Person icon | | | | | | | 106 | | | | | 121 | | | | | 112 | |

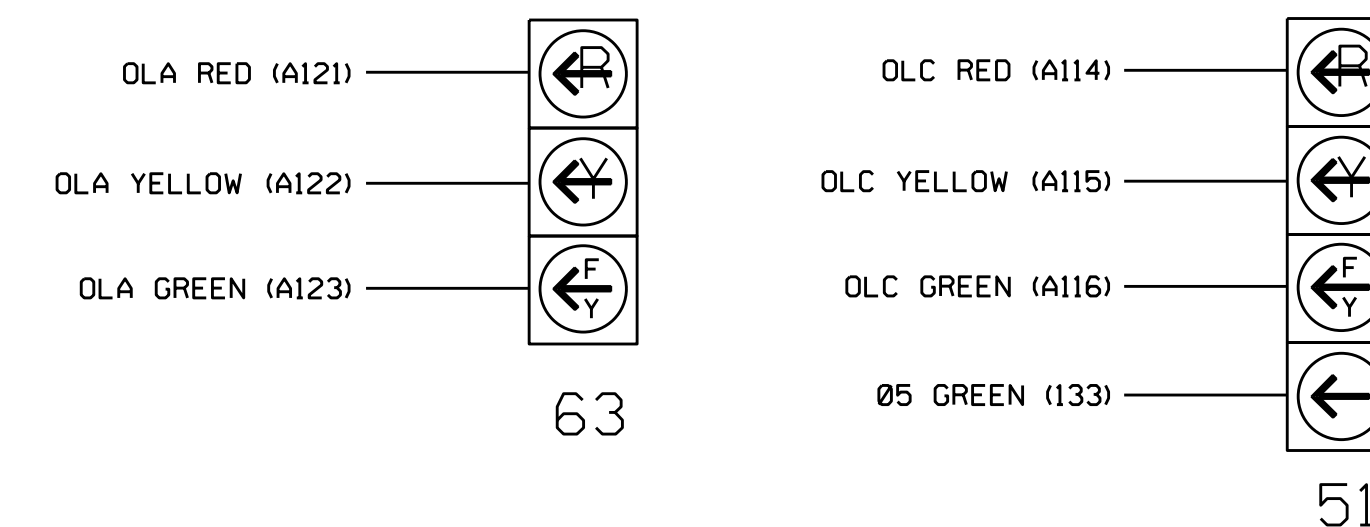
NU = Not Used

* Denotes install load resistor. See load resistor installation detail this sheet.

* See pictorial of head wiring in detail below.

FYA SIGNAL WIRING DETAIL

(wire signal heads as shown)



NOTE

The sequence display for signal head 51 requires special logic programming. See sheet 2 for programming instructions.

THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 07-0769
 DESIGNED: July 2014
 SEALED: 4/21/2015
 REVISED: N/A

Electrical Detail - Sheet 1 of 2

| | | | |
|--|--|--|---|
| ELECTRICAL AND PROGRAMMING DETAILS FOR: Prepared in the Offices of: 750 N. Greenfield Pkwy, Garner, NC 27529 | SR 1009 (North Main Street) at Church Avenue | | SEAL JOHN T. ROWE, JR. ENGINEER |
| | Division 7 PLAN DATE: September 2014 PREPARED BY: S. Armstrong | Guilford County REVIEWED BY: JTR DATE: 4/22/2015 | |

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