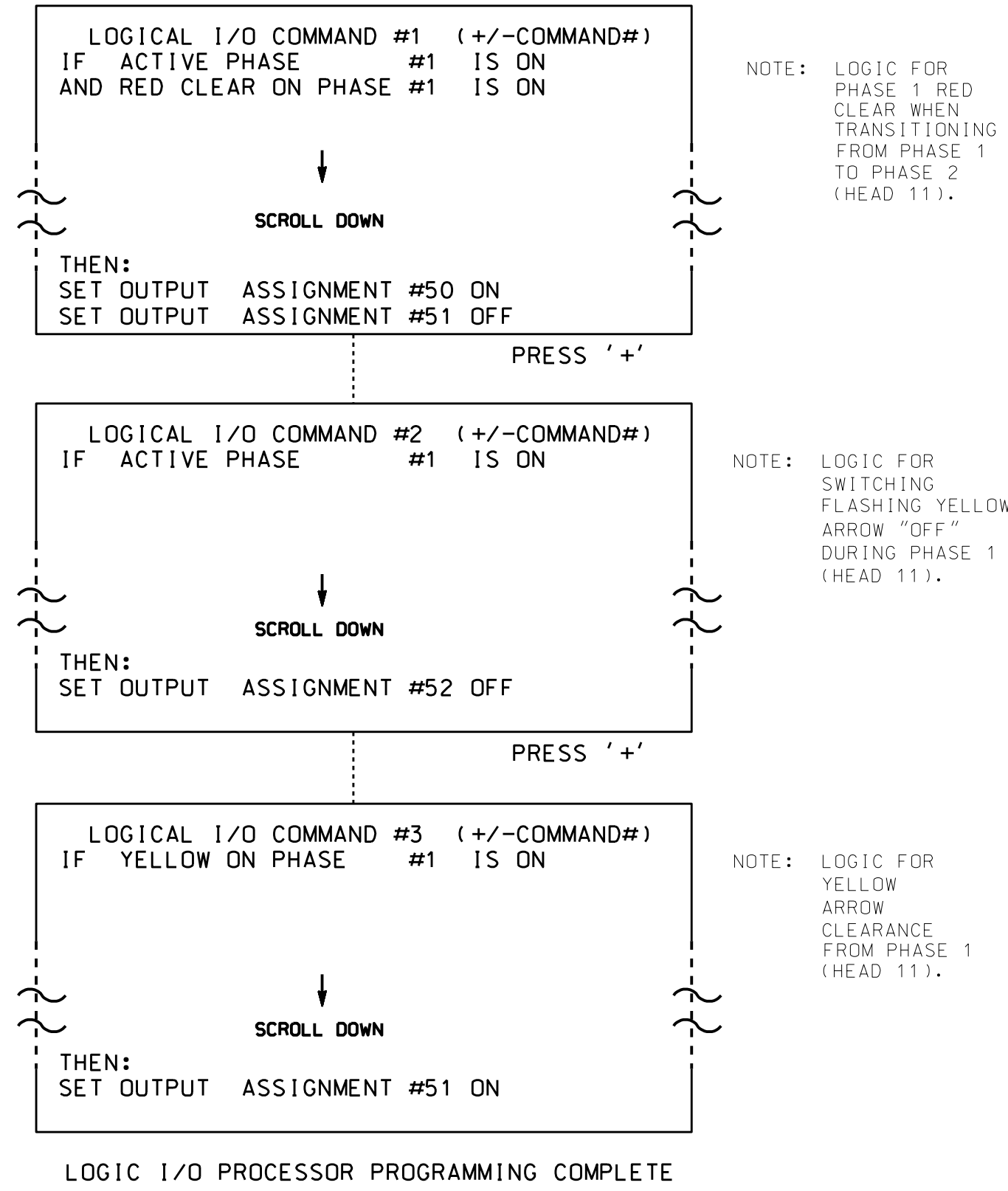


LOGICAL I/O PROCESSOR PROGRAMMING DETAIL TO PRODUCE SPECIAL FYA-PPLT SIGNAL SEQUENCE

(program controller as shown below)

- FROM MAIN MENU PRESS '2' (PHASE CONTROL), THEN '1' (PHASE CONTROL FUNCTIONS). SCROLL TO THE BOTTOM OF THE MENU AND ENABLE ACT LOGIC COMMANDS 1, 2, AND 3.
- FROM MAIN MENU PRESS '6' (OUTPUTS), THEN '3' (LOGICAL I/O PROCESSOR).

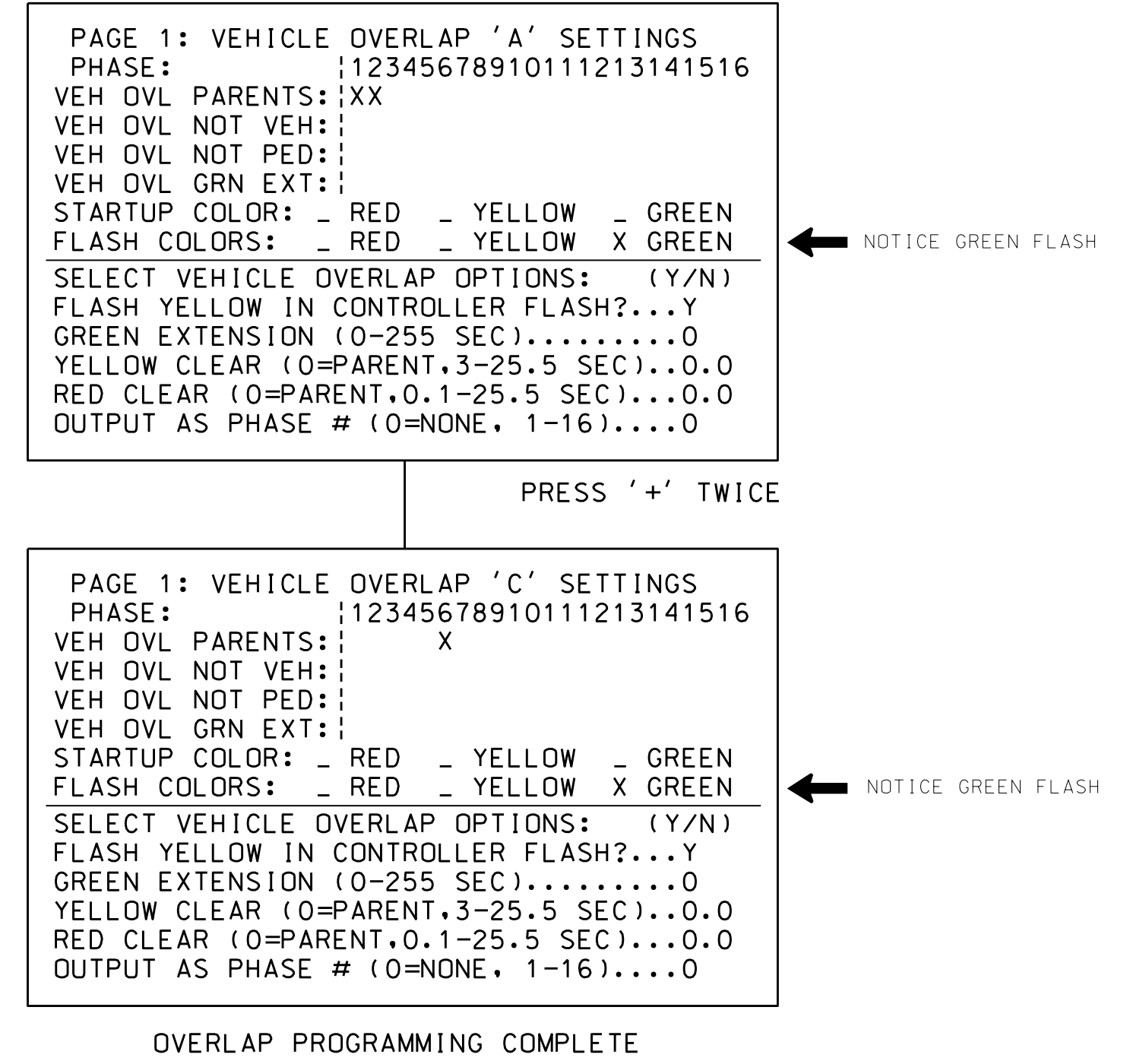


OUTPUT REFERENCE SCHEDULE	
OUTPUT 50	= Overlap A Red
OUTPUT 51	= Overlap A Yellow
OUTPUT 52	= Overlap A Green

OVERLAP PROGRAMMING DETAIL

(program controller as shown below)

FROM MAIN MENU PRESS '8' (OVERLAPS), THEN '1' (VEHICLE OVERLAP SETTINGS).



THIS ELECTRICAL DETAIL IS FOR
THE SIGNAL DESIGN: 07-0755
DESIGNED: August 2014
SEALED: 3/11/2015
REVISED: N/A

Electrical Detail - Sheet 2 of 3

	ELECTRICAL AND PROGRAMMING DETAILS FOR: SR 1009 (North Main Street) at State Avenue		
	Prepared In the Offices of: TRANSPORTATION MOBILITY AND SAFETY INSTITUTE 750 N. Grantfield Pkwy, Garner, NC 27529	Division 7 Guilford County High Point PLAN DATE: November 2014 REVIEWED BY: T. Joyce PREPARED BY: C. Strickland REVIEWED BY:	
REVISIONS		INIT. DATE	DATE
SIG. INVENTORY NO. 07-0755			

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