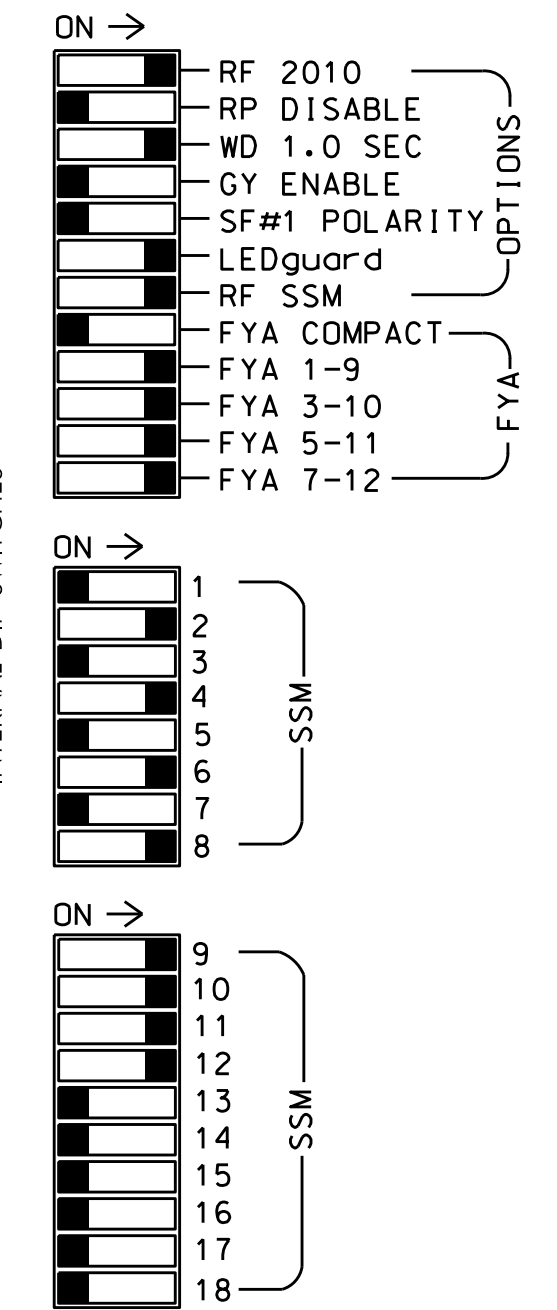
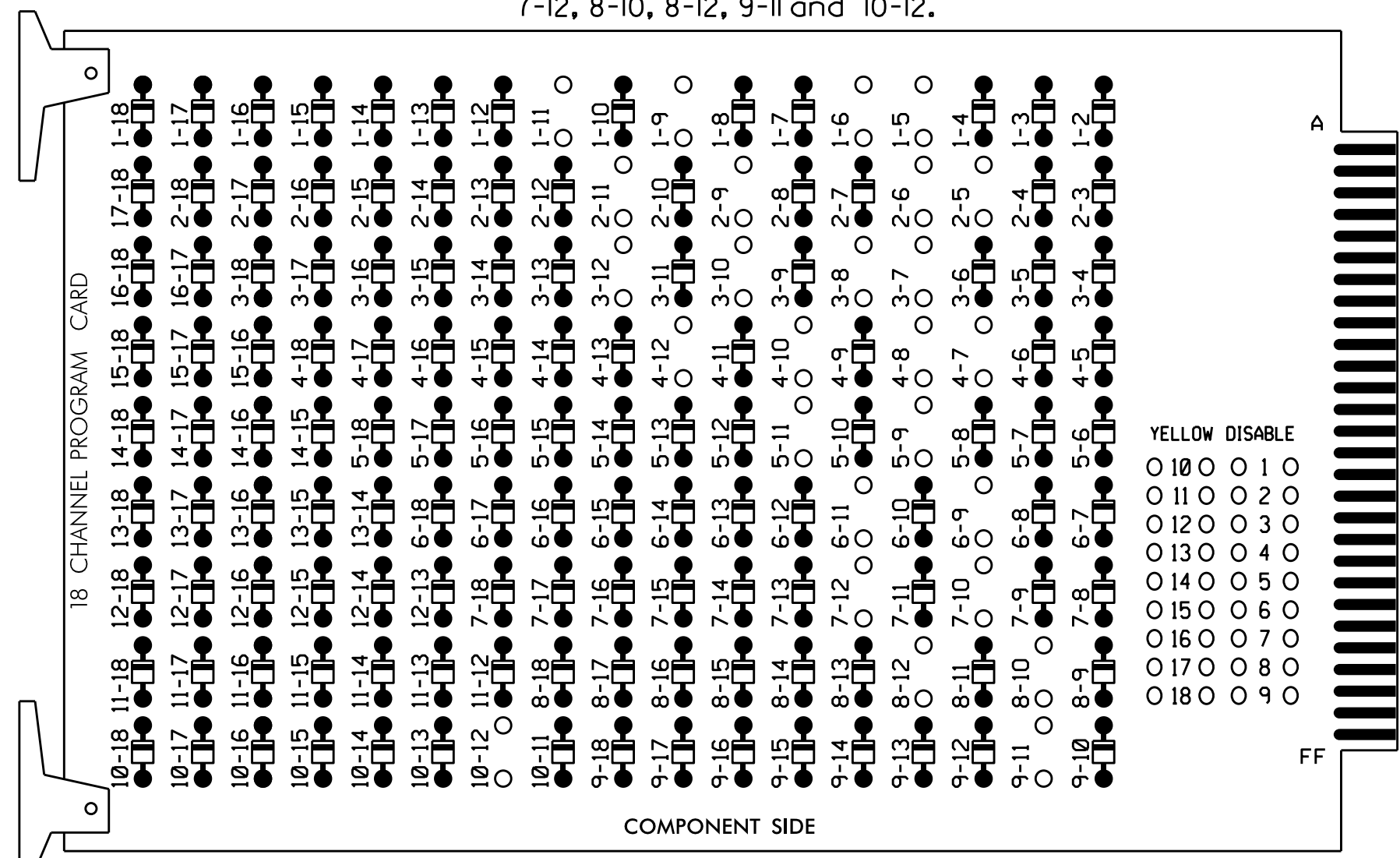


**EDI MODEL 2018EClip-NC CONFLICT MONITOR**

**PROGRAMMING DETAIL**

(remove jumpers and set switches as shown)

REMOVE DIODE JUMPERS 1-5, 1-6, 1-9, 1-11, 2-5, 2-6, 2-9, 2-11, 3-7, 3-8, 3-10, 3-12, 4-7, 4-8, 4-10, 4-12, 5-9, 5-11, 6-9, 6-11, 7-10, 7-12, 8-10, 8-12, 9-11 and 10-12.



REMOVE JUMPERS AS SHOWN

- NOTES:
- Card is provided with all diode jumpers in place. Removal of any jumper allows its channels to run concurrently.
  - Ensure jumpers SEL2-SEL5 and SEL9 are present on the monitor board.
  - Ensure that Red Enable is active at all times during normal operation.
  - Integrate monitor with Ethernet network in cabinet.

**NOTES**

- To prevent "flash-conflict" problems, insert red flash program blocks for all unused vehicle load switches in the output file. The installer shall verify that signal heads flash in accordance with the Signal Plans
- Program phases 4 and 8 for Dual Entry
- Enable Simultaneous Gap-Out for all phases
- Program phases 2 and 6 for Start Up In Green
- Program phases 2 and 6 for Yellow Flash, and overlaps 1 and 2 as Wag Overlaps
- The cabinet and controller are part of the High Point Signal System

**EQUIPMENT INFORMATION**

CONTROLLER.....2070  
 CABINET.....332 W/AUX  
 SOFTWARE.....ECONOLITE OASIS 3.03.32E OR LATEST APPROVED VERSION  
 CABINET MOUNT.....BASE  
 OUTPUT FILE POSITIONS...18 (12-STD; 6-AUX)  
 LOAD SWITCHES USED.....S1,S2,S4,S5,S7,S8,S10,S11  
 AUX S1,AUX S2,AUX S4,AUX S5  
 PHASES USED.....1,2,3,4,5,6,7,8  
 OVERLAP 'A'.....1+2  
 OVERLAP 'B'.....3+4  
 OVERLAP 'C'.....5+6  
 OVERLAP 'D'.....7+8

**SIGNAL HEAD HOOK-UP CHART**

LOAD SWITCH NO.	S1	S2	S3	S4	S5	S6	S7	S8	S9	S10	S11	S12	AUX S1	AUX S2	AUX S3	AUX S4	AUX S5	AUX S6
CMU CHANNEL NO.	1	2	13	3	4	14	5	6	15	7	8	16	9	10	17	11	12	18
PHASE	1	2	2 PED	3	4	4 PED	5	6	6 PED	7	8	8 PED	OLA	OLB	SPARE	OLC	OLD	SPARE
SIGNAL HEAD NO.	11	21,22	NU	31	41,42	NU	51	61,62	NU	71	81,82	NU	11	31	NU	51	71	NU
RED		128			101			134			107							
YELLOW	*	129		*	102		*	135		*	108							
GREEN		130			103			136			109							
RED ARROW													A121	A124		A114	A101	
YELLOW ARROW													A122	A125		A115	A102	
FLASHING YELLOW ARROW													A123	A126		A116	A103	
GREEN ARROW	127			118				133			124							

NU = Not Used  
 \* Denotes install load resistor. See load resistor installation detail this sheet.  
 ★ See pictorial of head wiring in detail below.

**INPUT FILE POSITION LAYOUT**

(front view)

FILE "I"	1	2	3	4	5	6	7	8	9	10	11	12	13	14
U	∅ 1	∅ 2	S	S	∅ 3	∅ 4	S	S	SYS. DET. S1	S	S	S	S	FS
L	1A	2A,2B	-	-	3A	4A	-	-	SYS. DET. S2	-	-	-	-	DC ISOLATOR
	NOT USED	∅ 2	-	-	NOT USED	NOT USED	-	-	-	-	-	-	-	DC ISOLATOR
		2C,2D	-	-	-	-	-	-	-	-	-	-	-	-
U	∅ 5	∅ 6	S	S	∅ 7	∅ 8	S	S	SYS. DET. S3	S	S	S	S	PRE1
L	5A	6A,6B	-	-	7A	8A	-	-	SYS. DET. S4	-	-	-	-	AC ISOLATOR
	NOT USED	∅ 6	-	-	NOT USED	NOT USED	-	-	-	-	-	-	-	NOT USED
		6C,6D	-	-	-	-	-	-	-	-	-	-	-	-

EX.: 1A, 2A, ETC. = LOOP NO.'S

FS = FLASH SENSE  
 ST = STOP TIME  
 PRE = PREEMPT

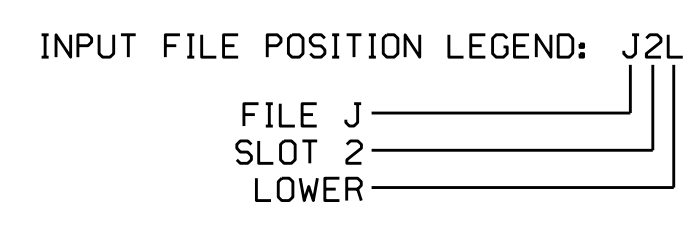
\* Wired Input - Do not populate slot with detector card

**INPUT FILE CONNECTION & PROGRAMMING CHART**

LOOP NO.	LOOP TERMINAL	INPUT FILE POS.	PIN NO.	INPUT ASSIGNMENT NO.	DETECTOR NO.	NEMA PHASE	CALL	EXTEND	FULL TIME DELAY	STRETCH TIME	DELAY TIME
1A <sup>1</sup>	TB2-1,2	I1U	56	18	1	1	Y	Y			15
		J4U	48	10	26	6	Y	Y			
2A,2B	TB2-5,6	I2U	39	1	2	2	Y	Y		1.6	
2C,2D	TB2-7,8	I2L	43	5	12	2	Y	Y			
3A <sup>2</sup>	TB4-5,6	I5U	58	20	3	3	Y	Y			15
		J8U	50	12	28	8	Y	Y			3
4A	TB4-9,10	I6U	41	3	4	4	Y	Y			10
5A <sup>3</sup>	TB3-1,2	J1U	55	17	5	5	Y	Y			15
		I4U	47	9	22	2	Y	Y			
6A,6B	TB3-5,6	J2U	40	2	6	6	Y	Y		1.6	
6C,6D	TB3-7,8	J2L	44	6	16	6	Y	Y			
7A <sup>4</sup>	TB5-5,6	J5U	57	19	7	7	Y	Y			15
		I8U	49	11	24	4	Y	Y			3
8A	TB5-9,10	J6U	42	4	8	8	Y	Y			10
* S1	TB6-9,10	I9U	60	22	11	SYS					
* S2	TB6-11,12	I9L	62	24	13	SYS					
* S3	TB7-9,10	J9U	59	21	15	SYS					
* S4	TB7-11,12	J9L	61	23	17	SYS					

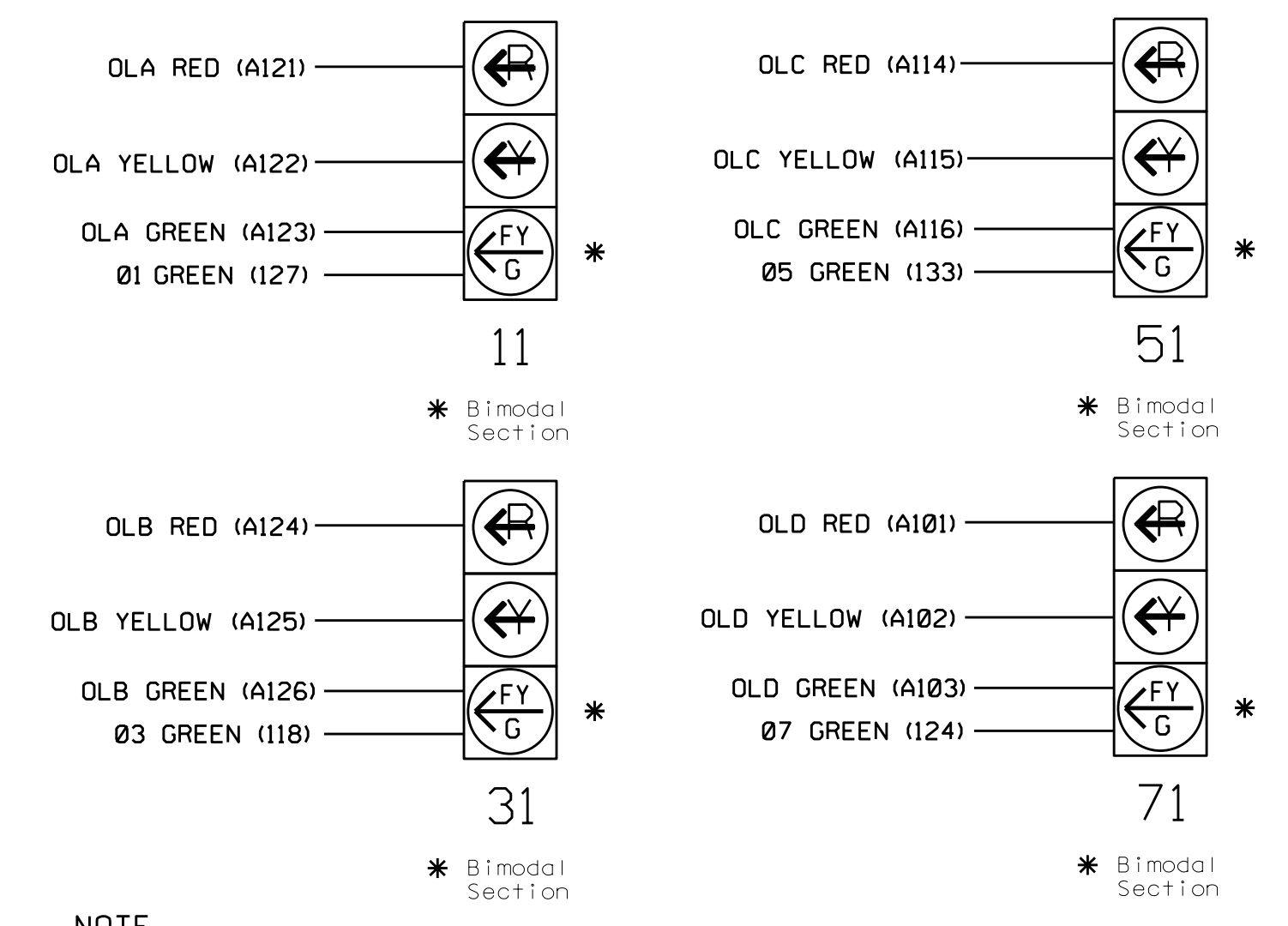
\* System detector only. Remove the vehicle phase assigned to this detector in the default programming.

- Add jumper from I1-W to J4-W, on rear of input file.
- Add jumper from I5-W to J8-W, on rear of input file.
- Add jumper from J1-W to I4-W, on rear of input file.
- Add jumper from J5-W to I8-W, on rear of input file.



**FYA SIGNAL WIRING DETAIL**

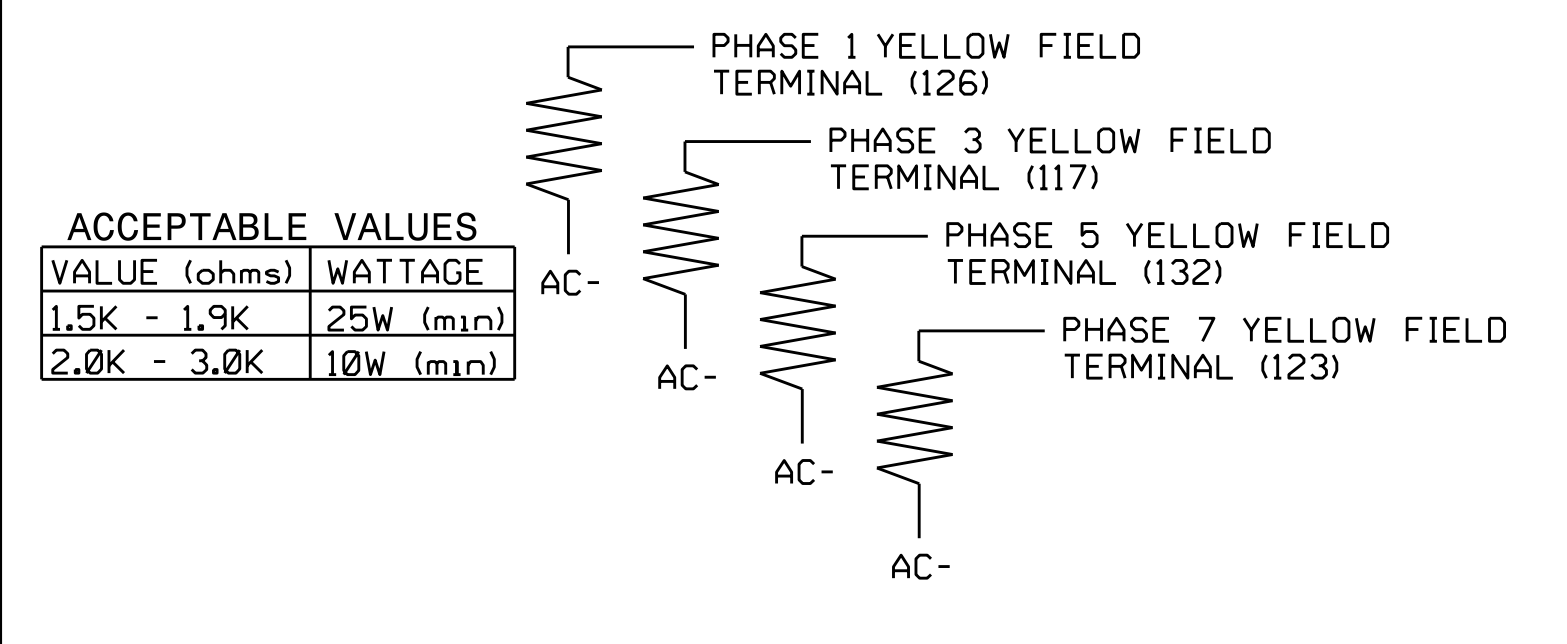
(wire signal heads as shown)



NOTE

- The sequence display for these signals require special logic programming. See sheet 2 for programming instructions.

**LOAD RESISTOR INSTALLATION DETAIL**



THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 07-0738  
 DESIGNED: August 2014  
 SEALED: 4/24/15  
 REVISED: N/A

Electrical Detail - Sheet 1 of 3

Electrical and Programming Details For: SR 1300 (West Fairfield Road) at SR 4053 (Surrett Drive)

Prepared In the Offices of: **TRANSFORMATIONAL MOBILITY AND SAFETY SOLUTIONS**

Division 7 Guilford County High Point

PLAN DATE: January 2015 REVIEWED BY: T. Joyce

PREPARED BY: B. SIMMONS REVIEWED BY:

REVISIONS: INIT. DATE

Seal: **GEORGE C. BROWN**, ENGINEER, No. 022013

DocuSigned by: **George C. Brown** 4/29/2015

750 N. Greenfield Pkwy, Garner, NC 27529

SIG. INVENTORY NO. 07-0738

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