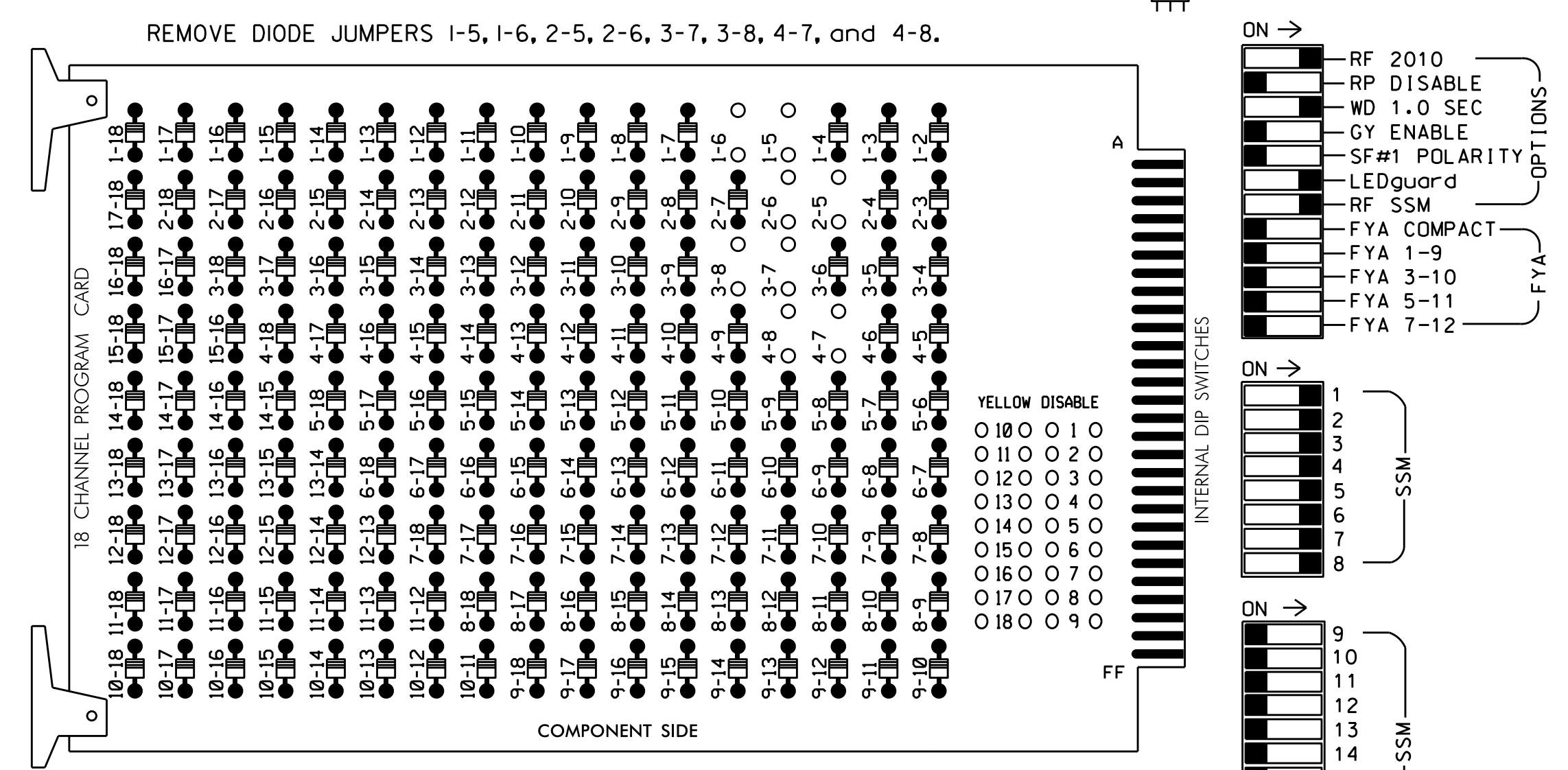


**EDI MODEL 2018ECLip-NC CONFLICT MONITOR PROGRAMMING DETAIL**

(remove jumpers and set switches as shown)



- NOTES:**
1. Card is provided with all diode jumpers in place. Removal of any jumper allows its channels to run concurrently.
  2. Ensure jumpers SEL2-SEL5 and SEL9 are present on the monitor board.
  3. Ensure that Red Enable is active at all times during normal operation.
  4. Integrate monitor with Ethernet network in cabinet.

**NOTES**

1. To prevent "flash-conflict" problems, insert red flash program blocks for all unused vehicle load switches in the output file. The installer shall verify that signal heads flash in accordance with the Signal Plans.
2. Enable Simultaneous Gap-Out for all phases.
3. Program phases 2 and 6 for Variable Initial and Gap Reduction.
4. Program phases 2 and 6 for Start Up In Green.
5. Program phases 2 and 6 for Yellow Flash.
6. The cabinet and controller are part of the High Point Signal System.

**EQUIPMENT INFORMATION**

CONTROLLER.....2070  
 CABINET.....332  
 SOFTWARE.....ECONOLITE OASIS  
 CABINET MOUNT.....BASE  
 OUTPUT FILE POSITIONS...12  
 LOAD SWITCHES USED.....S1,S2,S4,S5,S7,S8,S10,S11  
 PHASES USED.....1,2,3,4,5,6,7,8  
 OVERLAPS.....NONE

**SIGNAL HEAD HOOK-UP CHART**

| LOAD SWITCH NO. | S1    | S2    | S3    | S4  | S5    | S6    | S7    | S8    | S9    | S10   | S11   | S12   |
|-----------------|-------|-------|-------|-----|-------|-------|-------|-------|-------|-------|-------|-------|
| CMU CHANNEL NO. | 1     | 2     | 13    | 3   | 4     | 14    | 5     | 6     | 15    | 7     | 8     | 16    |
| PHASE           | 1     | 2     | 2 PED | 3   | 4     | 4 PED | 5     | 6     | 6 PED | 7     | 8     | 8 PED |
| SIGNAL HEAD NO. | 11,12 | 21,22 | NU    | 31  | 41,42 | NU    | 51,52 | 61,62 | NU    | 71,72 | 81,82 | NU    |
| RED             |       | 128   |       |     | 101   |       |       | 134   |       |       | 107   |       |
| YELLOW          |       | 129   |       |     | 102   |       |       | 135   |       |       | 108   |       |
| GREEN           |       | 130   |       |     | 103   |       |       | 136   |       |       | 109   |       |
| RED ARROW       | 125   |       |       | 116 |       |       | 131   |       |       | 122   |       |       |
| YELLOW ARROW    | 126   |       |       | 117 |       |       | 132   |       |       | 123   |       |       |
| GREEN ARROW     | 127   |       |       | 118 |       |       | 133   |       |       | 124   |       |       |

NU = Not Used

**INPUT FILE POSITION LAYOUT**

(front view)

| FILE "I" | 1  | 2        | 3   | 4        | 5        | 6        | 7            | 8   | 9            | 10  | 11  | 12           | 13           | 14          |
|----------|----|----------|-----|----------|----------|----------|--------------|-----|--------------|-----|-----|--------------|--------------|-------------|
| U        | S  | ∅ 1      | ∅ 2 | S        | ∅ 3      | ∅ 4      | SYS. DET. S1 | S   | SYS. DET. S3 | S   | S   | S            | S            | FS          |
| L        | 1A | 2A,2B    | ∅ 1 | NOT USED | 3A       | 4A,4B    | SYS. DET. S2 | ∅ 5 | ∅ 6          | ∅ 7 | ∅ 8 | SYS. DET. S5 | SYS. DET. S7 | DC ISOLATOR |
| U        | 1B | NOT USED | ∅ 5 | ∅ 6      | NOT USED | NOT USED | SYS. DET. S6 | S   | SYS. DET. S8 | S   | S   | S            | S            | DC ISOLATOR |
| L        | 5B | NOT USED | ∅ 5 | NOT USED | NOT USED | NOT USED | SYS. DET. S8 | S   | SYS. DET. S8 | S   | S   | S            | S            | DC ISOLATOR |

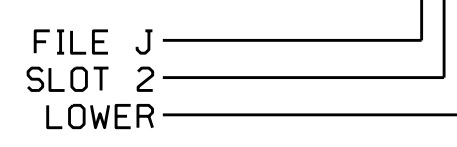
EX.: 1A, 2A, ETC. = LOOP NO.'S  
 FS = FLASH SENSE  
 ST = STOP TIME

**INPUT FILE CONNECTION & PROGRAMMING CHART**

| LOOP NO. | LOOP TERMINAL | INPUT FILE POS. | PIN NO. | INPUT ASSIGNMENT NO. | DETECTOR NO. | NEMA PHASE | CALL | EXTEND | FULL TIME DELAY | STRETCH TIME | DELAY TIME |
|----------|---------------|-----------------|---------|----------------------|--------------|------------|------|--------|-----------------|--------------|------------|
| 1A       | TB2-5,6       | I2U             | 39      | 1                    | 2            | 1          | Y    | Y      |                 |              | 3          |
| 1B       | TB2-7,8       | I2L             | 43      | 5                    | 12           | 1          | Y    | Y      |                 |              |            |
| 2A,2B    | TB2-9,10      | I3U             | 63      | 25                   | 32           | 2          | Y    | Y      |                 |              |            |
| 3A       | TB4-5,6       | I5U             | 58      | 20                   | 3            | 3          | Y    | Y      |                 |              | 3          |
| 4A,4B    | TB4-9,10      | I6U             | 41      | 3                    | 4            | 4          | Y    | Y      |                 |              | 5          |
| 5A       | TB3-5,6       | J2U             | 40      | 2                    | 6            | 5          | Y    | Y      |                 |              | 3          |
| 5B       | TB3-7,8       | J2L             | 44      | 6                    | 16           | 5          | Y    | Y      |                 |              |            |
| 6A,6B    | TB3-9,10      | J3U             | 64      | 26                   | 36           | 6          | Y    | Y      |                 |              |            |
| 7A,7B    | TB5-5,6       | J5U             | 57      | 19                   | 7            | 7          | Y    | Y      |                 |              | 3          |
| 8A,8B    | TB5-9,10      | J6U             | 42      | 4                    | 8            | 8          | Y    | Y      |                 |              | 5          |
| * S1     | TB6-1,2       | I7U             | 65      | 27                   | 34           | SYS        |      |        |                 |              |            |
| * S2     | TB6-3,4       | I7L             | 78      | 40                   | 44           | SYS        |      |        |                 |              |            |
| * S3     | TB6-9,10      | I9U             | 60      | 22                   | 11           | SYS        |      |        |                 |              |            |
| * S4     | TB6-11,12     | I9L             | 62      | 24                   | 13           | SYS        |      |        |                 |              |            |
| * S5     | TB7-1,2       | J7U             | 66      | 28                   | 38           | SYS        |      |        |                 |              |            |
| * S6     | TB7-3,4       | J7L             | 79      | 41                   | 48           | SYS        |      |        |                 |              |            |
| * S7     | TB7-9,10      | J9U             | 59      | 21                   | 15           | SYS        |      |        |                 |              |            |
| * S8     | TB7-11,12     | J9L             | 61      | 23                   | 17           | SYS        |      |        |                 |              |            |

\* System detector only. Remove the vehicle phase assigned to this detector in the default programming.

**INPUT FILE POSITION LEGEND: J2L**



THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 07-0737  
 DESIGNED: May 2014  
 SEALED: 3/31/2015  
 REVISED: N/A

**Electrical Detail**

Electrical and Programming Details for: **NC 68 (Eastchester Drive) at Johnson Street**

Prepared in the Offices of: **Transporatio Mobility and Safety Solutions**

Division 7 Guilford County High Point

PLAN DATE: August 2014 REVIEWED BY: *[Signature]*

PREPARED BY: S. Armstrong REVIEWED BY: *[Signature]*

REVISIONS: \_\_\_\_\_ INIT. DATE

DocuSigned by: **John T. Rowe, Jr.** 4/6/2015

750 N. Greenfield Pkwy, Garner, NC 27529

SEAL: JOHN T. ROWE, JR. ENGINEER SEAL 008453

SIG. INVENTORY NO. 07-0737

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