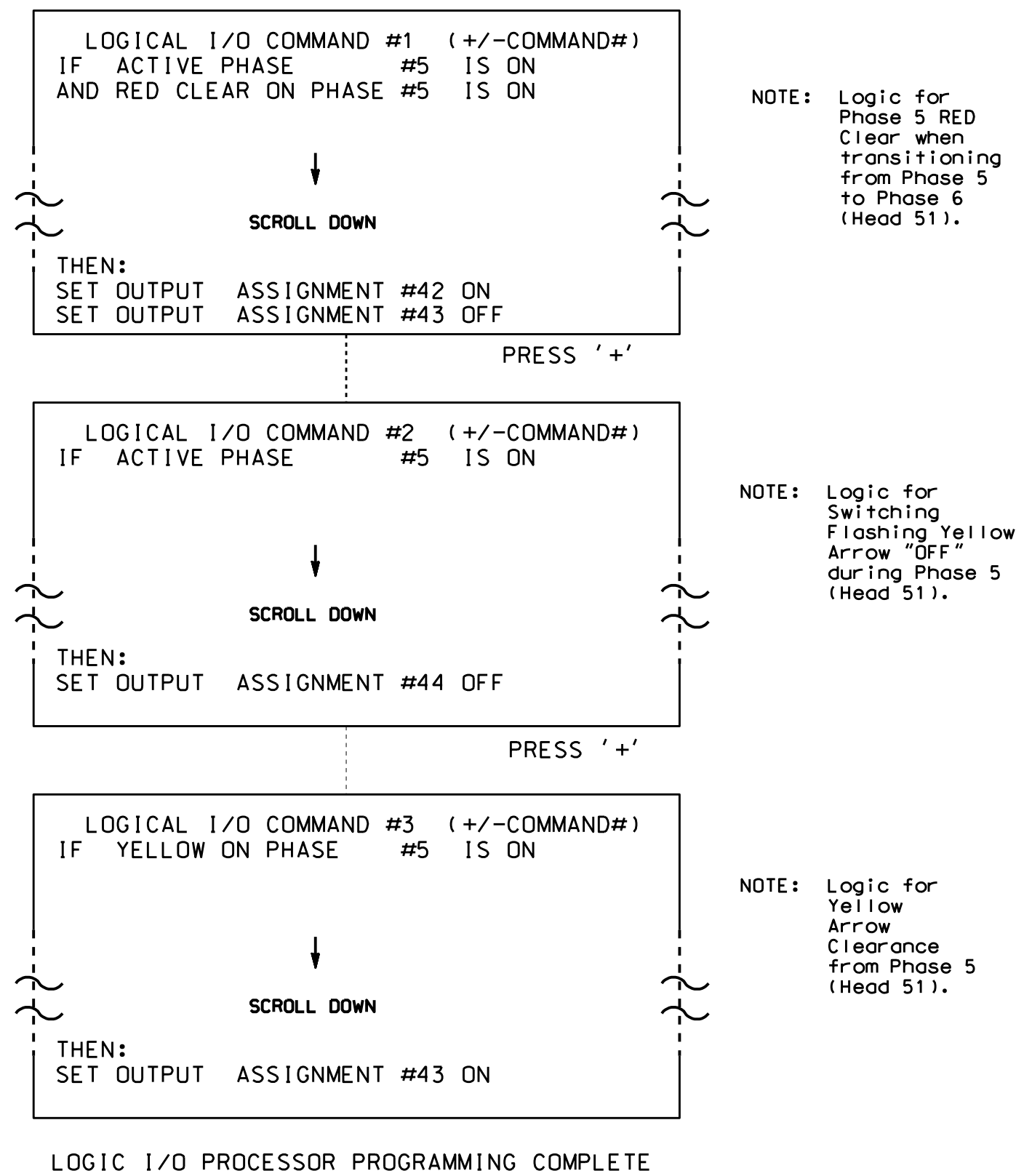


LOGICAL I/O PROCESSOR PROGRAMMING DETAIL TO PRODUCE SPECIAL FYA-PPLT SIGNAL SEQUENCE

(program controller as shown below)

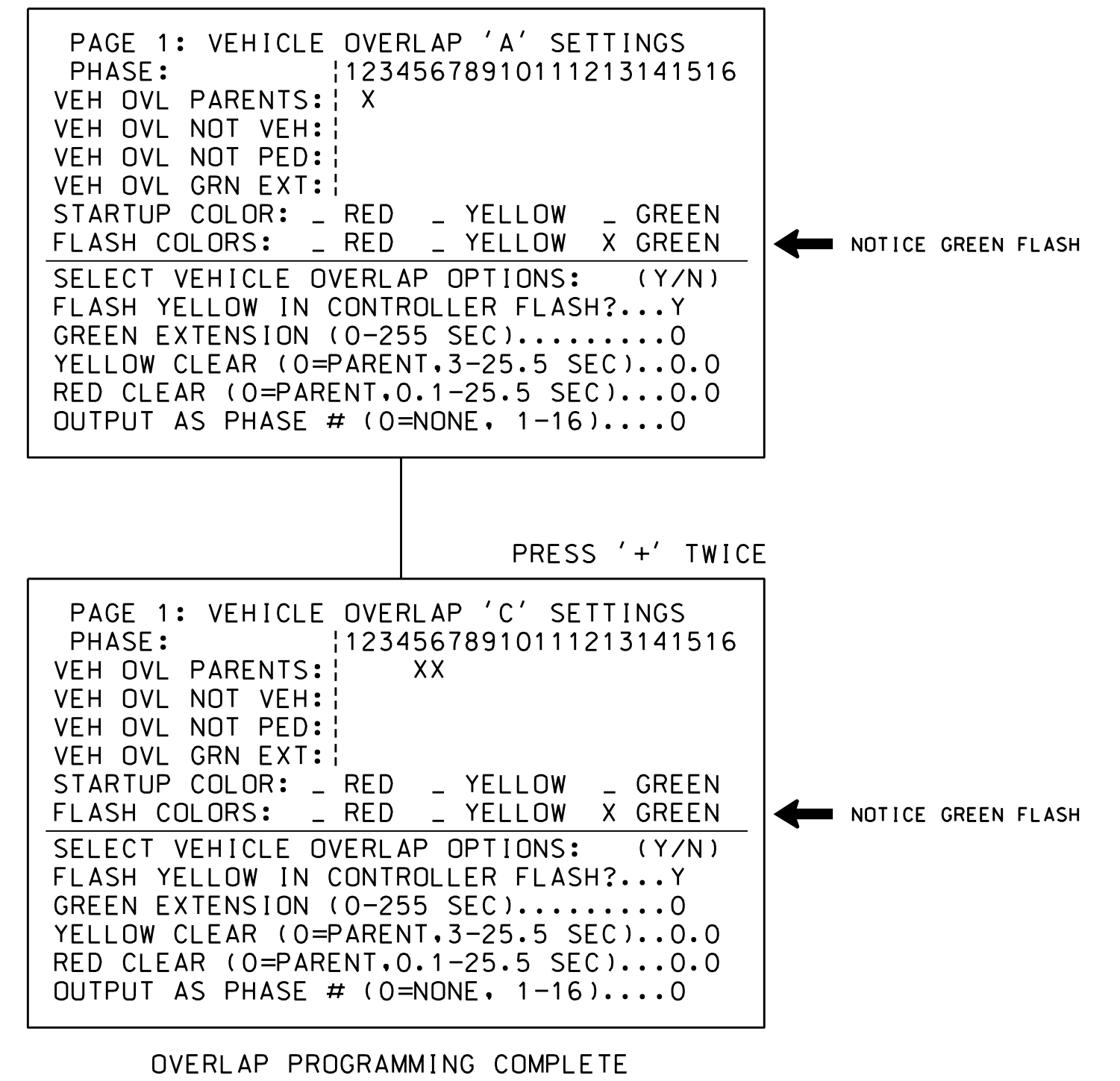
- From Main Menu press '2' (PHASE CONTROL), then '1' (PHASE CONTROL FUNCTIONS). Scroll to the bottom of the menu and Enable ACT Logic Commands 1, 2 and 3.
- From Main Menu press '6' (OUTPUTS), then '3' (LOGICAL I/O PROCESSOR).



OUTPUT REFERENCE SCHEDULE	
OUTPUT 42	= Overlap C Red
OUTPUT 43	= Overlap C Yellow
OUTPUT 44	= Overlap C Green

OVERLAP PROGRAMMING DETAIL (program controller as shown below)

From Main Menu press '8' (OVERLAPS), then '1' (VEHICLE OVERLAP SETTINGS).



THIS ELECTRICAL DETAIL IS FOR
THE SIGNAL DESIGN: 07-0208
DESIGNED: April 2014
SEALED: 3/17/15
REVISED: N/A

Electrical Detail - Sheet 2 of 2

	ELECTRICAL AND PROGRAMMING DETAILS FOR: SR 1009 (S. Main St.) at Archdale Road	SEAL NORTH CAROLINA PROFESSIONAL ENGINEER SEAL 022013 GEORGE C. BROWN
	Division 7 GUILFORD COUNTY High Point PLAN DATE: July 2014 REVIEWED BY: PREPARED BY: B. SIMMONS REVIEWED BY:	
Prepared In the Offices of: B. SIMMONS Signal Management Section		DocuSigned by: George C. Brown 3/19/2015 F12061ED08E8434 DATE SIG. INVENTORY NO. 07-0208

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