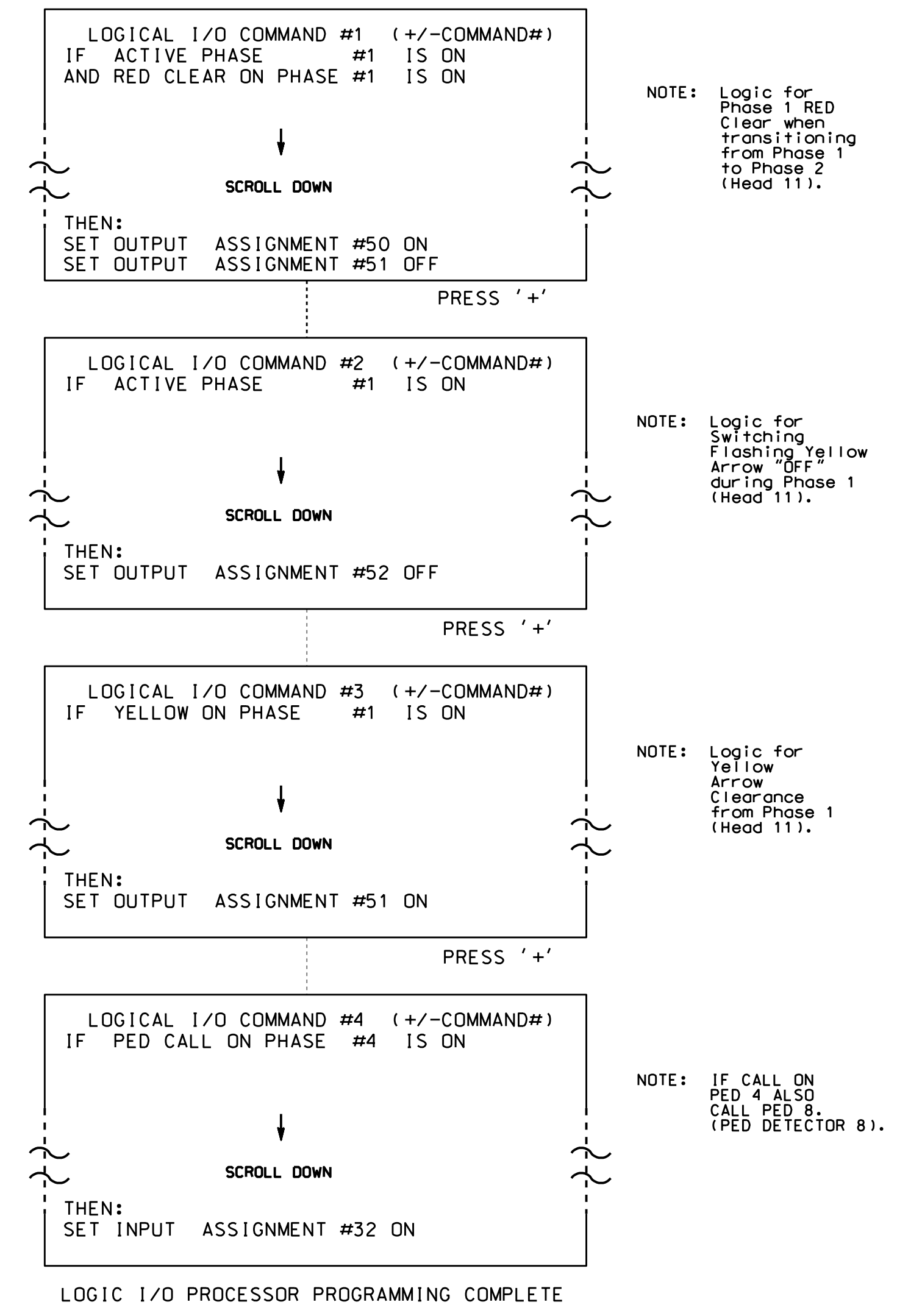


LOGICAL I/O PROCESSOR PROGRAMMING DETAIL TO PRODUCE SPECIAL FYA-PPLT SIGNAL SEQUENCE

(program controller as shown below)

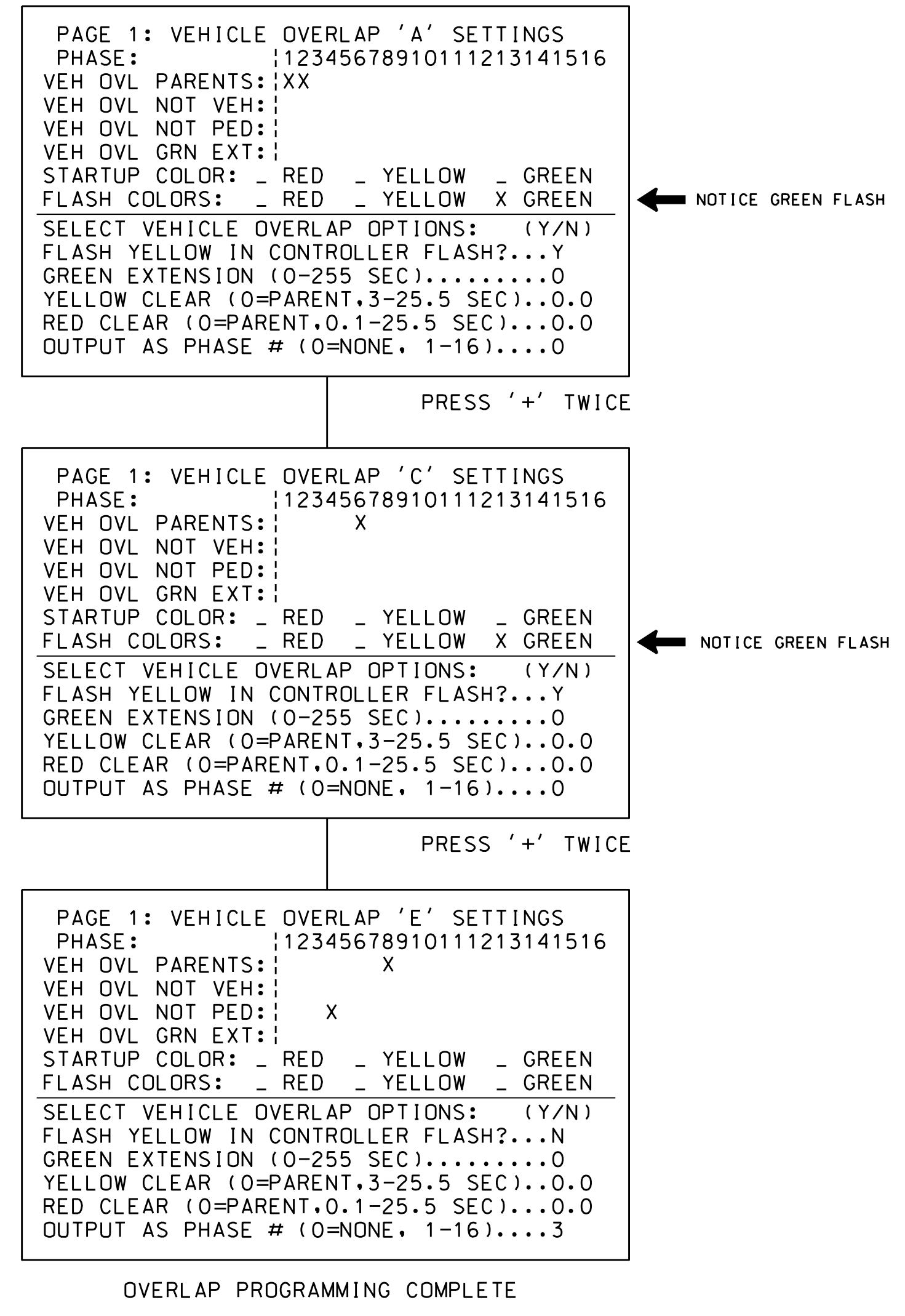
- From Main Menu press '2' (PHASE CONTROL), then '1' (PHASE CONTROL FUNCTIONS). Scroll to the bottom of the menu and Enable ACT Logic Commands 1, 2, 3 and 4.
- From Main Menu press '6' (OUTPUTS), then '3' (LOGICAL I/O PROCESSOR).



<u>IO REFERENCE SCHEDULE</u>	
OUTPUT 50	= Overlap A Red
OUTPUT 51	= Overlap A Yellow
OUTPUT 52	= Overlap A Green
INPUT 32	= PED Detector 8

OVERLAP PROGRAMMING DETAIL (program controller as shown below)

From Main Menu press '8' (OVERLAPS), then '1' (VEHICLE OVERLAP SETTINGS).



DYNAMIC BACK-UP CONTROL PROGRAMMING (program controller as shown below)

- From Main Menu press '2' (Phase Control), then '1' (Phase Control Functions). Scroll to the bottom of the menu and enable Dynamic/Backup Control Function 1.
- From Phase Control Functions Menu press '2' (Dynamic/Backup Control Functions).

```

DYNAMIC/BACKUP CONTROL FUNCTION #01
OVERLAPS: ABCDEFGHIJKLMNOP
IF OVERLAPS ARE ACTIVE:
OR PHASES: 12345678910111213141516
IF PHASES ARE ON: X
OMIT PHASES: X
CALL PHASES:
    
```

BACKUP PROTECTION PROGRAMMING COMPLETE

THIS ELECTRICAL DETAIL IS FOR
THE SIGNAL DESIGN: 09-0735
DESIGNED: May 2015
SEALED: 07/01/2015
REVISED: N/A

Electrical Detail - Sheet 2 of 2

ELECTRICAL AND PROGRAMMING DETAILS FOR:	NC 8 (Winston Road) at I-85 Bus./US 29 SB-US 64-70 WB Ramp		SEAL PROFESSIONAL ENGINEER SEAL 016286 MILTON I. DEAN
	Prepared In the Offices of: 750 N. Greenfield Pkwy, Garner, NC 27529	Division 9 Davidson County Lexington	
REVISIONS		INIT. DATE	DocuSigned by: Milton I. Dean 7/10/2015 CBB9728907A2497 DATE
SIG. INVENTORY NO. 09-0735			DATE

09-JUL-2015 14:40
 C:\P\TSS\TSS\SIGNAL\WORK\HARDWARE\SIG_Maps\Strickland\090735_sme.e_xx.e_dgn
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