

LOGICAL I/O PROCESSOR PROGRAMMING DETAIL
TO PRODUCE SPECIAL FYA-PPLT SIGNAL SEQUENCE

(program controller as shown below)

1. FROM MAIN MENU PRESS '2' (PHASE CONTROL), THEN '1' (PHASE CONTROL FUNCTIONS). SCROLL TO THE BOTTOM OF THE MENU AND ENABLE ACT LOGIC COMMANDS 4, 5 AND 6. IF ENABLED, DISABLE ACT LOGIC COMMANDS 1, 2 AND 3.
2. FROM MAIN MENU PRESS '6' (OUTPUTS), THEN '3' (LOGICAL I/O PROCESSOR).

LOGICAL I/O COMMAND #4 (+/-COMMAND#)
 IF ACTIVE PHASE #5 IS ON
 AND RED CLEAR ON PHASE #5 IS ON

↓
SCROLL DOWN

THEN:
 SET OUTPUT ASSIGNMENT #42 ON
 SET OUTPUT ASSIGNMENT #43 OFF

PRESS '+'

NOTE: LOGIC FOR PHASE 5 RED CLEAR WHEN TRANSITIONING FROM PHASE 5 TO PHASE 6 (HEAD 51).

LOGICAL I/O COMMAND #5 (+/-COMMAND#)
 IF ACTIVE PHASE #5 IS ON

↓
SCROLL DOWN

THEN:
 SET OUTPUT ASSIGNMENT #44 OFF

PRESS '+'

NOTE: LOGIC FOR SWITCHING FLASHING YELLOW ARROW "OFF" DURING PHASE 5 (HEAD 51).

LOGICAL I/O COMMAND #6 (+/-COMMAND#)
 IF YELLOW ON PHASE #5 IS ON

↓
SCROLL DOWN

THEN:
 SET OUTPUT ASSIGNMENT #43 ON

NOTE: LOGIC FOR YELLOW ARROW CLEARANCE FROM PHASE 5 (HEAD 51).

LOGIC I/O PROCESSOR PROGRAMMING COMPLETE

OUTPUT REFERENCE SCHEDULE

OUTPUT 42 = Overlap C Red
 OUTPUT 43 = Overlap C Yellow
 OUTPUT 44 = Overlap C Green

OVERLAP PROGRAMMING DETAIL

(program controller as shown below)

FROM MAIN MENU PRESS '8' (OVERLAPS), THEN '1' (VEHICLE OVERLAP SETTINGS).

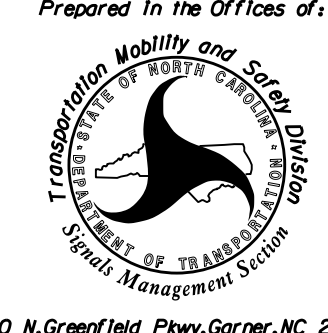
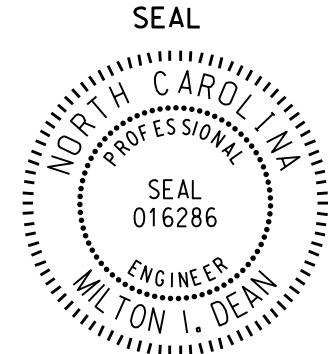
PRESS '+' TWICE

PAGE 1: VEHICLE OVERLAP 'C' SETTINGS
 PHASE: 12345678910111213141516
 VEH OVL PARENTS: XX
 VEH OVL NOT VEH:
 VEH OVL NOT PED:
 VEH OVL GRN EXT:
 STARTUP COLOR: _ RED _ YELLOW _ GREEN
 FLASH COLORS: _ RED _ YELLOW X GREEN ← NOTICE GREEN FLASH
 SELECT VEHICLE OVERLAP OPTIONS: (Y/N)
 FLASH YELLOW IN CONTROLLER FLASH?...Y
 GREEN EXTENSION (0-255 SEC)...0.0
 YELLOW CLEAR (0=PARENT,3-25.5 SEC)...0.0
 RED CLEAR (0=PARENT,0.1-25.5 SEC)...0.0
 OUTPUT AS PHASE # (0=NONE, 1-16)...0

OVERLAP PROGRAMMING COMPLETE

THIS ELECTRICAL DETAIL IS FOR
 THE SIGNAL DESIGN: 09-0735T3
 DESIGNED: May 2015
 SEALED: 07/01/2015
 REVISED: N/A

Electrical Detail - Temp 3 - Sheet 2 of 2

<p>ELECTRICAL AND PROGRAMMING DETAILS FOR:</p> <p align="center">Prepared In the Offices of:</p>  <p align="center">750 N. Greenfield Pkwy, Garner, NC 27529</p>	<p>NC 8 (Winston Road) at I-85 Bus./US 29 SB-US 64-70 WB Ramp</p> <p>Division 9 Davidson County Lexington</p> <p>PLAN DATE: June 2015 REVIEWED BY:</p> <p>PREPARED BY: C. Strickland REVIEWED BY:</p> <table border="1" style="width: 100%; border-collapse: collapse;"> <thead> <tr> <th>REVISIONS</th> <th>INIT.</th> <th>DATE</th> </tr> </thead> <tbody> <tr> <td> </td> <td> </td> <td> </td> </tr> </tbody> </table>	REVISIONS	INIT.	DATE				<p>SEAL</p>  <p>DocuSigned by: Milton I. Dean 7/10/2015</p> <p>DATE</p> <p>SIG. INVENTORY NO. 09-0735T3</p>
REVISIONS	INIT.	DATE						

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