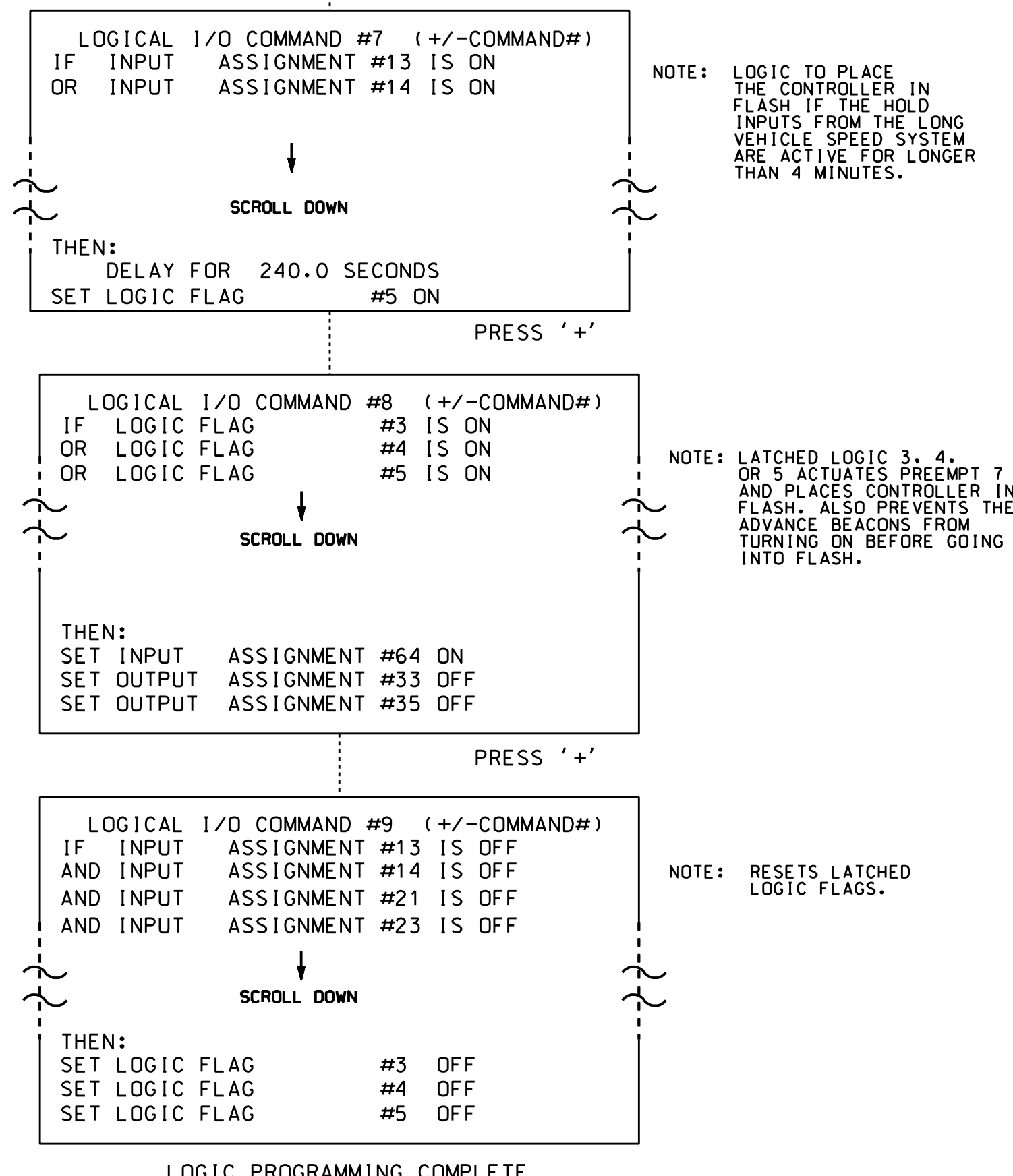
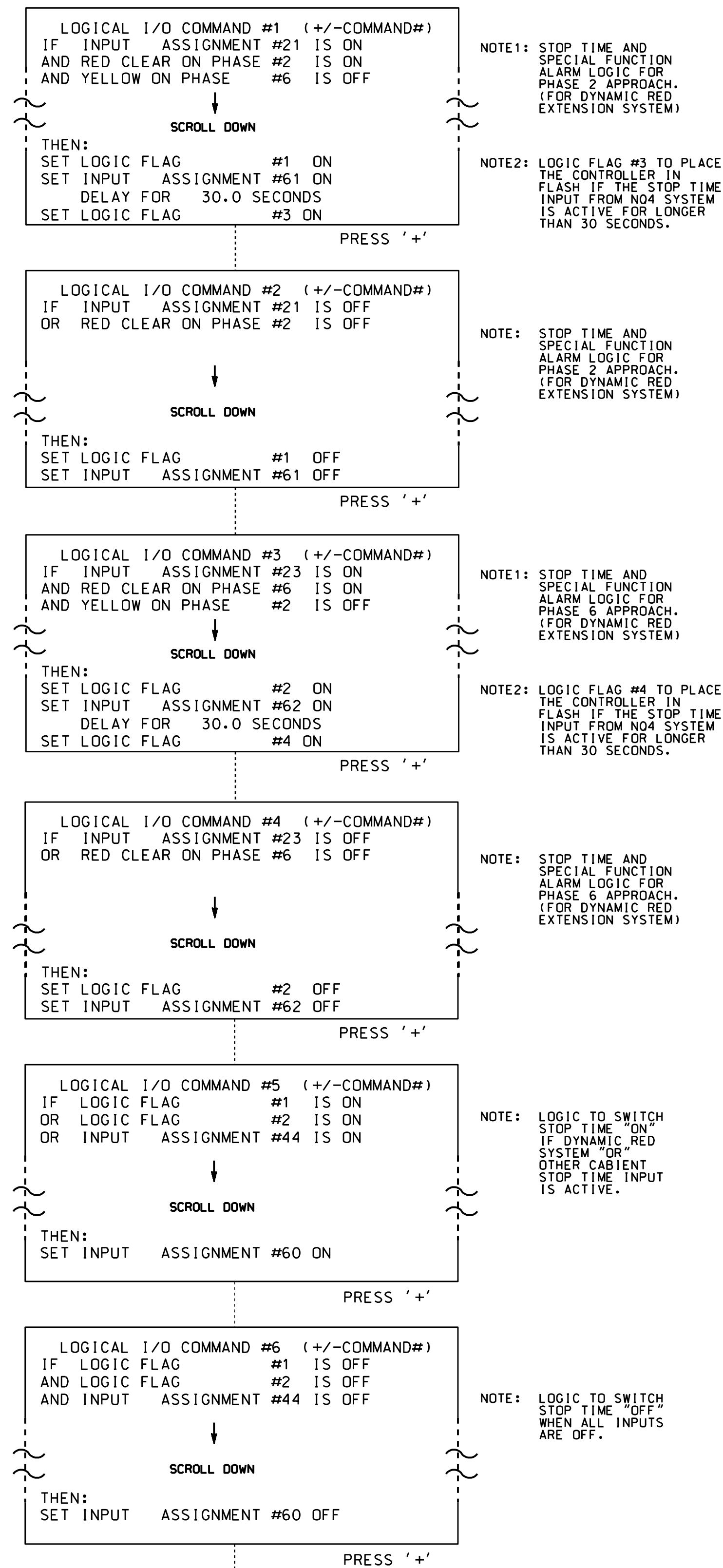


LOGICAL I/O PROCESSOR PROGRAMMING DETAIL FOR DYNAMIC RED EXTENSION SYSTEM

(program controller as shown below)

- FROM MAIN MENU PRESS '2' (PHASE CONTROL), THEN '1' (PHASE CONTROL FUNCTIONS). SCROLL TO THE BOTTOM OF THE MENU AND ENABLE ACT LOGIC COMMANDS 1, 2, 3, 4, 5, 6, 7, 8, AND 9.
- FROM MAIN MENU PRESS '6' (OUTPUTS), THEN '3' (LOGICAL I/O PROCESSOR).



! NOTE: THIS LOGIC IS BASED UPON CHANGES MADE TO INPUT MAPS WHICH ARE SHOWN ON SHEETS 3 AND 4 OF THIS ELECTRICAL DETAIL. !

INPUT/OUTPUT REFERENCE SCHEDULE
USE TO INTERPRET LOGIC PROCESSOR

INPUT 13	=	PHASE 2 HOLD
INPUT 14	=	PHASE 6 HOLD
INPUT 21	=	OUTPUT FROM DYNAMIC RED EXTENSION SYSTEM (PHASE 2)
INPUT 23	=	OUTPUT FROM DYNAMIC RED EXTENSION SYSTEM (PHASE 6)
INPUT 44	=	CABINET CIRCUITRY STOP TIME CONTROL INPUT
INPUT 60	=	STOP TIME
INPUT 61	=	SPECIAL FUNCTION ALARM 1
INPUT 62	=	SPECIAL FUNCTION ALARM 2
INPUT 64	=	PREEMPT 7
OUTPUT 33	=	ADVANCE BEACON
OUTPUT 35	=	ADVANCE BEACON

FLASH PREEMPTION PROGRAMMING DETAIL FOR PREEMPT 7

(program controller as shown below)

THIS PREEMPT PLACES THE CONTROLLER IN FLASH IF RED EXTENSION IS GREATER THAN 30 SECONDS, OR IF THE LVOD HOLD TIME EXCEEDS 240 SECONDS.

FROM MAIN MENU PRESS 'A' (PREEMPTION), THEN '1' (STANDARD PREEMPTIONS). PRESS 'NEXT' UNTIL PREEMPTION #7 IS REACHED.

PREEMPTION #7 SETTINGS (NEXT:1-10)	CLEAR/DWELL PHASES
INTERVAL/TIMING	GRN YEL RED
1 255 0.0 0.0	X X
2 0 0.0 0.0	X
3 0 0.0 0.0	
4 0 0.0 0.0	
5 1 0.0 0.0	X X

EXIT CALLS

OPTIONS

PRIORITY (Y/N TO SELECT)HIGH
 DELAY TIMER (0-255 SEC)0.0
 MIN GREEN BEFORE PRE (0= DEFAULT)....0
 PED CLEAR BEFORE PRE (0= DEFAULT)....0
 YELLOW CLEAR BEFORE PRE (0= DEFAULT)....0
 RED CLEAR BEFORE PRE (0= DEFAULT)....0
 DWELL MIN TIMER (0-255 SEC)10
 DWELL MAX TIMER (0=OFF,1-255MIN)0
 DWELL HOLD-OVER TIMER (0-255)0
 LATCH CALL?N
 LINK TO NEXT PREEMPT?N
 ENABLE BACKUP PROTECTION?N
 HOLD CLEAR 1 PHASES DURING DELAY? ...N
 FAST GREEN FLASH DWELL PHASES?N
 PED CLEARANCE THROUGH YELLOW?N
 INHIBIT OVERLAP GREEN EXTENSION? ...N
 SERVICE DURING SOFTWARE FLASH?N
 REST IN RED DURING DWELL INTERVAL? ..N
 FLASH DWELL INTERVAL?Y
 ALLOW PEDS IN DWELL INTERVAL?N
 RE-TIME DWELL INTERVAL?N
 OVERLAPS: ABCDEFGHIJKLMNOP
 DWELL INT FLASH YELLOW
 OMIT OVERLAPS:

THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 03-0342
 DESIGNED: December 2012
 SEALED: 12/6/12
 REVISED: 7/16/2015

Signal Upgrade - Sheet 2 of 10

	US 17 at NC 904 (Longwood Road/ Seaside Road)		
	Division 3 Brunswick County Grissettown	SEAL JOHN T. ROWE, JR. PROFESSIONAL ENGINEER 008453	
Prepared in the Offices of: 	PLAN DATE: December 2012 PREPARED BY: S. Armstrong	REVIEWED BY: JTR DATE: 7/20/2015	DocuSigned by: John T. Rowe, Jr. 12/10/2012
750 N. Greenfield Pkwy, Garner, NC 27529	Revised from stretch to Volume Density. (WSA)	DATE: 7/20/2015	DATE:
SIG. INVENTORY NO. 03-0342			

16-JUL-2015 15:13
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