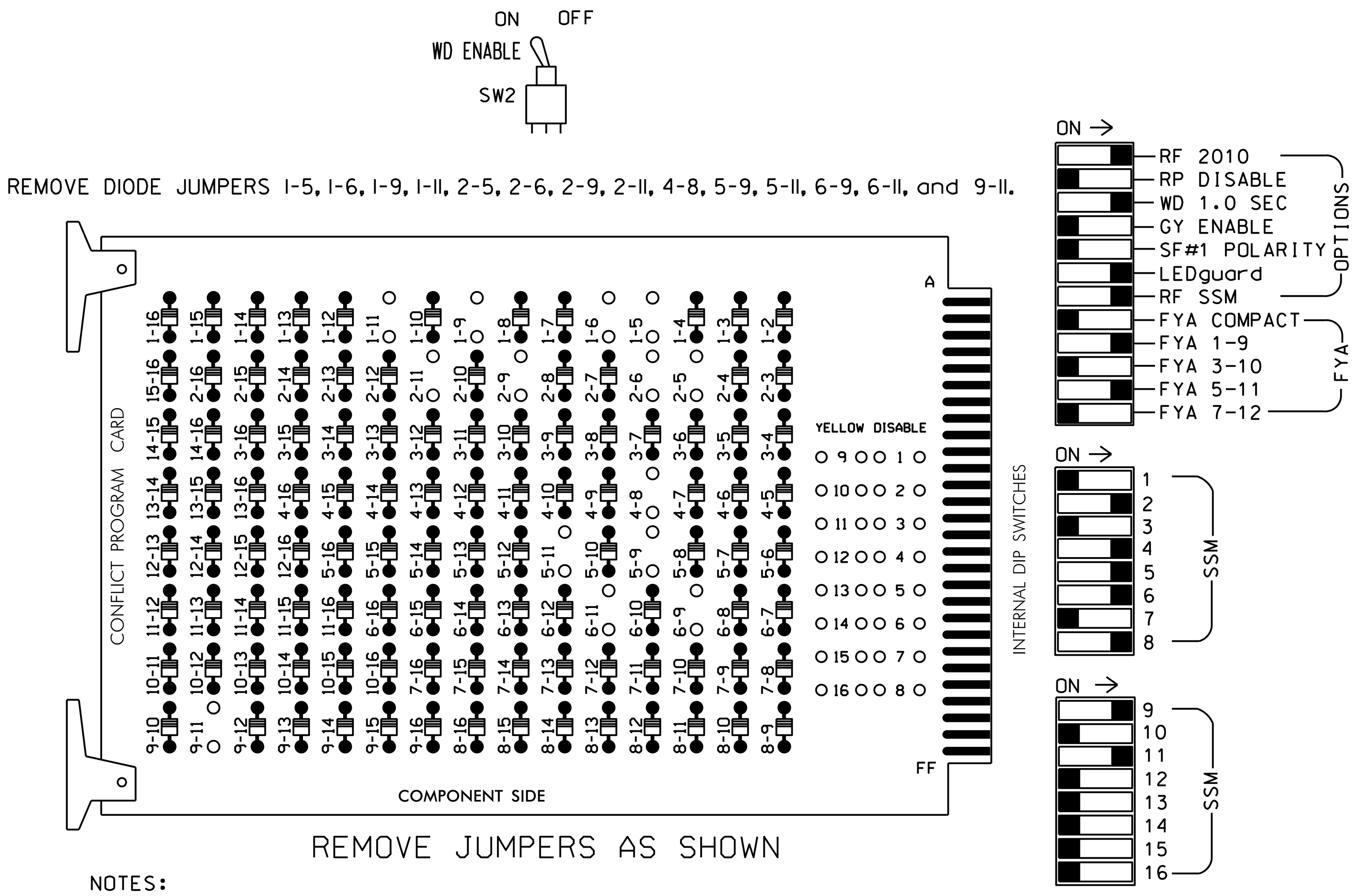
EDI MODEL 2010ECL-NC CONFLICT MONITOR PROGRAMMING DETAIL

(remove jumpers and set switches as shown)

REMOVE DIODE JUMPERS 1-5, 1-6, 1-9, 1-11, 2-5, 2-6, 2-9, 2-11, 4-8, 5-9, 5-11, 6-9, 6-11, and 9-11.



REMOVE JUMPERS AS SHOWN

NOTES:

1. Card is provided with all diode jumpers in place. Removal of any jumper allows its channels to run concurrently.
2. Make sure jumpers SEL2-SEL5 are present on the monitor board.

NOTES

1. To prevent "flash-conflict" problems, insert red flash program blocks for all unused vehicle load switches in the output file. The installer shall verify that signal heads flash in accordance with the Signal Plans.
2. Ensure that Red Enable is active at all times during normal operation. To prevent Red Failures on unused monitor channels, tie unused red monitor inputs 1,3, 7,10,12,13,14,15 & 16 to load switch AC+ per the cabinet manufacturer's instructions.
3. Program phases 4 and 8 for Dual Entry.
4. Enable Simultaneous Gap-Out for all phases.
5. Program phases 2 and 6 for Variable Initial and Gap Reduction.
6. Program phases 2 and 6 for Start Up In Green.
7. Program phases 2 and 6 for Yellow Flash, and overlap 1 as Wag Overlaps.
8. Ensure Auxiliary Output File is compatible with existing cabinet. Auxiliary Output File required to conform to CALTRANS *Traffic Signal Control Equipment Specifications.*
9. The cabinet and controller are part of the Wilmington Signal System.

SIGNAL HEAD HOOK-UP CHART

LOAD SWITCH NO.	S1	S2	S2P	S3	S4	S4P	S5	S5	S6	S6P	S7	S8	S8P	S9	S10	S11	S12	S13	S14
PHASE	1	2	2 PED	3	4	4 PED	5		6	6 PED	7	8	8 PED	OLA	OLB	SPARE	OLC	OLD	SPARE
SIGNAL HEAD NO.	11★	21,22	NU	NU	41,42	NU	42	51★	61,62	NU	NU	81,82	NU	11★	NU	NU	51★	NU	NU
RED		128			101		✳		134			107							
YELLOW	✳	129			102				135			108							
GREEN		130			103				136			109							
RED ARROW														A121			A114		
YELLOW ARROW							132							A122			A115		
FLASHING YELLOW ARROW														A123			A116		
GREEN ARROW	127						133	133											

NU = Not Used

✳ Denotes install load resistor. See load resistor installation detail this sheet.

★ See pictorial of head wiring in detail below.

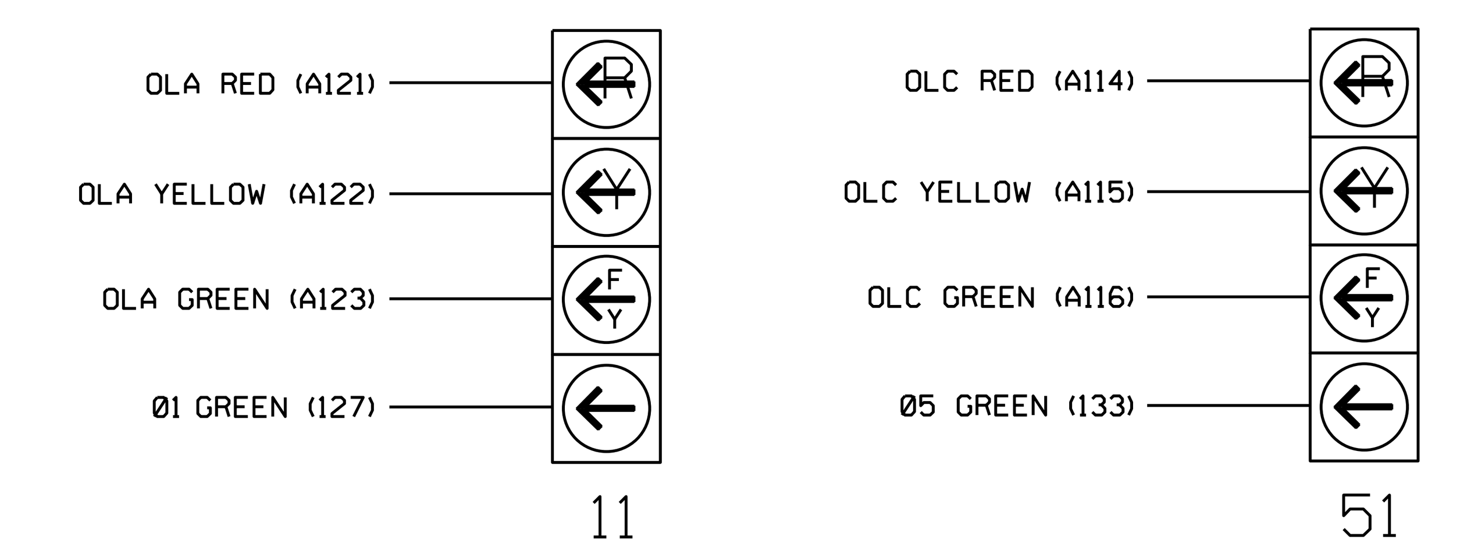
EQUIPMENT INFORMATION

CONTROLLER.............2070L
✳ CABINET................332 W/ AUX
SOFTWARE...............ECONOLITE OASIS
CABINET MOUNT..........BASE
OUTPUT FILE POSITIONS...18 WITH AUX. OUTPUT FILE
LOAD SWITCHES USED......S1,S2,S4,S5,S6,S8,S9,S12
PHASES USED.............1,2,4,5,6,8
OVERLAP "A".............1+2
OVERLAP "B".............NOT USED
OVERLAP "C".............5+6
OVERLAP "D".............NOT USED

✳ In NOTES section refer to note 8.

FYA SIGNAL WIRING DETAIL

(wire signal heads as shown)



NOTE

The sequence display for signal heads 11 and 51 requires special logic programming. See sheet 2 for programming instructions.

INPUT FILE POSITION LAYOUT

(front view)

		1	2	3	4	5	6	7	8	9	10	11	12	13	14
FILE "I"	U	ø1 1A	ø2 2A	SLOT EMPTY	WIRED INPUT ⊗	SLOT EMPTY	ø4 4A	SLOT EMPTY	SLOT EMPTY	SLOT EMPTY	SLOT EMPTY	SLOT EMPTY	SLOT EMPTY	SLOT EMPTY	FS DC ISOLATOR
	L	NOT USED	ø2 2B				ø4 4B								ST DC ISOLATOR
FILE "J"	U	ø5 5A	ø5 5B	ø6 6A	WIRED INPUT ⊗	SLOT EMPTY	ø8 8A	SLOT EMPTY	SLOT EMPTY	SLOT EMPTY	SLOT EMPTY	SLOT EMPTY	SLOT EMPTY	SLOT EMPTY	SLOT EMPTY
	L	NOT USED	NOT USED	ø6 6B			ø8 8B								

EX.: 1A, 2A, ETC. = LOOP NO.'S

⊗ Wired Input - Do not populate slot with detector card

FS = FLASH SENSE
ST = STOP TIME

INPUT FILE CONNECTION & PROGRAMMING CHART

LOOP NO.	LOOP TERMINAL	INPUT FILE POS.	PIN NO.	INPUT ASSIGNMENT NO.	DETECTOR NO.	NEMA PHASE	CALL	EXTEND	FULL TIME DELAY	STRETCH TIME	DELAY TIME
1A[1]	TB2-1,2	I1U	56	18	1	1	Y	Y			10
	-	J4U	48	10	26	6	Y	Y	Y		3
2A	TB2-5,6	I2U	39	1	2	2	Y	Y			
2B	TB2-7,8	I2L	43	5	12	2	Y	Y			
4A	TB4-9,10	I6U	41	3	4	4	Y	Y			
4B	TB4-11,12	I6L	45	7	14	4	Y	Y			15
5A[2]	TB3-1,2	J1U	55	17	5	5	Y	Y			10
	-	I4U	47	9	22	2	Y	Y	Y		3
5B	TB3-5,6	J2U	40	2	6	5	Y	Y			15
6A	TB3-9,10	J3U	64	26	36	6	Y	Y			
6B	TB3-11,12	J3L	77	39	46	6	Y	Y			
8A	TB5-9,10	J6U	42	4	8	8	Y	Y			5
8B	TB5-11,12	J6L	46	8	18	8	Y	Y			10

[1] Add jumper from I1-W to J4-W, on rear of input file.

[2] Add jumper from J1-W to I4-W, on rear of input file.

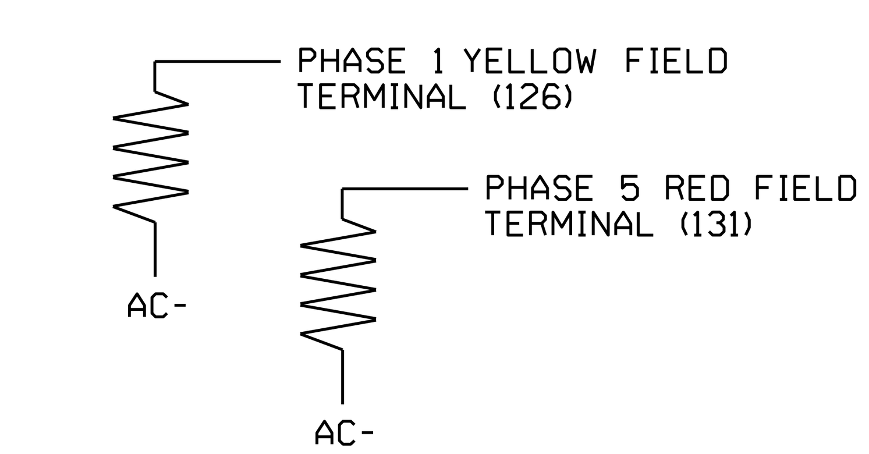
INPUT FILE POSITION LEGEND: J2L

FILE J
SLOT 2
LOWER

LOAD RESISTOR INSTALLATION DETAIL

(install resistors as shown below)

ACCEPTABLE VALUES	
VALUE (ohms)	WATTAGE
1.5K - 1.9K	25W (min)
2.0K - 3.0K	10W (min)



THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 03-0524
DESIGNED: 07/24
SEALED: 7/24/2015
REVISED: N/A

Electrical Detail - Sheet 1 of 2

ELECTRICAL AND PROGRAMMING DETAILS FOR:

Prepared in the Offices of:
Transportation Mobility and Safety Division
Signals Management Section
750 N.Greenfield Pkwy,Garner,NC 27529

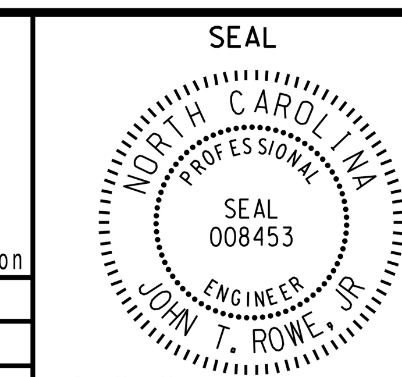
US 421 (Carolina Beach Road) at Echo Farms Boulevard/ George Anderson Drive

Division 3 New Hanover County Wilmington

PLAN DATE: July 2015	REVIEWED BY: JTR
PREPARED BY: S. Armstrong	REVIEWED BY:

REVISIONS	INIT.	DATE

SEAL



DocuSigned by: John T. Rowe, Jr. 7/27/2015
641D60C145EE4F5...
DATE

SIG. INVENTORY NO. 03-0524

27-JUL-2015 08:02
S:*ITS&SU\ITS Signals\Workgroups\Sig Man\Armstrong\030524_sm_ele.xxx.dgn
sarmstrong