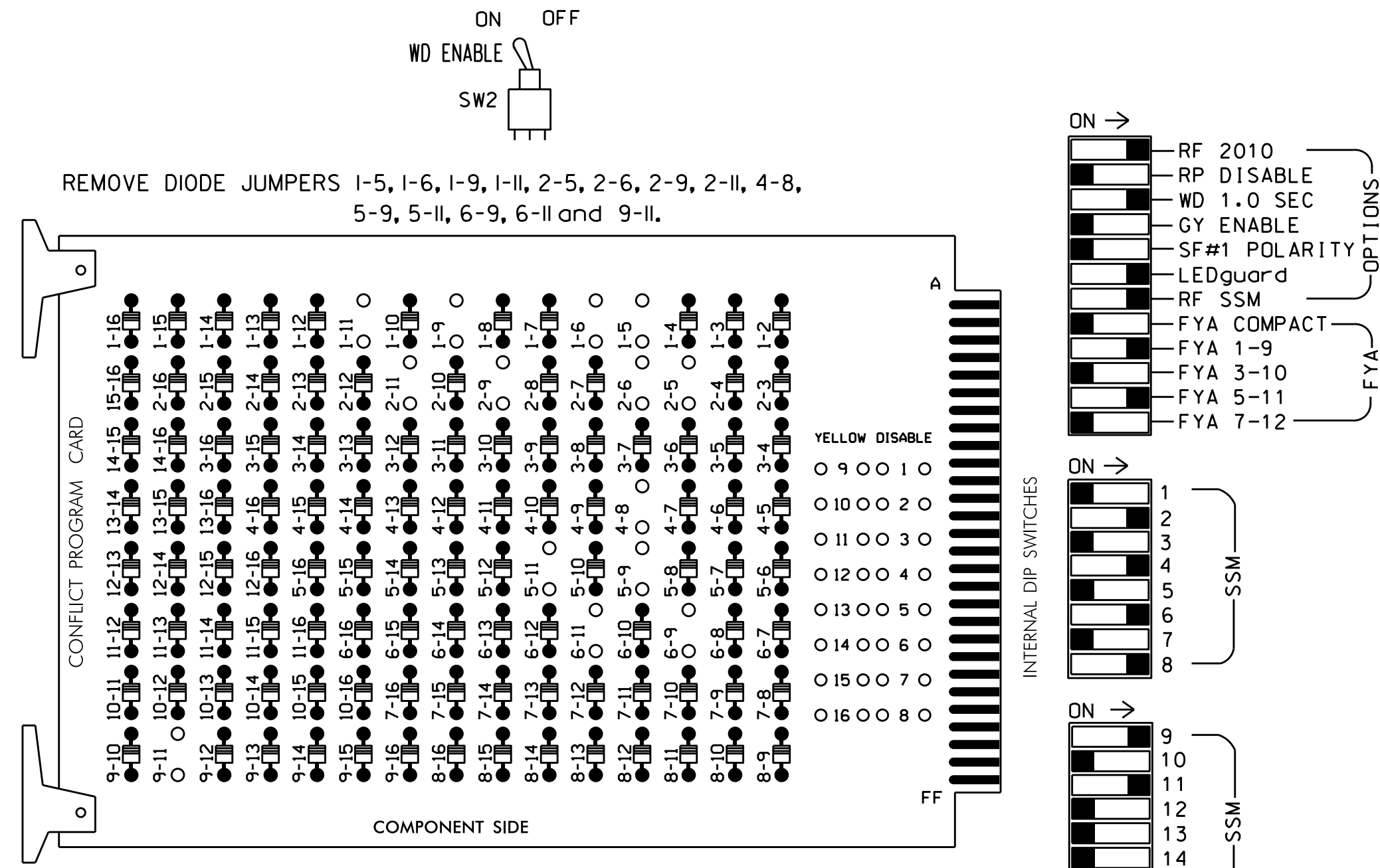


**EDI MODEL 2010ECL-NC CONFLICT MONITOR
PROGRAMMING DETAIL**

(remove jumpers and set switches as shown)



NOTES:

- Card is provided with all diode jumpers in place. Removal of any jumper allows its channels to run concurrently.
- Make sure jumpers SEL2-SEL5 are present on the monitor board.

INPUT FILE POSITION LAYOUT

(front view)

	1	2	3	4	5	6	7	8	9	10	11	12	13	14	
FILE "I" U	∅ 1	∅ 2	-OF-S	-OF-S	-OF-S	∅ 4	-OF-S	-OF-S	-OF-S	-OF-S	-OF-S	-OF-S	-OF-S	-OF-S	FS
"I" L	1A	2A	-OF-S	-OF-S	-OF-S	4A	-OF-S	-OF-S	-OF-S	-OF-S	-OF-S	-OF-S	-OF-S	-OF-S	DC ISOLATOR
	NOT USED	∅ 2	-OF-S	-OF-S	-OF-S	NOT USED	-OF-S	-OF-S	-OF-S	-OF-S	-OF-S	-OF-S	-OF-S	-OF-S	ST
FILE "J" U	∅ 5	∅ 6	-OF-S	-OF-S	-OF-S	∅ 8	-OF-S	-OF-S	-OF-S	-OF-S	-OF-S	-OF-S	-OF-S	-OF-S	DC ISOLATOR
"J" L	5A	6A	-OF-S	-OF-S	-OF-S	8A	-OF-S	-OF-S	-OF-S	-OF-S	-OF-S	-OF-S	-OF-S	-OF-S	
	NOT USED	∅ 6	-OF-S	-OF-S	-OF-S	∅ 8	-OF-S	-OF-S	-OF-S	-OF-S	-OF-S	-OF-S	-OF-S	-OF-S	
		6B	-OF-S	-OF-S	-OF-S		-OF-S	-OF-S	-OF-S	-OF-S	-OF-S	-OF-S	-OF-S	-OF-S	

EX.: 1A, 2A, ETC. = LOOP NO.'S

FS = FLASH SENSE
ST = STOP TIME

NOTES

- To prevent "flash-conflict" problems, insert red flash program blocks for all unused vehicle load switches in the output file. The installer shall verify that signal heads flash in accordance with the Signal Plans.
- Ensure that Red Enable is active at all times during normal operation. To prevent Red Failures on unused monitor channels, tie unused red monitor inputs 1,3,5,7,10,12,13,14,15 & 16 to load switch AC+ per the cabinet manufacturer's instructions.
- Program phases 4 and 8 for Dual Entry.
- Enable Simultaneous Gap-Out for all phases.
- Program phases 2 and 6 for Variable Initial and Gap Reduction.
- Program phases 2 and 6 for Start Up In Green.
- Program phases 2 and 6 for Yellow Flash, and overlap 1 as Wag Overlaps.
- The cabinet and controller are part of the Wilmington Signal System.

EQUIPMENT INFORMATION

CONTROLLER.....2070L
CABINET.....332 W/ AUX
SOFTWARE.....ECONOLITE OASIS
CABINET MOUNT.....BASE
OUTPUT FILE POSITIONS...18 WITH AUX. OUTPUT FILE
LOAD SWITCHES USED.....S1,S2,S4,S5,S6,S8,S9,S12
PHASES USED.....1,2,4,5,6,8
OVERLAP "A".....1+2
OVERLAP "B".....NOT USED
OVERLAP "C".....5+6
OVERLAP "D".....NOT USED

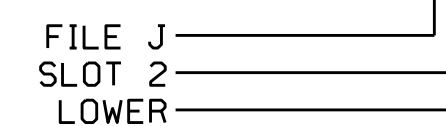
INPUT FILE CONNECTION & PROGRAMMING CHART

LOOP NO.	LOOP TERMINAL	INPUT FILE POS.	PIN NO.	INPUT ASSIGNMENT NO.	DETECTOR NO.	NEMA PHASE	CALL	EXTEND	FULL TIME DELAY	STRETCH TIME	DELAY TIME
1A ¹	TB2-1,2	I1U	56	18	1	1	Y	Y			10
	-	J4U	48	10	26	6	Y	Y	Y		3
2A	TB2-5,6	I2U	39	1	2	2	Y	Y			
2B	TB2-7,8	I2L	43	5	12	2	Y	Y			
4A	TB4-9,10	I6U	41	3	4	4	Y	Y			10
5A ²	TB3-1,2	J1U	55	17	5	5	Y	Y			10
	-	I4U	47	9	22	2	Y	Y	Y		3
6A	TB3-5,6	J2U	40	2	6	6	Y	Y			
6B	TB3-7,8	J2L	44	6	16	6	Y	Y			
8A	TB5-9,10	J6U	42	4	8	8	Y	Y			3
8B	TB5-11,12	J6L	46	8	18	8	Y	Y			15

¹Add jumper from I1-W to J4-W, on rear of input file.

²Add jumper from J1-W to I4-W, on rear of input file.

INPUT FILE POSITION LEGEND: J2L



SIGNAL HEAD HOOK-UP CHART

LOAD SWITCH NO.	S1	S2	S2P	S3	S4	S4P	S5	S6	S6P	S7	S8	S8P	S9	S10	S11	S12	S13	S14
PHASE	1*	2	2 PED	3	4	4 PED	5*	6	6 PED	7	8	8 PED	OL*	OLB	SPARE	OLC*	OLD	SPARE
SIGNAL HEAD NO.	11	21,22	NU	NU	41,42	NU	51	61,62	NU	NU	81,82	NU	11	NU	NU	51	NU	NU
RED		128			101			134			107							
YELLOW	*	129			102		*	135			108							
GREEN		130			103			136			109							
RED ARROW																A121		A114
YELLOW ARROW																A122		A115
FLASHING YELLOW ARROW																A123		A116
GREEN ARROW	127							133										

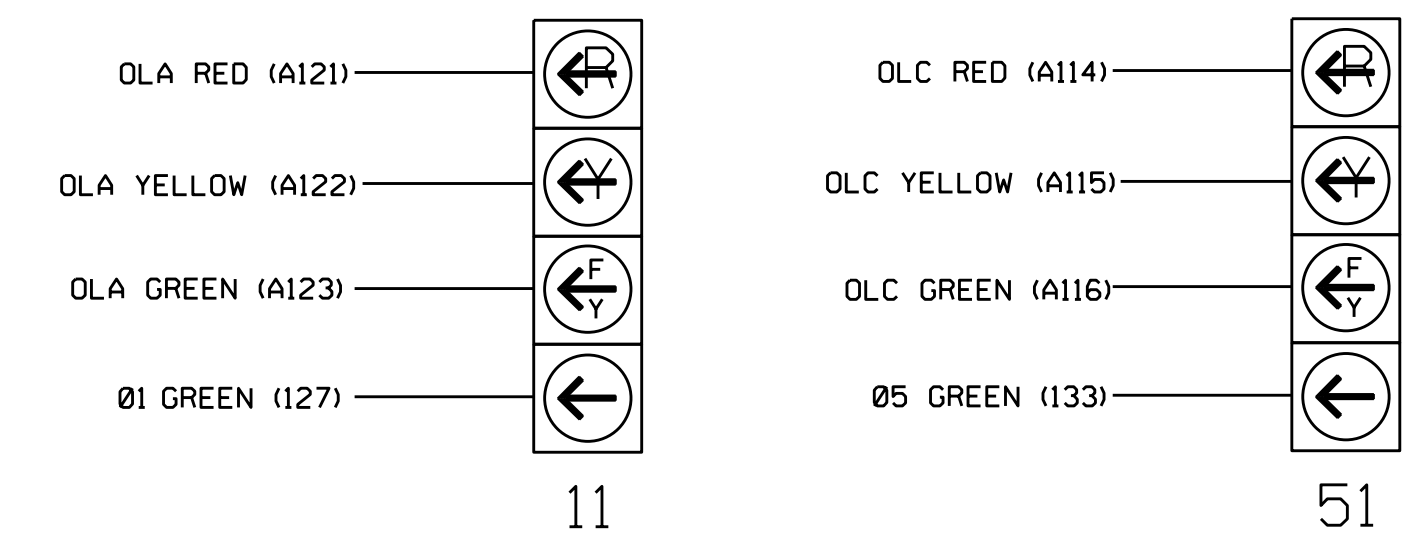
NU = Not Used

* Denotes install load resistor. See load resistor installation detail this sheet.

★ See pictorial of head wiring in detail below.

4 SECTION FYA PPLT SIGNAL WIRING DETAIL

(wire signal heads as shown)



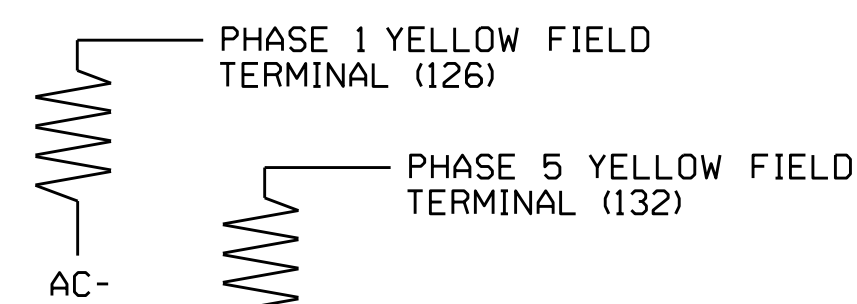
NOTE

- The sequence display for this signal requires special logic programming. See sheet 2 for programming instructions.

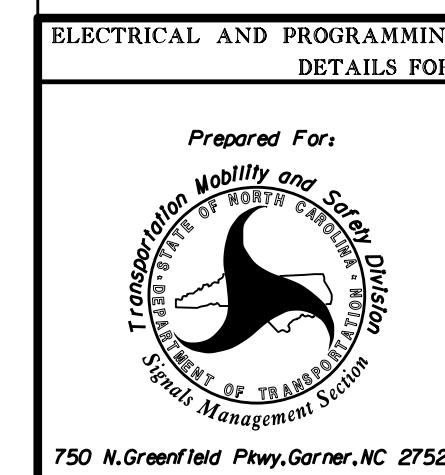
LOAD RESISTOR INSTALLATION DETAIL

(install resistors as shown below)

ACCEPTABLE VALUES	VALUE (ohms)	WATTAGE
	1.5K - 1.9K	25W (min)
	2.0K - 3.0K	10W (min)



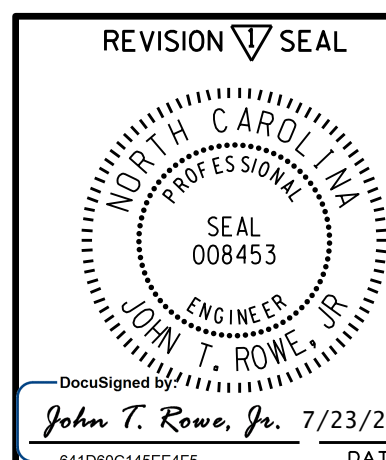
ELECTRICAL DETAIL SHEET 1 OF 2



US 421 (CAROLINA BEACH ROAD)
AT
SR 1247 (ANTOINETTE DRIVE) //
LOWE'S ENTRANCE

DIVISION 3 NEW HANOVER COUNTY WILMINGTON
PLAN DATE: JANUARY 2013 REVIEWED BY: SL PHILLIPS
PREPARED BY: SP PENNINGTON REVIEWED BY:

REVISIONS
Removed stretch time from phase 2 and 6 loops. (USA) gtr DATE 7/23/2015



Not a certified document as to the Original Document but only as to the Revisions - This document originally issued and sealed by Sracie L. Phillips, #032607, on 1/8/13. This document is only certified as to the revisions.