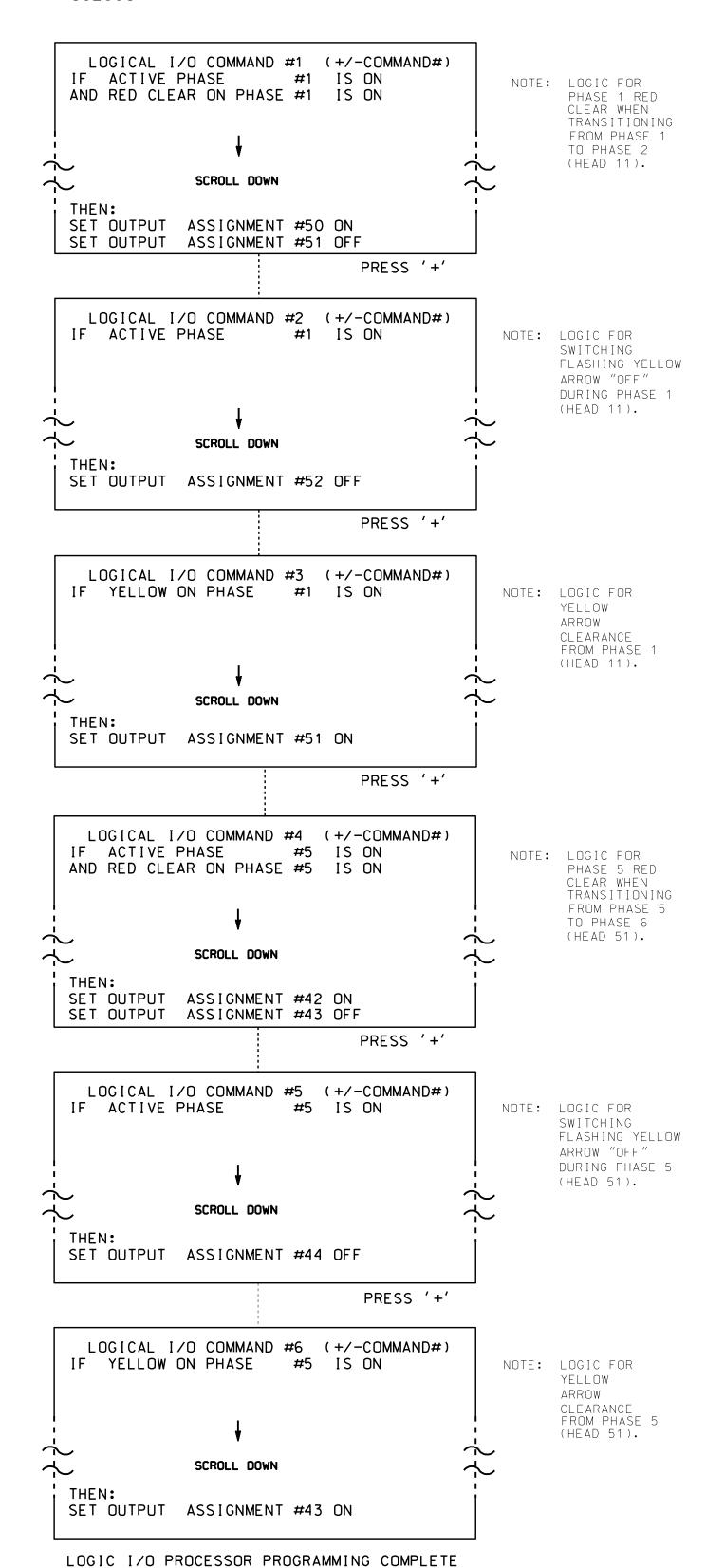
(program controller as shown below)

- 1. FROM MAIN MENU PRESS '2' (PHASE CONTROL), THEN '1' (PHASE CONTROL FUNCTIONS). SCROLL TO THE BOTTOM OF THE MENU AND ENABLE ACT LOGIC COMMANDS 1, 2, 3, 4, 5 AND 6.
- 2. FROM MAIN MENU PRESS '6' (OUTPUTS), THEN '3' (LOGICAL I/O PROCESSOR).



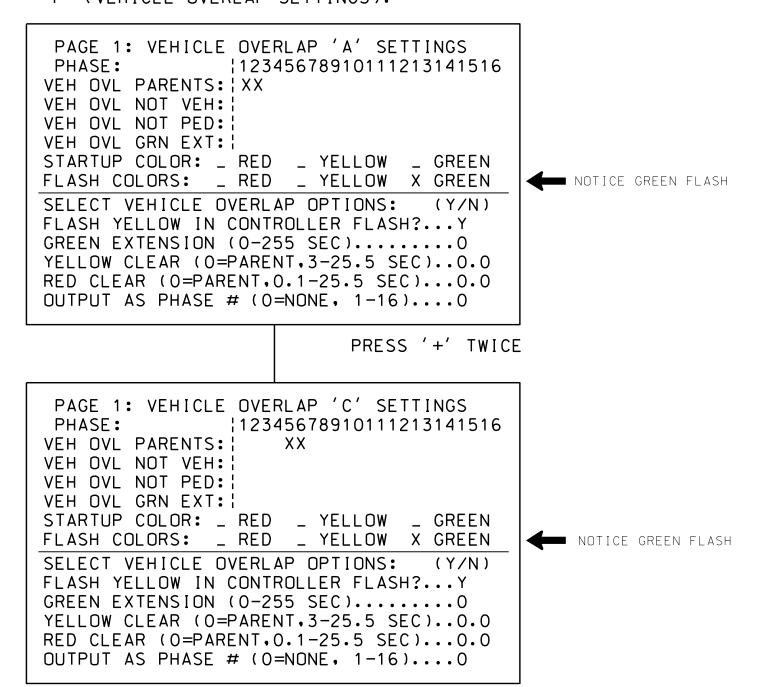
OUTPUT REFERENCE SCHEDULE USE TO INTERPRET LOGIC PROCESSOR OUTPUT 42 = Overlap C Red OUTPUT 43 = Overlap C Yellow OUTPUT 44 = Overlap C Green OUTPUT 50 = Overlap A Red OUTPUT 51 = Overlap A Yellow OUTPUT 52 = Overlap A Green

PROJECT REFERENCE NO. Sig. 6.2 B-5136

OVERLAP PROGRAMMING DETAIL

(program controller as shown below)

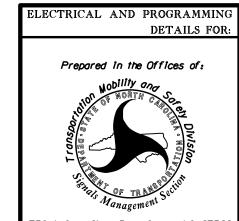
FROM MAIN MENU PRESS '8' (OVERLAPS). THEN '1' (VEHICLE OVERLAP SETTINGS).



OVERLAP PROGRAMMING COMPLETE

THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 10-0820 DESIGNED: November 2014 SEALED: 12-15-14 REVISED: N/A

ELECTRICAL DETAIL SHEET 2 OF 2



US 29-601 / US 29-601-NC 73 (Concord Parkway North) at NC 73 (Davidson Parkway) Florence Street

Cabarrus County ____ps Concord ivision 10 PLAN DATE: December 2014 | REVIEWED BY: | 978 PREPARED BY: James Peterson | REVIEWED BY:

REVISIONS

SEAL

008453

750 N.Greenfield Pkwy, Garner, NC 27529

INIT. DATE