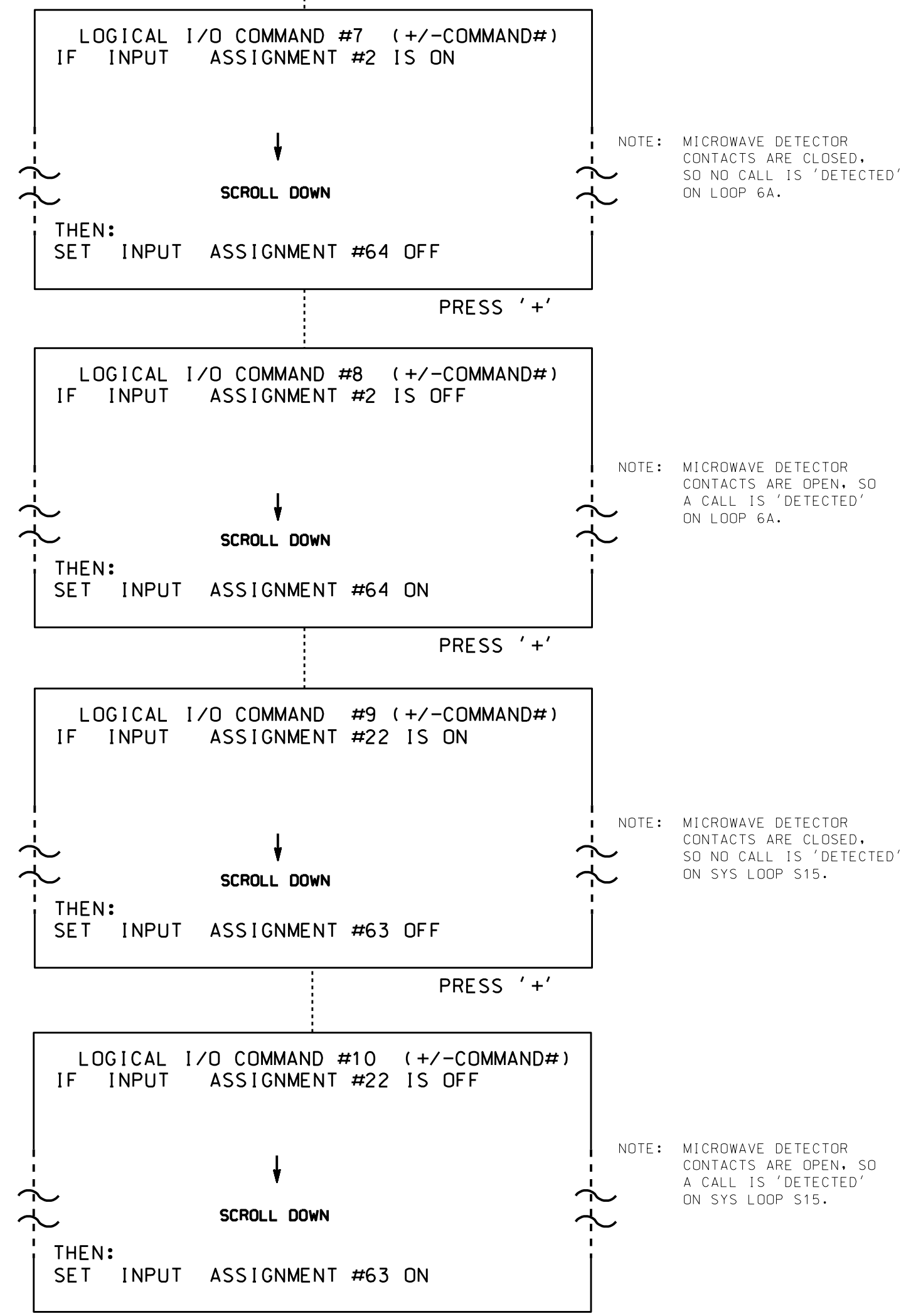
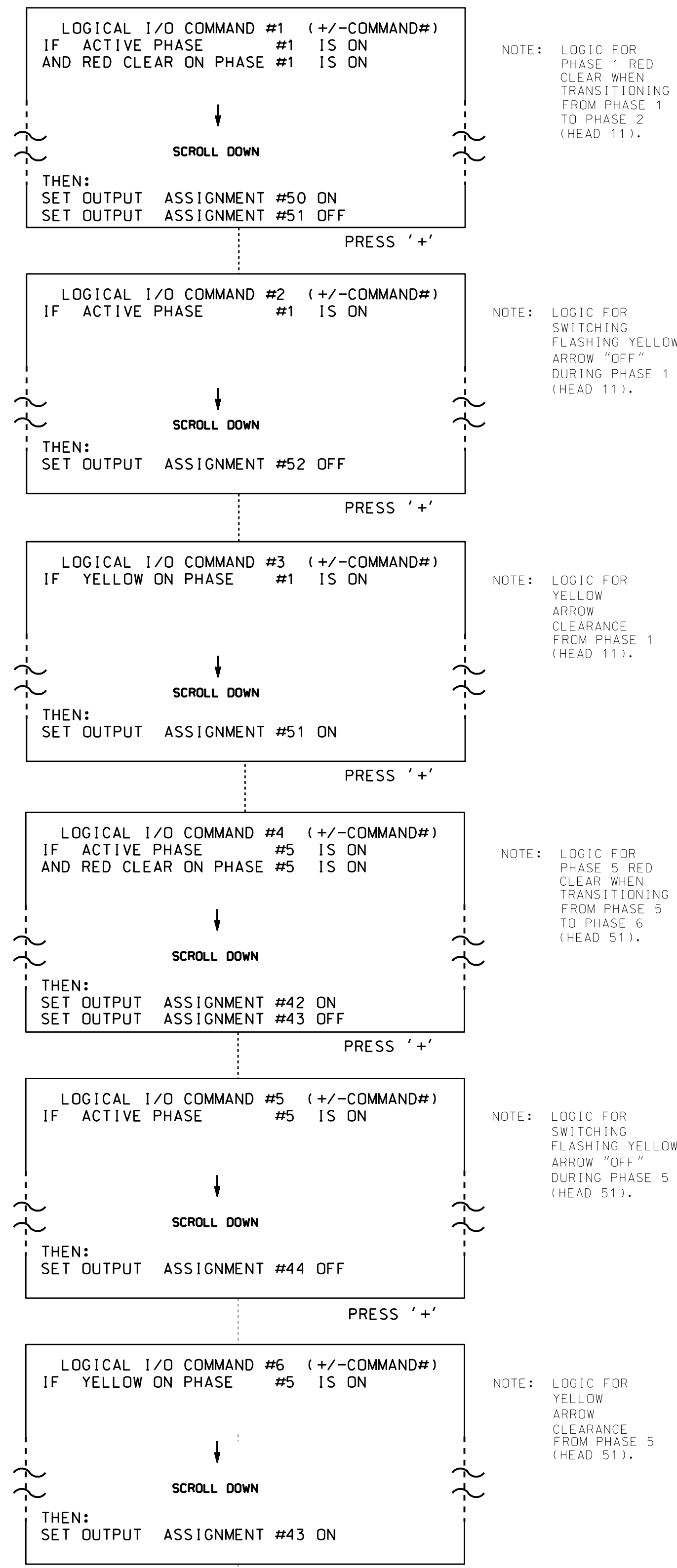


**LOGICAL I/O PROCESSOR PROGRAMMING DETAIL**  
**TO PRODUCE SPECIAL FYA-PPLT SIGNAL SEQUENCE**

(program controller as shown below)

- FROM MAIN MENU PRESS '2' (PHASE CONTROL), THEN '1' (PHASE CONTROL FUNCTIONS). SCROLL TO THE BOTTOM OF THE MENU AND ENABLE ACT LOGIC COMMANDS 1, 2, 3, 4, 5, 6, 7, 8, 9 AND 10.
- FROM MAIN MENU PRESS '6' (OUTPUTS), THEN '3' (LOGICAL I/O PROCESSOR).



LOGIC I/O PROCESSOR PROGRAMMING COMPLETE

**INPUT/OUTPUT REFERENCE SCHEDULE**  
 USE TO INTERPRET LOGIC PROCESSOR

OUTPUT 42	=	Overlap C Red
OUTPUT 43	=	Overlap C Yellow
OUTPUT 44	=	Overlap C Green
OUTPUT 50	=	Overlap A Red
OUTPUT 51	=	Overlap A Yellow
OUTPUT 52	=	Overlap A Green
INPUT 2	=	Microwave Detector Physical Input (Not Enabled)
INPUT 64	=	Dummy Microwave Detector Input (Detector 6)
INPUT 22	=	Microwave Detector Physical Input (Not Enabled)
INPUT 63	=	Dummy Microwave Detector Input (Detector 11)

**OVERLAP PROGRAMMING DETAIL**

(program controller as shown below)

FROM MAIN MENU PRESS '8' (OVERLAPS), THEN '1' (VEHICLE OVERLAP SETTINGS).

PAGE 1: VEHICLE OVERLAP 'A' SETTINGS  
 PHASE: 12345678910111213141516  
 VEH OVL PARENTS: XX  
 VEH OVL NOT VEH: :  
 VEH OVL NOT PED: :  
 VEH OVL GRN EXT: :  
 STARTUP COLOR: - RED - YELLOW - GREEN  
 FLASH COLORS: - RED - YELLOW X GREEN  
 SELECT VEHICLE OVERLAP OPTIONS: (Y/N)  
 FLASH YELLOW IN CONTROLLER FLASH?...Y  
 GREEN EXTENSION (0-255 SEC)...0.0  
 YELLOW CLEAR (0=PARENT,3-25.5 SEC)...0.0  
 RED CLEAR (0=PARENT,0.1-25.5 SEC)...0.0  
 OUTPUT AS PHASE # (0=NONE, 1-16)...0

← NOTICE GREEN FLASH

PRESS '+' TWICE

PAGE 1: VEHICLE OVERLAP 'C' SETTINGS  
 PHASE: 12345678910111213141516  
 VEH OVL PARENTS: XX  
 VEH OVL NOT VEH: :  
 VEH OVL NOT PED: :  
 VEH OVL GRN EXT: :  
 STARTUP COLOR: - RED - YELLOW - GREEN  
 FLASH COLORS: - RED - YELLOW X GREEN  
 SELECT VEHICLE OVERLAP OPTIONS: (Y/N)  
 FLASH YELLOW IN CONTROLLER FLASH?...Y  
 GREEN EXTENSION (0-255 SEC)...0.0  
 YELLOW CLEAR (0=PARENT,3-25.5 SEC)...0.0  
 RED CLEAR (0=PARENT,0.1-25.5 SEC)...0.0  
 OUTPUT AS PHASE # (0=NONE, 1-16)...0

← NOTICE GREEN FLASH

OVERLAP PROGRAMMING COMPLETE

THIS ELECTRICAL DETAIL IS FOR  
 THE SIGNAL DESIGN: 10-0820 T2  
 DESIGNED: November 2014  
 SEALED: 12-15-14  
 REVISED: N/A

ELECTRICAL DETAIL SHEET 2 OF 4

	US 29-601 / US 29-601-NC 73 (Concord Parkway North) at NC 73 (Davidson Parkway) / Florence Street		
	Division 10 Cabarrus County PLAN DATE: December 2014 PREPARED BY: James Peterson	ds Concord REVIEWED BY: JTR REVIEWED BY:	

16-DEC-2014 07:43  
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 JTPeterson