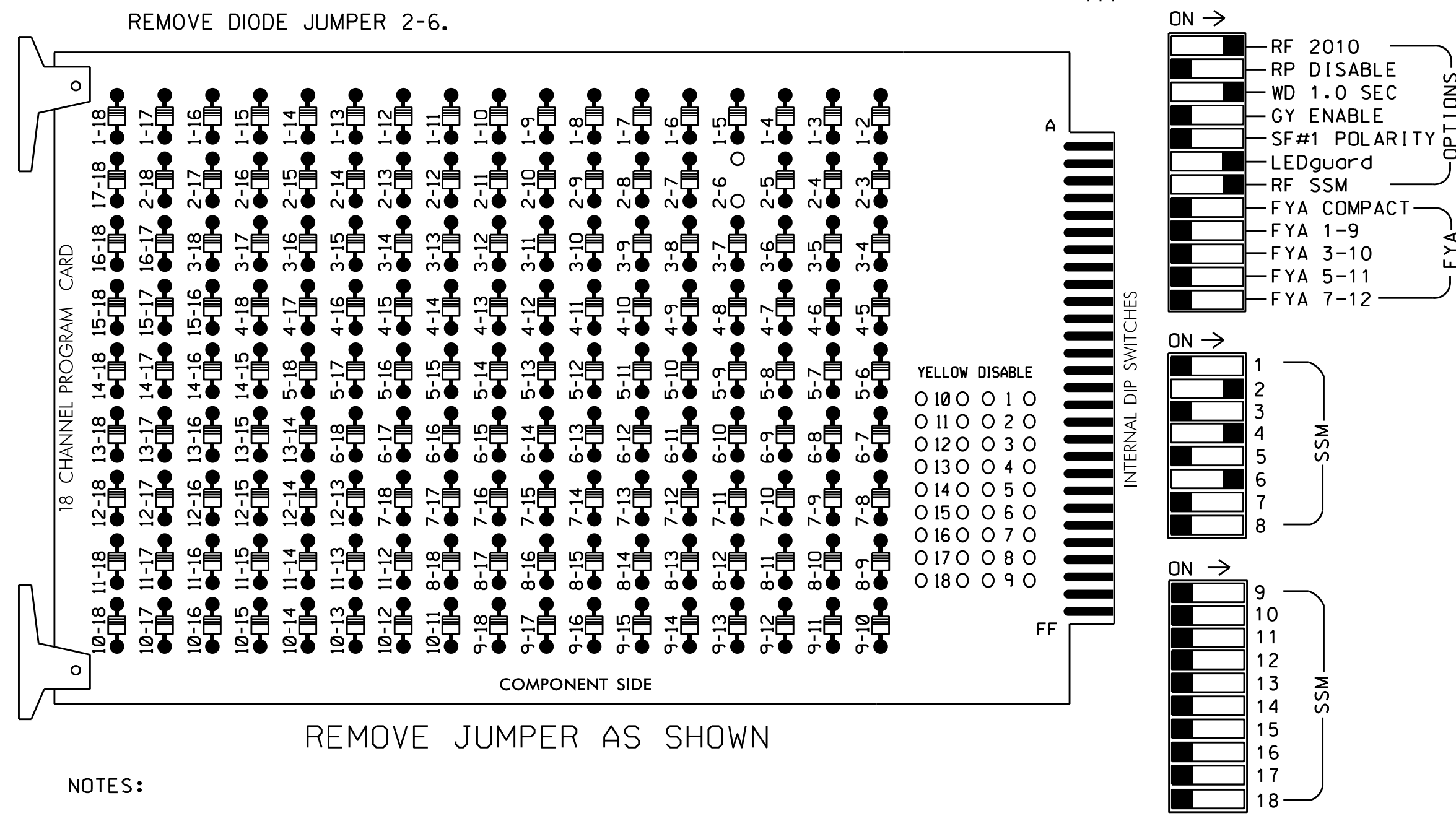


**EDI MODEL 2018ECL-NC CONFLICT MONITOR  
PROGRAMMING DETAIL**

(remove jumpers and set switches as shown)



**NOTES:**

1. Card is provided with all diode jumpers in place. Removal of any jumper allows its channels to run concurrently.
2. Ensure jumpers SEL2-SEL5 and SEL9 are present on the monitor board.
3. Ensure that Red Enable is active at all times during normal operation.
4. Connect serial cable from conflict monitor to comm. port 1 of 2070 controller. Ensure conflict monitor communicates with 2070.

**NOTES**

1. To prevent "flash-conflict" problems, insert red flash program blocks for all unused vehicle load switches in the output file. The installer shall verify that signal heads flash in accordance with the Signal Plans.
2. Enable Simultaneous Gap-Out for all phases.
3. Program phases 2 and 6 for Variable Initial and Gap Reduction.
4. Program phases 2 and 6 for Start Up In Green.
5. Program phases 2 and 6 for Yellow Flash.

**EQUIPMENT INFORMATION**

CONTROLLER.....2070L  
 CABINET.....336  
 SOFTWARE.....ECONOLITE OASIS  
 CABINET MOUNT.....POLE  
 OUTPUT FILE POSITIONS...12  
 LOAD SWITCHES USED.....S2,S5,S8  
 PHASES USED.....2,4,6  
 OVERLAPS.....NONE

**SIGNAL HEAD HOOK-UP CHART**

LOAD SWITCH NO.	S1	S2	S3	S4	S5	S6	S7	S8	S9	S10	S11	S12
CMU CHANNEL NO.	1	2	13	3	4	14	5	6	15	7	8	16
PHASE	1	2	2 PED	3	4	4 PED	5	6	6 PED	7	8	8 PED
SIGNAL HEAD NO.	NU	21,22	NU	NU	41,42 43	NU	NU	61,62	NU	NU	NU	NU
RED		128			101			134				
YELLOW		129			102			135				
GREEN		130			103			136				
RED ARROW												
YELLOW ARROW												
GREEN ARROW												

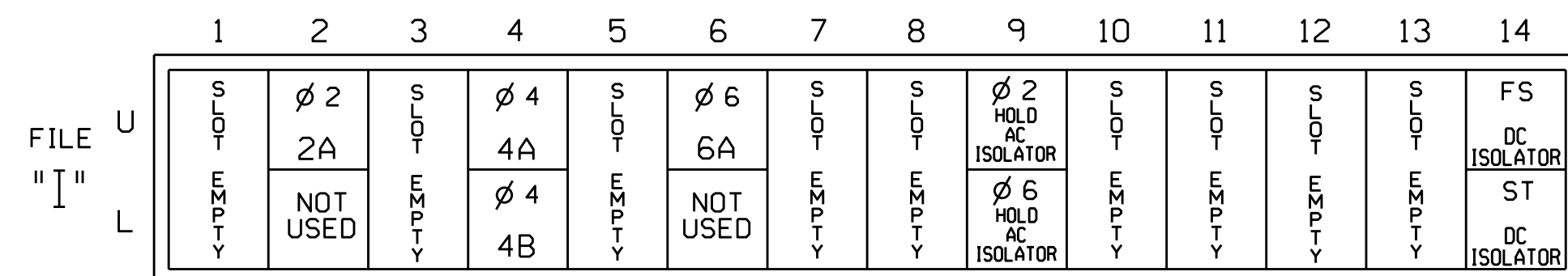
NU = Not Used

**SOLAR POWERED BEACON NOTE**

Install solar powered beacons per manufacturer's directions and flash continuously. The locations are shown on the Signal Design Plan and may be field adjusted accordingly by the NCDOT engineer.

**INPUT FILE POSITION LAYOUT**

(front view)



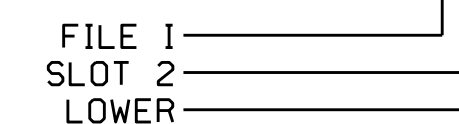
EX. : 1A, 2A, ETC. = LOOP NO.'S

FS = FLASH SENSE  
 ST = STOP TIME

**INPUT FILE CONNECTION & PROGRAMMING CHART**

LOOP NO.	LOOP TERMINAL	INPUT FILE POS.	PIN NO.	INPUT ASSIGNMENT NO.	DETECTOR NO.	NEMA PHASE	CALL	EXTEND	FULL TIME DELAY	STRETCH TIME	DELAY TIME
2A	TB21-3,4	I2U	39	1	2	2	Y	Y			
4A	TB21-7,8	I4U	41	3	4	4	Y	Y			
4B	TB23-7,8	I4L	45	7	14	4	Y	Y			15
6A	TB21-11,12	I6U	40	2	6	6	Y	Y			

**INPUT FILE POSITION LEGEND: I2L**



**THIS ELECTRICAL PLAN SUPERSEDES THE PLAN ORIGINALLY SEALED ON 8/19/11.**

THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 07-0282  
 DESIGNED: January 2015  
 SEALED: 1/27/2015  
 REVISED: N/A

Electrical Detail - Temporary Signal (TMP Phase I) - Sheet 1 of 4

Electrical and Programming Details For:

Prepared In the Offices of:

750 N. Greenfield Pkwy, Garner, NC 27529

US 220 at NC 68

Division 7 Rockingham County NE of Stokesdale

PLAN DATE: January 2015 REVIEWED BY: JTR

PREPARED BY: S. Armstrong REVIEWED BY:

REVISIONS INIT. DATE

DocuSigned by: John T. Rowe, Jr. 1/28/2015

SEAL: SEAL 008453 ENGINEER JOHN T. ROWE, JR.

SIG. INVENTORY NO. 07-0282

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