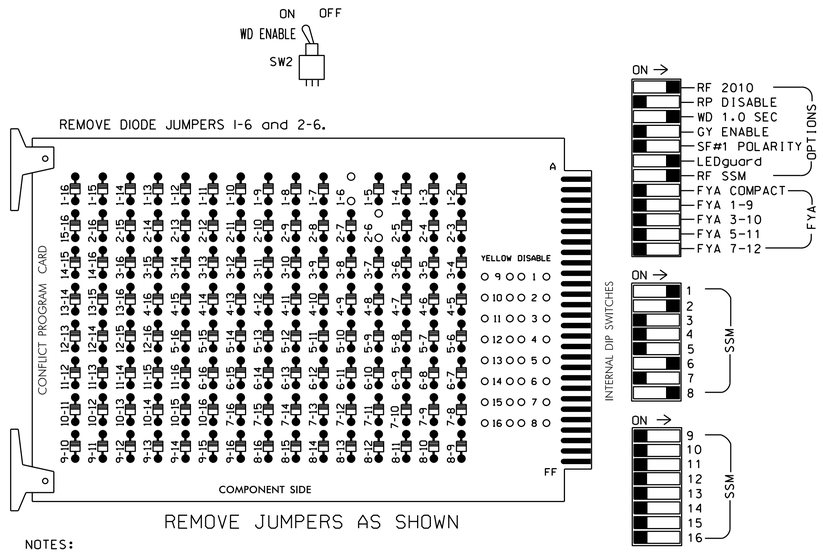
PROJECT REFERENCE NO.	SHEET NO.
U-3465	Sig. 3.1

EDI MODEL 2010ECL-NC CONFLICT MONITOR PROGRAMMING DETAIL

(remove jumpers and set switches as shown)



REMOVE JUMPERS AS SHOWN

NOTES:

1. Card is provided with all diode jumpers in place. Removal of any jumper allows its channels to run concurrently.
2. Make sure jumpers SEL2-SEL5 are present on the monitor board.

NOTE: a new program card fully populated with diode jumpers will likely have to be provided for this design.

■ = DENOTES POSITION OF SWITCH

INPUT FILE POSITION LAYOUT

(front view)

FILE "I"	1	2	3	4	5	6	7	8	9	10	11	12	13	14
U	Ø 1 1A	Ø 2 2A	SLOT EMPTY	SLOT EMPTY	SLOT EMPTY	Ø 6 6A	SLOT EMPTY	Ø 8 8A	SLOT EMPTY	SLOT EMPTY	SLOT EMPTY	SLOT EMPTY	SLOT EMPTY	FS DC ISOLATOR
L	⊗ WIRED INPUT	NOT USED				NOT USED		Ø 8 8B						ST DC ISOLATOR

EX.: 1A, 2A, ETC. = LOOP NO.'S

⊗ Wired Input - turn off channel 2.

FS = FLASH SENSE
ST = STOP TIME

INPUT FILE CONNECTION & PROGRAMMING CHART

LOOP NO.	LOOP TERMINAL	INPUT FILE POS.	PIN NO.	INPUT ASSIGNMENT NO.	DETECTOR NO.	NEMA PHASE	CALL	EXTEND	FULL TIME DELAY	STRETCH TIME	DELAY TIME
1A[1]	TB21-1,2	I1U	56	18	1	1	Y	Y			
	-	I1L	47	9	22	6	Y	Y	Y	2.0	5
2A	TB21-3,4	I2U	39	1	2	2	Y	Y			
6A	TB21-11,12	I6U	40	2	6	6	Y	Y			
8A	TB22-1,2	I8U	42	4	8	8	Y	Y			3
8B	TB24-1,2	I8L	46	8	18	8	Y	Y			10

[1] Add jumper from I1-F to I1-W, on rear of input file.

~~Add jumper from I3-F to I3-W, on rear of input file.~~

~~Add jumper from I7-F to I7-W, on rear of input file.~~

! IMPORTANT! Remove the jumpers as shown above, if present.

INPUT FILE POSITION LEGEND: I2L
FILE I
SLOT 2
LOWER

NOTES

1. To prevent "flash-conflict" problems, insert red flash program blocks for all unused vehicle load switches in the output file. The installer shall verify that signal heads flash in accordance with the Signal Plans.
2. Ensure that Red Enable is active at all times during normal operation. To prevent Red Failures on unused monitor channels, tie unused red monitor inputs 3,4,5, 7,9,10,11,12,13,14,15 & 16 to load switch AC+ per the cabinet manufacturer's instructions.
3. Restore To Factory Defaults before programming the controller per this electrical plan.
4. Enable Simultaneous Gap-Out for all phases.
5. Program phase 2 for Variable Initial.
6. Program phases 2 and 6 for Gap Reduction.
7. Program phases 2 and 6 for Start Up In Green.
8. Program phases 2 and 6 for Yellow Flash.

EQUIPMENT INFORMATION

CONTROLLER.....2070
CABINET.....336
SOFTWARE.....ECONOLITE OASIS
CABINET MOUNT.....POLE
OUTPUT FILE POSITIONS...12
LOAD SWITCHES USED.....S1,S2,S6,S8
PHASES USED.....1,2,6,8
OVERLAPS.....NONE

PED YELLOW CONFLICT MONITOR WIRING DETAIL

(FOR McCAIN CABINET ONLY)

In order to use FYA COMPACT mode on the 2010ECL-NC Monitor, the cabinet must be wired such that the (unused) Ped Yellow load switch outputs are wired to the conflict monitor as follows: From 2 PY (field term. 114) to chan. 9 green (monitor pin 13), from 4 PY (field term. 105) to chan. 9 yellow (monitor pin 16), from 6 PY (field term. 120) to chan. 10 green (monitor pin R), and from 8 PY (field term. 111) to chan. 10 yellow (monitor pin U).

This is accomplished through a keyed plug connection found on the inside panel of the output file.

Fold down rear panel of output file and find a set of 3 white keyed connectors. Plug together the two connectors labeled as shown below:

1-2PY	1-CMU-13
2-4PY	2-CMU-16
3-6PY	3-CMU-R
4-8PY	4-CMU-U

! REMOVE ANY CONNECTIONS THAT WERE MADE AS DESCRIBED ABOVE.

SIGNAL HEAD HOOK-UP CHART

LOAD SWITCH NO.	S1	S2	S2P	S3	S4	S4P	S5	S6	S6P	S7	S8	S8P
PHASE	1	2	2 PED	3	4	4 PED	5	6	6 PED	7	8	8 PED
SIGNAL HEAD NO.	61,82	21,22 23	NU	NU	NU	NU	NU	61,62 63	NU	NU	81,82 83	NU
RED	✳	128						134			107	
YELLOW		129						135			108	
GREEN		130						136			109	
RED ARROW												
YELLOW ARROW	126											
GREEN ARROW	127											

NU = Not Used

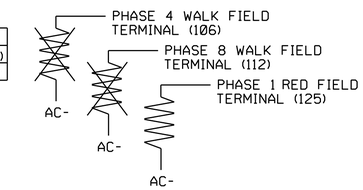
✳ Denotes install load resistor. See load resistor installation detail below.

LOAD RESISTOR INSTALLATION DETAIL

(install resistor as shown below)

ACCEPTABLE VALUES

VALUE (ohms)	WATTAGE
1.5K - 1.9K	25W (min)
2.0K - 3.0K	10W (min)



! IMPORTANT! Remove resistors as shown above, if present.

THIS ELECTRICAL DETAIL IS FOR
THE SIGNAL DESIGN: 06-0642T2
DESIGNED: May 2014
SEALED: 6/20/14
REVISED: N/A

Electrical Detail - Temporary Design 2 (Phase I - Steps 3 & 4)

ELECTRICAL AND PROGRAMMING DETAILS FOR:

Prepared in the Offices of:
Transportation Mobility and Safety Division
Signal Management Section
750 N.Greenfield Pkwy,Garner,NC 27529

SR 1121 (Ray Road)
at
SR 1120 (Overhills Road)

Division 6 Warnett County DS Spring Lake

PLAN DATE:	June 2014	REVIEWED BY:	JTR
PREPARED BY:	S. Armstrong	REVIEWED BY:	

REVISIONS	INIT.	DATE

SEAL
NORTH CAROLINA PROFESSIONAL SEAL 008453 ENGINEER JOHN T. ROWE, JR.

DocuSigned by: John T. Rowe, Jr. 6/26/2014
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DATE

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