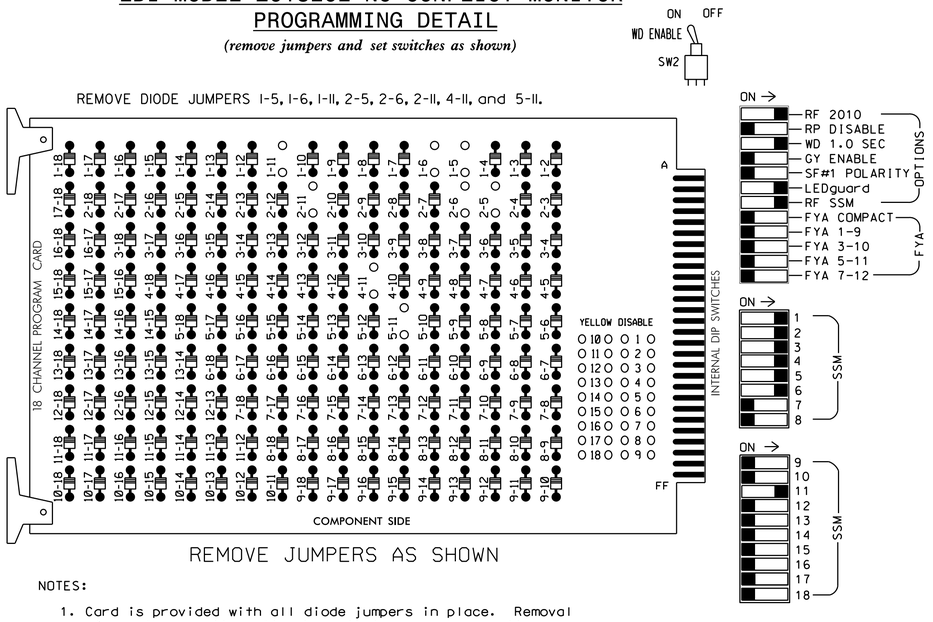
PROJECT REFERENCE NO.	SHEET NO.
U-3465	Sig. 9.1

EDI MODEL 2018ECL-NC CONFLICT MONITOR PROGRAMMING DETAIL

(remove jumpers and set switches as shown)



REMOVE JUMPERS AS SHOWN

NOTES:

1. Card is provided with all diode jumpers in place. Removal of any jumper allows its channels to run concurrently.
2. Ensure jumpers SEL2-SEL5 and SEL9 are present on the monitor board.
3. Ensure that Red Enable is active at all times during normal operation.
4. Connect serial cable from conflict monitor to comm. port 1 of 2070 controller. Ensure conflict monitor communicates with 2070.

SIGNAL HEAD HOOK-UP CHART

LOAD SWITCH NO.	S1	S2	S3	S4		S5		S6	S7	S8	S9	S10	S11	S12	AUX S1	AUX S2	AUX S3	AUX S4	AUX S5	AUX S6
CMU CHANNEL NO.	1	2	13	3		4		14	5	6	15	7	8	16	9	10	17	11	12	18
PHASE	1	2	2 PED	3		4		4 PED	5	6	6 PED	7	8	8 PED	OLA	OLB	SPARE	OLC	OLD	SPARE
SIGNAL HEAD NO.	11	21,22	NU	31	32	41	42	NU	51,52	61,62	NU	NU	NU	NU	NU	NU	NU	53,54	NU	NU
RED		128		116	116	101	101			134										
YELLOW		129		117	117	102	102			135										
GREEN		130		118	118	103	103			136										
RED ARROW	125								131									A114		
YELLOW ARROW	126								132									A115		
GREEN ARROW	127			118		103			133									A116		

NU = Not Used

NOTE: for signal heads 53 and 54 to flash concurrently with 41 and 42, remove the wire from O1-6 on the rear of the output file and terminate it on O1-8.

EQUIPMENT INFORMATION

CONTROLLER.....2070L
CABINET.....332 W/ AUX
SOFTWARE.....ECONOLITE OASIS
CABINET MOUNT.....BASE
OUTPUT FILE POSITIONS...18 WITH AUX FILE
LOAD SWITCHES USED.....S1,S2,S4,S5,S7,S8,AUX S4
PHASES USED.....1,2,3,4,5,6
OVERLAP A.....NOT USED
OVERLAP B.....NOT USED
OVERLAP C.....4+5
OVERLAP D.....NOT USED

NOTES

1. To prevent "flash-conflict" problems, insert red flash program blocks for all unused vehicle load switches in the output file. The installer shall verify that signal heads flash in accordance with the Signal Plans.
2. Enable Simultaneous Gap-Out for all phases.
3. Program phases 2 and 6 for Variable Initial and Gap Reduction.
4. Program phases 2 and 6 for Start Up In Green.
5. Program phases 2 and 6 for Yellow Flash.

INPUT FILE POSITION LAYOUT

(front view)

		1	2	3	4	5	6	7	8	9	10	11	12	13	14
FILE "I"	U	Ø 1 1A	Ø 2 2A	SLOT EMPTY	SLOT EMPTY	SLOT EMPTY	Ø 3 3A	Ø 4 4A	SLOT EMPTY	SLOT EMPTY	SLOT EMPTY	SLOT EMPTY	SLOT EMPTY	SLOT EMPTY	FS DC ISOLATOR
	L	NOT USED	Ø 2 2B				Ø 3 3B	Ø 4 4B							ST DC ISOLATOR
FILE "J"	U	Ø 5 5A	Ø 5 5B	Ø 6 6A	SLOT EMPTY	SLOT EMPTY	SLOT EMPTY	SLOT EMPTY	SLOT EMPTY	Ø 5 5D	SLOT EMPTY	SLOT EMPTY	SLOT EMPTY	SLOT EMPTY	SLOT EMPTY
	L	NOT USED	Ø 5 5C	Ø 6 6B						NOT USED					

EX.: 1A, 2A, ETC. = LOOP NO.'S

FS = FLASH SENSE
ST = STOP TIME

INPUT FILE CONNECTION & PROGRAMMING CHART

LOOP NO.	LOOP TERMINAL	INPUT FILE POS.	PIN NO.	INPUT ASSIGNMENT NO.	DETECTOR NO.	NEMA PHASE	CALL	EXTEND	FULL TIME DELAY	STRETCH TIME	DELAY TIME
1A	TB2-1,2	I1U	56	18	1	1	Y	Y			
2A	TB2-5,6	I2U	39	1	2	2	Y	Y			
2B	TB2-7,8	I2L	43	5	12	2	Y	Y			
3A	TB4-9,10	I6U	41	3	4	3	Y	Y			3
3B	TB4-11,12	I6L	45	7	14	3	Y	Y			10
4A	TB6-1,2	I7U	65	27	34	4	Y	Y			
4B	TB6-3,4	I7L	78	40	44	4	Y	Y			
5A	TB3-1,2	J1U	55	17	5	5	Y	Y			
5B	TB3-5,6	J2U	40	2	6	5	Y	Y			
5C	TB3-7,8	J2L	44	6	16	5	Y	Y			15
5D	TB7-9,10	J9U	59	21	15	5	Y	Y			15
6A	TB3-9,10	J3U	64	26	36	6	Y	Y			
6B	TB3-11,12	J3L	77	39	46	6	Y	Y			

INPUT FILE POSITION LEGEND: J2L
FILE J
SLOT 2
LOWER

THIS ELECTRICAL DETAIL IS FOR
THE SIGNAL DESIGN: 06-0437
DESIGNED: June 2014
SEALED: 6/20/2014
REVISED: N/A

OVERLAP PROGRAMMING DETAIL

(program controller as shown below)

FROM MAIN MENU PRESS '8' (OVERLAPS), THEN '1' (VEHICLE OVERLAP SETTINGS).

PRESS '+' TWICE

```
PAGE 1: VEHICLE OVERLAP 'C' SETTINGS
PHASE:          ¦123456789101112131415 16
VEH OVL PARENTS:¦   XX
VEH OVL NOT VEH:¦
VEH OVL NOT PED:¦
VEH OVL GRN EXT:¦
STARTUP COLOR: _ RED  _ YELLOW  _ GREEN
FLASH COLORS:  _ RED  _ YELLOW  _ GREEN
SELECT VEHICLE OVERLAP OPTIONS:   (Y/N)
FLASH YELLOW IN CONTROLLER FLASH?...N
GREEN EXTENSION (0-255 SEC)........0
YELLOW CLEAR (0=PARENT,3-25.5 SEC)..0.0
RED CLEAR (0=PARENT,0.1-25.5 SEC)...0.0
OUTPUT AS PHASE # (0=NONE, 1-16)....0
```

OVERLAP PROGRAMMING COMPLETE

Electrical Detail - (Final)

ELECTRICAL AND PROGRAMMING DETAILS FOR:

Prepared in the Offices of:

Transportation Mobility and Safety Division
STATE OF NORTH CAROLINA
DEPARTMENT OF TRANSPORTATION
Signal Management Section

750 N.Greenfield Pkwy,Garner,NC 27529

NC 210
at
SR 1121 (Ray Road)/
SR 2051 (Sandclay Road)

Division 6 Harnett County DS Spring Lake

PLAN DATE:	June 2014	REVIEWED BY:	JTR
PREPARED BY:	S. Armstrong	REVIEWED BY:	

REVISIONS	INIT.	DATE

SEAL

NORTH CAROLINA PROFESSIONAL SEAL 008453 ENGINEER JOHN T. ROWE, JR.

DocuSigned by: John T. Rowe, Jr. 6/25/2014
541D0C1A5EE4E4... DATE

SIG. INVENTORY NO. 06-0437

25-JUN-2014 07:19
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armstrong