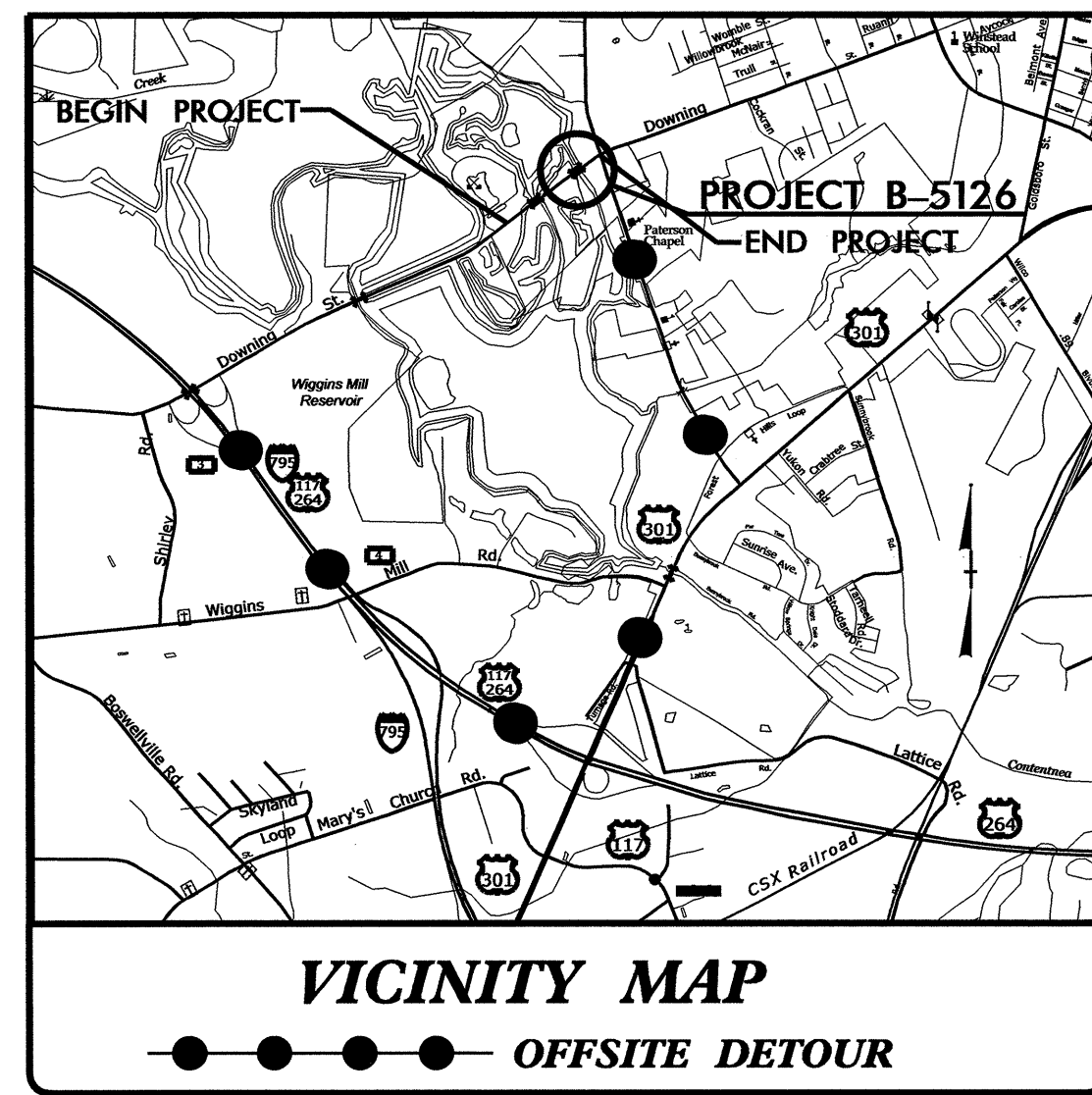


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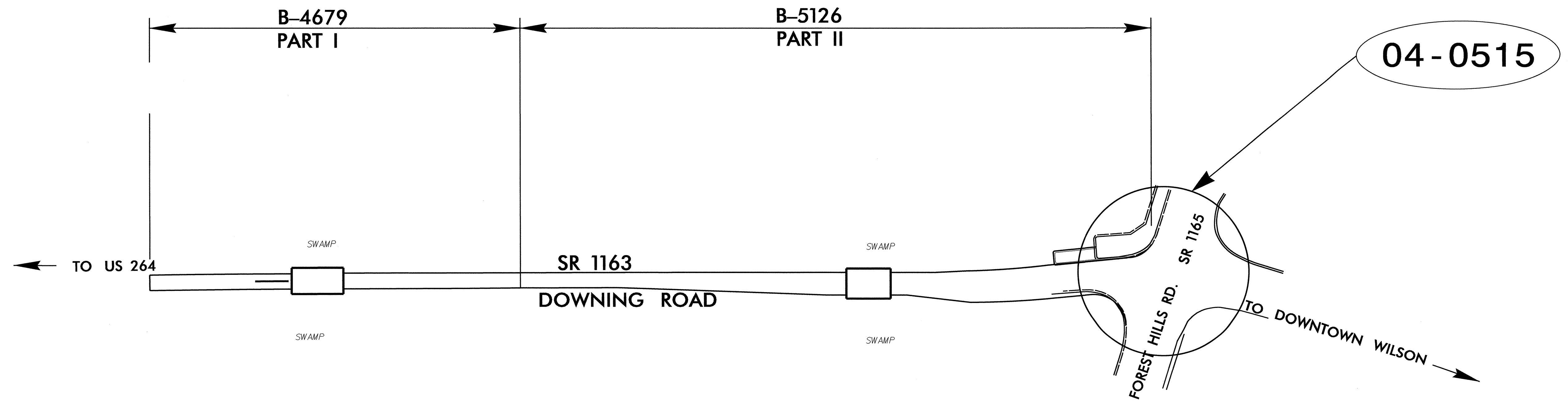
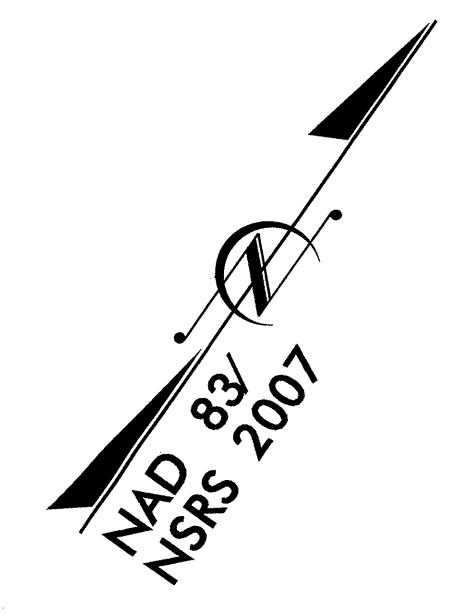


STATE OF NORTH CAROLINA
DIVISION OF HIGHWAYS

WILSON COUNTY

LOCATION: BRIDGES NO. 65 AND NO. 66 OVER SWAMPS OF CONTENTNEA CK OVERFLOW/WIGGINS MILL RESERVOIR ON SR 1163 (DOWNING ROAD) IN WILSON

TYPE OF WORK: TRAFFIC SIGNALS.



Refer to "Roadway Standard Drawings NCDOT" dated January 2012 and "Standard Specifications for Roads and Structures" dated January 2012.

Index of Plans		
Sheet #	Reference #	Location/Description
Sig. 1	-----	Title Sheet
Sig. 2-7	04-0515	SR 1165 (Forest Hills Road) at SR 1163 (Downing Street)

INTELLIGENT TRANSPORTATION AND SIGNALS UNIT
Contacts:
Jason P. Galloway, PE - Eastern Region Signals Project Engineer
John T. Rowe Jr., PE - Signal Equipment Design Engineer

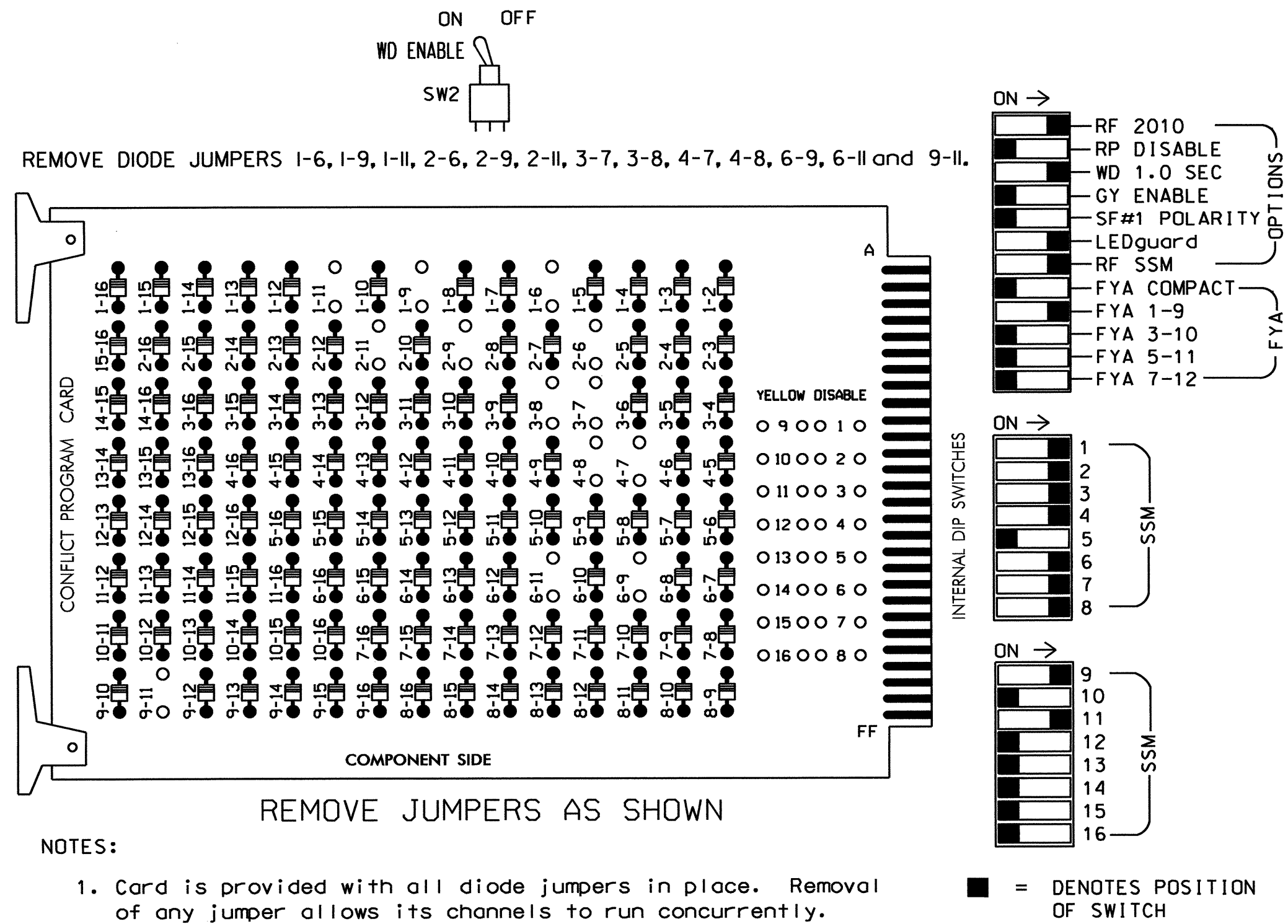
Prepared in the Office of:
DIVISION OF HIGHWAYS
TRANSPORTATION MOBILITY AND SAFETY BRANCH

750 N. Greenfield Parkway, Garner, NC 27529

I9-FEB-2013 15:34 P:\TIP\Projects-B\B5126\Traffic\Signals\Design\Titlesheet\B-5126_RdJ_tsh.dgn

EDI MODEL 2010ECL-NC CONFLICT MONITOR PROGRAMMING DETAIL

(remove jumpers and set switches as shown)



NOTES

- To prevent "flash-conflict" problems, insert red flash program blocks for all unused vehicle load switches in the output file. The installer shall verify that signal heads flash in accordance with the Signal Plans.
- Ensure that Red Enable is active at all times during normal operation. To prevent Red Failures on unused monitor channels, tie unused red monitor inputs 5,10,12,13,14,15 & 16 to load switch AC+ per the cabinet manufacturer's instructions.
- Enable Simultaneous Gap-Out for all phases.
- Program phases 2 and 6 for Variable Initial and Gap Reduction.
- Program phases 2 and 6 for Start Up In Green.
- Program phases 2 and 6 for Yellow Flash, and overlap 1 as Wag Overlaps.

EQUIPMENT INFORMATION

CONTROLLER.....2070L
CABINET.....332 /W/ AUX
SOFTWARE.....ECONOLITE OASIS
CABINET MOUNT.....BASE
OUTPUT FILE POSITIONS...18 WITH AUX. OUTPUT FILE
LOAD SWITCHES USED.....S1,S2,S3,S4,S6,S7,S8,S9,S12.
PHASES USED.....1,2,3,4,6,7,8.
OVERLAP "A".....1+2
OVERLAP "B".....NOT USED
OVERLAP "C".....6
OVERLAP "D".....NOT USED

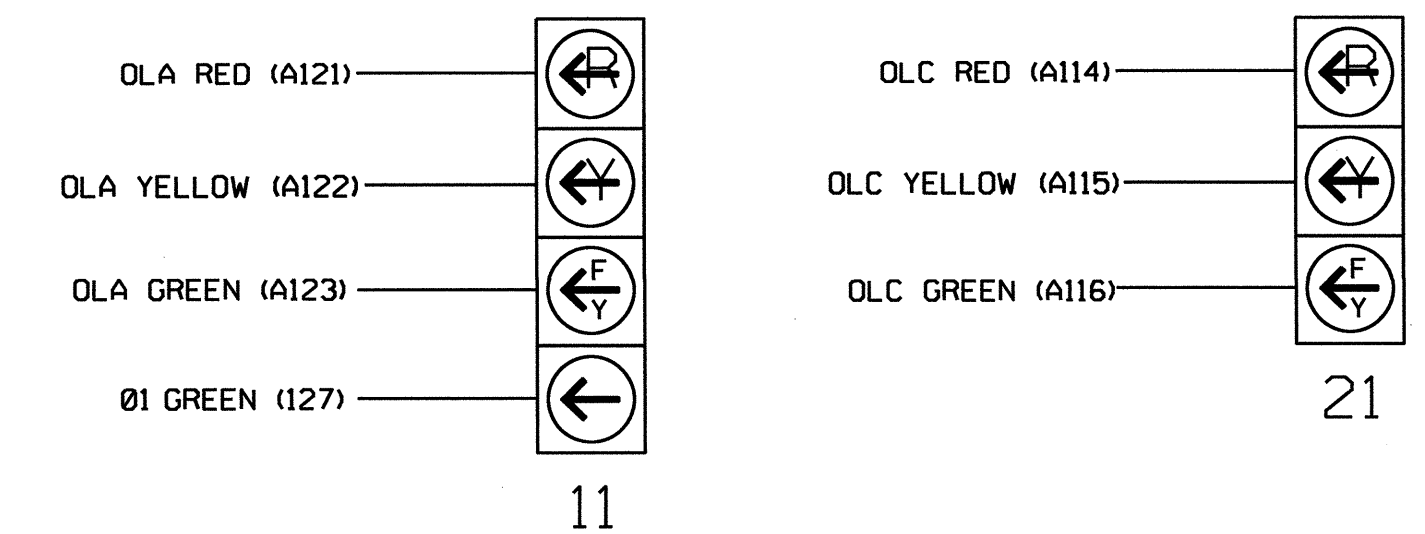
SIGNAL HEAD HOOK-UP CHART

LOAD SWITCH NO.	S1	S2	S2P	S3	S4	S4P	S5	S6	S6P	S7	S8	S8P	S9	S10	S11	S12	S13	S14
PHASE	1	2	2 PED	3	4	4 PED	5	6	6 PED	7	8	8 PED	9	10	11	12	13	14
SIGNAL HEAD NO.	11	82	22,23	31	41,42	NU	NU	61,62	NU	71	81,82	NU	11	NU	NU	21	NU	NU
RED	*	128			101			134			107							
YELLOW		129			102			135			108							
GREEN		130			103			136			109							
RED ARROW					116						122			A121				A114
YELLOW ARROW	126				117						123			A122				A115
FLASHING YELLOW ARROW														A123				A116
GREEN ARROW	127	127			118						124							

NU = Not Used
* Install load resistor. See load resistor installation detail this sheet.
★ See pictorial of head wiring in detail below.

FYA SIGNAL WIRING DETAIL

(wire signal heads as shown)



NOTE
The sequence display for signal head 11 requires special logic programming. See sheet 2 for programming instructions.

INPUT FILE POSITION LAYOUT

(from view)

FILE "I"	1	2	3	4	5	6	7	8	9	10	11	12	13	14
U	∅ 1	∅ 1	∅ 2	S	∅ 3	NOT USED	∅ 4	S	S	S	S	S	S	FS
L	1A	1B	2A,2B	∅ 2	3A	4C	NOT USED	∅ 4	∅ 4	∅ 4	∅ 4	∅ 4	∅ 4	DC ISOLATOR
U	NOT USED	NOT USED	∅ 2	∅ 2	NOT USED	∅ 4	NOT USED	∅ 4	∅ 4	∅ 4	∅ 4	∅ 4	∅ 4	DC ISOLATOR
L	S	∅ 6	S	S	∅ 7	∅ 8	S	S	S	S	S	S	S	S
U	∅ 6	6A,6B	∅ 6	∅ 6	7A	8A	∅ 8	∅ 8	∅ 8	∅ 8	∅ 8	∅ 8	∅ 8	∅ 8
L	NOT USED	NOT USED	∅ 6	∅ 6	NOT USED	∅ 8	∅ 8	∅ 8	∅ 8	∅ 8	∅ 8	∅ 8	∅ 8	∅ 8

EX.: 1A, 2A, ETC. = LOOP NO.'S

FS = FLASH SENSE
ST = STOP TIME

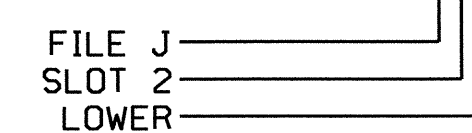
⊗ Wired Input - Do not populate slot with detector card

INPUT FILE CONNECTION & PROGRAMMING CHART

LOOP NO.	LOOP TERMINAL	INPUT FILE POS.	PIN NO.	INPUT ASSIGNMENT NO.	DETECTOR NO.	NEMA PHASE	CALL	EXTEND	FULL TIME DELAY	STRETCH TIME	DELAY TIME
1A ¹	TB2-1,2	I1U	56	18	1	1	Y	Y	Y		10
	-	J4U	48	10	26	6	Y	Y	Y		3
1B	TB2-5,6	I2U	39	1	2	1	Y	Y	Y		15
2A,2B	TB2-9,10	I3U	63	25	32	2	Y	Y	Y		
2C	TB2-11,12	I3L	76	38	42	2	Y	Y	Y		3
3A	TB4-5,6	I5U	58	20	3	3	Y	Y	Y		3
4B	TB4-11,12	I6L	45	7	14	4	Y	Y	Y		
4C	TB6-1,2	I7U	65	27	34	4	Y	Y	Y		
6A,6B	TB3-5,6	J2U	40	2	6	6	Y	Y	Y		
7A	TB5-5,6	J5U	57	19	7	7	Y	Y	Y		3
8A	TB5-9,10	J6U	42	4	8	8		Y	Y		3,1
8B	TB5-11,12	J6L	46	8	18	8	Y	Y	Y		

¹Add jumper from I1-W to J4-W. on rear of input file.

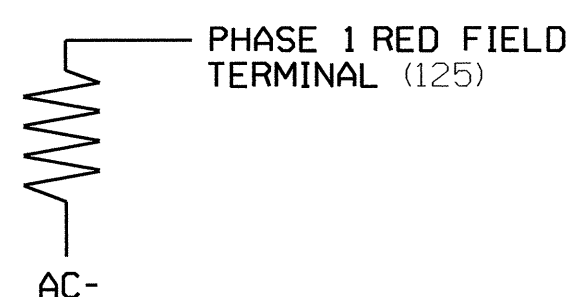
INPUT FILE POSITION LEGEND: J2L



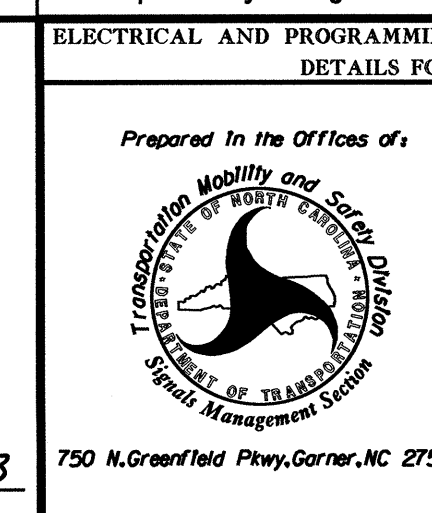
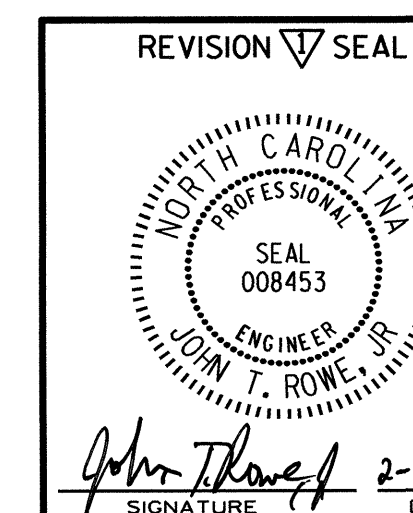
LOAD RESISTOR INSTALLATION DETAIL

(install resistor as shown below)

VALUE (ohms)	WATTAGE
1.5K - 1.9K	25W (min)
2.0K - 3.0K	10W (min)



Temporary Signal - Sheet 1 of 2



SR 1165 (Forest Hills Road)
at
SR 1163 (Downing Street)

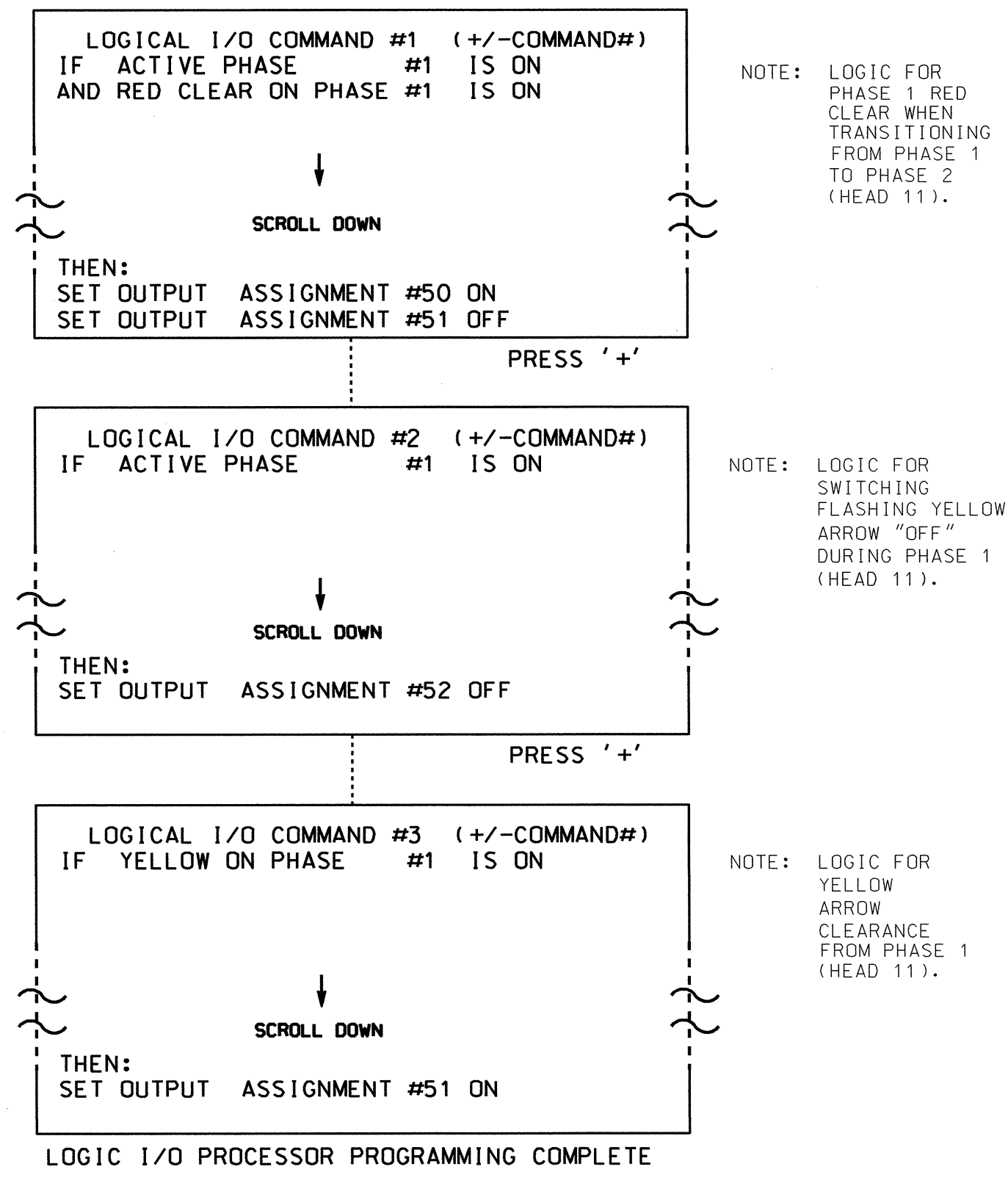
REVISIONS	INIT.	DATE
Removed Loop 4A. (WSA)	JTR	2-18-13

SEAL
This document originally issued and sealed by George C. Brown, #022013, on 09/30/10. This media shall not be considered a certified document.
SIGNATURE DATE
SIG. INVENTORY NO. 04-0515T

**LOGICAL I/O PROCESSOR PROGRAMMING DETAIL
TO PRODUCE SPECIAL FYA-PPLT SIGNAL SEQUENCE**

(program controller as shown below)

- FROM MAIN MENU PRESS '2' (PHASE CONTROL), THEN '1' (PHASE CONTROL FUNCTIONS). SCROLL TO THE BOTTOM OF THE MENU AND ENABLE ACT LOGIC COMMANDS 1, 2 and 3.
- FROM MAIN MENU PRESS '6' (OUTPUTS), THEN '3' (LOGICAL I/O PROCESSOR).



OUTPUT REFERENCE SCHEDULE	
OUTPUT 50 =	Overlap A Red
OUTPUT 51 =	Overlap A Yellow
OUTPUT 52 =	Overlap A Green

OVERLAP PROGRAMMING DETAIL

(program controller as shown below)

FROM MAIN MENU PRESS '8' (OVERLAPS), THEN '1' (VEHICLE OVERLAP SETTINGS).

PAGE 1: VEHICLE OVERLAP 'A' SETTINGS
 PHASE: 12345678910111213141516
 VEH OVL PARENTS: XX
 VEH OVL NOT VEH:
 VEH OVL NOT PED:
 VEH OVL GRN EXT:
 STARTUP COLOR: _ RED _ YELLOW _ GREEN
 FLASH COLORS: _ RED _ YELLOW X GREEN
 SELECT VEHICLE OVERLAP OPTIONS: (Y/N)
 FLASH YELLOW IN CONTROLLER FLASH?...Y
 GREEN EXTENSION (0-255 SEC)...0
 YELLOW CLEAR (0=PARENT,3-25.5 SEC)...0.0
 RED CLEAR (0=PARENT,0.1-25.5 SEC)...0.0
 OUTPUT AS PHASE # (0=NONE, 1-16)...0

← NOTICE GREEN FLASH

PRESS '+' TWICE

PAGE 1: VEHICLE OVERLAP 'C' SETTINGS
 PHASE: 12345678910111213141516
 VEH OVL PARENTS: X
 VEH OVL NOT VEH:
 VEH OVL NOT PED:
 VEH OVL GRN EXT:
 STARTUP COLOR: _ RED _ YELLOW _ GREEN
 FLASH COLORS: _ RED _ YELLOW X GREEN
 SELECT VEHICLE OVERLAP OPTIONS: (Y/N)
 FLASH YELLOW IN CONTROLLER FLASH?...Y
 GREEN EXTENSION (0-255 SEC)...0
 YELLOW CLEAR (0=PARENT,3-25.5 SEC)...0.0
 RED CLEAR (0=PARENT,0.1-25.5 SEC)...0.0
 OUTPUT AS PHASE # (0=NONE, 1-16)...0

← NOTICE GREEN FLASH

OVERLAP PROGRAMMING COMPLETE

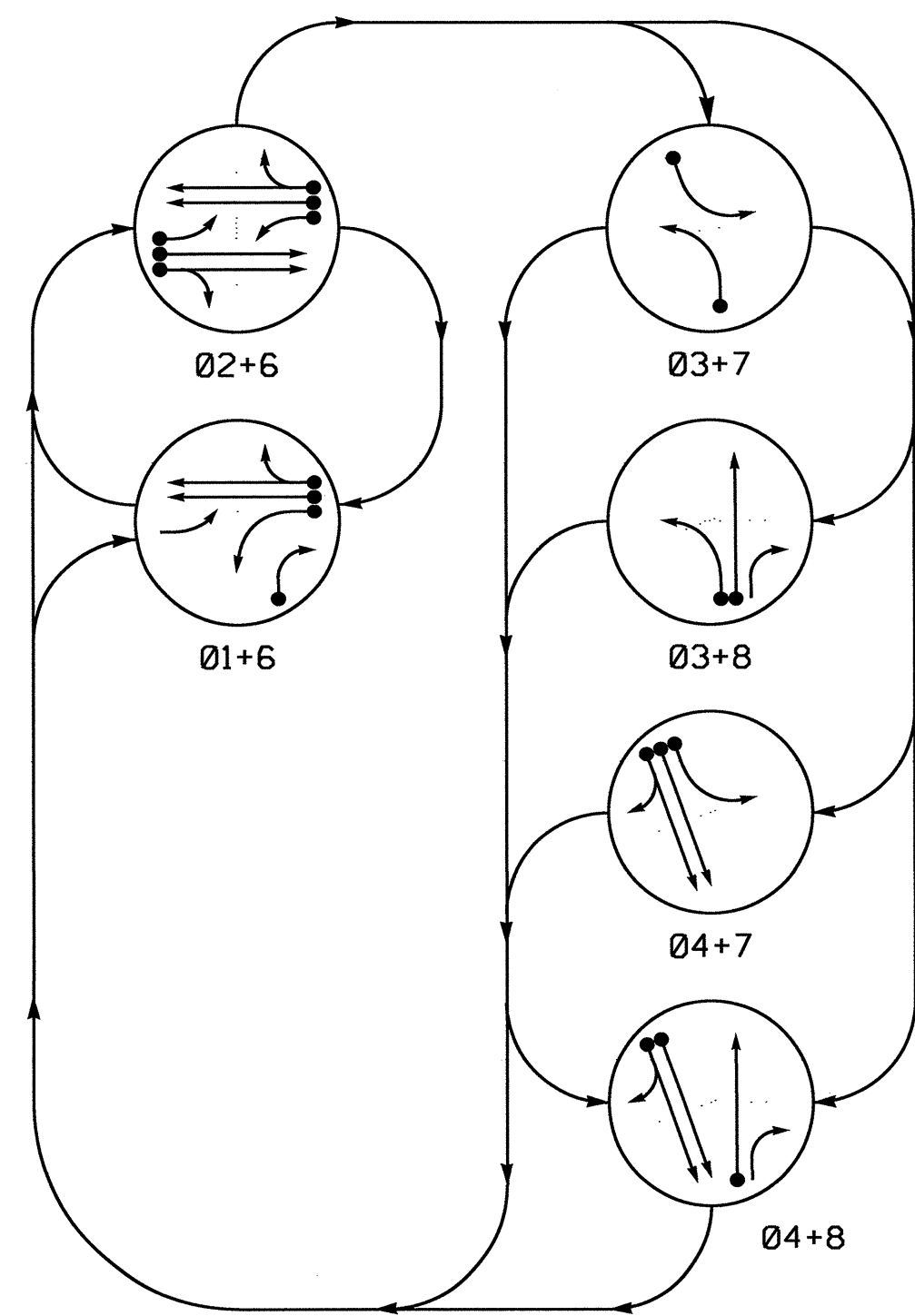
▽ THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 04-0515T
 DESIGNED: January 2013
 SEALED: 2/14/13
 REVISED: N/A

Temporary Signal - Sheet 2 of 2

REVISION SEAL 	ELECTRICAL AND PROGRAMMING DETAILS FOR: 	SR 1165 (Forest Hills Road) at SR 1163 (Downing Street)	SEAL This document originally issued and sealed by George C. Brown, #022013, on 09/30/10. This media shall not be considered a certified document.
		Division 04 Wilson County Wilson PLAN DATE: 9-23-10 REVIEWED BY: D.T. Joyce PREPARED BY: D.H. Spaulding REVIEWED BY: REVISIONS INIT. DATE 2-18-13	SIGNATURE DATE SIG. INVENTORY NO. 04-0515T

15-FEB-2013 08:07
 C:\Users\jrowe\Documents\Signal\Work\040515_Sig_04_XXX.dgn
 20130215

PHASING DIAGRAM



PHASING DIAGRAM DETECTION LEGEND

- ←●→ DETECTED MOVEMENT
- ←○→ UNDETECTED MOVEMENT (OVERLAP)
- UNSIGNALIZED MOVEMENT
- PEDESTRIAN MOVEMENT

SIGNAL FACE	PHASE					
	01+6	02+6	03+7	03+8	04+7	04+8
11	Y	Y	Y	Y	Y	Y
21	Y	Y	Y	Y	Y	Y
22,23	R	G	R	R	R	Y
31	R	R	R	R	R	R
41,42	R	R	R	R	G	R
61,62	G	G	R	R	R	Y
71	R	R	Y	R	R	R
81	R	R	R	G	R	R
82	Y	R	R	G	R	R

OASIS 2070L LOOP & DETECTOR INSTALLATION CHART

LOOP	SIZE (FT)	DISTANCE FROM STOPBAR (FT)	TURNS	NEW LOOP	DETECTOR PROGRAMMING							
					PHASE	CALLING	EXTENSION	FULL TIME DELAY	STRETCH TIME	DELAY TIME	SYSTEM LOOP	NEW CARD
1A	6X60	0	2-4-2	-	1	Y	Y	-	-	10	-	-
1B	6X60	0	2-4-2	-	6	Y	Y	Y	-	3	-	-
2A,2B	6X6	300	5	-	2	Y	Y	-	-	-	-	-
2C	6X60	+5	2-4-2	-	2	Y	Y	Y	-	3	-	-
3A	6X60	0	2-4-2	-	3	Y	Y	-	-	3	-	-
4A	6X6	300	5	Y	4	-	Y	-	2.4	-	-	-
4B	6X40	0	2-4-2	Y	4	Y	Y	-	-	-	-	-
4C	6X40	0	2-4-2	Y	4	Y	Y	-	-	5	-	-
4D	6X15	+10	3	Y	4	Y	Y	-	-	15	-	-
6A,6B	6X6	300	5	-	6	Y	Y	-	-	-	-	-
7A	6X40	0	2-4-2	Y	7	Y	Y	-	-	3	-	-
8A	6X6	300	5	-	8	-	Y	-	3:1	-	-	-
8B	6X60	0	2-4-2	-	8	Y	Y	-	-	-	-	-

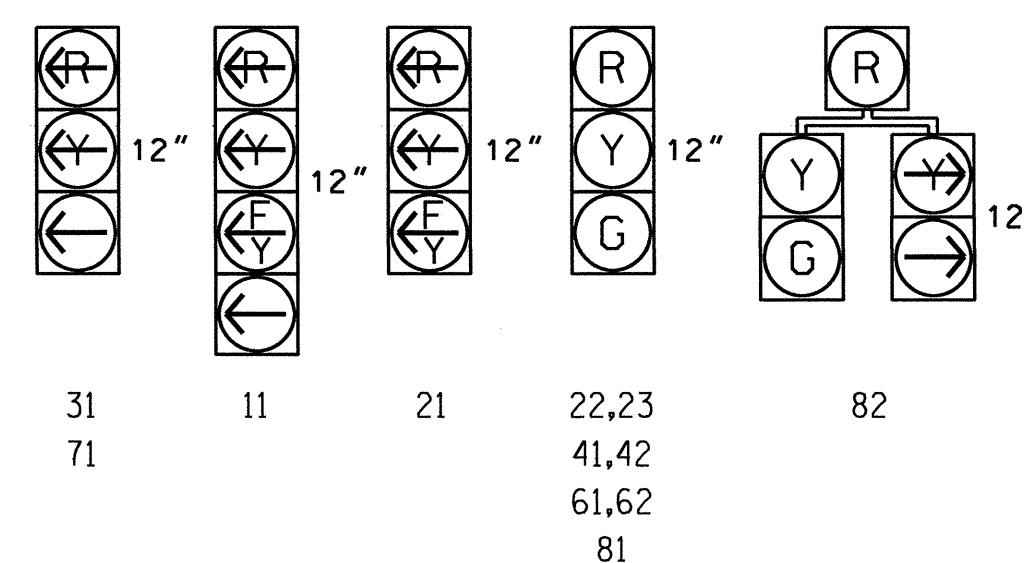
6 Phase Fully Actuated Isolated

NOTES

- Refer to "Roadway Standard Drawings NCDOT" dated January 2012 and "Standard Specifications for Roads and Structures" dated January 2012.
- Do not program signal for late night flashing operation unless otherwise directed by the Engineer.
- Phase 1 may be lagged.
- Phase 3 and/or phase 7 may be lagged.
- Set all detector units to presence mode.
- In the event of loop replacement, refer to the current ITS and Signals Design Manual and submit a Plan of Record to the Signal Design Section.

SIGNAL FACE I.D.

All Heads L.E.D.



FEATURE	OASIS 2070L TIMING CHART							
	PHASE							
	1	2	3	4	6	7	8	
Min Green 1*	7	12	7	7	12	7	7	
Extension 1*	1.0	6.0	1.0	2.0	6.0	1.0	1.0	
Max Green 1*	25	90	25	30	90	25	30	
Yellow Clearance	3.0	4.6	3.0	4.6	4.6	3.0	4.6	
Red Clearance	2.4	1.7	3.2	1.7	1.7	3.3	1.4	
Walk 1*	-	-	-	-	-	-	-	
Don't Walk 1	-	-	-	-	-	-	-	
Dynamic MaxMax 3*	45	-	45	-	-	-	-	
Dynamic Max Adjust*	10.0	-	10.0	-	-	-	-	
Seconds Per Actuation*	-	2.0	-	-	2.0	-	-	
Max Variable Initial*	-	34	-	-	34	-	-	
Time Before Reduction*	-	15	-	-	15	-	-	
Time To Reduce*	-	45	-	-	45	-	-	
Minimum Gap	-	3.0	-	-	3.0	-	-	
Recall Mode	-	MIN RECALL	-	-	MIN RECALL	-	-	
Vehicle Call Memory	-	YELLOW	-	-	YELLOW	-	-	
Dual Entry	-	-	-	-	-	-	-	
Simultaneous Gap	ON	ON	ON	ON	ON	ON	ON	

* These values may be field adjusted. Do not adjust Min Green and Extension times for phases 2 and 6 lower than what is shown. Min Green for all other phases should not be lower than 4 seconds.

LEGEND

- | PROPOSED | EXISTING |
|----------|----------|
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| | |
| | |

Final Design

750 N. Greenfield Pkwy, Garner, NC 27529

SR 1165 (Forest Hills Road) At SR 1163 (Downing Street)

Division 4 Wilson County Wilson

PLAN DATE: January 2013 REVIEWED BY: PLA

PREPARED BY: JPG REVIEWED BY:

SCALE 1"=40'

SEAL

SEAL 29904

ENGINEER

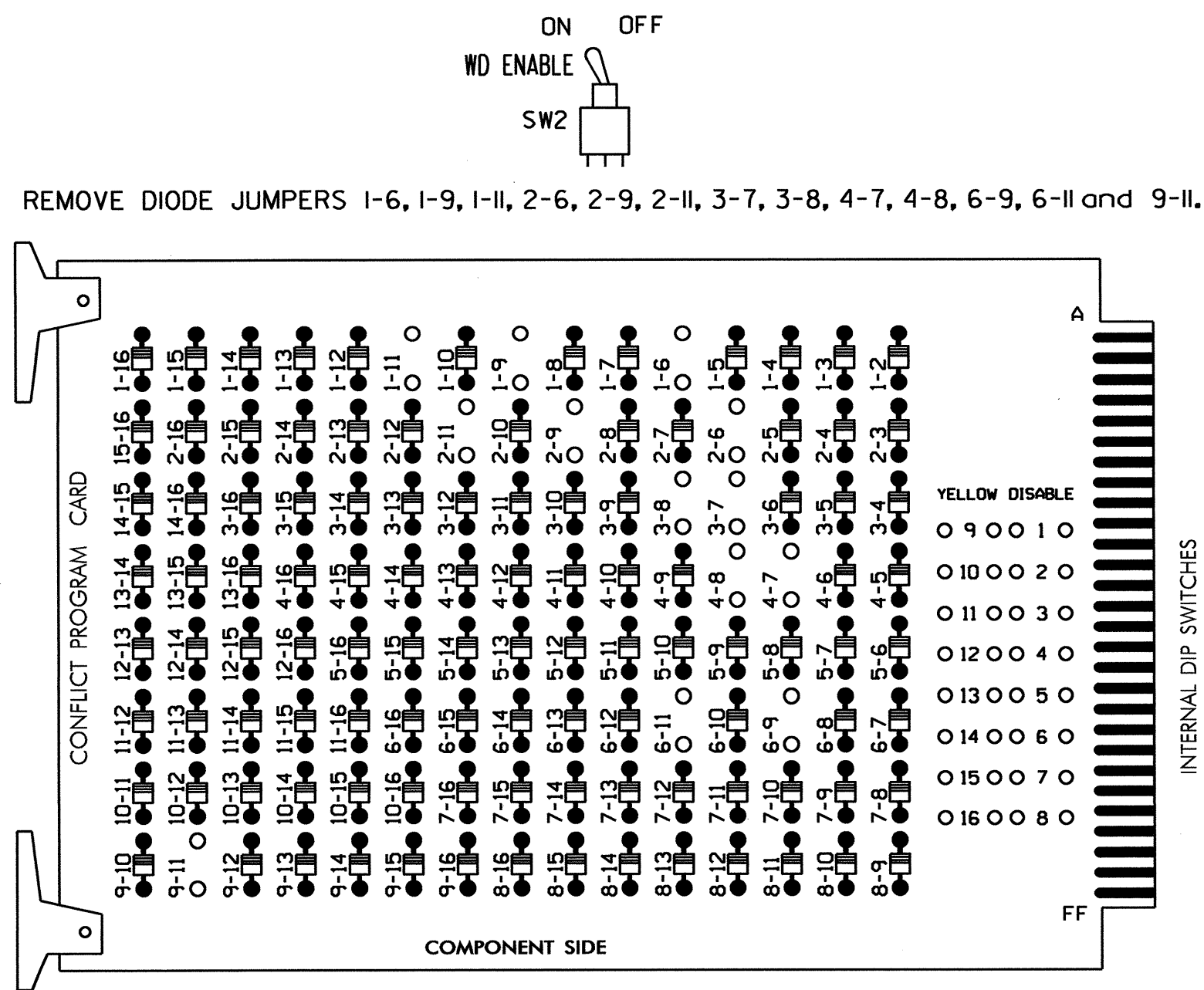
DATE 2/14/13

SIGNATURE

SIG. INVENTORY NO. 04-0515

EDI MODEL 2010ECL-NC CONFLICT MONITOR PROGRAMMING DETAIL

(remove jumpers and set switches as shown)



NOTES:

- Card is provided with all diode jumpers in place. Removal of any jumper allows its channels to run concurrently.
- Make sure jumpers SEL2-SEL5 are present on the monitor board.

NOTES

- To prevent "flash-conflict" problems, insert red flash program blocks for all unused vehicle load switches in the output file. The installer shall verify that signal heads flash in accordance with the Signal Plans.
- Ensure that Red Enable is active at all times during normal operation. To prevent Red Failures on unused monitor channels, tie unused red monitor inputs 5,10,12,13,14,15 & 16 to load switch AC+ per the cabinet manufacturer's instructions.
- Enable Simultaneous Gap-Out for all phases.
- Program phases 2 and 6 for Variable Initial and Gap Reduction.
- Program phases 2 and 6 for Start Up In Green.
- Program phases 2 and 6 for Yellow Flash, and overlap 1 as Wag Overlaps.

EQUIPMENT INFORMATION

CONTROLLER.....2070L
 CABINET.....332 /W/ AUX
 SOFTWARE.....ECONOLITE OASIS
 CABINET MOUNT.....BASE
 OUTPUT FILE POSITIONS...18 WITH AUX. OUTPUT FILE
 LOAD SWITCHES USED.....S1,S2,S3,S4,S6,S7,S8,S9,S12.
 PHASES USED.....1,2,3,4,6,7,8.
 OVERLAP "A".....1+2
 OVERLAP "B".....NOT USED
 OVERLAP "C".....6
 OVERLAP "D".....NOT USED

SIGNAL HEAD HOOK-UP CHART

LOAD SWITCH NO.	S1	S2	S2P	S3	S4	S4P	S5	S6	S6P	S7	S8	S8P	S9	S10	S11	S12	S13	S14
PHASE	1	2	2 PED	3	4	4 PED	5	6	6 PED	7	8	8 PED	OLA	OLB	SPARE	OLC	OLD	SPARE
SIGNAL HEAD NO.	11	82	22,23	31	41,42	NU	NU	61,62	NU	71	81,82	NU	11	NU	NU	21	NU	NU
RED	*	128			101			134			107							
YELLOW		129			102			135			108							
GREEN		130			103			136			109							
RED ARROW					116						122			A121			A114	
YELLOW ARROW	126				117						123			A122			A115	
FLASHING YELLOW ARROW														A123			A116	
GREEN ARROW	127	127			118						124							

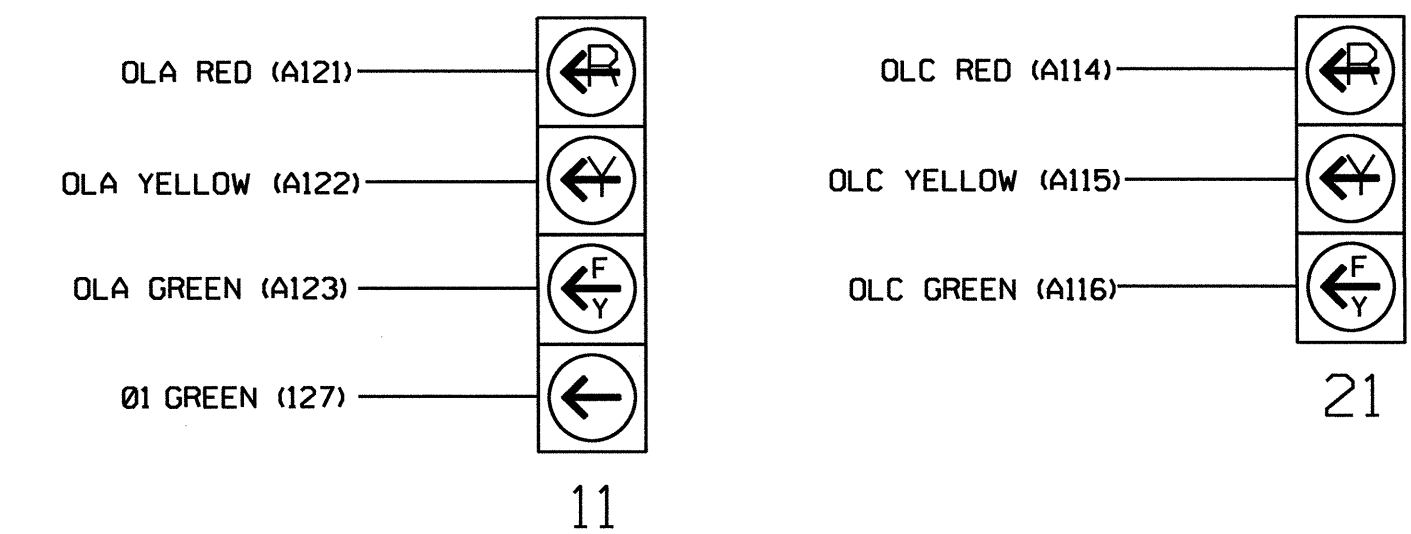
NU = Not Used

* Denotes install load resistor. See load resistor installation detail this sheet.

★ See pictorial of head wiring in detail below.

FYA SIGNAL WIRING DETAIL

(wire signal heads as shown)



NOTE

The sequence display for signal head 11 requires special logic programming. See sheet 2 for programming instructions.

INPUT FILE POSITION LAYOUT

(front view)

FILE "I"	1	2	3	4	5	6	7	8	9	10	11	12	13	14
U	∅ 1	∅ 1	∅ 2	∅ 3	∅ 4	∅ 4	∅ 4	S	S	S	S	S	S	FS
L	1A	1B	2A,2B	3A	4A	4C	4B	4D	S	S	S	S	S	DC ISOLATOR
	NOT USED	NOT USED	∅ 2	NOT USED	∅ 4	∅ 4								ST
			2C		4B	4D								DC ISOLATOR
U	S	∅ 6	S	∅ 7	∅ 8	S	S	S	S	S	S	S	S	S
L	∅ 6	6A,6B	∅ 7	7A	8A	∅ 8	∅ 8	8B						
	NOT USED	NOT USED	NOT USED	NOT USED	∅ 8	8B								

EX. : 1A, 2A, ETC. = LOOP NO.'S

FS = FLASH SENSE
 ST = STOP TIME

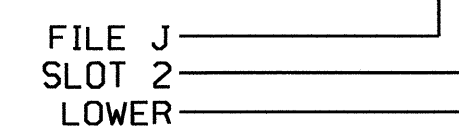
⊗ Wired Input - Do not populate slot with detector card

INPUT FILE CONNECTION & PROGRAMMING CHART

LOOP NO.	LOOP TERMINAL	INPUT FILE POS.	PIN NO.	INPUT ASSIGNMENT NO.	DETECTOR NO.	NEMA PHASE	CALL	EXTEND	FULL TIME DELAY	STRETCH TIME	DELAY TIME
1A ¹	TB2-1,2	I1U	56	18	1	1	Y	Y			10
	-	J4U	48	10	26	6	Y	Y	Y		3
1B	TB2-5,6	I2U	39	1	2	1	Y	Y			15
2A,2B	TB2-9,10	I3U	63	25	32	2	Y	Y			
2C	TB2-11,12	I3L	76	38	42	2	Y	Y	Y		3
3A	TB4-5,6	I5U	58	20	3	3	Y	Y			3
4A	TB4-9,10	I6U	41	3	4	4	Y	Y		2.4	
4B	TB4-11,12	I6L	45	7	14	4	Y	Y			
4C	TB6-1,2	I7U	65	27	34	4	Y	Y			5
4D	TB6-3,4	I7L	78	40	44	4	Y	Y			15
6A,6B	TB3-5,6	J2U	40	2	6	6	Y	Y			
7A	TB5-5,6	J5U	57	19	7	7	Y	Y			3
8A	TB5-9,10	J6U	42	4	8	8	Y	Y			3.1
8B	TB5-11,12	J6L	46	8	18	8	Y	Y			

¹Add jumper from I1-W to J4-W, on rear of input file.

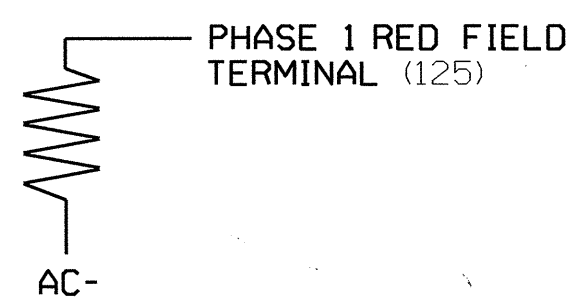
INPUT FILE POSITION LEGEND: J2L



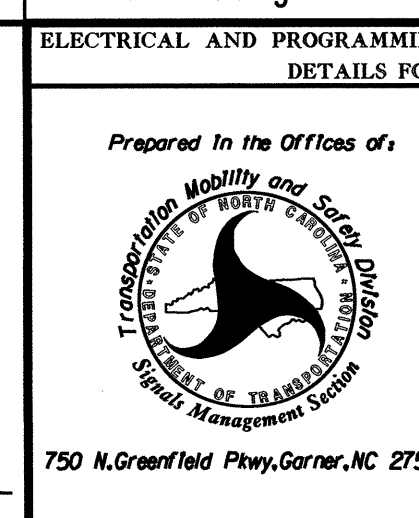
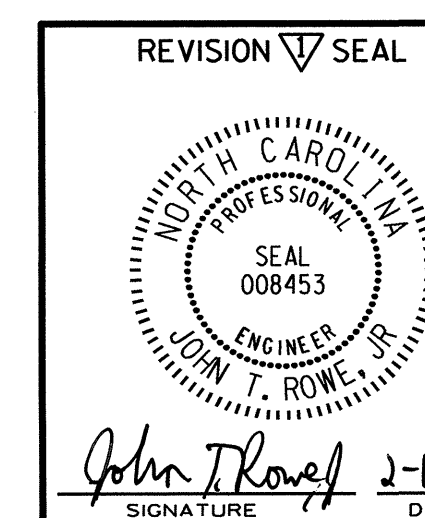
LOAD RESISTOR INSTALLATION DETAIL

(install resistor as shown below)

VALUE (ohms)	WATTAGE
1.5K - 1.9K	25W (min)
2.0K - 3.0K	10W (min)



Final Design - Sheet 1 of 2



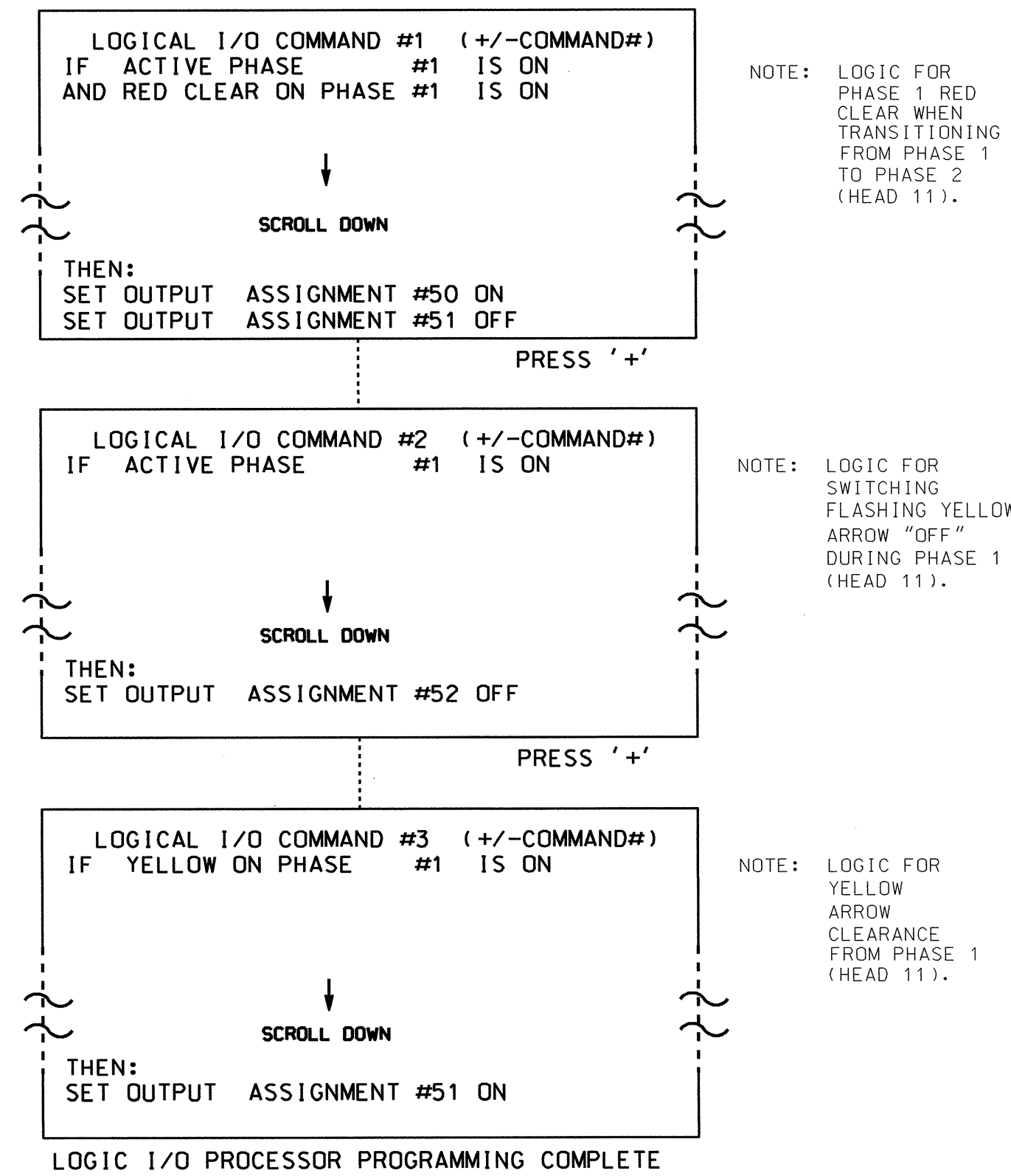
SR 1165 (Forest Hills Road)		at		SR 1163 (Downing Street)	
Division 04	Wilson County	Wilson			
PLAN DATE: 9-23-10	REVIEWED BY: D.T. Joyce				
PREPARED BY: D.H. Spaulding	REVIEWED BY:				
REVISIONS		INIT.	DATE		
Add Loop 4A and 4D. (MSA)		JTK	2-19-13		
SIGNATURE		DATE			

SEAL
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 SIG. INVENTORY NO. 04-0515

**LOGICAL I/O PROCESSOR PROGRAMMING DETAIL
TO PRODUCE SPECIAL FYA-PPLT SIGNAL SEQUENCE**

(program controller as shown below)

1. FROM MAIN MENU PRESS '2' (PHASE CONTROL), THEN '1' (PHASE CONTROL FUNCTIONS). SCROLL TO THE BOTTOM OF THE MENU AND ENABLE ACT LOGIC COMMANDS 1, 2 and 3.
2. FROM MAIN MENU PRESS '6' (OUTPUTS), THEN '3' (LOGICAL I/O PROCESSOR).



OUTPUT REFERENCE SCHEDULE	
OUTPUT 50	= Overlap A Red
OUTPUT 51	= Overlap A Yellow
OUTPUT 52	= Overlap A Green

OVERLAP PROGRAMMING DETAIL

(program controller as shown below)

FROM MAIN MENU PRESS '8' (OVERLAPS), THEN '1' (VEHICLE OVERLAP SETTINGS).

PAGE 1: VEHICLE OVERLAP 'A' SETTINGS
 PHASE: 12345678910111213141516
 VEH OVL PARENTS: XX
 VEH OVL NOT VEH:
 VEH OVL NOT PED:
 VEH OVL GRN EXT:
 STARTUP COLOR: - RED - YELLOW - GREEN
 FLASH COLORS: - RED - YELLOW X GREEN
 SELECT VEHICLE OVERLAP OPTIONS: (Y/N)
 FLASH YELLOW IN CONTROLLER FLASH?...Y
 GREEN EXTENSION (0-255 SEC)...0
 YELLOW CLEAR (0=PARENT,3-25.5 SEC)...0.0
 RED CLEAR (0=PARENT,0.1-25.5 SEC)...0.0
 OUTPUT AS PHASE # (0=NONE, 1-16)...0

← NOTICE GREEN FLASH

PRESS '+' TWICE

PAGE 1: VEHICLE OVERLAP 'C' SETTINGS
 PHASE: 12345678910111213141516
 VEH OVL PARENTS: X
 VEH OVL NOT VEH:
 VEH OVL NOT PED:
 VEH OVL GRN EXT:
 STARTUP COLOR: - RED - YELLOW - GREEN
 FLASH COLORS: - RED - YELLOW X GREEN
 SELECT VEHICLE OVERLAP OPTIONS: (Y/N)
 FLASH YELLOW IN CONTROLLER FLASH?...Y
 GREEN EXTENSION (0-255 SEC)...0
 YELLOW CLEAR (0=PARENT,3-25.5 SEC)...0.0
 RED CLEAR (0=PARENT,0.1-25.5 SEC)...0.0
 OUTPUT AS PHASE # (0=NONE, 1-16)...0

← NOTICE GREEN FLASH

OVERLAP PROGRAMMING COMPLETE

THIS ELECTRICAL DETAIL IS FOR
 THE SIGNAL DESIGN: 04-0515
 DESIGNED: January 2013
 SEALED: 2/14/13
 REVISED: N/A

Final Design - Sheet 2 of 2

REVISION SEAL 	ELECTRICAL AND PROGRAMMING DETAILS FOR: Prepared In the Offices of: 750 N. Greenfield Pkwy, Garner, NC 27529	SR 1165 (Forest Hills Road) at SR 1163 (Downing Street)	SEAL This document originally issued and sealed by George C. Brown, #022013, on 09/30/10. This media shall not be considered a certified document.
		Division 04 Wilson County Wilson PLAN DATE: 9-23-10 REVIEWED BY: D.T. Joyce PREPARED BY: D.H. Spaulding REVIEWED BY: REVISIONS: INIT. DATE Add loop 4A and 4D. (W&A) JTR 2-14-13	SIGNATURE DATE SIG. INVENTORY NO. 04-0515