PROJECT REFERENCE NO.	SHEET NO.
N/A	Sig. 1

8 Phase
Fully Actuated
Isolated

43158.3.1

PHASING DIAGRAM

PHASING DIAGRAM DETECTION LEGEND

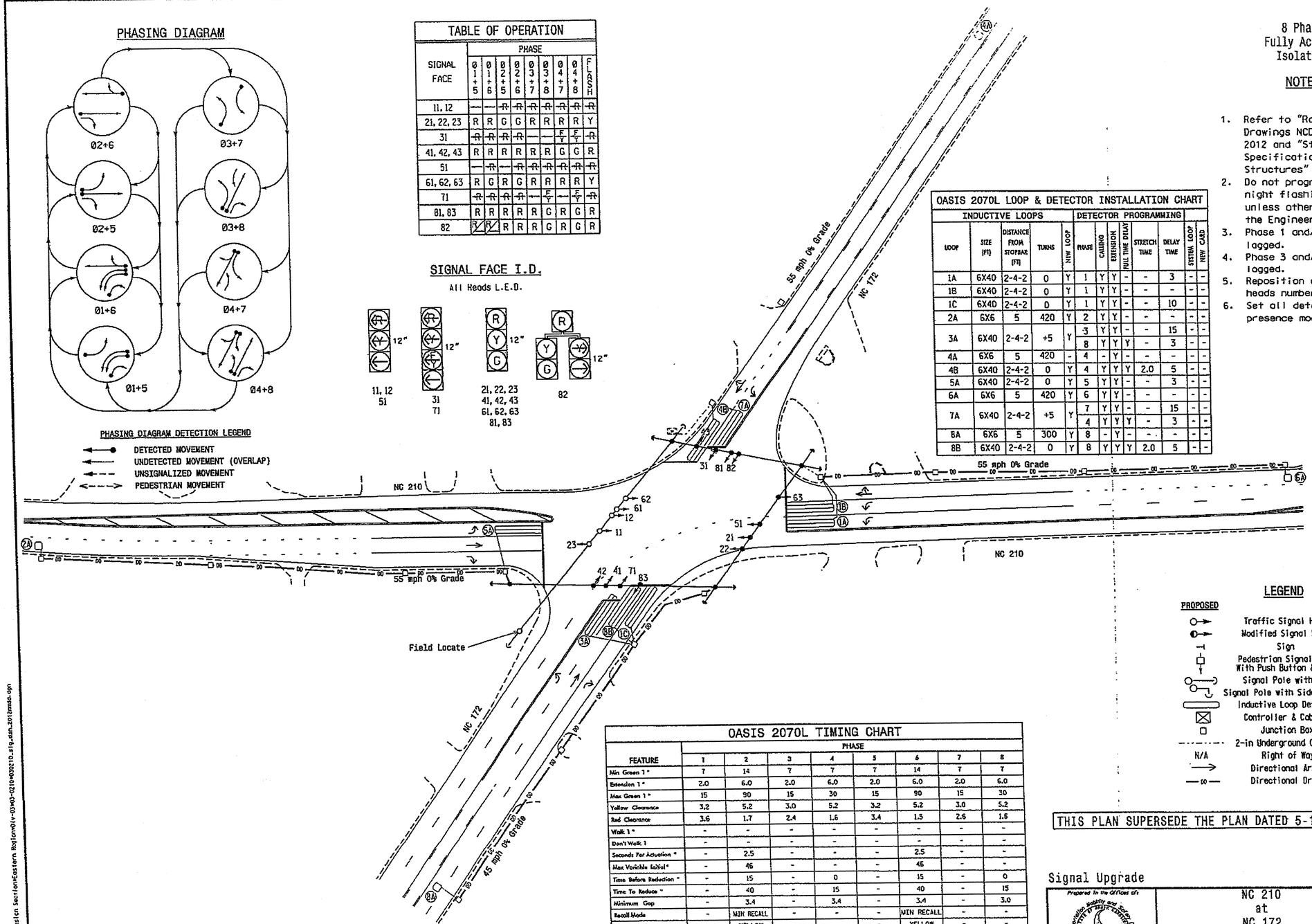
- DETECTED MOVEMENT
- UNDETECTED MOVEMENT (OVERLAP)
- UNSIGNALIZED MOVEMENT
- PEDESTRIAN MOVEMENT

TABLE OF OPERATION

SIGNAL FACE	PHASE 01+5	01+6	02+5	02+6	03+7	03+8	04+7	04+8	FLASH
11, 12	—	—	~~R~~	~~R~~	~~R~~	~~R~~	~~R~~	~~R~~	~~R~~
21, 22, 23	R	R	G	G	R	R	R	R	Y
31	~~R~~	~~R~~	~~R~~	~~R~~	—	—	FY	FY	~~R~~
41, 42, 43	R	R	R	R	R	R	G	G	R
51	—	~~R~~	—	~~R~~	~~R~~	~~R~~	~~R~~	~~R~~	~~R~~
61, 62, 63	R	G	R	G	R	R	R	R	Y
71	~~R~~	~~R~~	~~R~~	~~R~~	—	FY	—	FY	~~R~~
81, 83	R	R	R	R	R	G	R	G	R
82	R/~~R~~	R/~~R~~	R	R	R	G	R	G	R

SIGNAL FACE I.D.

All Heads L.E.D.



OASIS 2070L LOOP & DETECTOR INSTALLATION CHART

INDUCTIVE LOOPS					DETECTOR PROGRAMMING						
LOOP	SIZE (FT)	DISTANCE FROM STOPBAR (FT)	TURNS	NEW LOOP	PHASE	CALLING	EXTENSION	FULL TIME DELAY	STRETCH TIME	DELAY TIME	SYSTEM LOOP	NEW CARD
1A	6X40	2-4-2	0	Y	1	Y	Y	-	-	3	-	-
1B	6X40	2-4-2	0	Y	1	Y	Y	-	-	-	-	-
1C	6X40	2-4-2	0	Y	1	Y	Y	-	-	10	-	-
2A	6X6	5	420	Y	2	Y	Y	-	-	-	-	-
3A	6X40	2-4-2	+5	Y	3	Y	Y	-	-	15	-	-
					8	Y	Y	Y	-	3	-	-
4A	6X6	5	420	-	4	-	Y	-	-	-	-	-
4B	6X40	2-4-2	0	Y	4	Y	Y	Y	2.0	5	-	-
5A	6X40	2-4-2	0	Y	5	Y	Y	-	-	3	-	-
6A	6X6	5	420	Y	6	Y	Y	-	-	-	-	-
7A	6X40	2-4-2	+5	Y	7	Y	Y	-	-	15	-	-
					4	Y	Y	Y	-	3	-	-
8A	6X6	5	300	Y	8	-	Y	-	-	-	-	-
8B	6X40	2-4-2	0	Y	8	Y	Y	Y	2.0	5	-	-

NOTES

1. Refer to "Roadway Standard Drawings NCDOT" dated January 2012 and "Standard Specifications for Roads and Structures" dated January 2012.
2. Do not program signal for late night flashing operation unless otherwise directed by the Engineer.
3. Phase 1 and/or phase 5 may be lagged.
4. Phase 3 and/or phase 7 may be lagged.
5. Reposition existing signal heads numbered 21 & 22.
6. Set all detector units to presence mode.

LEGEND

PROPOSED		EXISTING
	Traffic Signal Head	
	Modified Signal Head	N/A
	Sign	
	Pedestrian Signal Head With Push Button & Sign	
	Signal Pole with Guy	
	Signal Pole with Sidewalk Guy	
	Inductive Loop Detector	
	Controller & Cabinet	
	Junction Box	
	2-in Underground Conduit	
N/A	Right of Way	
	Directional Arrow	
	Directional Drill	N/A

THIS PLAN SUPERSEDE THE PLAN DATED 5-17-2012

OASIS 2070L TIMING CHART

FEATURE	PHASE 1	2	3	4	5	6	7	8
Min Green 1 *	7	14	7	7	7	14	7	7
Extension 1 *	2.0	6.0	2.0	6.0	2.0	6.0	2.0	6.0
Max Green 1 *	15	90	15	30	15	90	15	30
Yellow Clearance	3.2	5.2	3.0	5.2	3.2	5.2	3.0	5.2
Red Clearance	3.6	1.7	2.4	1.6	3.4	1.5	2.6	1.6
Walk 1 *	-	-	-	-	-	-	-	-
Don't Walk 1	-	-	-	-	-	-	-	-
Seconds Per Actuation *	-	2.5	-	-	-	2.5	-	-
Max Variable Initial *	-	46	-	-	-	46	-	-
Time Before Reduction *	-	15	-	0	-	15	-	0
Time To Reduce *	-	40	-	15	-	40	-	15
Minimum Gap	-	3.4	-	3.4	-	3.4	-	3.0
Recall Mode	-	MIN RECALL	-	-	-	MIN RECALL	-	-
Vehicle Call Memory	-	YELLOW	-	-	-	YELLOW	-	-
Dual Entry	-	-	-	ON	-	-	-	ON
Simultaneous Gap	ON	ON	ON	ON	ON	ON	ON	ON

* These values may be field adjusted. Do not adjust Min Green and Extension times for phases 2 and 6 lower than what is shown. Min Green for all other phases should not be lower than 4 seconds.

Signal Upgrade

Prepared in the Offices of:
Transportation Mobility and Safety Division
Traffic Engineering Branch
Signal Design Section
750 N.Greenfield Pkwy,Garner,NC 27529

NC 210
at
NC 172

Division 3 Onslow County N. of Sneads Ferry

PLAN DATE: April 2012	REVIEWED BY: J. P. Galloway
PREPARED BY: I. O. Umozurike	REVIEWED BY:

SCALE 0 40 1"=40'

REVISIONS	INIT.	DATE

SEAL
NORTH CAROLINA PROFESSIONAL SEAL 29904 ENGINEER JASON P. GALLOWAY

SIGNATURE 3/5/12 DATE

SIG. INVENTORY NO. 03-0210

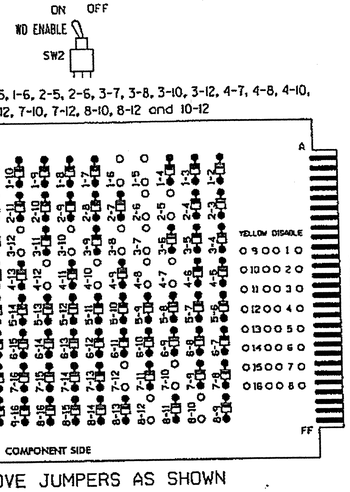
DGN:03-0210.dgn
DATE:3/5/2012 9:19:05
USER:umozurike
DESCRIPTION:S:\Signals\Signal Design Section\Eastern Region\div-03\03-0210\03-0210_sig_dsn_2012msda.dgn

PROJECT REFERENCE NO.	SHEET NO.
N/A	Sig. 2

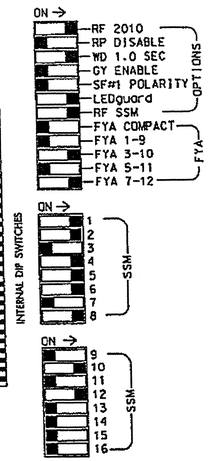
43518.3.1

EDI MODEL 2010ECL-NC CONFLICT MONITOR PROGRAMMING DETAIL

(remove jumpers and set switches as shown)



REMOVE JUMPERS AS SHOWN



■ = DENOTES POSITION OF SWITCH

NOTES:

1. Card is provided with all diode jumpers in place. Removal of any jumper allows its channels to run concurrently.
2. Make sure jumpers SEL2-SEL5 are present on the monitor board.

INPUT FILE POSITION LAYOUT

(front view)

		1	2	3	4	5	6	7	8	9	10	11	12	13	14
FILE "I"	U	SLOT EMPTY	Ø1 1A	Ø2 2A	SLOT EMPTY	Ø3 3A	Ø4 4A	SLOT EMPTY	WIRED INPUT ⊗	SLOT EMPTY	SLOT EMPTY	SLOT EMPTY	SLOT EMPTY	SLOT EMPTY	FS DC ISOLATOR
	L		Ø1 1B	Ø1 1C		NOT USED	Ø4 4B								ST DC ISOLATOR
FILE "J"	U	Ø5 5A	Ø6 6A	SLOT EMPTY	SLOT EMPTY	Ø7 7A	Ø8 8A	SLOT EMPTY	WIRED INPUT ⊗	SLOT EMPTY	SLOT EMPTY	SLOT EMPTY	SLOT EMPTY	SLOT EMPTY	SLOT EMPTY
	L	NOT USED	NOT USED			NOT USED	Ø8 8B								

EX.: 1A, 2A, ETC. = LOOP NO.'S

FS = FLASH SENSE
ST = STOP TIME

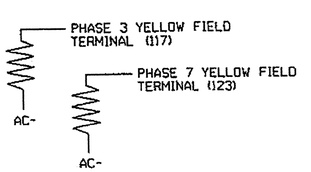
⊗ Wired Input - Do not populate slot with detector card

LOAD RESISTOR INSTALLATION DETAIL

(install resistors as shown below)

ACCEPTABLE VALUES

VALUE (ohms)	WATTAGE
1.5K - 1.9K	25W (min)
2.0K - 3.0K	10W (min)



NOTES

1. To prevent "flash-conflict" problems, insert red flash program blocks for all unused vehicle load switches in the output file. The installer shall verify that signal heads flash in accordance with the Signal Plans.
2. Ensure that Red Enable is active at all times during normal operation. To prevent Red Failures on unused monitor channels, tie unused red monitor inputs 3,7,9, 11,13,14,15 & 16 to load switch AC+ per the cabinet manufacturer's instructions.
3. Program phases 4 and 8 for Dual Entry.
4. Enable Simultaneous Gap-Out for all phases.
5. Program phases 2 and 6 for Variable Initial and phases 2,4,6 and 8 for Gap Reduction.
6. Program phases 2 and 6 for Start Up In Green.
7. Program phases 2 and 6 for Yellow Flash, and overlap 2 as WAG Overlaps.

EQUIPMENT INFORMATION

CONTROLLER.....2070L
CABINET.....332 /W/ AUX
SOFTWARE.....ECONOLITE OASIS
CABINET MOUNT.....BASE
OUTPUT FILE POSITIONS...18 WITH AUX. OUTPUT FILE
LOAD SWITCHES USED.....S1,S2,S3,S4,S5,S6,S7,S8,S10, S13.
PHASES USED.....1,2,3,4,5,6,7,8.
OVERLAP "A".....NOT USED
OVERLAP "B".....3+4
OVERLAP "C".....NOT USED
OVERLAP "D".....7+8

INPUT FILE CONNECTION & PROGRAMMING CHART

LOOP NO.	LOOP TERMINAL	INPUT FILE POS.	PIN NO.	INPUT ASSIGNMENT NO.	DETECTOR NO.	NEMA PHASE	CALL	EXTEND	FULL TIME DELAY	STRETCH TIME	DELAY TIME
1A	TB2-5,6	I2U	39	1	2	1	Y	Y			3
1B	TB2-7,8	I2L	43	5	12	1	Y	Y			
1C	TB2-11,12	I3L	76	38	42	1	Y	Y			10
2A	TB2-9,10	I3U	63	25	32	2	Y	Y			
3A[1]	TB4-5,6	I5U	58	20	3	3	Y	Y			15
	-	J8U	58	12	28	8	Y	Y	Y		3
4A	TB4-9,10	I6U	41	3	4	4		Y			
4B	TB4-11,12	I6L	45	7	14	4	Y	Y	Y	2.0	5
5A	TB3-1,2	J1U	55	17	5	5	Y	Y			3
6A	TB3-5,6	J2U	40	2	6	6	Y	Y			
7A[2]	TB5-5,6	J5U	57	19	7	7	Y	Y			15
	-	I8U	49	11	24	4	Y	Y	Y		3
8A	TB5-9,10	J6U	42	4	8	8		Y			
8B	TB5-11,12	J6L	46	8	18	8	Y	Y	Y	2.0	5

[1] Add jumper from I5-W to J8-W, on rear of input file.
[2] Add jumper from J5-W to I8-W, on rear of input file.

INPUT FILE POSITION LEGEND: J2L
FILE J
SLOT 2
LOWER

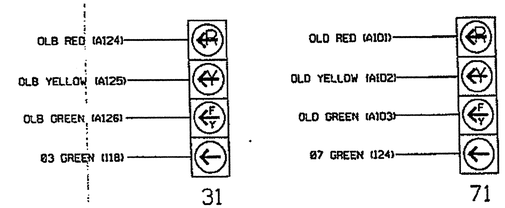
SIGNAL HEAD HOOK-UP CHART

LOAD SWITCH NO.	S1		S2	S2P	S3	S4	S4P	S5	S6	S6P	S7	S8	S8P	S9	S10	S11	S12	S13	S14
PHASE	1		2	2 PED	3	4	4 PED	5	6	6 PED	7	8	8 PED	OLA	OLB	SPARE	OLC	OLD	SPARE
SIGNAL HEAD NO.	11,12	62	21,22 23	NU	31★	41,42 43	NU	51	61,62 63	NU	71★	81,82 83	NU	NU	31★	NU	NU	71★	NU
RED			128			101			134			107							
YELLOW			129		*	102			135		*	108							
GREEN			130			103			136			109							
RED ARROW	125							131							A124			A101	
YELLOW ARROW	126	126						132							A125			A102	
FLASHING YELLOW ARROW															A126			A103	
GREEN ARROW	127	127			118			133			124								

NU = Not Used
* Denotes install load resistor. See load resistor installation detail this sheet.
★ See pictorial of head wiring in detail below.

4 SECTION FYA PPLT SIGNAL WIRING DETAIL

(wire signal heads as shown)



NOTE

1. The sequence display for these signals require special logic programming. See sheet 2 of 2 for programming instructions.

THIS ELECTRICAL DETAIL IS FOR
THE SIGNAL DESIGN: 03-0210
DESIGNED: April 2012
SEALED: 05/25/12
REVISED: N/A

THIS ELECTRICAL DETAIL SUPERSEDES THE DETAIL SEALED ON 05/18/12

ELECTRICAL DETAIL SHEET 1 OF 2

ELECTRICAL AND PROGRAMMING DETAILS FOR:

NC 210
at
NC 172

Prepared in the Offices of:
Transportation Mobility and Safety
Division of Highways
Traffic Engineering Branch
Signal Management Section
750 N.Greenfield Pkwy,Garner,NC 27529

Division 03 | Onslow County | N. of Sneads Ferry
PLAN DATE: May 2012 | REVIEWED BY: T. Jyn
PREPARED BY: C. Strickland | REVIEWED BY:

REVISIONS	INIT.	DATE

SEAL
NORTH CAROLINA PROFESSIONAL SEAL 022013 ENGINEER GEORGE C. BROWN
SIGNATURE | DATE 5/25/12
SIG. INVENTORY NO. 03-0210

29-MAY-2012 09:51
S:\TITSSGN\TS Signals\Workprogs\ssig Vers5\rlctrlcfg\03210_sm_01b.xxx.dgn
desmr(rlcfg)

PROJECT REFERENCE NO.	SHEET NO.
N/A	Sig. 3

43518.3.1

LOGICAL I/O PROCESSOR PROGRAMMING DETAIL
TO PRODUCE SPECIAL FYA-PPLT SIGNAL SEQUENCE

(program controller as shown below)

1. FROM MAIN MENU PRESS '2' (PHASE CONTROL), THEN '1' (PHASE CONTROL FUNCTIONS). SCROLL TO THE BOTTOM OF THE MENU AND ENABLE ACT LOGIC COMMANDS 1, 2, 3, 4, 5 AND 6.

2. FROM MAIN MENU PRESS '6' (OUTPUTS), THEN '3' (LOGICAL I/O PROCESSOR).

```
LOGICAL I/O COMMAND #1  (+/-COMMAND#)
IF  ACTIVE PHASE        #3  IS ON
AND RED CLEAR ON PHASE #3  IS ON

SCROLL DOWN

THEN:
SET OUTPUT  ASSIGNMENT #47 ON
SET OUTPUT  ASSIGNMENT #48 OFF
```

NOTE: LOGIC FOR PHASE 3 RED CLEAR WHEN TRANSITIONING FROM PHASE 3 TO PHASE 4 (HEAD 31).

PRESS '+'

```
LOGICAL I/O COMMAND #2  (+/-COMMAND#)
IF  ACTIVE PHASE        #3  IS ON

SCROLL DOWN

THEN:
SET OUTPUT  ASSIGNMENT #49 OFF
```

NOTE: LOGIC FOR SWITCHING FLASHING YELLOW ARROW "OFF" DURING PHASE 3 (HEAD 31).

PRESS '+'

```
LOGICAL I/O COMMAND #3  (+/-COMMAND#)
IF  YELLOW ON PHASE     #3  IS ON

SCROLL DOWN

THEN:
SET OUTPUT  ASSIGNMENT #48 ON
```

NOTE: LOGIC FOR YELLOW ARROW CLEARANCE FROM PHASE 3 (HEAD 31).

PRESS '+'

```
LOGICAL I/O COMMAND #4  (+/-COMMAND#)
IF  ACTIVE PHASE        #7  IS ON
AND RED CLEAR ON PHASE #7  IS ON

SCROLL DOWN

THEN:
SET OUTPUT  ASSIGNMENT #39 ON
SET OUTPUT  ASSIGNMENT #40 OFF
```

NOTE: LOGIC FOR PHASE 7 RED CLEAR WHEN TRANSITIONING FROM PHASE 7 TO PHASE 8 (HEAD 71).

PRESS '+'

```
LOGICAL I/O COMMAND #5  (+/-COMMAND#)
IF  ACTIVE PHASE        #7  IS ON

SCROLL DOWN

THEN:
SET OUTPUT  ASSIGNMENT #41 OFF
```

NOTE: LOGIC FOR SWITCHING FLASHING YELLOW ARROW "OFF" DURING PHASE 7 (HEAD 71).

PRESS '+'

```
LOGICAL I/O COMMAND #6  (+/-COMMAND#)
IF  YELLOW ON PHASE     #7  IS ON

SCROLL DOWN

THEN:
SET OUTPUT  ASSIGNMENT #40 ON
```

NOTE: LOGIC FOR YELLOW ARROW CLEARANCE FROM PHASE 7 (HEAD 71).

LOGIC I/O PROCESSOR PROGRAMMING COMPLETE

OUTPUT REFERENCE SCHEDULE

USE TO INTERPRET LOGIC PROCESSOR

OUTPUT 39 = Overlap D Red
OUTPUT 40 = Overlap D Yellow
OUTPUT 41 = Overlap D Green
OUTPUT 47 = Overlap B Red
OUTPUT 48 = Overlap B Yellow
OUTPUT 49 = Overlap B Green

OVERLAP PROGRAMMING DETAIL

(program controller as shown below)

FROM MAIN MENU PRESS '8' (OVERLAPS), THEN '1' (VEHICLE OVERLAP SETTINGS).

PRESS '+'

```
PAGE 1: VEHICLE OVERLAP 'B' SETTINGS
PHASE:          :123456789101112131415 16
VEH OVL PARENTS::  XX
VEH OVL NOT VEH::
VEH OVL NOT PED::
VEH OVL GRN EXT::
STARTUP COLOR: _ RED  _ YELLOW  _ GREEN
FLASH COLORS:  _ RED  _ YELLOW  X GREEN
SELECT VEHICLE OVERLAP OPTIONS:   (Y/N)
FLASH YELLOW IN CONTROLLER FLASH?...N
GREEN EXTENSION (0-255 SEC).........0
YELLOW CLEAR (0=PARENT,3-25.5 SEC)..0.0
RED CLEAR (0=PARENT,0.1-25.5 SEC)...0.0
OUTPUT AS PHASE # (0=NONE, 1-16)....0
```

← NOTICE GREEN FLASH

PRESS '+' TWICE

```
PAGE 1: VEHICLE OVERLAP 'D' SETTINGS
PHASE:          :123456789101112131415 16
VEH OVL PARENTS::     XX
VEH OVL NOT VEH::
VEH OVL NOT PED::
VEH OVL GRN EXT::
STARTUP COLOR: _ RED  _ YELLOW  _ GREEN
FLASH COLORS:  _ RED  _ YELLOW  X GREEN
SELECT VEHICLE OVERLAP OPTIONS:   (Y/N)
FLASH YELLOW IN CONTROLLER FLASH?...N
GREEN EXTENSION (0-255 SEC).........0
YELLOW CLEAR (0=PARENT,3-25.5 SEC)..0.0
RED CLEAR (0=PARENT,0.1-25.5 SEC)...0.0
OUTPUT AS PHASE # (0=NONE, 1-16)....0
```

← NOTICE GREEN FLASH

OVERLAP PROGRAMMING COMPLETE

FLASHER CIRCUIT MODIFICATION DETAIL

IN ORDER TO INSURE THAT SIGNALS FLASH CONCURRENTLY ON THE SAME APPROACH, MAKE THE FOLLOWING FLASHER CIRCUIT CHANGES:

1. ON REAR OF PDA - REMOVE WIRE FROM TERM. T2-4 AND TERMINATE ON T2-2.
2. ON REAR OF PDA - REMOVE WIRE FROM TERM. T2-5 AND TERMINATE ON T2-3.
3. REMOVE FLASHER UNIT 2.

THE CHANGES LISTED ABOVE TIES ALL PHASES AND OVERLAPS TO FLASHER UNIT 1.

THIS ELECTRICAL DETAIL IS FOR
THE SIGNAL DESIGN: 03-0210
DESIGNED: April 2012
SEALED: 05/25/12
REVISED: N/A

THIS ELECTRICAL DETAIL SUPERSEDES THE DETAIL SEALED ON 05/18/12

ELECTRICAL DETAIL SHEET 2 OF 2

ELECTRICAL AND PROGRAMMING DETAILS FOR:

NC 210
at
NC 172

Prepared in the Offices of:
Transportation Mobility and Safety Division
Traffic Engineering Branch
Signals Management Section
750 N.Greenfield Pkwy,Garner,NC 27529

Division 03 | Onslow County | W. of Sneads Ferry

PLAN DATE: May 2012 | REVIEWED BY: T.L.M.
PREPARED BY: C. Strickland | REVIEWED BY:

REVISIONS	INIT.	DATE

SEAL
NORTH CAROLINA PROFESSIONAL SEAL 022013 ENGINEER GEORGE C. BROWN

SIGNATURE | DATE: 5/21/12

SIG. INVENTORY NO. 03-0210

25-MAY-2012 08:41
S:\TTSSIG\175 Signals\Work\groups\Sig Mon\Strickl\ordr\03210.sm.elt.xxx.dgn
$$$$$$$$$$$$