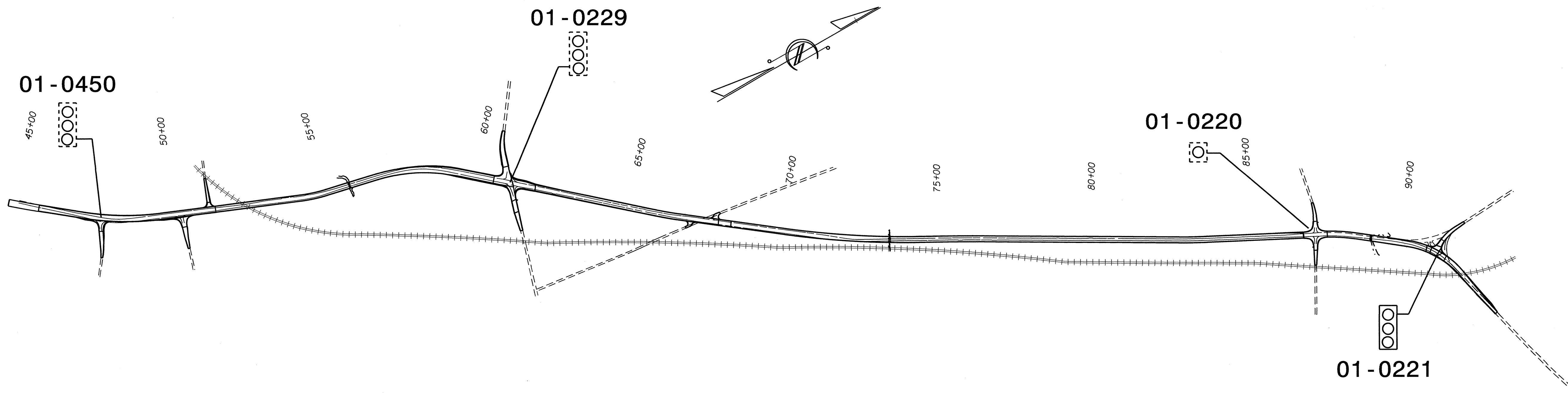


**VICINITY MAP**

**CAMDEN COUNTY**

**LOCATION: US 158 - FROM NORTH OF SR 1257 (HAVENWOOD DRIVE) TO EAST OF NC 34 IN BELCROSS**

**TYPE OF WORK: SIGNALS**



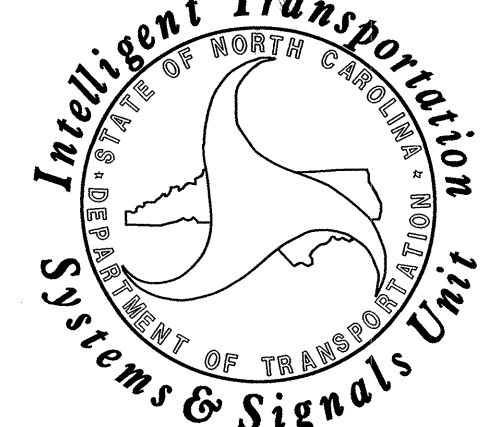
Sheet #	Reference #	Index of Plans Location/Description
Sig. 1		Title Sheet
Sig. 2-12	01-0450	US 158 at SR 1139 (Country Club Road)
Sig. 13-32	01-0229	US 158 at NC 343
Sig. 33-35	01-0220	US 158 at SR SR 1145 (Belcross Road/Lambs Road)
Sig. 36-43	01-0221	US 158 at NC 34
Sig. 44-48	NA	Standard Drawings for Metal Poles
Sig. 49-51	NA	Inductive Detection Loop Plate Drawings

**INTELLIGENT TRANSPORTATION AND SIGNALS UNIT**

*Contacts:*

**Jason Galloway, PE - East Region Signal Project Engineer**  
**George Brown, PE - Signal Equipment Design Engineer**

Prepared in the Offices of:  
**TRANSPORTATION MOBILITY AND SAFETY DIVISION**



03-SEP-2000 15:37 S:\TTS&S\UNIT5\Workgroups\TIP Projects\R-2414B\Signals\Design\Titlesheet\R2414B\_sig\_tsh\_20100907.dgn



















**EMERGENCY VEHICLE PREEMPTION PROGRAMMING DETAIL**

*(program controller as shown below)*

From Main Menu press 'A' (Preemption), then '1' (Standard Preemptions). Press 'NEXT' as needed to advance to Preempts 3 and 5.

Table with 2 columns: PREEMPTION #3 SETTINGS and CLEAR/DWELL PHASES. Rows include interval/timing and exit calls.

Table with 2 columns: OPTIONS and PREEMPTION #5 SETTINGS. Rows include priority, delay timer, and overlap settings.

PRESS 'NEXT' TWICE

Table with 2 columns: PREEMPTION #5 SETTINGS and CLEAR/DWELL PHASES. Rows include interval/timing and exit calls.

Table with 2 columns: OPTIONS and PREEMPTION #5 SETTINGS. Rows include priority, delay timer, and overlap settings.

PROGRAMMING COMPLETE

\* TIME DEFAULTS TO TIME USED BY PHASE DURING NORMAL OPERATION

NOTE! PROGRAM EXTEND TIME ON ALL 'OPTICOM' DETECTOR UNITS FOR 2 SEC.

**LOGICAL I/O PROCESSOR PROGRAMMING DETAIL TO PRODUCE SPECIAL FYA-PPLT SIGNAL SEQUENCE**

*(program controller as shown below)*

- 1. FROM MAIN MENU PRESS '2' (PHASE CONTROL), THEN '1' (PHASE CONTROL FUNCTIONS)...
2. FROM MAIN MENU PRESS '6' (OUTPUTS), THEN '3' (LOGICAL I/O PROCESSOR).

LOGICAL I/O COMMAND #1 (+/-COMMAND#) IF ACTIVE PHASE #1 IS ON AND RED CLEAR ON PHASE #1 IS ON. THEN: SET OUTPUT ASSIGNMENT #50 ON, SET OUTPUT ASSIGNMENT #51 OFF. PRESS '+'

NOTE: LOGIC FOR PHASE 1 RED CLEAR WHEN TRANSITIONING FROM PHASE 1 TO PHASE 2 (HEAD 11).

LOGICAL I/O COMMAND #2 (+/-COMMAND#) IF ACTIVE PHASE #1 IS ON. THEN: SET OUTPUT ASSIGNMENT #52 OFF. PRESS '+'

NOTE: LOGIC FOR SWITCHING FLASHING YELLOW ARROW "OFF" DURING PHASE 1 (HEAD 11).

LOGICAL I/O COMMAND #3 (+/-COMMAND#) IF YELLOW ON PHASE #1 IS ON. THEN: SET OUTPUT ASSIGNMENT #51 ON.

NOTE: LOGIC FOR YELLOW ARROW CLEARANCE FROM PHASE 1 (HEAD 11).

LOGIC I/O PROCESSOR PROGRAMMING COMPLETE

OUTPUT REFERENCE SCHEDULE: OUTPUT 50 = Overlap A Red, OUTPUT 51 = Overlap A Yellow, OUTPUT 52 = Overlap A Green

**OVERLAP PROGRAMMING DETAIL**

*(program controller as shown below)*

FROM MAIN MENU PRESS '8' (OVERLAPS), THEN '1' (VEHICLE OVERLAP SETTINGS).

PAGE 1: VEHICLE OVERLAP 'A' SETTINGS. PHASE: 12345678910111213141516. VEH OVL PARENTS: XX. FLASH COLORS: RED YELLOW GREEN. SELECT VEHICLE OVERLAP OPTIONS: FLASH YELLOW IN CONTROLLER FLASH?..Y

NOTICE GREEN FLASH

PRESS '+' THREE TIMES

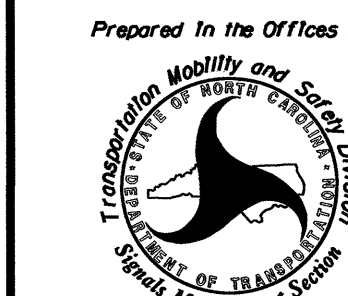
PAGE 1: VEHICLE OVERLAP 'D' SETTINGS. PHASE: 12345678910111213141516. VEH OVL PARENTS: X. FLASH COLORS: RED YELLOW GREEN. SELECT VEHICLE OVERLAP OPTIONS: FLASH YELLOW IN CONTROLLER FLASH?..N

OVERLAP PROGRAMMING COMPLETE

THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 01-0450. DESIGNED: April 2010. SEALED: 08/19/10. REVISED:

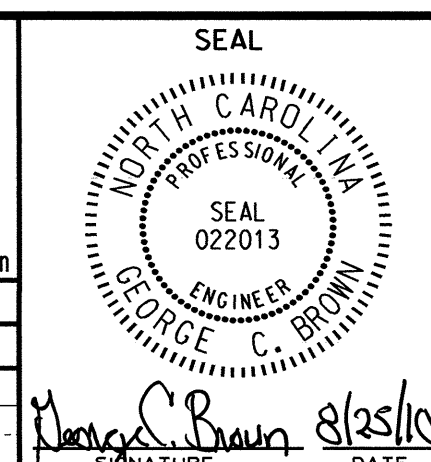
ELECTRICAL DETAIL - FINAL - SHEET 2 OF 2

ELECTRICAL AND PROGRAMMING DETAILS FOR:



750 N. Greenfield Pkwy, Garner, NC 27529

Table with fields for Division 1, Camden County, Camden, PLAN DATE: July 2010, REVIEWED BY: T. Szyl, PREPARED BY: C. Strickland, REVIEWED BY: GEORGE C. BROWN.



SIGNATURE DATE, SIG. INVENTORY NO. 01-0450











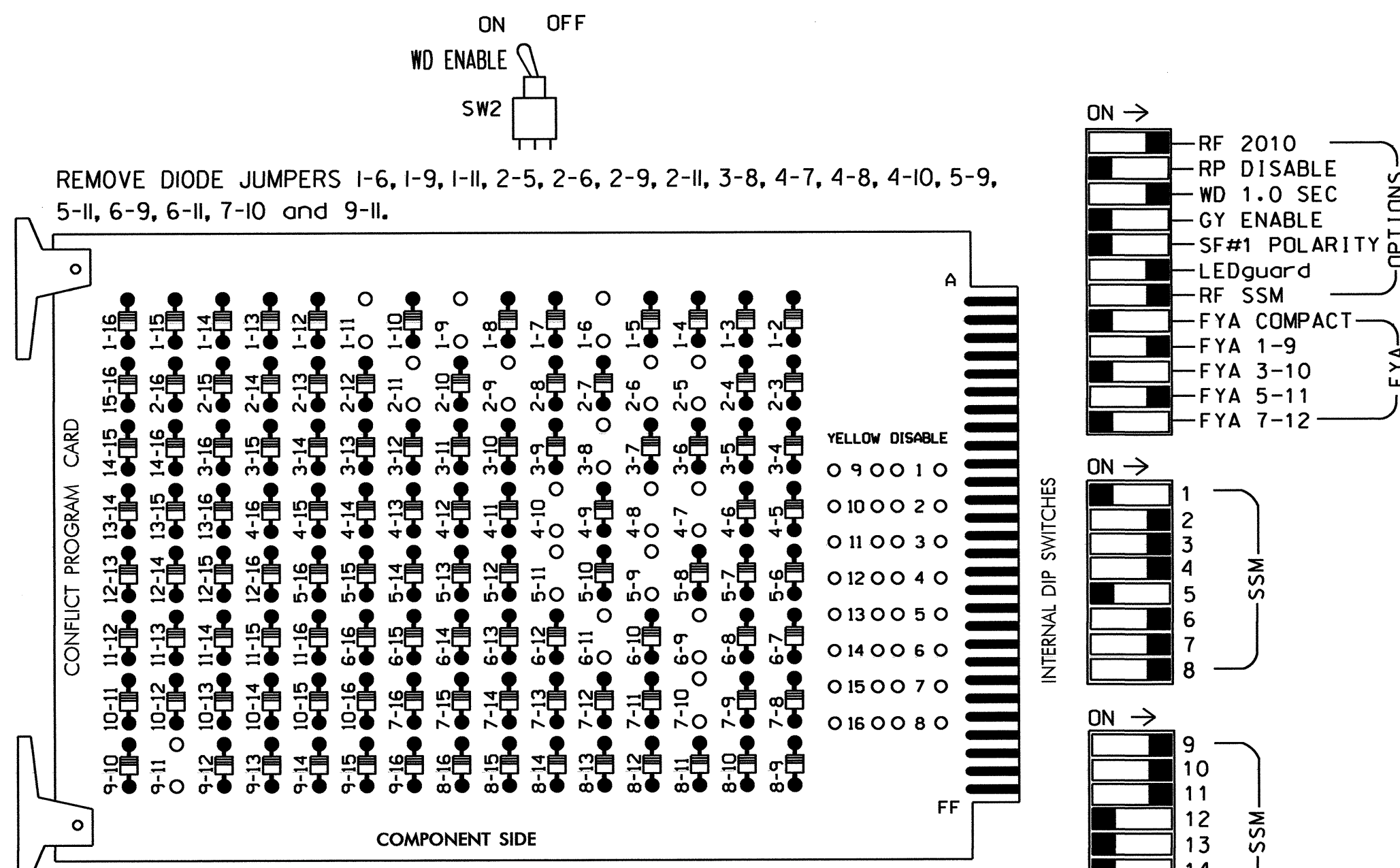






### EDI MODEL 2010ECL-NC CONFLICT MONITOR PROGRAMMING DETAIL

(remove jumpers and set switches as shown)



- NOTES:
- Card is provided with all diode jumpers in place. Removal of any jumper allows its channels to run concurrently.
  - Make sure jumpers SEL2-SEL5 are present on the monitor board.

### NOTES

- To prevent "flash-conflict" problems, insert red flash program blocks for all unused vehicle load switches in the output file. The installer shall verify that signal heads flash in accordance with the Signal Plans.
- Ensure that Red Enable is active at all times during normal operation. To prevent Red Failures on unused monitor channels, tie unused red monitor inputs 1,5, 12,13,14,15 & 16 to load switch AC+ per the cabinet manufacturer's instructions.
- Program phases 4 and 8 for Dual Entry.
- Enable Simultaneous Gap-Out for all phases.
- Program phases 2 and 6 for Start Up In Green.
- Program phases 2 and 6 for Yellow Flash, and overlap 1 as Wag Overlaps.

### PREEMPT ONLY PHASE OMIT NOTE

(program controller as shown below)

From Main Menu press '2' (Phase Control). Then '1' (Phase Control Functions). Program Phases 1 and 3 for 'Omit Phase' and Phases 2, 4, 5, 6, 7 and 8 for 'Startup Calls'. This is to prevent Phases 1 and 3 from being served when not in Preempt.

### EQUIPMENT INFORMATION

CONTROLLER.....2070L  
 CABINET.....332 /W/ AUX  
 SOFTWARE.....ECONOLITE OASIS  
 CABINET MOUNT.....BASE  
 OUTPUT FILE POSITIONS...18 WITH AUX. OUTPUT FILE  
 LOAD SWITCHES USED.....S1,S2,S3,S4,S5,S6,S7,S8,S9,S10,S12  
 PHASES USED.....\*1,2,\*3,4,5,6,7,8  
 OVERLAP "A".....1+2  
 OVERLAP "B".....7  
 OVERLAP "C".....5+6  
 OVERLAP "D".....NOT USED

\*PHASES 1 AND 3 USED IN PREEMPT SEQUENCE ONLY

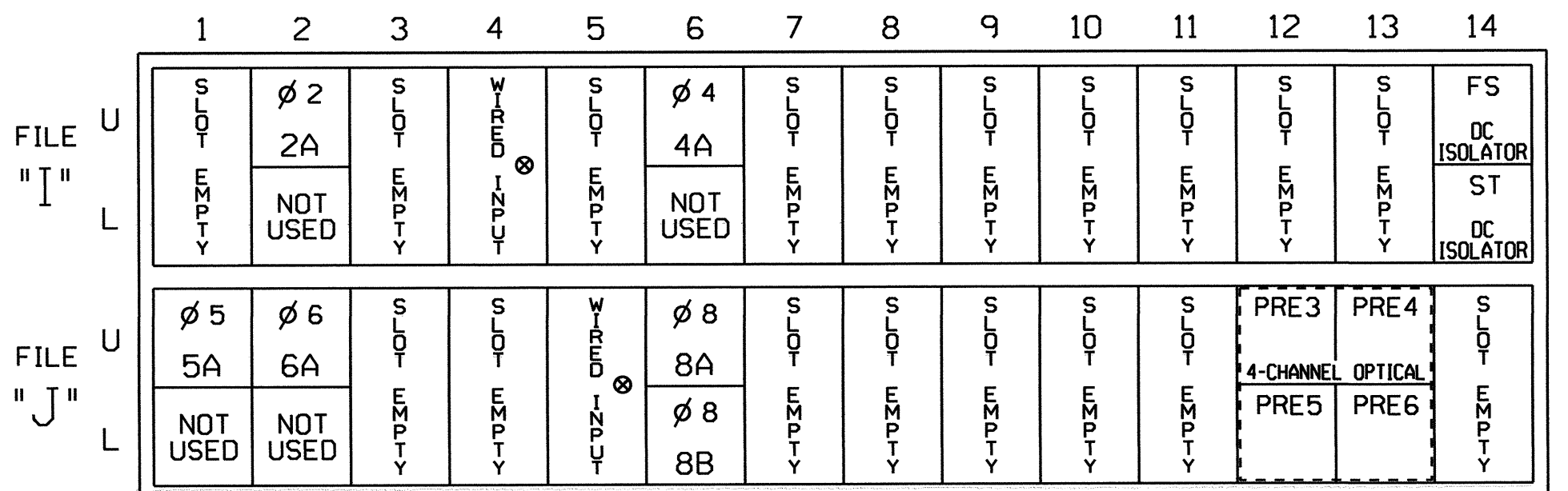
### SIGNAL HEAD HOOK-UP CHART

LOAD SWITCH NO.	S1	S2	S2P	S3	S4	S4P	S5	S6	S6P	S7	S8	S8P	S9	S10	S11	S12	S13	S14
PHASE	1	2	2 PED	3	4	4 PED	5	6	6 PED	7	8	8 PED	OLA	OLB	SPARE	OLC	OLD	SPARE
SIGNAL HEAD NO.	61*	21,22	NU	81	41,42	NU	51*	62,63	NU	41	81,82	NU	61*	63	NU	51*	NU	NU
RED		128		*	101			134		*	107			*				
YELLOW	*	129			102		*	135			108							
GREEN		130			103			136			109							
RED ARROW														A121			A114	
YELLOW ARROW					117						123			A122	A125		A115	
FLASHING YELLOW ARROW														A123			A116	
GREEN ARROW	127				118			133			124						A126	

NU = Not Used  
 \* Denotes install load resistor. See load resistor installation detail this sheet.  
 \* See pictorial of head wiring in detail below.

### INPUT FILE POSITION LAYOUT

(front view)



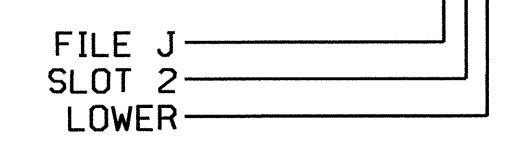
EX.: 1A, 2A, ETC. = LOOP NO.'S  
 FS = FLASH SENSE  
 ST = STOP TIME  
 ⊗ Wired Input - Do not populate slot with detector card

### INPUT FILE CONNECTION & PROGRAMMING CHART

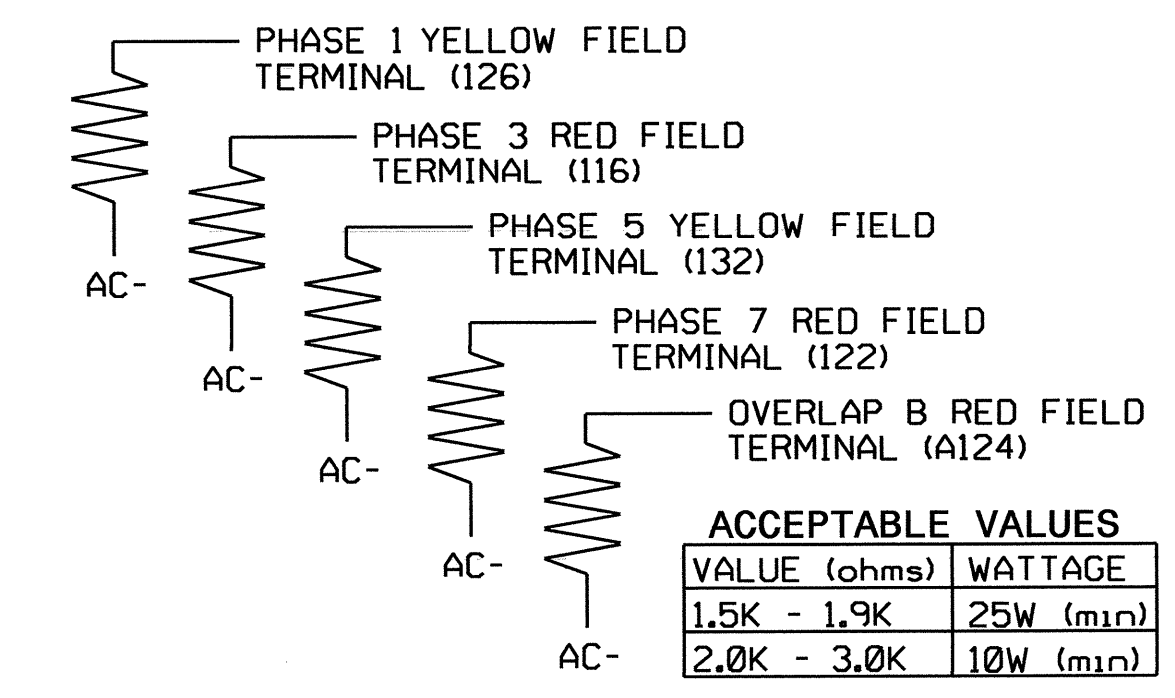
LOOP NO.	LOOP TERMINAL	INPUT FILE POS.	PIN NO.	INPUT ASSIGNMENT NO.	DETECTOR NO.	NEMA PHASE	CALL	EXTEND	FULL TIME DELAY	STRETCH TIME	DELAY TIME
2A	T82-5,6	I2U	39	1	2	2	Y	Y			
4A <sup>1</sup>	-	J5U	57	19	7	7	Y	Y			15
	T84-9,10	I6U	41	3	4	4	Y	Y			3
5A <sup>2</sup>	T83-1,2	J1U	55	17	5	5	Y	Y			15
	-	I4U	47	9	22	2	Y	Y			
6A	T83-5,6	J2U	40	2	6	6	Y	Y			
8A	T85-9,10	J6U	42	4	8	8	Y	Y			3
8B	T85-11,12	J6L	46	8	18	8	Y	Y			15

- Add jumper from I6-F to J5-F, on rear of input file.
- Add jumper from J1-W to I4-W, on rear of input file.

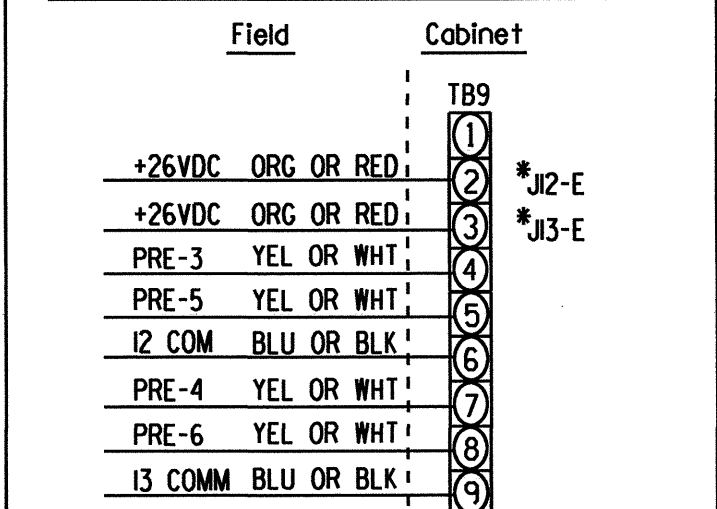
INPUT FILE POSITION LEGEND: J2L



### LOAD RESISTOR INSTALLATION DETAIL



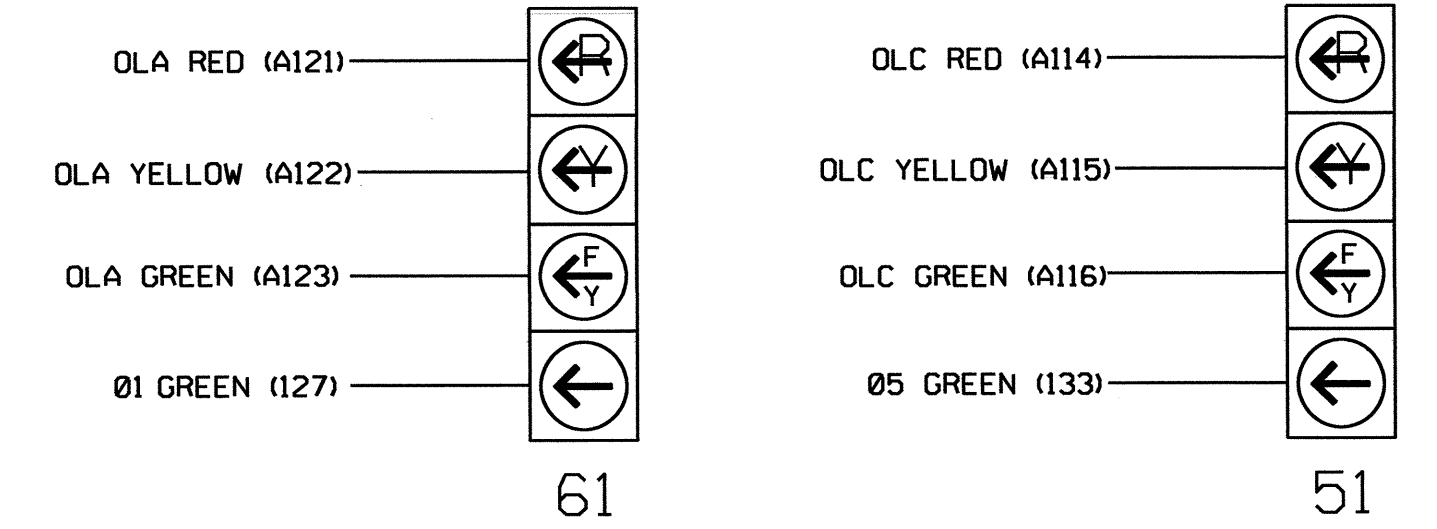
### OPTICOM FIELD WIRE DETAIL



\*Assuming T89-2 & T89-3 are unused on the J File, move wires on J1-J & J11-K (Twisted Pair) to J12-E & J13-E Respectively.

### 4 SECTION FYA PPLT SIGNAL WIRING DETAIL

(wire signal heads as shown)



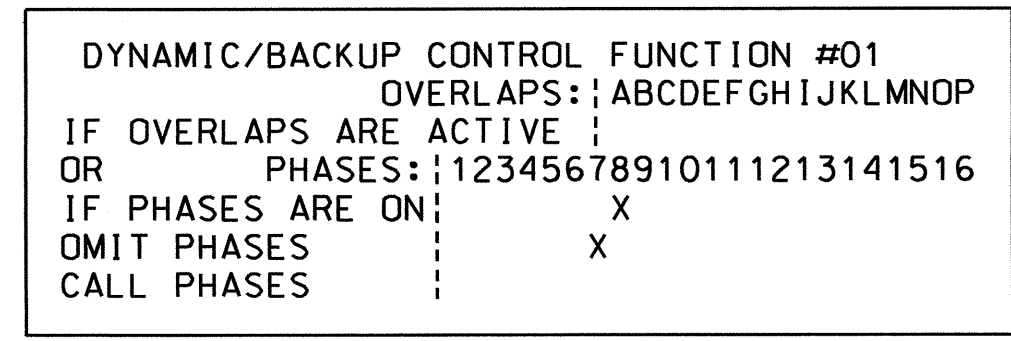
### NOTE

- The sequence display for this signal requires special logic programming. See sheet 2 of 3 for programming instructions.

### DYNAMIC BACK-UP CONTROL PROGRAMMING

(program controller as shown below)

- From Main Menu press '2' (Phase Control), then '1' (Phase Control Functions). Scroll to the bottom of the menu and enable Dynamic/Backup Control Function 1.
- From Phase Control Functions Menu press '2' (Dynamic/Backup Control Functions).



BACKUP PROTECTION PROGRAMMING COMPLETE

ELECTRICAL DETAIL - TEMP 2 - SHEET 1 OF 3

Electrical and Programming Details For:

Prepared In the Offices of:

750 N. Greenfield Pkwy, Garner, NC 27529

US 158 at NC 343

Division 1 Camden County Camden

PLAN DATE: July 2010 REVISIONS: T. J. J. / C. Strickland

DESIGNED: March 2010 PREPARED BY: C. Strickland

SEALED: 08/19/10

REVISOR: DATE: INITIALS: DATE: SIGNATURE: DATE: INVENTORY NO. 01-022912

THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 01-0229T2  
 DESIGNED: March 2010  
 SEALED: 08/19/10  
 REVISED:

## LOGICAL I/O PROCESSOR PROGRAMMING DETAIL TO PRODUCE SPECIAL FYA-PPLT SIGNAL SEQUENCE

(program controller as shown below)

1. FROM MAIN MENU PRESS '2' (PHASE CONTROL), THEN '1' (PHASE CONTROL FUNCTIONS). SCROLL TO THE BOTTOM OF THE MENU AND ENABLE ACT LOGIC COMMANDS 1, 2, 3, 4, 5 AND 6.
2. FROM MAIN MENU PRESS '6' (OUTPUTS), THEN '3' (LOGICAL I/O PROCESSOR).

LOGICAL I/O COMMAND #1 (+/-COMMAND#)  
IF ACTIVE PHASE #1 IS ON  
AND RED CLEAR ON PHASE #1 IS ON

↓  
SCROLL DOWN

THEN:  
SET OUTPUT ASSIGNMENT #50 ON  
SET OUTPUT ASSIGNMENT #51 OFF

PRESS '+'

NOTE: LOGIC FOR PHASE 1 RED CLEAR WHEN TRANSITIONING FROM PHASE 1 (EV PRE 4) TO PHASE 2 (HEAD 61).

LOGICAL I/O COMMAND #2 (+/-COMMAND#)  
IF ACTIVE PHASE #1 IS ON

↓  
SCROLL DOWN

THEN:  
SET OUTPUT ASSIGNMENT #52 OFF

PRESS '+'

NOTE: LOGIC FOR SWITCHING FLASHING YELLOW ARROW "OFF" DURING PHASE 1 (HEAD 61).

LOGICAL I/O COMMAND #3 (+/-COMMAND#)  
IF YELLOW ON PHASE #1 IS ON

↓  
SCROLL DOWN

THEN:  
SET OUTPUT ASSIGNMENT #51 ON

PRESS '+'

NOTE: LOGIC FOR YELLOW ARROW CLEARANCE FROM PHASE 1 (HEAD 61).

LOGICAL I/O COMMAND #4 (+/-COMMAND#)  
IF ACTIVE PHASE #5 IS ON  
AND RED CLEAR ON PHASE #5 IS ON

↓  
SCROLL DOWN

THEN:  
SET OUTPUT ASSIGNMENT #42 ON  
SET OUTPUT ASSIGNMENT #43 OFF

PRESS '+'

NOTE: LOGIC FOR PHASE 5 RED CLEAR WHEN TRANSITIONING FROM PHASE 5 TO PHASE 6 (HEAD 51).

LOGICAL I/O COMMAND #5 (+/-COMMAND#)  
IF ACTIVE PHASE #5 IS ON

↓  
SCROLL DOWN

THEN:  
SET OUTPUT ASSIGNMENT #44 OFF

PRESS '+'

NOTE: LOGIC FOR SWITCHING FLASHING YELLOW ARROW "OFF" DURING PHASE 5 (HEAD 51).

LOGICAL I/O COMMAND #6 (+/-COMMAND#)  
IF YELLOW ON PHASE #5 IS ON

↓  
SCROLL DOWN

THEN:  
SET OUTPUT ASSIGNMENT #43 ON

PRESS '+'

NOTE: LOGIC FOR YELLOW ARROW CLEARANCE FROM PHASE 5 (HEAD 51).

LOGIC I/O PROCESSOR PROGRAMMING COMPLETE

### OUTPUT REFERENCE SCHEDULE

- OUTPUT 42 = Overlap C Red
- OUTPUT 43 = Overlap C Yellow
- OUTPUT 44 = Overlap C Green
- OUTPUT 50 = Overlap A Red
- OUTPUT 51 = Overlap A Yellow
- OUTPUT 52 = Overlap A Green

## OVERLAP PROGRAMMING DETAIL

(program controller as shown below)

- FROM MAIN MENU PRESS '8' (OVERLAPS), THEN '1' (VEHICLE OVERLAP SETTINGS).

PAGE 1: VEHICLE OVERLAP 'A' SETTINGS  
PHASE: 12345678910111213141516  
VEH OVL PARENTS: XX  
VEH OVL NOT VEH:  
VEH OVL NOT PED:  
VEH OVL GRN EXT:  
STARTUP COLOR: - RED - YELLOW - GREEN  
FLASH COLORS: - RED - YELLOW X GREEN

← NOTICE GREEN FLASH

SELECT VEHICLE OVERLAP OPTIONS: (Y/N)  
FLASH YELLOW IN CONTROLLER FLASH?...Y  
GREEN EXTENSION (0-255 SEC)...0.0  
YELLOW CLEAR (0=PARENT,3-25.5 SEC)...0.0  
RED CLEAR (0=PARENT,0.1-25.5 SEC)...0.0  
OUTPUT AS PHASE # (0=NONE, 1-16)...0

PRESS '+'

PAGE 1: VEHICLE OVERLAP 'B' SETTINGS  
PHASE: 12345678910111213141516  
VEH OVL PARENTS: X  
VEH OVL NOT VEH:  
VEH OVL NOT PED:  
VEH OVL GRN EXT:  
STARTUP COLOR: - RED - YELLOW - GREEN  
FLASH COLORS: - RED - YELLOW - GREEN

SELECT VEHICLE OVERLAP OPTIONS: (Y/N)  
FLASH YELLOW IN CONTROLLER FLASH?...N  
GREEN EXTENSION (0-255 SEC)...0.0  
YELLOW CLEAR (0=PARENT,3-25.5 SEC)...0.0  
RED CLEAR (0=PARENT,0.1-25.5 SEC)...0.0  
OUTPUT AS PHASE # (0=NONE, 1-16)...0

PRESS '+'

PAGE 1: VEHICLE OVERLAP 'C' SETTINGS  
PHASE: 12345678910111213141516  
VEH OVL PARENTS: XX  
VEH OVL NOT VEH:  
VEH OVL NOT PED:  
VEH OVL GRN EXT:  
STARTUP COLOR: - RED - YELLOW - GREEN  
FLASH COLORS: - RED - YELLOW X GREEN

← NOTICE GREEN FLASH

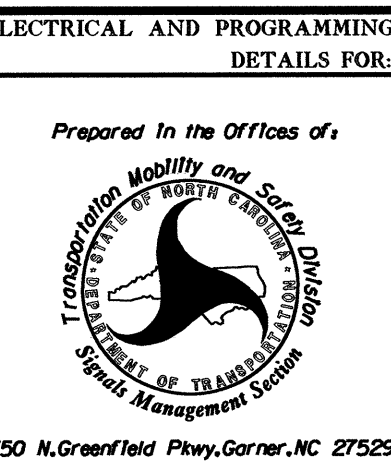
SELECT VEHICLE OVERLAP OPTIONS: (Y/N)  
FLASH YELLOW IN CONTROLLER FLASH?...Y  
GREEN EXTENSION (0-255 SEC)...0.0  
YELLOW CLEAR (0=PARENT,3-25.5 SEC)...0.0  
RED CLEAR (0=PARENT,0.1-25.5 SEC)...0.0  
OUTPUT AS PHASE # (0=NONE, 1-16)...0

OVERLAP PROGRAMMING COMPLETE

THIS ELECTRICAL DETAIL IS FOR  
THE SIGNAL DESIGN: 01-0229T2  
DESIGNED: March 2010  
SEALED: 08/19/10  
REVISED:

24-AUG-2010 09:22 S:\ITS\SS\WITS\_Signal\smo\groups\sig\_Mom\trickland\0229\_sm\el\_0000.dgn

ELECTRICAL DETAIL - TEMP 2 - SHEET 2 OF 3

	<p><b>US 158</b> at <b>NC 343</b></p>	<p>SEAL NORTH CAROLINA PROFESSIONAL ENGINEER SEAL 022013 GEORGE C. BROWN</p>
	<p>Division 1 Camden County Camden</p>	
	<p>PLAN DATE: July 2010 PREPARED BY: C. Strickland</p>	<p>REVIEWED BY: T. J. [Signature] REVIEWED BY:</p>
<p>750 N. Greenfield Pkwy, Garner, NC 27529</p>		<p>[Signature] 8/26/10 DATE</p>

SIG. INVENTORY NO. 01-0229T2

**EMERGENCY VEHICLE PREEMPTION PROGRAMMING DETAIL**

(program controller as shown below)

From Main Menu press 'A' (Preemption), then '1' (Standard Preemptions). Press 'NEXT' as needed to advance to Preempts 3, 4, 5 and 6.

PREEMPTION #3		SETTINGS (NEXT:1-10)											
INTERVAL/TIMING		CLEAR/DWELL PHASES											
GRN	YEL	RED	1	2	3	4	5	6	7	8	9	10	
1	255	0.0*	0.0*	X	X								
2	0	0.0	0.0										
3	0	0.0	0.0										
4	0	0.0	0.0										
5	0	0.0	0.0										

EXIT CALLS

OPTIONS

PRIORITY (Y/N TO SELECT) .....MED

DELAY TIMER (0-255 SEC) .....0

MIN GREEN BEFORE PRE (0= DEFAULT)....1

PED CLEAR BEFORE PRE (0= DEFAULT)....0

YELLOW CLEAR BEFORE PRE (0= DEFAULT).0.0\*

RED CLEAR BEFORE PRE (0= DEFAULT)....0.0\*

DWELL MIN TIMER (0-255 SEC) .....7

DWELL MAX TIMER (0=OFF.1-255MIN) ....0

DWELL HOLD-OVER TIMER (0-255) .....0

LATCH CALL? .....N

LINK TO NEXT PREEMPT? .....N

ENABLE BACKUP PROTECTION? .....N

HOLD CLEAR 1 PHASES DURING DELAY? ..N

FAST GREEN FLASH DWELL PHASES? .....N

PED CLEARANCE THROUGH YELLOW? .....N

INHIBIT OVERLAP GREEN EXTENSION? ..N

SERVICE DURING SOFTWARE FLASH? .....N

REST IN RED DURING DWELL INTERVAL? ..N

FLASH DWELL INTERVAL? .....N

ALLOW PEDS IN DWELL INTERVAL? .....N

RE-TIME DWELL INTERVAL? .....N

OVERLAPS: ABCDEFGHIJKLMNPO

DWELL INT FLASH YELLOW

OMIT OVERLAPS:

PRESS 'NEXT'

PREEMPTION #4		SETTINGS (NEXT:1-10)											
INTERVAL/TIMING		CLEAR/DWELL PHASES											
GRN	YEL	RED	1	2	3	4	5	6	7	8	9	10	
1	255	3.9*	1.9*	X	X								
2	0	0.0	0.0										
3	0	0.0	0.0										
4	0	0.0	0.0										
5	0	0.0	0.0										

EXIT CALLS

OPTIONS

PRIORITY (Y/N TO SELECT) .....MED

DELAY TIMER (0-255 SEC) .....0

MIN GREEN BEFORE PRE (0= DEFAULT)....1

PED CLEAR BEFORE PRE (0= DEFAULT)....0

YELLOW CLEAR BEFORE PRE (0= DEFAULT).0.0\*

RED CLEAR BEFORE PRE (0= DEFAULT)....0.0\*

DWELL MIN TIMER (0-255 SEC) .....7

DWELL MAX TIMER (0=OFF.1-255MIN) ....0

DWELL HOLD-OVER TIMER (0-255) .....0

LATCH CALL? .....N

LINK TO NEXT PREEMPT? .....N

ENABLE BACKUP PROTECTION? .....N

HOLD CLEAR 1 PHASES DURING DELAY? ..N

FAST GREEN FLASH DWELL PHASES? .....N

PED CLEARANCE THROUGH YELLOW? .....N

INHIBIT OVERLAP GREEN EXTENSION? ..N

SERVICE DURING SOFTWARE FLASH? .....N

REST IN RED DURING DWELL INTERVAL? ..N

FLASH DWELL INTERVAL? .....N

ALLOW PEDS IN DWELL INTERVAL? .....N

RE-TIME DWELL INTERVAL? .....N

OVERLAPS: ABCDEFGHIJKLMNPO

DWELL INT FLASH YELLOW

OMIT OVERLAPS:

PRESS 'NEXT'

PREEMPTION #5		SETTINGS (NEXT:1-10)											
INTERVAL/TIMING		CLEAR/DWELL PHASES											
GRN	YEL	RED	1	2	3	4	5	6	7	8	9	10	
1	255	0.0*	0.0*	X	X								
2	0	0.0	0.0										
3	0	0.0	0.0										
4	0	0.0	0.0										
5	0	0.0	0.0										

EXIT CALLS

OPTIONS

PRIORITY (Y/N TO SELECT) .....MED

DELAY TIMER (0-255 SEC) .....0

MIN GREEN BEFORE PRE (0= DEFAULT)....1

PED CLEAR BEFORE PRE (0= DEFAULT)....0

YELLOW CLEAR BEFORE PRE (0= DEFAULT).0.0\*

RED CLEAR BEFORE PRE (0= DEFAULT)....0.0\*

DWELL MIN TIMER (0-255 SEC) .....7

DWELL MAX TIMER (0=OFF.1-255MIN) ....0

DWELL HOLD-OVER TIMER (0-255) .....0

LATCH CALL? .....N

LINK TO NEXT PREEMPT? .....N

ENABLE BACKUP PROTECTION? .....Y

HOLD CLEAR 1 PHASES DURING DELAY? ..N

FAST GREEN FLASH DWELL PHASES? .....N

PED CLEARANCE THROUGH YELLOW? .....N

INHIBIT OVERLAP GREEN EXTENSION? ..N

SERVICE DURING SOFTWARE FLASH? .....N

REST IN RED DURING DWELL INTERVAL? ..N

FLASH DWELL INTERVAL? .....N

ALLOW PEDS IN DWELL INTERVAL? .....N

RE-TIME DWELL INTERVAL? .....N

OVERLAPS: ABCDEFGHIJKLMNPO

DWELL INT FLASH YELLOW

OMIT OVERLAPS: X

PRESS 'NEXT'

PREEMPTION #6		SETTINGS (NEXT:1-10)											
INTERVAL/TIMING		CLEAR/DWELL PHASES											
GRN	YEL	RED	1	2	3	4	5	6	7	8	9	10	
1	255	3.8	1.8	X	X								
2	0	0.0	0.0										
3	0	0.0	0.0										
4	0	0.0	0.0										
5	0	0.0	0.0										

EXIT CALLS

OPTIONS

PRIORITY (Y/N TO SELECT) .....MED

DELAY TIMER (0-255 SEC) .....0

MIN GREEN BEFORE PRE (0= DEFAULT)....1

PED CLEAR BEFORE PRE (0= DEFAULT)....0

YELLOW CLEAR BEFORE PRE (0= DEFAULT).0.0\*

RED CLEAR BEFORE PRE (0= DEFAULT)....0.0\*

DWELL MIN TIMER (0-255 SEC) .....7

DWELL MAX TIMER (0=OFF.1-255MIN) ....0

DWELL HOLD-OVER TIMER (0-255) .....0

LATCH CALL? .....N

LINK TO NEXT PREEMPT? .....N

ENABLE BACKUP PROTECTION? .....Y

HOLD CLEAR 1 PHASES DURING DELAY? ..N

FAST GREEN FLASH DWELL PHASES? .....N

PED CLEARANCE THROUGH YELLOW? .....N

INHIBIT OVERLAP GREEN EXTENSION? ..N

SERVICE DURING SOFTWARE FLASH? .....N

REST IN RED DURING DWELL INTERVAL? ..N

FLASH DWELL INTERVAL? .....N

ALLOW PEDS IN DWELL INTERVAL? .....N

RE-TIME DWELL INTERVAL? .....N

OVERLAPS: ABCDEFGHIJKLMNPO

DWELL INT FLASH YELLOW

OMIT OVERLAPS:

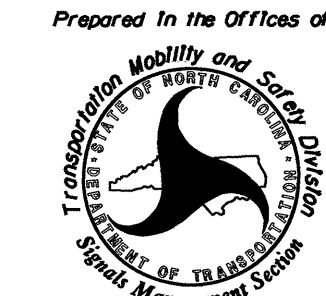
PROGRAMMING COMPLETE

\* TIME DEFAULTS TO TIME USED BY PHASE DURING NORMAL OPERATION

**NOTE!**  
PROGRAM EXTEND TIME ON ALL 'OPTICOM' DETECTOR UNITS FOR 2 SEC.

THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 01-0229T2  
DESIGNED: March 2010  
SEALED: 08/19/10  
REVISED:

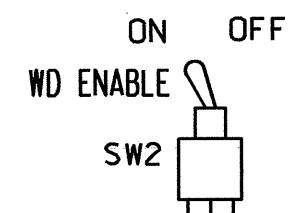
ELECTRICAL DETAIL - TEMP 2 - SHEET 3 OF 3

 Prepared In the Office of: 750 N. Greenfield Pkwy, Garner, NC 27529	US 158 at NC 343		SEAL NORTH CAROLINA PROFESSIONAL ENGINEER SEAL 022013 GEORGE C. BROWN
	Division 1 Camden County Camden	REVIEWED BY: T. Syde	
PREPARED BY: C. Strickland	REVISIONS	DATE	SIGNATURE: George C. Brown 8/26/10 DATE
SIG. INVENTORY NO. 01-0229T2			

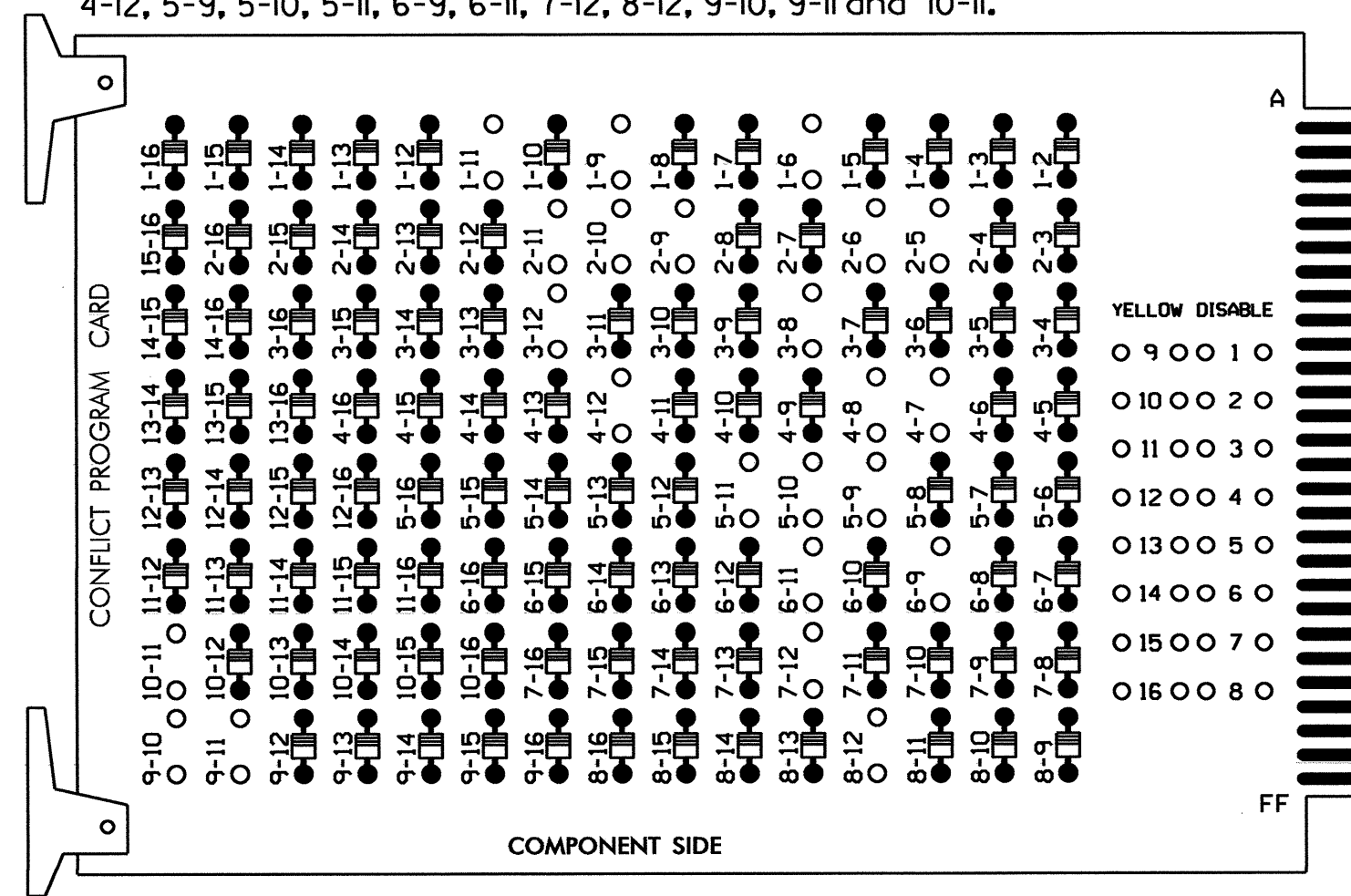


### EDI MODEL 2010ECL-NC CONFLICT MONITOR PROGRAMMING DETAIL

(remove jumpers and set switches as shown)



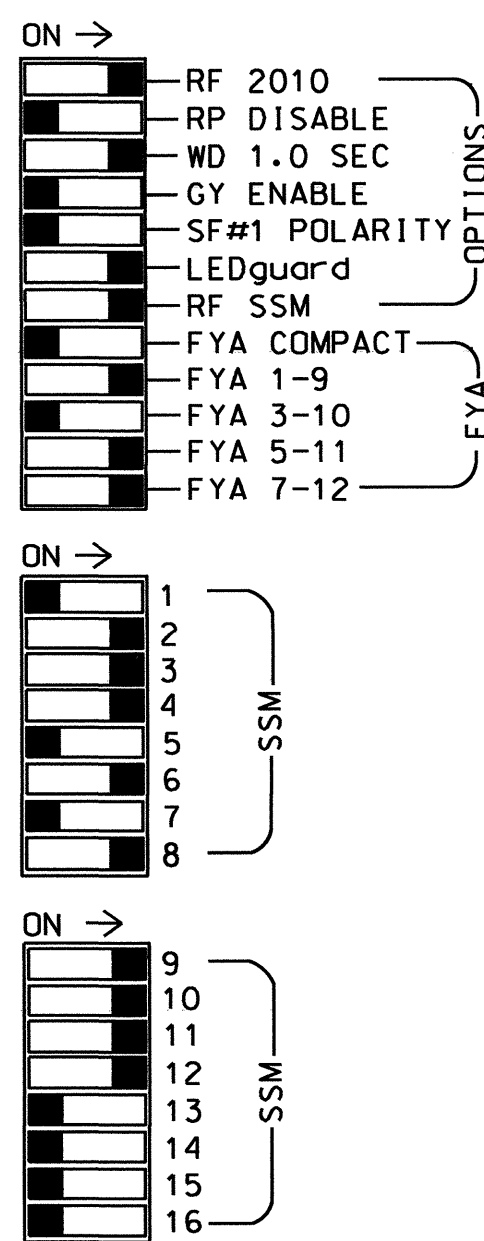
REMOVE DIODE JUMPERS 1-6, 1-9, 1-11, 2-5, 2-6, 2-9, 2-10, 2-11, 3-8, 3-12, 4-7, 4-8, 4-12, 5-9, 5-10, 5-11, 6-9, 6-11, 7-12, 8-12, 9-10, 9-11 and 10-11.



REMOVE JUMPERS AS SHOWN

NOTES:

- Card is provided with all diode jumpers in place. Removal of any jumper allows its channels to run concurrently.
- Make sure jumpers SEL2-SEL5 are present on the monitor board.



■ = DENOTES POSITION OF SWITCH

### NOTES

- To prevent "flash-conflict" problems, insert red flash program blocks for all unused vehicle load switches in the output file. The installer shall verify that signal heads flash in accordance with the Signal Plans.
- Ensure that Red Enable is active at all times during normal operation. To prevent Red Failures on unused monitor channels, tie unused red monitor inputs 1,5, 7,13,14,15 & 16 to load switch AC+ per the cabinet manufacturer's instructions.
- Program phases 4 and 8 for Dual Entry.
- Enable Simultaneous Gap-Out for all phases.
- Program phases 2 and 6 for Start Up In Green.
- Program phases 2 and 6 for Yellow Flash, and overlap 1 as Wag Overlaps.

### PREEMPT ONLY PHASE OMIT NOTE

(program controller as shown below)

From Main Menu press '2' (Phase Control). Then '1' (Phase Control Functions). Program Phases 1, 3 and 7 for 'Omit Phase' and Phases 2, 4, 5, 6, and 8 for 'Startup Calls'. This is to prevent Phases 1, 3 and 7 from being served when not in Preempt.

### EQUIPMENT INFORMATION

CONTROLLER.....2070L  
 CABINET.....332 /W/ AUX  
 SOFTWARE.....ECONOLITE OASIS  
 CABINET MOUNT.....BASE  
 OUTPUT FILE POSITIONS...18 WITH AUX. OUTPUT FILE  
 LOAD SWITCHES USED.....S1,S2,S3,S4,S5,S6,S7,S8,S9,S10,S12,S13  
 PHASES USED.....\*1,2,\*3,4,5,6,\*7,8  
 OVERLAP "A".....1+2+5  
 OVERLAP "B".....5  
 OVERLAP "C".....5+6  
 OVERLAP "D".....7+8

\*PHASES 1, 3 AND 7 USED IN PREEMPT SEQUENCE ONLY

### SIGNAL HEAD HOOK-UP CHART

LOAD SWITCH NO.	S1	S2	S2P	S3	S4	S4P	S5	S6	S6P	S7	S8	S8P	S9	S10	S11	S12	S13	S14
PHASE	1	2	2 PED	3	4	4 PED	5	6	6 PED	7	8	8 PED	OLA	OLB	SPARE	OLC	OLD	SPARE
SIGNAL HEAD NO.	61*	21,22	NU	81	42,43	NU	51*	62,63	NU	41*	81,82	NU	61*	43	NU	51*	41*	NU
RED		128		*	101			134			107		*					
YELLOW	*	129			102		*	135		*	108							
GREEN		130			103			136			109							
RED ARROW														A121			A114	A101
YELLOW ARROW					117									A122	A125		A115	A102
FLASHING YELLOW ARROW														A123			A116	A103
GREEN ARROW	127			118			133			124				A126				

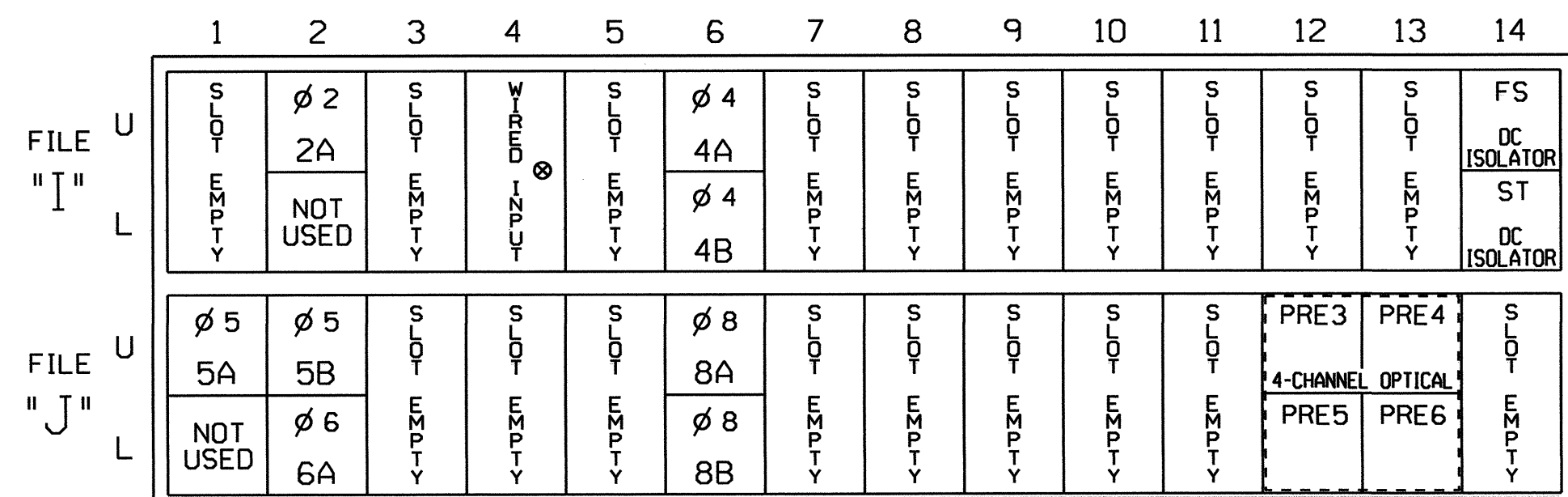
NU = Not Used

\* Denotes install load resistor. See load resistor installation detail this sheet.

\* See pictorial of head wiring in detail below.

### INPUT FILE POSITION LAYOUT

(front view)



EX.: 1A, 2A, ETC. = LOOP NO.'S

FS = FLASH SENSE  
 ST = STOP TIME

⊗ Wired Input - Do not populate slot with detector card

### INPUT FILE CONNECTION & PROGRAMMING CHART

LOOP NO.	LOOP TERMINAL	INPUT FILE POS.	PIN NO.	INPUT ASSIGNMENT NO.	DETECTOR NO.	NEMA PHASE	CALL	EXTEND	FULL TIME DELAY	STRETCH TIME	DELAY TIME
2A	TB2-5,6	I2U	39	1	2	2	Y	Y			
4A	TB4-9,10	I6U	41	3	4	4	Y	Y			3
4B	TB4-11,12	I6L	45	7	14	4	Y	Y			
5A <sup>1</sup>	TB3-1,2	J1U	55	17	5	5	Y	Y			15
		I4U	47	9	22	2	Y	Y			
5B	TB3-5,6	J2U	40	2	6	5	Y	Y			15
6A	TB3-7,8	J2L	44	6	16	6	Y	Y			
8A	TB5-9,10	J6U	42	4	8	8	Y	Y			3
8B	TB5-11,12	J6L	46	8	18	8	Y	Y			15

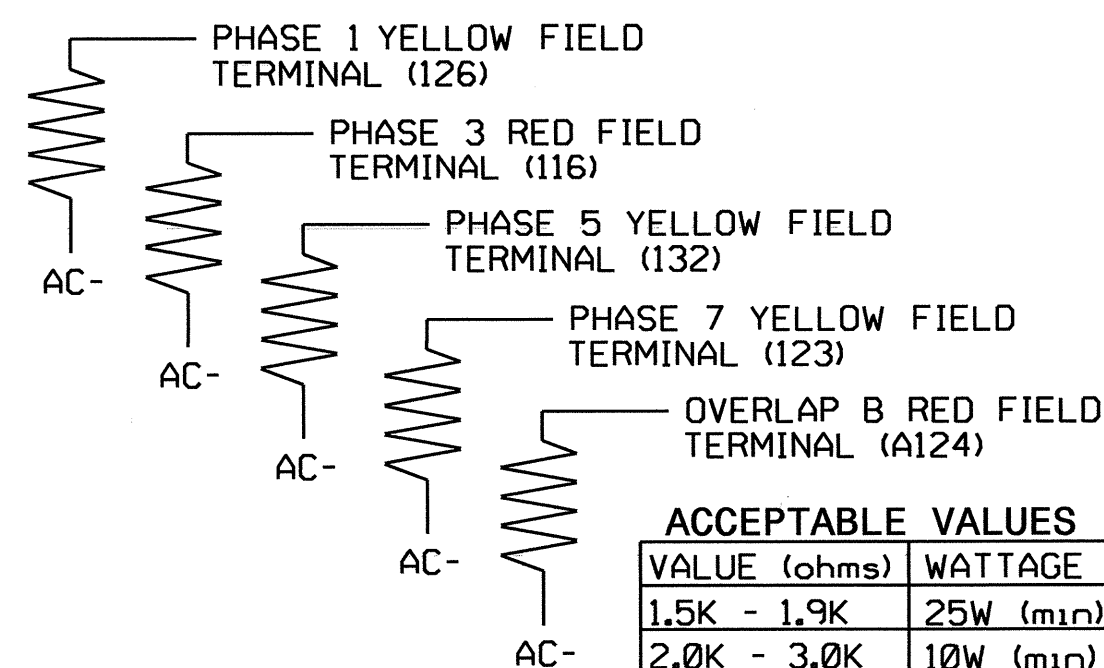
<sup>1</sup>Add jumper from J1-W to I4-W, on rear of input file.

! IMPORTANT: Remove jumper from I6-F to J5-F, on rear of input file.

INPUT FILE POSITION LEGEND: J2L

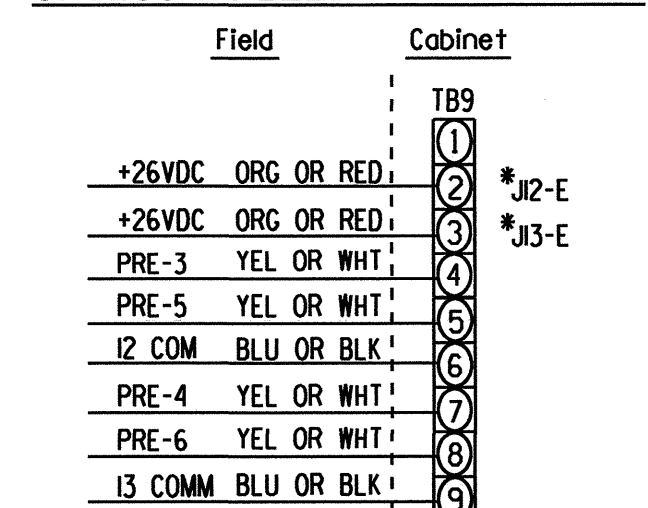


### LOAD RESISTOR INSTALLATION DETAIL



VALUE (ohms)	WATTAGE
1.5K - 1.9K	25W (min)
2.0K - 3.0K	10W (min)

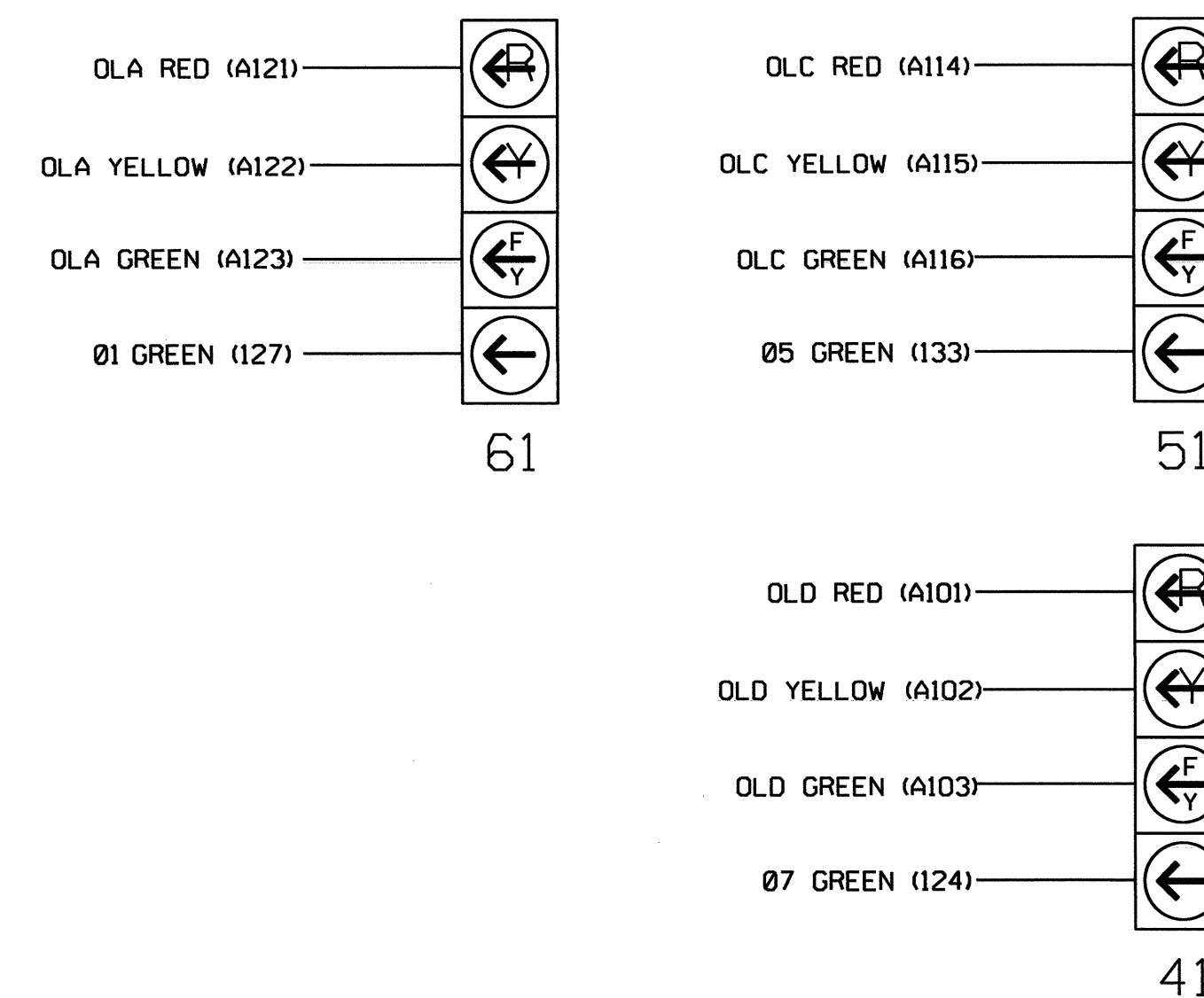
### OPTICOM FIELD WIRE DETAIL



\* Assuming TB9-2 & TB9-3 are unused on the J File, move wires on J1-J & J11-K (Twisted Pair) to J12-E & J13-E Respectively.

### 4 SECTION FYA PPLT SIGNAL WIRING DETAIL

(wire signal heads as shown)



NOTE

1. The sequence display for these signals require special logic programming. See sheet 2 of 3 for programming instructions.

THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 01-0229T3  
 DESIGNED: March 2010  
 SEALED: 08/19/10  
 REVISED:

ELECTRICAL DETAIL - TEMP 3 - SHEET 1 OF 3

	Prepared In the Offices of: 	US 158 at NC 343	SEAL NORTH CAROLINA PROFESSIONAL ENGINEER SEAL 022013 GEORGE C. BROWN	
	Division 1 PLAN DATE: July 2010 PREPARED BY: C. Strickland	Camden County REVIEWED BY: T. Joyce REVIEWED BY:	Camden	REVISIONS INIT. DATE
	750 N. Greenfield Pkwy, Garner, NC 27529	SIGNATURE: <i>George C. Brown</i> DATE: 8/27/10	DATE	DATE
	SIG. INVENTORY NO. 01-0229T3			DATE

### LOGICAL I/O PROCESSOR PROGRAMMING DETAIL TO PRODUCE SPECIAL FYA-PPLT SIGNAL SEQUENCE

(program controller as shown below)

- FROM MAIN MENU PRESS '2' (PHASE CONTROL), THEN '1' (PHASE CONTROL FUNCTIONS), SCROLL TO THE BOTTOM OF THE MENU AND ENABLE ACT LOGIC COMMANDS 1, 2, 3, 4, 5, 6, 7, 8 AND 9.
- FROM MAIN MENU PRESS '6' (OUTPUTS), THEN '3' (LOGICAL I/O PROCESSOR).

LOGICAL I/O COMMAND #1 (+/-COMMAND#)  
IF ACTIVE PHASE #1 IS ON  
AND RED CLEAR ON PHASE #1 IS ON

SCROLL DOWN

THEN:  
SET OUTPUT ASSIGNMENT #50 ON  
SET OUTPUT ASSIGNMENT #51 OFF

PRESS '+'

NOTE: LOGIC FOR PHASE 1 RED CLEAR WHEN TRANSITIONING FROM PHASE 1 (EV PRE 4) TO PHASE 2 (HEAD 61).

LOGICAL I/O COMMAND #2 (+/-COMMAND#)  
IF ACTIVE PHASE #1 IS ON

SCROLL DOWN

THEN:  
SET OUTPUT ASSIGNMENT #52 OFF

PRESS '+'

NOTE: LOGIC FOR SWITCHING FLASHING YELLOW ARROW "OFF" DURING PHASE 1 (HEAD 61).

LOGICAL I/O COMMAND #3 (+/-COMMAND#)  
IF YELLOW ON PHASE #1 IS ON

SCROLL DOWN

THEN:  
SET OUTPUT ASSIGNMENT #51 ON

PRESS '+'

NOTE: LOGIC FOR YELLOW ARROW CLEARANCE FROM PHASE 1 (HEAD 61).

LOGICAL I/O COMMAND #4 (+/-COMMAND#)  
IF ACTIVE PHASE #5 IS ON  
AND RED CLEAR ON PHASE #5 IS ON

SCROLL DOWN

THEN:  
SET OUTPUT ASSIGNMENT #42 ON  
SET OUTPUT ASSIGNMENT #43 OFF

PRESS '+'

NOTE: LOGIC FOR PHASE 5 RED CLEAR WHEN TRANSITIONING FROM PHASE 5 TO PHASE 6 (HEAD 51).

LOGICAL I/O COMMAND #5 (+/-COMMAND#)  
IF ACTIVE PHASE #5 IS ON

SCROLL DOWN

THEN:  
SET OUTPUT ASSIGNMENT #44 OFF

PRESS '+'

NOTE: LOGIC FOR SWITCHING FLASHING YELLOW ARROW "OFF" DURING PHASE 5 (HEAD 51).

LOGICAL I/O COMMAND #6 (+/-COMMAND#)  
IF YELLOW ON PHASE #5 IS ON

SCROLL DOWN

THEN:  
SET OUTPUT ASSIGNMENT #43 ON

PRESS '+'

NOTE: LOGIC FOR YELLOW ARROW CLEARANCE FROM PHASE 5 (HEAD 51).

LOGICAL I/O COMMAND #7 (+/-COMMAND#)  
IF ACTIVE PHASE #7 IS ON  
AND RED CLEAR ON PHASE #7 IS ON

SCROLL DOWN

THEN:  
SET OUTPUT ASSIGNMENT #39 ON  
SET OUTPUT ASSIGNMENT #40 OFF

PRESS '+'

NOTE: LOGIC FOR PHASE 7 RED CLEAR WHEN TRANSITIONING FROM PHASE 7 (EV PRE 5) TO PHASE 8 (HEAD 41).

LOGICAL I/O COMMAND #8 (+/-COMMAND#)  
IF ACTIVE PHASE #7 IS ON

SCROLL DOWN

THEN:  
SET OUTPUT ASSIGNMENT #41 OFF

PRESS '+'

NOTE: LOGIC FOR SWITCHING FLASHING YELLOW ARROW "OFF" DURING PHASE 7 (HEAD 41).

LOGICAL I/O COMMAND #9 (+/-COMMAND#)  
IF YELLOW ON PHASE #7 IS ON

SCROLL DOWN

THEN:  
SET OUTPUT ASSIGNMENT #40 ON

PRESS '+'

NOTE: LOGIC FOR YELLOW ARROW CLEARANCE FROM PHASE 7 (HEAD 41).

LOGIC I/O PROCESSOR PROGRAMMING COMPLETE

**OUTPUT REFERENCE SCHEDULE**  
USE TO INTERPRET LOGIC PROCESSOR

OUTPUT 39 = Overlap D Red  
OUTPUT 40 = Overlap D Yellow  
OUTPUT 41 = Overlap D Green  
OUTPUT 42 = Overlap C Red  
OUTPUT 43 = Overlap C Yellow  
OUTPUT 44 = Overlap C Green  
OUTPUT 50 = Overlap A Red  
OUTPUT 51 = Overlap A Yellow  
OUTPUT 52 = Overlap A Green

### OVERLAP PROGRAMMING DETAIL

(program controller as shown below)

FROM MAIN MENU PRESS '8' (OVERLAPS), THEN '1' (VEHICLE OVERLAP SETTINGS).

PAGE 1: VEHICLE OVERLAP 'A' SETTINGS  
PHASE: 12345678910111213141516  
VEH OVL PARENTS: XX X  
VEH OVL NOT VEH: :  
VEH OVL NOT PED: :  
VEH OVL GRN EXT: :  
STARTUP COLOR: - RED - YELLOW - GREEN  
FLASH COLORS: - RED - YELLOW X GREEN  
SELECT VEHICLE OVERLAP OPTIONS: (Y/N)  
FLASH YELLOW IN CONTROLLER FLASH?...Y  
GREEN EXTENSION (0-255 SEC)...0  
YELLOW CLEAR (0=PARENT.3-25.5 SEC)...0  
RED CLEAR (0=PARENT.0.1-25.5 SEC)...0  
OUTPUT AS PHASE # (0=NONE, 1-16)...0

NOTICE GREEN FLASH

PAGE 1: VEHICLE OVERLAP 'B' SETTINGS  
PHASE: 12345678910111213141516  
VEH OVL PARENTS: X  
VEH OVL NOT VEH: :  
VEH OVL NOT PED: :  
VEH OVL GRN EXT: :  
STARTUP COLOR: - RED - YELLOW - GREEN  
FLASH COLORS: - RED - YELLOW - GREEN  
SELECT VEHICLE OVERLAP OPTIONS: (Y/N)  
FLASH YELLOW IN CONTROLLER FLASH?...N  
GREEN EXTENSION (0-255 SEC)...0  
YELLOW CLEAR (0=PARENT.3-25.5 SEC)...0  
RED CLEAR (0=PARENT.0.1-25.5 SEC)...0  
OUTPUT AS PHASE # (0=NONE, 1-16)...0

PRESS '+'

PAGE 1: VEHICLE OVERLAP 'C' SETTINGS  
PHASE: 12345678910111213141516  
VEH OVL PARENTS: XX  
VEH OVL NOT VEH: :  
VEH OVL NOT PED: :  
VEH OVL GRN EXT: :  
STARTUP COLOR: - RED - YELLOW - GREEN  
FLASH COLORS: - RED - YELLOW X GREEN  
SELECT VEHICLE OVERLAP OPTIONS: (Y/N)  
FLASH YELLOW IN CONTROLLER FLASH?...Y  
GREEN EXTENSION (0-255 SEC)...0  
YELLOW CLEAR (0=PARENT.3-25.5 SEC)...0  
RED CLEAR (0=PARENT.0.1-25.5 SEC)...0  
OUTPUT AS PHASE # (0=NONE, 1-16)...0

NOTICE GREEN FLASH

PAGE 1: VEHICLE OVERLAP 'D' SETTINGS  
PHASE: 12345678910111213141516  
VEH OVL PARENTS: XX  
VEH OVL NOT VEH: :  
VEH OVL NOT PED: :  
VEH OVL GRN EXT: :  
STARTUP COLOR: - RED - YELLOW - GREEN  
FLASH COLORS: - RED - YELLOW X GREEN  
SELECT VEHICLE OVERLAP OPTIONS: (Y/N)  
FLASH YELLOW IN CONTROLLER FLASH?...N  
GREEN EXTENSION (0-255 SEC)...0  
YELLOW CLEAR (0=PARENT.3-25.5 SEC)...0  
RED CLEAR (0=PARENT.0.1-25.5 SEC)...0  
OUTPUT AS PHASE # (0=NONE, 1-16)...0

NOTICE GREEN FLASH

OVERLAP PROGRAMMING COMPLETE

### DYNAMIC BACK-UP CONTROL PROGRAMMING

(program controller as shown below)

- From Main Menu press '2' (Phase Control), then '1' (Phase Control Functions). Scroll to the bottom of the menu and enable Dynamic/Backup Control Function 1.
- From Phase Control Functions Menu press '2' (Dynamic/Backup Control Functions).

DYNAMIC/BACKUP CONTROL FUNCTION #01  
OVERLAPS: ABCDEFGHIJKLMNP  
IF OVERLAPS ARE ACTIVE :  
OR PHASES: 12345678910111213141516  
IF PHASES ARE ON : X  
OMIT PHASES : X  
CALL PHASES : X

BACKUP PROTECTION PROGRAMMING COMPLETE

! IMPORTANT: Disable Dynamic/Backup Control Function 1.

THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 01-0229T3  
DESIGNED: March 2010  
SEALED: 08/19/10  
REVISED:

### FLASHER CIRCUIT MODIFICATION DETAIL

IN ORDER TO INSURE THAT SIGNALS FLASH CONCURRENTLY ON THE SAME APPROACH, MAKE THE FOLLOWING FLASHER CIRCUIT CHANGES:

- ON REAR OF PDA - REMOVE WIRE FROM TERM. T2-4 AND TERMINATE ON T2-2.
- ON REAR OF PDA - REMOVE WIRE FROM TERM. T2-5 AND TERMINATE ON T2-3.
- REMOVE FLASHER UNIT 2.

THE CHANGES LISTED ABOVE TIES ALL PHASES AND OVERLAPS TO FLASHER UNIT 1.

ELECTRICAL DETAIL - TEMP 3 - SHEET 2 OF 3

Prepared In the Offices of:  
TRANSPORTATION MOBILITY AND SAFETY DIVISION  
DEPARTMENT OF TRANSPORTATION  
Signal Management Section  
750 N. Greenfield Pkwy, Garner, NC 27529

US 158 at NC 343	
Division 1 Camden County Camden	REVIEWED BY: T. J. [Signature]
PLAN DATE: July 2010	REVIEWED BY:
PREPARED BY: C. Strickland	REVIEWED BY:
REVISIONS	INIT. DATE

SEAL  
NORTH CAROLINA PROFESSIONAL ENGINEER  
SEAL 022013  
G. GEORGE C. BROWN  
[Signature]  
DATE 8/27/10  
SIG. INVENTORY NO. 01-0229T3

24-AUG-2010 11:20 S:\ITS\SSJ\ITS\_Signals\Kgr\output\sig\_Mom\51\F1\CKI\and\010229\_sml.e [a\_xxx].dgn

### EMERGENCY VEHICLE PREEMPTION PROGRAMMING DETAIL

(program controller as shown below)

From Main Menu press 'A' (Preemption), then '1' (Standard Preemptions). Press 'NEXT' as needed to advance to Preempts 3, 4, 5 and 6.

PREEMPTION #3	SETTINGS (NEXT:1-10)
INTERVAL/TIMING	CLEAR/DWELL PHASES
GRN YEL RED	12345678910111213141516
1 255 0.0* 0.0*	X X
2 0 0.0 0.0	
3 0 0.0 0.0	
4 0 0.0 0.0	
5 0 0.0 0.0	

EXIT CALLS	OPTIONS
PRIORITY (Y/N TO SELECT) .....	MED
DELAY TIMER (0-255 SEC) .....	0
MIN GREEN BEFORE PRE (0= DEFAULT)....	1
PED CLEAR BEFORE PRE (0= DEFAULT)....	0
YELLOW CLEAR BEFORE PRE (0= DEFAULT).0.0*	0.0*
RED CLEAR BEFORE PRE (0= DEFAULT)....0.0*	0.0*
DWELL MIN TIMER (0-255 SEC) .....	7
DWELL MAX TIMER (0=OFF,1-255MIN) ....	0
DWELL HOLD-OVER TIMER (0-255) .....	0
LATCH CALL? .....	N
LINK TO NEXT PREEMPT? .....	N
ENABLE BACKUP PROTECTION? .....	N
HOLD CLEAR 1 PHASES DURING DELAY? ..	N
FAST GREEN FLASH DWELL PHASES? .....	N
PED CLEARANCE THROUGH YELLOW? .....	N
INHIBIT OVERLAP GREEN EXTENSION? ..	N
SERVICE DURING SOFTWARE FLASH? .....	N
REST IN RED DURING DWELL INTERVAL? ..	N
FLASH DWELL INTERVAL? .....	N
ALLOW PDS IN DWELL INTERVAL? .....	N
RE-TIME DWELL INTERVAL? .....	N
OVERLAPS: .....	ABCDEFGHIJKLMNPO
DWELL INT FLASH YELLOW	
OMIT OVERLAPS:	X

PRESS 'NEXT'

PREEMPTION #4	SETTINGS (NEXT:1-10)
INTERVAL/TIMING	CLEAR/DWELL PHASES
GRN YEL RED	12345678910111213141516
1 255 3.9 2.5	X X
2 0 0.0 0.0	
3 0 0.0 0.0	
4 0 0.0 0.0	
5 0 0.0 0.0	

EXIT CALLS	OPTIONS
PRIORITY (Y/N TO SELECT) .....	MED
DELAY TIMER (0-255 SEC) .....	0
MIN GREEN BEFORE PRE (0= DEFAULT)....	1
PED CLEAR BEFORE PRE (0= DEFAULT)....	0
YELLOW CLEAR BEFORE PRE (0= DEFAULT).0.0*	0.0*
RED CLEAR BEFORE PRE (0= DEFAULT)....0.0*	0.0*
DWELL MIN TIMER (0-255 SEC) .....	7
DWELL MAX TIMER (0=OFF,1-255MIN) ....	0
DWELL HOLD-OVER TIMER (0-255) .....	0
LATCH CALL? .....	N
LINK TO NEXT PREEMPT? .....	N
ENABLE BACKUP PROTECTION? .....	N
HOLD CLEAR 1 PHASES DURING DELAY? ..	N
FAST GREEN FLASH DWELL PHASES? .....	N
PED CLEARANCE THROUGH YELLOW? .....	N
INHIBIT OVERLAP GREEN EXTENSION? ..	N
SERVICE DURING SOFTWARE FLASH? .....	N
REST IN RED DURING DWELL INTERVAL? ..	N
FLASH DWELL INTERVAL? .....	N
ALLOW PDS IN DWELL INTERVAL? .....	N
RE-TIME DWELL INTERVAL? .....	N
OVERLAPS: .....	ABCDEFGHIJKLMNPO
DWELL INT FLASH YELLOW	
OMIT OVERLAPS:	

PRESS 'NEXT'

PREEMPTION #5	SETTINGS (NEXT:1-10)
INTERVAL/TIMING	CLEAR/DWELL PHASES
GRN YEL RED	12345678910111213141516
1 255 3.8 2.4	X X
2 0 0.0 0.0	
3 0 0.0 0.0	
4 0 0.0 0.0	
5 0 0.0 0.0	

EXIT CALLS	OPTIONS
PRIORITY (Y/N TO SELECT) .....	MED
DELAY TIMER (0-255 SEC) .....	0
MIN GREEN BEFORE PRE (0= DEFAULT)....	1
PED CLEAR BEFORE PRE (0= DEFAULT)....	0
YELLOW CLEAR BEFORE PRE (0= DEFAULT).0.0*	0.0*
RED CLEAR BEFORE PRE (0= DEFAULT)....0.0*	0.0*
DWELL MIN TIMER (0-255 SEC) .....	7
DWELL MAX TIMER (0=OFF,1-255MIN) ....	0
DWELL HOLD-OVER TIMER (0-255) .....	0
LATCH CALL? .....	N
LINK TO NEXT PREEMPT? .....	N
ENABLE BACKUP PROTECTION? .....	N
HOLD CLEAR 1 PHASES DURING DELAY? ..	N
FAST GREEN FLASH DWELL PHASES? .....	N
PED CLEARANCE THROUGH YELLOW? .....	N
INHIBIT OVERLAP GREEN EXTENSION? ..	N
SERVICE DURING SOFTWARE FLASH? .....	N
REST IN RED DURING DWELL INTERVAL? ..	N
FLASH DWELL INTERVAL? .....	N
ALLOW PDS IN DWELL INTERVAL? .....	N
RE-TIME DWELL INTERVAL? .....	N
OVERLAPS: .....	ABCDEFGHIJKLMNPO
DWELL INT FLASH YELLOW	
OMIT OVERLAPS:	

PRESS 'NEXT'

PREEMPTION #6	SETTINGS (NEXT:1-10)
INTERVAL/TIMING	CLEAR/DWELL PHASES
GRN YEL RED	12345678910111213141516
1 255 3.8 2.4	X X
2 0 0.0 0.0	
3 0 0.0 0.0	
4 0 0.0 0.0	
5 0 0.0 0.0	

EXIT CALLS	OPTIONS
PRIORITY (Y/N TO SELECT) .....	MED
DELAY TIMER (0-255 SEC) .....	0
MIN GREEN BEFORE PRE (0= DEFAULT)....	1
PED CLEAR BEFORE PRE (0= DEFAULT)....	0
YELLOW CLEAR BEFORE PRE (0= DEFAULT).0.0*	0.0*
RED CLEAR BEFORE PRE (0= DEFAULT)....0.0*	0.0*
DWELL MIN TIMER (0-255 SEC) .....	7
DWELL MAX TIMER (0=OFF,1-255MIN) ....	0
DWELL HOLD-OVER TIMER (0-255) .....	0
LATCH CALL? .....	N
LINK TO NEXT PREEMPT? .....	N
ENABLE BACKUP PROTECTION? .....	N
HOLD CLEAR 1 PHASES DURING DELAY? ..	N
FAST GREEN FLASH DWELL PHASES? .....	N
PED CLEARANCE THROUGH YELLOW? .....	N
INHIBIT OVERLAP GREEN EXTENSION? ..	N
SERVICE DURING SOFTWARE FLASH? .....	N
REST IN RED DURING DWELL INTERVAL? ..	N
FLASH DWELL INTERVAL? .....	N
ALLOW PDS IN DWELL INTERVAL? .....	N
RE-TIME DWELL INTERVAL? .....	N
OVERLAPS: .....	ABCDEFGHIJKLMNPO
DWELL INT FLASH YELLOW	
OMIT OVERLAPS:	

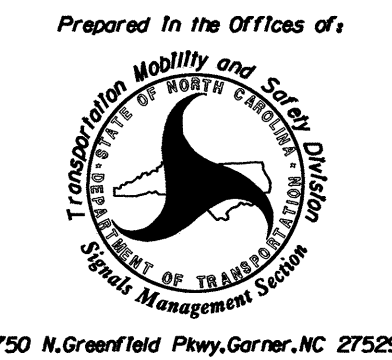
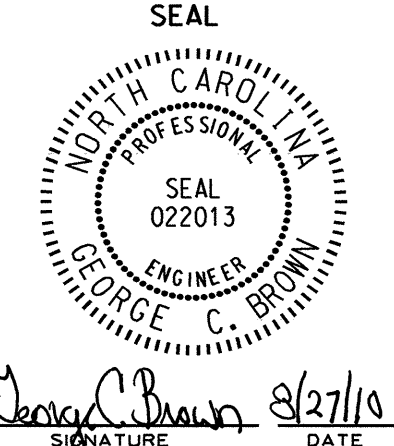
PROGRAMMING COMPLETE

\* TIME DEFAULTS TO TIME USED BY PHASE DURING NORMAL OPERATION

**NOTE!**  
PROGRAM EXTEND TIME ON ALL 'OPTICOM' DETECTOR UNITS FOR 2 SEC.

THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 01-0229T3  
DESIGNED: March 2010  
SEALED: 08/19/10  
REVISED:

ELECTRICAL DETAIL - TEMP 3 - SHEET 3 OF 3

	<b>US 158</b> at <b>NC 343</b>		
	Division 1 Camden County Camden		
Prepared In the Offices of: C. Strickland	PLAN DATE: July 2010 REVIEWED BY: T. Inge		
REVISIONS	INIT. DATE		
SIGNATURE: <i>George C. Brown</i>		DATE: 8/27/10	
SIG. INVENTORY NO. 01-0229T3			

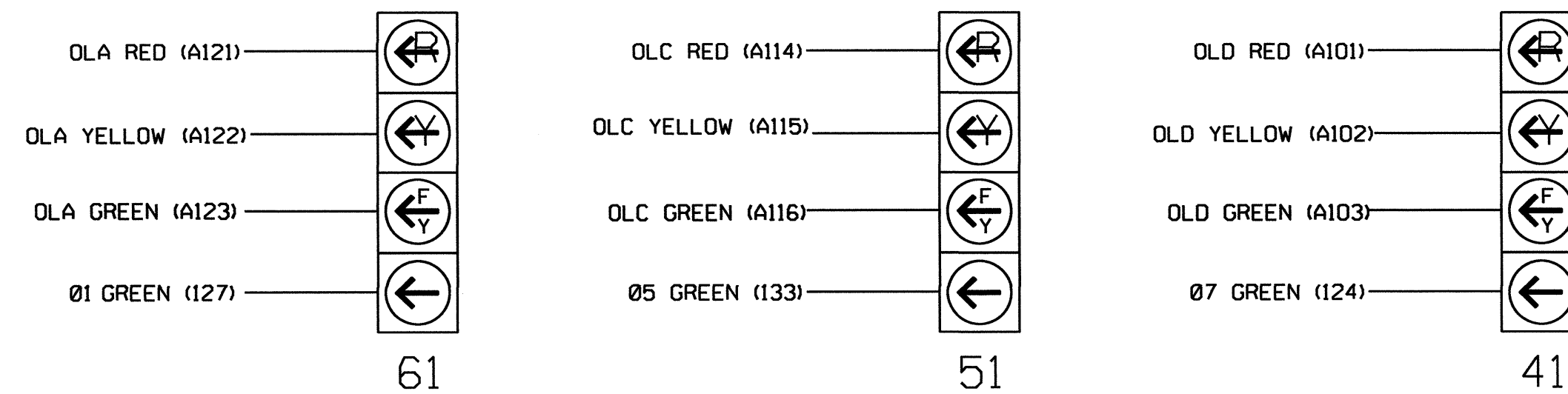






### 4 SECTION FYA PPLT SIGNAL WIRING DETAIL

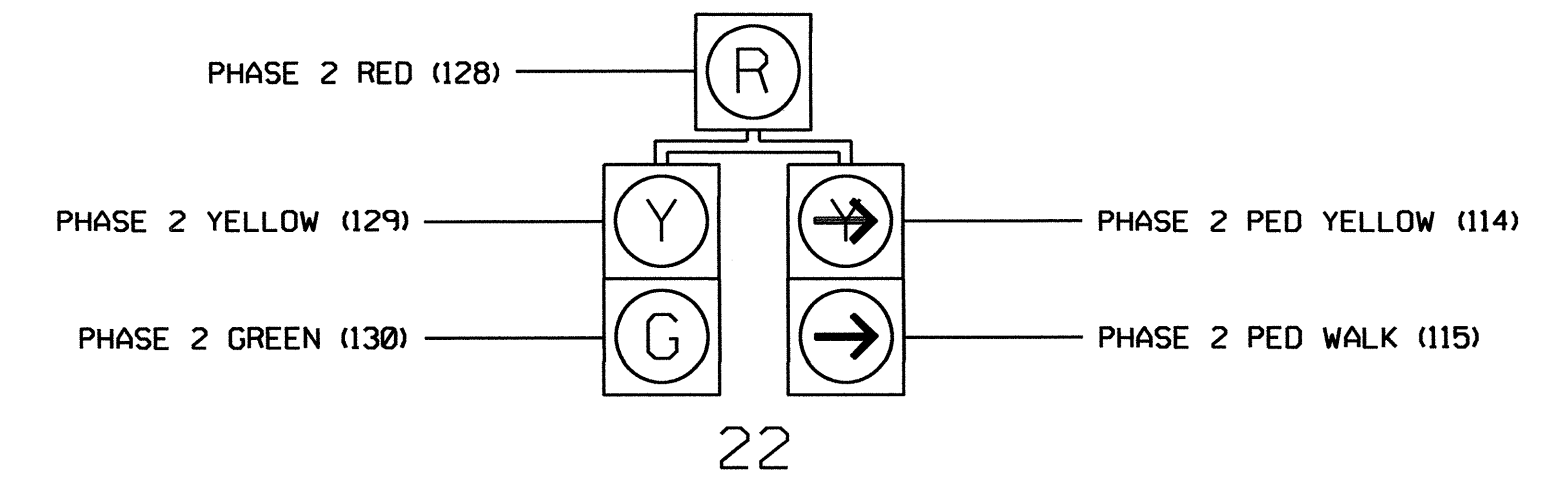
(wire signal heads as shown)



**NOTE**

1. The sequence display for these signals require special logic programming. See sheet 4 of 5 for programming instructions.

### OVERLAP 'E' (SIGNAL HEAD 22) WIRING DETAIL



**NOTE:**

1. See 'Overlap E (Signal Head 22) Wiring & Programming Instructions' notes on this sheet.

### OVERLAP 'E' (SIGNAL HEAD 22) WIRING & PROGRAMMING INSTRUCTIONS

1. Install load switch in slot 'S2P'. (See 'Signal Head Hook-up Chart' on sheet 1).
2. If present, remove existing factory-installed load resistor from Ped Yellow field terminal 114.
3. Ensure installation of load resistor on 2 Ped Don't Walk field terminal 113. (See 'Load Resistor Installation Detail' sheet 1 of 5).
4. Add a jumper wire from rear of the card edge connector of the conflict monitor from pin 8 (channel 13 yellow) to signal head field terminal 114 (S2P yellow).  
(channel 13 yellow) pin 8 ----- field terminal 114 (S2P yellow)
5. Ensure Output Assignments for Overlap 'E' are programmed as shown in details sheet 3 of 5.
6. Ensure Overlap 'E' is programmed as shown on Overlap programming Detail sheet 4 of 5.
7. Ensure PRE6 (EVP 6) is programmed to omit Overlap 'E'. (See 'Emergency Vehicle Preemption Programming Detail' sheet 5 of 5).

### FLASHER CIRCUIT MODIFICATION DETAIL

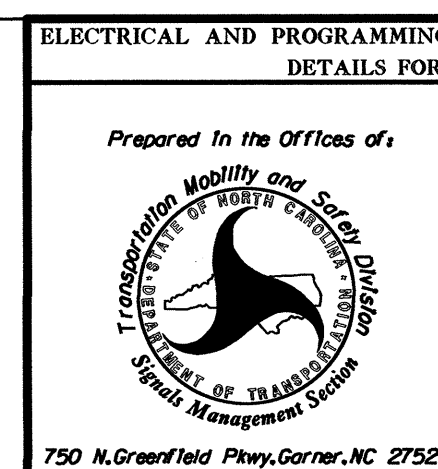
IN ORDER TO INSURE THAT SIGNALS FLASH CONCURRENTLY ON THE SAME APPROACH, MAKE THE FOLLOWING FLASHER CIRCUIT CHANGES:

1. ON REAR OF PDA - REMOVE WIRE FROM TERM. T2-4 AND TERMINATE ON T2-2.
2. ON REAR OF PDA - REMOVE WIRE FROM TERM. T2-5 AND TERMINATE ON T2-3.
3. REMOVE FLASHER UNIT 2.

THE CHANGES LISTED ABOVE TIES ALL PHASES AND OVERLAPS TO FLASHER UNIT 1.

ELECTRICAL DETAIL - FINAL - SHEET 2 OF 5

THIS ELECTRICAL DETAIL IS FOR  
THE SIGNAL DESIGN: 01-0229  
DESIGNED: March 2010  
SEALED: 08/26/10  
REVISED:



US 158 at NC 343	
Division 1	Camden County
PLAN DATE: July 2010	REVIEWED BY: T. J. J.
PREPARED BY: C. Strickland	REVIEWED BY:
REVISIONS	INIT. DATE

SEAL  
NORTH CAROLINA  
PROFESSIONAL  
ENGINEER  
SEAL  
022013  
GEORGE C. BROWN  
SIGNATURE  
DATE  
8/30/10

SIG. INVENTORY NO. 01-0229

## "2 PED" to OVERLAP "E" OUTPUT ASSIGNMENT PROGRAMMING DETAIL *(program controller as shown below)*

- FROM MAIN MENU PRESS '6' (OUTPUTS), THEN '1' (OUTPUT ASSIGNMENTS).
- WITH CURSOR IN "OUTPUT ASSIGNMENT #" FIELD, USE + KEY TO FIND THE OUTPUT ASSIGNMENT NUMBER 9, AS SHOWN BELOW.
- PROGRAM CONTROLLER AS SHOWN:

```

PAGE:1 C1 PIN:10 PEDESTRIAN PHASE
OUTPUT ASSIGNMENT #.....9
FREQUENCY (0=DEFAULT) (0-25.5 HZ)...0.0
DUTY CYCLE (0=DEFAULT) (0 - 100%)...0
MODE (0=SOLID, 1=FLASH)...0
SELECT ASSIGNMENT:
NOT ENABLED.....
VEHICLE PHASE.....
PEDESTRIAN PHASE.....Y
VEHICLE OVERLAP.....Y
PEDESTRIAN OVERLAP.....
WATCHDOG.....
DETECTOR RESET.....
ADVANCE BEACON.....
OUT OF PHASE FLASHER.....
CONTROLLER FLASH.....
RUN FREE.....
RESERVED.....
PREEMPT.....
SOFT PREEMPT.....
ANY PREEMPT.....
COORDINATION PLAN.....
OFFSET.....
PHASE CHECK.....
PHASE ON.....
PHASE NEXT.....
    
```

SCROLL DOWN TO VIEW ALL DATA

EXISTING DEFAULT ENTRY

```

PAGE:1 C1 PIN:10 PEDESTRIAN PHASE
SELECT VEHICLE OVERLAP (A=1, P=16)...5
SELECT COLOR (0=RED,1=YEL,2=GRN)...0
    
```

WHEN A "Y" IS ENTERED FOR "VEHICLE OVERLAP" THE SCREEN SHOWN ABOVE WILL APPEAR. ENTER DATA AS SHOWN. PRESS ENTER AFTER ENTERING DATA, THEN ESC.

DISPLAY WILL NOW SHOW THE SPECIFIED OUTPUT ASSIGNED AS "VEHICLE OVERLAP" AS SHOWN BELOW:

```

PAGE:1 C1 PIN:10 VEHICLE OVERLAP
OUTPUT ASSIGNMENT #.....9
FREQUENCY (0=DEFAULT) (0-25.5 HZ)...0.0
DUTY CYCLE (0=DEFAULT) (0 - 100%)...0
MODE (0=SOLID, 1=FLASH)...0
SELECT ASSIGNMENT:
NOT ENABLED.....
VEHICLE PHASE.....
PEDESTRIAN PHASE.....
VEHICLE OVERLAP.....Y
PEDESTRIAN OVERLAP.....
WATCHDOG.....
DETECTOR RESET.....
ADVANCE BEACON.....
OUT OF PHASE FLASHER.....
CONTROLLER FLASH.....
RUN FREE.....
RESERVED.....
PREEMPT.....
SOFT PREEMPT.....
ANY PREEMPT.....
COORDINATION PLAN.....
OFFSET.....
PHASE CHECK.....
PHASE ON.....
PHASE NEXT.....
    
```

VEHICLE OVERLAP E (RED) LOAD SWITCH S2P

- FROM MAIN MENU PRESS '6' (OUTPUTS), THEN '1' (OUTPUT ASSIGNMENTS).
- WITH CURSOR IN "OUTPUT ASSIGNMENT #" FIELD, USE + KEY TO FIND THE OUTPUT ASSIGNMENT NUMBER 10, AS SHOWN BELOW.
- PROGRAM CONTROLLER AS SHOWN:

```

PAGE:1 C1 PIN:11 PEDESTRIAN PHASE
OUTPUT ASSIGNMENT #.....10
FREQUENCY (0=DEFAULT) (0-25.5 HZ)...0.0
DUTY CYCLE (0=DEFAULT) (0 - 100%)...0
MODE (0=SOLID, 1=FLASH)...0
SELECT ASSIGNMENT:
NOT ENABLED.....
VEHICLE PHASE.....
PEDESTRIAN PHASE.....Y
VEHICLE OVERLAP.....Y
PEDESTRIAN OVERLAP.....
WATCHDOG.....
DETECTOR RESET.....
ADVANCE BEACON.....
OUT OF PHASE FLASHER.....
CONTROLLER FLASH.....
RUN FREE.....
RESERVED.....
PREEMPT.....
SOFT PREEMPT.....
ANY PREEMPT.....
COORDINATION PLAN.....
OFFSET.....
PHASE CHECK.....
PHASE ON.....
PHASE NEXT.....
    
```

SCROLL DOWN TO VIEW ALL DATA

EXISTING DEFAULT ENTRY

```

PAGE:1 C1 PIN:11 PEDESTRIAN PHASE
SELECT VEHICLE OVERLAP (A=1, P=16)...5
SELECT COLOR (0=RED,1=YEL,2=GRN)...2
    
```

WHEN A "Y" IS ENTERED FOR "VEHICLE OVERLAP" THE SCREEN SHOWN ABOVE WILL APPEAR. ENTER DATA AS SHOWN. PRESS ENTER AFTER ENTERING DATA, THEN ESC.

DISPLAY WILL NOW SHOW THE SPECIFIED OUTPUT ASSIGNED AS "VEHICLE OVERLAP" AS SHOWN BELOW:

```

PAGE:1 C1 PIN:11 VEHICLE OVERLAP
OUTPUT ASSIGNMENT #.....10
FREQUENCY (0=DEFAULT) (0-25.5 HZ)...0.0
DUTY CYCLE (0=DEFAULT) (0 - 100%)...0
MODE (0=SOLID, 1=FLASH)...0
SELECT ASSIGNMENT:
NOT ENABLED.....
VEHICLE PHASE.....
PEDESTRIAN PHASE.....
VEHICLE OVERLAP.....Y
PEDESTRIAN OVERLAP.....
WATCHDOG.....
DETECTOR RESET.....
ADVANCE BEACON.....
OUT OF PHASE FLASHER.....
CONTROLLER FLASH.....
RUN FREE.....
RESERVED.....
PREEMPT.....
SOFT PREEMPT.....
ANY PREEMPT.....
COORDINATION PLAN.....
OFFSET.....
PHASE CHECK.....
PHASE ON.....
PHASE NEXT.....
    
```

VEHICLE OVERLAP E (GREEN) LOAD SWITCH S2P

- FROM MAIN MENU PRESS '6' (OUTPUTS), THEN '1' (OUTPUT ASSIGNMENTS).
- WITH CURSOR IN "OUTPUT ASSIGNMENT #" FIELD, USE + KEY TO FIND THE OUTPUT ASSIGNMENT NUMBER 33, AS SHOWN BELOW.
- PROGRAM CONTROLLER AS SHOWN:

```

PAGE:1 C1 PIN:35 NOT ENABLED
OUTPUT ASSIGNMENT #.....33
FREQUENCY (0=DEFAULT) (0-25.5 HZ)...0.0
DUTY CYCLE (0=DEFAULT) (0 - 100%)...0
MODE (0=SOLID, 1=FLASH)...0
SELECT ASSIGNMENT:
NOT ENABLED.....Y
VEHICLE PHASE.....
PEDESTRIAN PHASE.....
VEHICLE OVERLAP.....Y
PEDESTRIAN OVERLAP.....
WATCHDOG.....
DETECTOR RESET.....
ADVANCE BEACON.....
OUT OF PHASE FLASHER.....
CONTROLLER FLASH.....
RUN FREE.....
RESERVED.....
PREEMPT.....
SOFT PREEMPT.....
ANY PREEMPT.....
COORDINATION PLAN.....
OFFSET.....
PHASE CHECK.....
PHASE ON.....
PHASE NEXT.....
    
```

SCROLL DOWN TO VIEW ALL DATA

EXISTING DEFAULT ENTRY

```

PAGE:1 C1 PIN:35 NOT ENABLED
SELECT VEHICLE OVERLAP (A=1, P=16)...5
SELECT COLOR (0=RED,1=YEL,2=GRN)...1
    
```

WHEN A "Y" IS ENTERED FOR "VEHICLE OVERLAP" THE SCREEN SHOWN ABOVE WILL APPEAR. ENTER DATA AS SHOWN. PRESS ENTER AFTER ENTERING DATA, THEN ESC.

DISPLAY WILL NOW SHOW THE SPECIFIED OUTPUT ASSIGNED AS "VEHICLE OVERLAP" AS SHOWN BELOW:

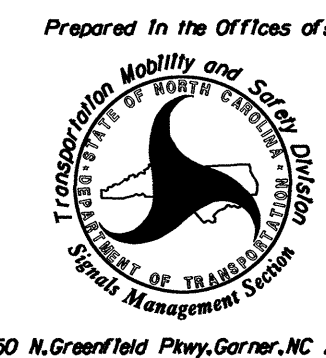

```

PAGE:1 C1 PIN:35 VEHICLE OVERLAP
OUTPUT ASSIGNMENT #.....33
FREQUENCY (0=DEFAULT) (0-25.5 HZ)...0.0
DUTY CYCLE (0=DEFAULT) (0 - 100%)...0
MODE (0=SOLID, 1=FLASH)...0
SELECT ASSIGNMENT:
NOT ENABLED.....
VEHICLE PHASE.....
PEDESTRIAN PHASE.....
VEHICLE OVERLAP.....Y
PEDESTRIAN OVERLAP.....
WATCHDOG.....
DETECTOR RESET.....
ADVANCE BEACON.....
OUT OF PHASE FLASHER.....
CONTROLLER FLASH.....
RUN FREE.....
RESERVED.....
PREEMPT.....
SOFT PREEMPT.....
ANY PREEMPT.....
COORDINATION PLAN.....
OFFSET.....
PHASE CHECK.....
PHASE ON.....
PHASE NEXT.....
    
```

VEHICLE OVERLAP E (YELLOW) LOAD SWITCH S2P

THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 01-0229  
DESIGNED: March 2010  
SEALED: 08/26/10  
REVISED:

ELECTRICAL DETAIL - FINAL - SHEET 3 OF 5

 <p>750 N. Greenfield Pkwy, Garner, NC 27529</p>	Prepared In the Offices of US 158 at NC 343		
	Division 1 PLAN DATE: March 2010 PREPARED BY: C. Strickland	Camden County REVIEWED BY: T. Lopez REVIEWED BY:	

George C. Brown 8/26/10  
SIGNATURE DATE  
SIG. INVENTORY NO. 01-0229

27-AUG-2010 09:11:18 S:\gms\work\cnc\cupa\sig\Man\517\ch\cmd\010229\_sml.e (e-xxxx.dgn)

## LOGICAL I/O PROCESSOR PROGRAMMING DETAIL TO PRODUCE SPECIAL FYA-PPLT SIGNAL SEQUENCE

(program controller as shown below)

1. FROM MAIN MENU PRESS '2' (PHASE CONTROL), THEN '1' (PHASE CONTROL FUNCTIONS), SCROLL TO THE BOTTOM OF THE MENU AND ENABLE ACT LOGIC COMMANDS 1, 2, 3, 4, 5, 6, 7, 8 AND 9.
2. FROM MAIN MENU PRESS '6' (OUTPUTS), THEN '3' (LOGICAL I/O PROCESSOR).

LOGICAL I/O COMMAND #1 (+/-COMMAND#)  
IF ACTIVE PHASE #1 IS ON  
AND RED CLEAR ON PHASE #1 IS ON

↓  
SCROLL DOWN

THEN:  
SET OUTPUT ASSIGNMENT #50 ON  
SET OUTPUT ASSIGNMENT #51 OFF

PRESS '+'

NOTE: LOGIC FOR PHASE 1 RED CLEAR WHEN TRANSITIONING FROM PHASE 1 (EV PRE 4) TO PHASE 2 (HEAD 61).

LOGICAL I/O COMMAND #2 (+/-COMMAND#)  
IF ACTIVE PHASE #1 IS ON

↓  
SCROLL DOWN

THEN:  
SET OUTPUT ASSIGNMENT #52 OFF

PRESS '+'

NOTE: LOGIC FOR SWITCHING FLASHING YELLOW ARROW "OFF" DURING PHASE 1 (HEAD 61).

LOGICAL I/O COMMAND #3 (+/-COMMAND#)  
IF YELLOW ON PHASE #1 IS ON

↓  
SCROLL DOWN

THEN:  
SET OUTPUT ASSIGNMENT #51 ON

PRESS '+'

NOTE: LOGIC FOR YELLOW ARROW CLEARANCE FROM PHASE 1 (HEAD 61).

LOGICAL I/O COMMAND #4 (+/-COMMAND#)  
IF ACTIVE PHASE #5 IS ON  
AND RED CLEAR ON PHASE #5 IS ON

↓  
SCROLL DOWN

THEN:  
SET OUTPUT ASSIGNMENT #42 ON  
SET OUTPUT ASSIGNMENT #43 OFF

PRESS '+'

NOTE: LOGIC FOR PHASE 5 RED CLEAR WHEN TRANSITIONING FROM PHASE 5 TO PHASE 6 (HEAD 51).

LOGICAL I/O COMMAND #5 (+/-COMMAND#)  
IF ACTIVE PHASE #5 IS ON

↓  
SCROLL DOWN

THEN:  
SET OUTPUT ASSIGNMENT #44 OFF

PRESS '+'

NOTE: LOGIC FOR SWITCHING FLASHING YELLOW ARROW "OFF" DURING PHASE 5 (HEAD 51).

LOGICAL I/O COMMAND #6 (+/-COMMAND#)  
IF YELLOW ON PHASE #5 IS ON

↓  
SCROLL DOWN

THEN:  
SET OUTPUT ASSIGNMENT #43 ON

PRESS '+'

NOTE: LOGIC FOR YELLOW ARROW CLEARANCE FROM PHASE 5 (HEAD 51).

LOGICAL I/O COMMAND #7 (+/-COMMAND#)  
IF ACTIVE PHASE #7 IS ON  
AND RED CLEAR ON PHASE #7 IS ON

↓  
SCROLL DOWN

THEN:  
SET OUTPUT ASSIGNMENT #39 ON  
SET OUTPUT ASSIGNMENT #40 OFF

PRESS '+'

NOTE: LOGIC FOR PHASE 7 RED CLEAR WHEN TRANSITIONING FROM PHASE 7 (EV PRE 5) TO PHASE 8 (HEAD 41).

LOGICAL I/O COMMAND #8 (+/-COMMAND#)  
IF ACTIVE PHASE #7 IS ON

↓  
SCROLL DOWN

THEN:  
SET OUTPUT ASSIGNMENT #41 OFF

PRESS '+'

NOTE: LOGIC FOR SWITCHING FLASHING YELLOW ARROW "OFF" DURING PHASE 7 (HEAD 41).

LOGICAL I/O COMMAND #9 (+/-COMMAND#)  
IF YELLOW ON PHASE #7 IS ON

↓  
SCROLL DOWN

THEN:  
SET OUTPUT ASSIGNMENT #40 ON

PRESS '+'

NOTE: LOGIC FOR YELLOW ARROW CLEARANCE FROM PHASE 7 (HEAD 41).

LOGIC I/O PROCESSOR PROGRAMMING COMPLETE

### OUTPUT REFERENCE SCHEDULE

USE TO INTERPRET LOGIC PROCESSOR

OUTPUT 39 = Overlap D Red  
OUTPUT 40 = Overlap D Yellow  
OUTPUT 41 = Overlap D Green  
OUTPUT 42 = Overlap C Red  
OUTPUT 43 = Overlap C Yellow  
OUTPUT 44 = Overlap C Green  
OUTPUT 50 = Overlap A Red  
OUTPUT 51 = Overlap A Yellow  
OUTPUT 52 = Overlap A Green

## OVERLAP PROGRAMMING DETAIL

(program controller as shown below)

- FROM MAIN MENU PRESS '8' (OVERLAPS), THEN '1' (VEHICLE OVERLAP SETTINGS).

PAGE 1: VEHICLE OVERLAP 'A' SETTINGS  
PHASE: 12345678910111213141516  
VEH OVL PARENTS: XX X  
VEH OVL NOT VEH:  
VEH OVL NOT PED:  
VEH OVL GRN EXT:  
STARTUP COLOR: - RED - YELLOW - GREEN  
FLASH COLORS: - RED - YELLOW X GREEN  
SELECT VEHICLE OVERLAP OPTIONS: (Y/N)  
FLASH YELLOW IN CONTROLLER FLASH?...Y  
GREEN EXTENSION (0-255 SEC)...0  
YELLOW CLEAR (0=PARENT,3-25.5 SEC)...0.0  
RED CLEAR (0=PARENT,0.1-25.5 SEC)...0.0  
OUTPUT AS PHASE # (0=NONE, 1-16)...0

← NOTICE GREEN FLASH

PRESS '+'

PAGE 1: VEHICLE OVERLAP 'B' SETTINGS  
PHASE: 12345678910111213141516  
VEH OVL PARENTS: X  
VEH OVL NOT VEH:  
VEH OVL NOT PED:  
VEH OVL GRN EXT:  
STARTUP COLOR: - RED - YELLOW - GREEN  
FLASH COLORS: - RED - YELLOW - GREEN  
SELECT VEHICLE OVERLAP OPTIONS: (Y/N)  
FLASH YELLOW IN CONTROLLER FLASH?...N  
GREEN EXTENSION (0-255 SEC)...0  
YELLOW CLEAR (0=PARENT,3-25.5 SEC)...0.0  
RED CLEAR (0=PARENT,0.1-25.5 SEC)...0.0  
OUTPUT AS PHASE # (0=NONE, 1-16)...0

PRESS '+'

PAGE 1: VEHICLE OVERLAP 'C' SETTINGS  
PHASE: 12345678910111213141516  
VEH OVL PARENTS: XX  
VEH OVL NOT VEH:  
VEH OVL NOT PED:  
VEH OVL GRN EXT:  
STARTUP COLOR: - RED - YELLOW - GREEN  
FLASH COLORS: - RED - YELLOW X GREEN  
SELECT VEHICLE OVERLAP OPTIONS: (Y/N)  
FLASH YELLOW IN CONTROLLER FLASH?...Y  
GREEN EXTENSION (0-255 SEC)...0  
YELLOW CLEAR (0=PARENT,3-25.5 SEC)...0.0  
RED CLEAR (0=PARENT,0.1-25.5 SEC)...0.0  
OUTPUT AS PHASE # (0=NONE, 1-16)...0

← NOTICE GREEN FLASH

PRESS '+'

PAGE 1: VEHICLE OVERLAP 'D' SETTINGS  
PHASE: 12345678910111213141516  
VEH OVL PARENTS: X XX  
VEH OVL NOT VEH:  
VEH OVL NOT PED:  
VEH OVL GRN EXT:  
STARTUP COLOR: - RED - YELLOW - GREEN  
FLASH COLORS: - RED - YELLOW X GREEN  
SELECT VEHICLE OVERLAP OPTIONS: (Y/N)  
FLASH YELLOW IN CONTROLLER FLASH?...N  
GREEN EXTENSION (0-255 SEC)...0  
YELLOW CLEAR (0=PARENT,3-25.5 SEC)...0.0  
RED CLEAR (0=PARENT,0.1-25.5 SEC)...0.0  
OUTPUT AS PHASE # (0=NONE, 1-16)...0

← NOTICE GREEN FLASH

PRESS '+'

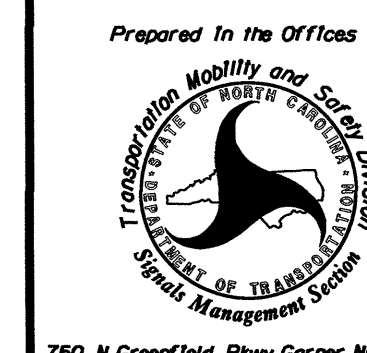
PAGE 1: VEHICLE OVERLAP 'E' SETTINGS  
PHASE: 12345678910111213141516  
VEH OVL PARENTS: X  
VEH OVL NOT VEH:  
VEH OVL NOT PED:  
VEH OVL GRN EXT:  
STARTUP COLOR: - RED - YELLOW - GREEN  
FLASH COLORS: - RED - YELLOW - GREEN  
SELECT VEHICLE OVERLAP OPTIONS: (Y/N)  
FLASH YELLOW IN CONTROLLER FLASH?...N  
GREEN EXTENSION (0-255 SEC)...0  
YELLOW CLEAR (0=PARENT,3-25.5 SEC)...0.0  
RED CLEAR (0=PARENT,0.1-25.5 SEC)...0.0  
OUTPUT AS PHASE # (0=NONE, 1-16)...0

OVERLAP PROGRAMMING COMPLETE

ELECTRICAL DETAIL - FINAL - SHEET 4 OF 5

THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 01-0229  
DESIGNED: March 2010  
SEALED: 08/26/10  
REVISED:

ELECTRICAL AND PROGRAMMING DETAILS FOR:



750 N. Greenfield Pkwy, Garner, NC 27529

US 158 at NC 343	
Division 1	Camden County
Camden	Camden
PLANNED BY: C. Strickland	REVIEWED BY: T. J. J.
PREPARED BY: C. Strickland	REVIEWED BY:
REVISIONS	INIT. DATE

SEAL  
NORTH CAROLINA  
PROFESSIONAL ENGINEER  
SEAL 022013  
GEORGE C. BROWN  
SIGNATURE  
DATE

SIG. INVENTORY NO. 01-0229







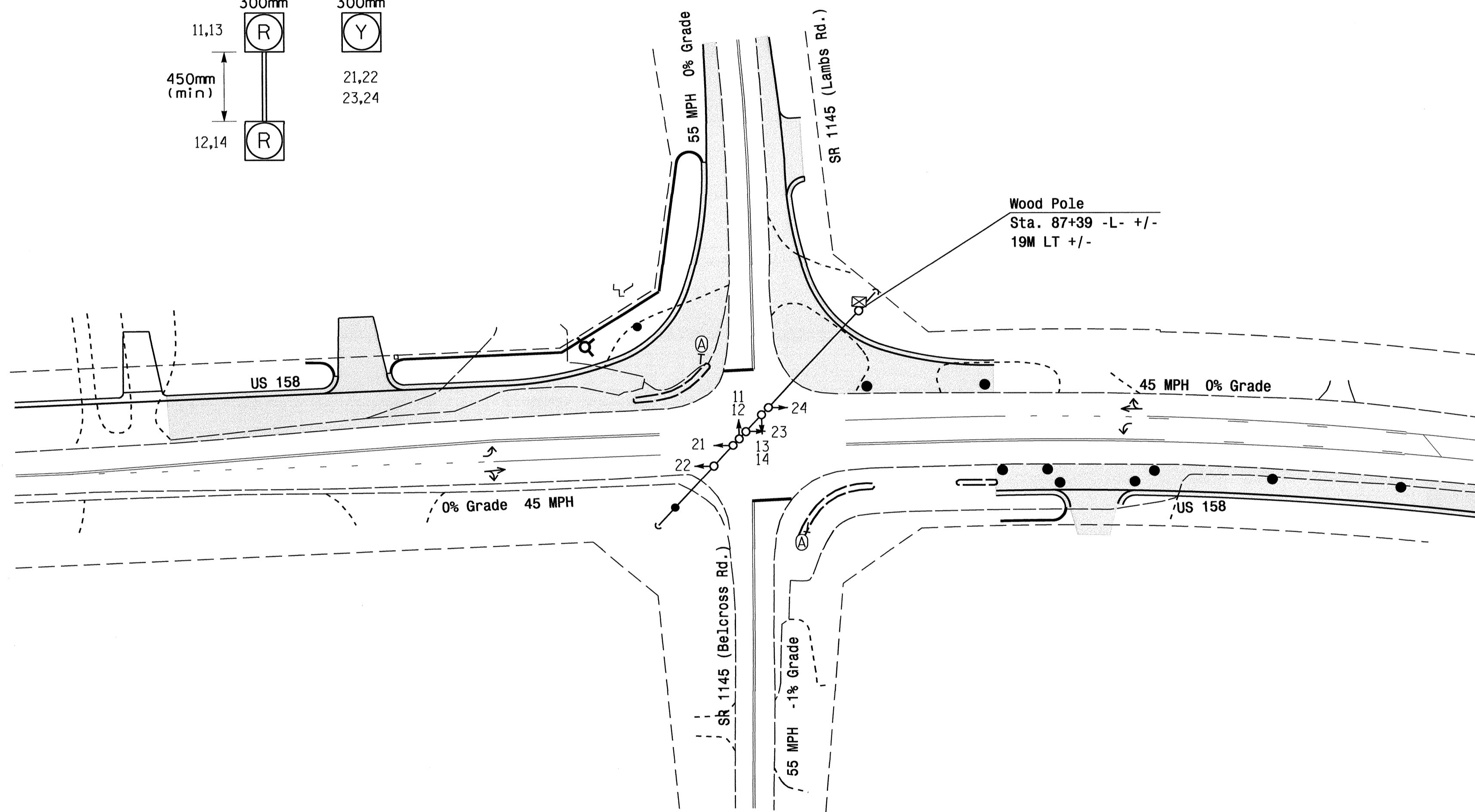
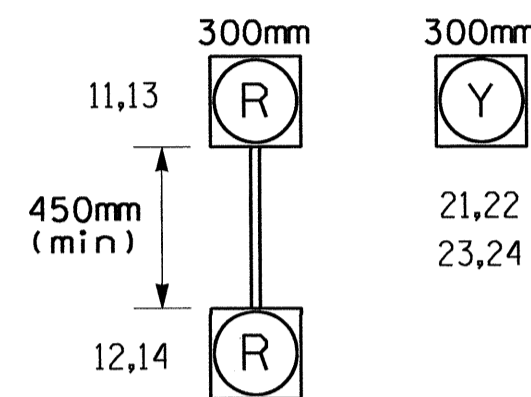


**TABLE OF OPERATION**

SIGNAL FACE	INTERVAL	
	1	2
11,13	ON	OFF
12,14	OFF	ON
21,22	ON	OFF
23,24	OFF	ON

**SIGNAL FACE I.D.**

All Heads L.E.D.



Flasher

**NOTES**

1. Refer to "Roadway Standard Drawings NCDOT" dated July 2006 and "Standard Specifications for Roads and Structures" dated July 2006.
2. Flash vertically mounted heads alternately.
3. Flash horizontally mounted heads concurrently.

**LEGEND**

PROPOSED	EXISTING
	N/A
N/A	

01-SEP-2010 15:42 S:\13350\13350\13350\SIGNAL\WORKGROUPS\TIP - Projects\84-241\845\gnat\sig\01-0220T1\_1.sig.dwg, 2010mdd.dgn

**Signal Upgrade/Temp 1 Phase I**

750 N. Grant St. Raleigh, NC 27699

**US 158 At SR 1145 (Belcross Road)/(Lambs Road)**

Division 1 Camden County Belcross

PLAN DATE: April 2010 REVIEWED BY: PLA

PREPARED BY: JPG REVIEWED BY:

SEAL

DATE

REVISIONS	INIT.	DATE

SCALE 500:1

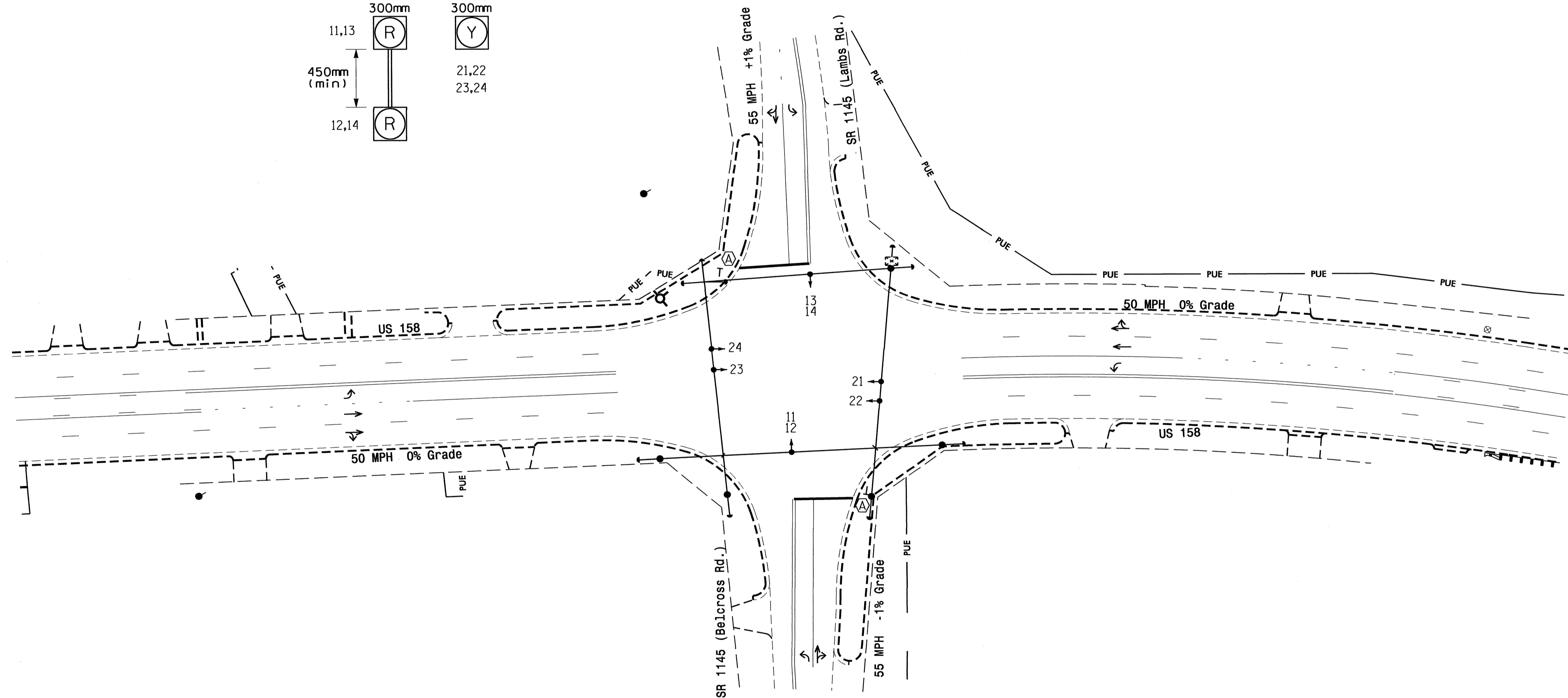
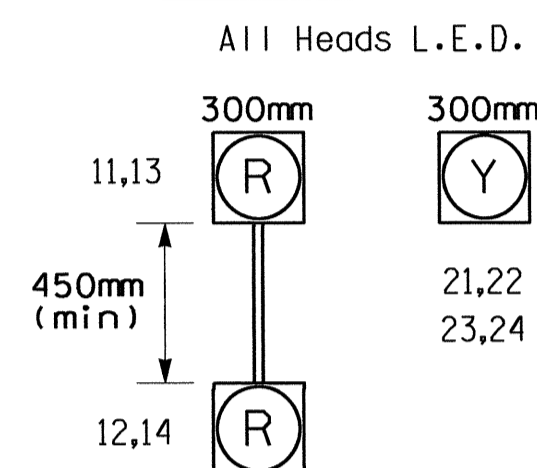
SIG. INVENTORY NO. 01-0220T1



**TABLE OF OPERATION**

SIGNAL FACE	INTERVAL	
	1	2
11,13	ON	OFF
12,14	OFF	ON
21,22	ON	OFF
23,24	OFF	ON

**SIGNAL FACE I.D.**



Flasher

**NOTES**

1. Refer to "Roadway Standard Drawings NCDOT" dated July 2006 and "Standard Specifications for Roads and Structures" dated July 2006.
2. Flash vertically mounted heads alternately.
3. Flash horizontally mounted heads concurrently.

**LEGEND**

PROPOSED	EXISTING
	N/A
N/A	

**Signal Upgrade/Final**

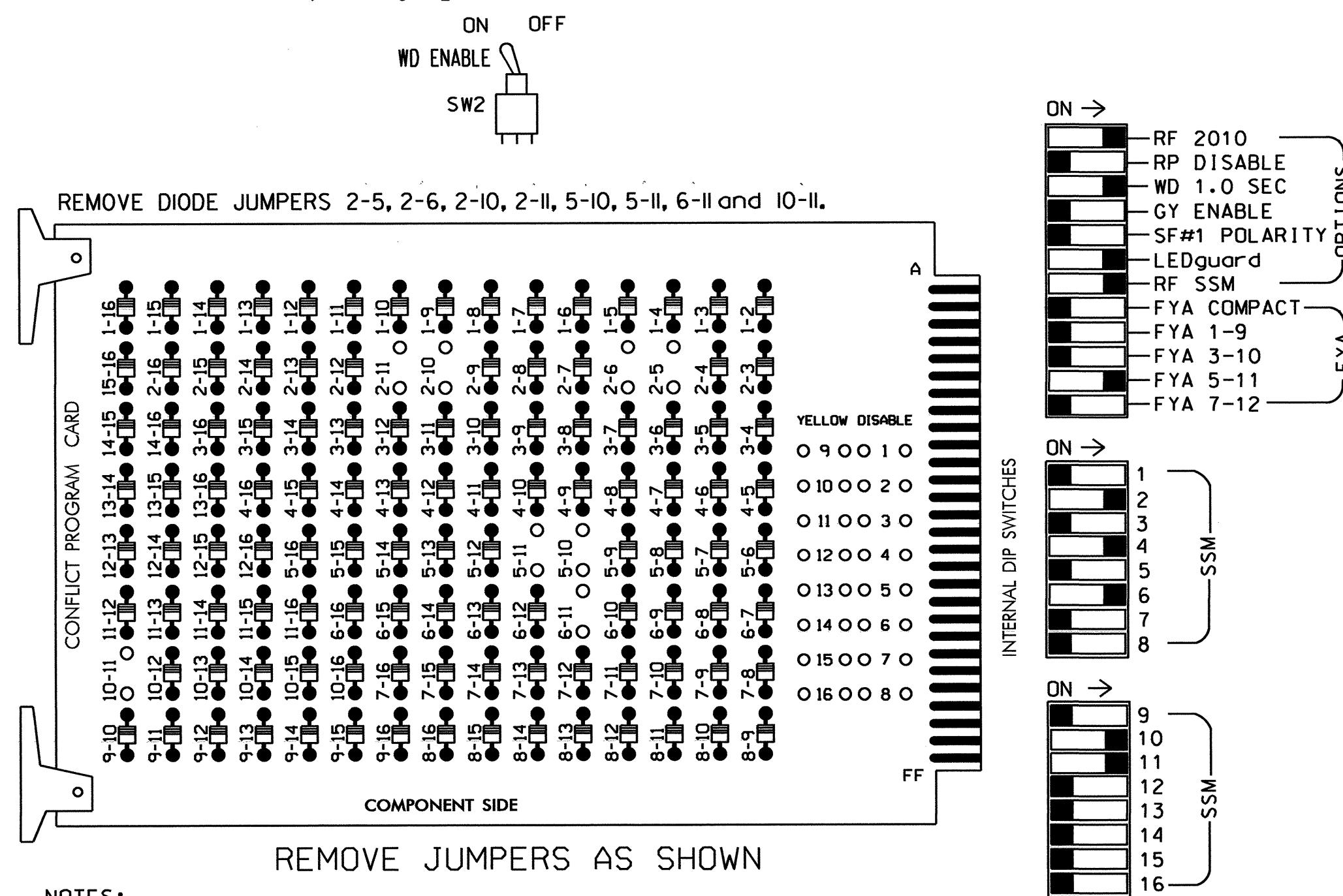
	<b>US 158</b> <b>At</b> <b>SR 1145</b> <b>(Belcross Road)/(Lambs Road)</b>		
	Division 1 Camden County Belcross		
PLAN DATE: April 2010	REVIEWED BY: PLA		
PREPARED BY: JPG	REVIEWED BY:		
SCALE: 500:1		REVISIONS	INIT. DATE
		SIGNATURE:	DATE: 9/2/10
		SIG. INVENTORY NO. 01-0220	

01-SEP-2010 15:43 S:\ITSS\SUM\ITS\_S\Signal\work\groups\TIP Projects\SR-2414B\Signal\as\bas\gms\gms1-0220.dwg - sig.dgn - 2010mod.dgn



### EDI MODEL 2010ECL-NC CONFLICT MONITOR PROGRAMMING DETAIL

(remove jumpers and set switches as shown)



**NOTES:**

- Card is provided with all diode jumpers in place. Removal of any jumper allows its channels to run concurrently.
- Make sure jumpers SEL2-SEL5 are present on the monitor board.

### NOTES

- To prevent "flash-conflict" problems, insert red flash program blocks for all unused vehicle load switches in the output file. The installer shall verify that signal heads flash in accordance with the Signal Plans.
- Ensure that Red Enable is active at all times during normal operation. To prevent Red Failures on unused monitor channels, tie unused red monitor inputs 1,3,5, 7,8,9,12,13,14,15 & 16 to load switch AC+ per the cabinet manufacturer's instructions.
- Enable Simultaneous Gap-Out for all phases.
- Program phases 2 and 6 for Variable Initial and Gap Reduction.
- Program phases 2 and 6 for Start Up In Green.
- Program phases 2 and 6 for Yellow Flash.

### EQUIPMENT INFORMATION

CONTROLLER.....2070L  
 CABINET.....332 /W/ AUX  
 SOFTWARE.....ECONOLITE OASIS  
 CABINET MOUNT.....BASE  
 OUTPUT FILE POSITIONS...18 WITH AUX. OUTPUT FILE  
 LOAD SWITCHES USED.....S2,S4,S5,S6,S10,S12  
 PHASES USED.....2,4,5,6  
 OVERLAP "A".....NOT USED  
 OVERLAP "B".....5  
 OVERLAP "C".....5+6  
 OVERLAP "D".....NOT USED

### SIGNAL HEAD HOOK-UP CHART

LOAD SWITCH NO.	S1	S2	S2P	S3	S4	S4P	S5	S6	S6P	S7	S8	S8P	S9	S10	S11	S12	S13	S14
PHASE	1	2	2 PED	3	4	4 PED	5	6	6 PED	7	8	8 PED	OLA	OLB	SPARE	OLC	OLD	SPARE
SIGNAL HEAD NO.	NU	21,22	NU	NU	41,42	NU	51★	61,62	NU	NU	NU	NU	NU	42	NU	51★	NU	NU
RED		128			101			134						*				
YELLOW		129			102		*	135										
GREEN		130			103			136										
RED ARROW																		A114
YELLOW ARROW																		A125
FLASHING YELLOW ARROW																		A115
GREEN ARROW								133						A126				A116

NU = Not Used  
 \* Denotes install load resistor. See load resistor installation detail this sheet.  
 ★ See pictorial of head wiring in detail below.

### INPUT FILE POSITION LAYOUT

(front view)

FILE	U	1	2	3	4	5	6	7	8	9	10	11	12	13	14
"I"	U	∅ 2	2A	∅ 3	∅ 4	∅ 5	∅ 6	∅ 7	∅ 8	∅ 9	∅ 10	∅ 11	∅ 12	∅ 13	FS
	L	NOT USED	NOT USED	∅ 4	∅ 4	∅ 4	∅ 4	∅ 4	∅ 4	∅ 4	∅ 4	∅ 4	∅ 4	∅ 4	DC ISOLATOR
"J"	U	∅ 5	∅ 6	∅ 7	∅ 8	∅ 9	∅ 10	∅ 11	∅ 12	∅ 13	∅ 14	PRE3	PRE4	PRE5	ST
	L	NOT USED	NOT USED	∅ 7	∅ 8	∅ 9	∅ 10	∅ 11	∅ 12	∅ 13	∅ 14	4-CHANNEL OPTICAL	NOT USED	DC ISOLATOR	

EX.: 1A, 2A, ETC. = LOOP NO.'S

FS = FLASH SENSE  
 ST = STOP TIME

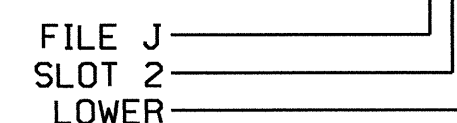
⊗ Wired Input - Do not populate slot with detector card

### INPUT FILE CONNECTION & PROGRAMMING CHART

LOOP NO.	LOOP TERMINAL	INPUT FILE POS.	PIN NO.	INPUT ASSIGNMENT NO.	DETECTOR NO.	NEMA PHASE	CALL	EXTEND	FULL TIME DELAY	STRETCH TIME	DELAY TIME
2A	TB2-5,6	I2U	39	1	2	2	Y	Y			
4A	TB4-9,10	I6U	41	3	4	4	Y	Y			3
4B	TB4-11,12	I6L	45	7	14	4	Y	Y			15
5A <sup>1</sup>	TB3-1,2	J1U	55	17	5	5	Y	Y			15
	-	I4U	47	9	22	2	Y	Y	Y		3
6A	TB3-5,6	J2U	40	2	6	6	Y	Y			

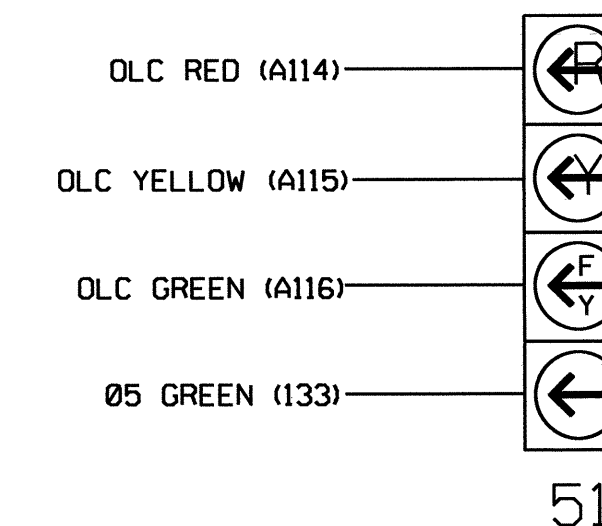
<sup>1</sup>Add jumper from J1-W to I4-W, on rear of input file.

INPUT FILE POSITION LEGEND: J2L



### 4 SECTION FYA PPLT SIGNAL WIRING DETAIL

(wire signal heads as shown)



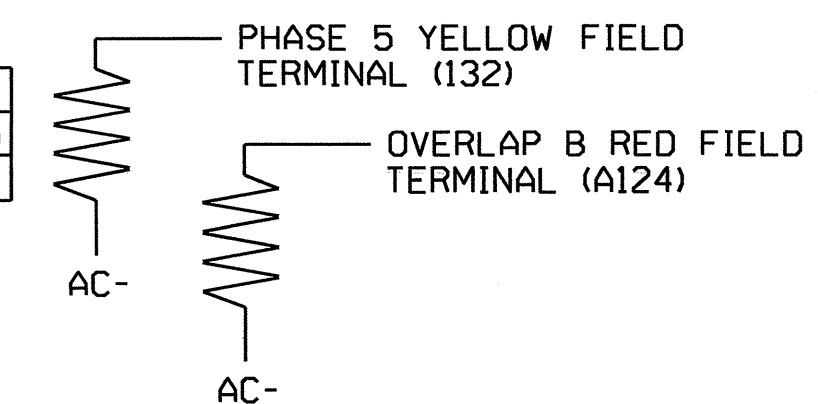
**NOTE**

- The sequence display for signal head 51 requires special logic programming. See sheet 2 of 2 for programming instructions.

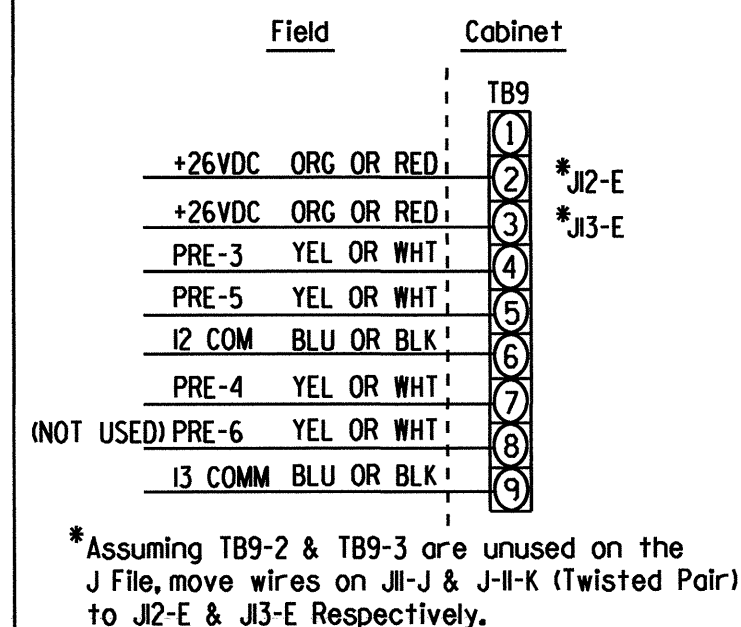
### LOAD RESISTOR INSTALLATION DETAIL

(install resistors as shown below)

VALUE (ohms)	WATTAGE
1.5K - 1.9K	25W (min)
2.0K - 3.0K	10W (min)



### OPTICOM FIELD WIRE DETAIL



\*Assuming TB9-2 & TB9-3 are unused on the J File, move wires on J1-J & J11-K (Twisted Pair) to J2-E & J3-E Respectively.

THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 01-0221T1  
 DESIGNED: March 2010  
 SEALED: 08/24/10  
 REVISED:

### ELECTRICAL DETAIL - TEMP 1 - SHEET 1 OF 2

	US 158 at NC 34		
	Division 1 PLAN DATE: August 2010 PREPARED BY: C. Strickland	Camden County Belcross REVIEWED BY: T. Vogel REVIEWED BY:	
REVISIONS INIT. DATE	REVISIONS INIT. DATE	SIGNATURE DATE	SIGNATURE DATE

**LOGICAL I/O PROCESSOR PROGRAMMING DETAIL  
TO PRODUCE SPECIAL FYA-PPLT SIGNAL SEQUENCE**

(program controller as shown below)

- FROM MAIN MENU PRESS '2' (PHASE CONTROL), THEN '1' (PHASE CONTROL FUNCTIONS), SCROLL TO THE BOTTOM OF THE MENU AND ENABLE ACT LOGIC COMMANDS 1, 2 AND 3.
- FROM MAIN MENU PRESS '6' (OUTPUTS), THEN '3' (LOGICAL I/O PROCESSOR).

LOGICAL I/O COMMAND #1 (+/-COMMAND#)  
IF ACTIVE PHASE #5 IS ON  
AND RED CLEAR ON PHASE #5 IS ON

SCROLL DOWN

THEN:  
SET OUTPUT ASSIGNMENT #42 ON  
SET OUTPUT ASSIGNMENT #43 OFF

NOTE: LOGIC FOR PHASE 5 RED CLEAR WHEN TRANSITIONING FROM PHASE 5 TO PHASE 6 (HEAD 51).

LOGICAL I/O COMMAND #2 (+/-COMMAND#)  
IF ACTIVE PHASE #5 IS ON

SCROLL DOWN

THEN:  
SET OUTPUT ASSIGNMENT #44 OFF

NOTE: LOGIC FOR SWITCHING FLASHING YELLOW ARROW "OFF" DURING PHASE 5 (HEAD 51).

LOGICAL I/O COMMAND #3 (+/-COMMAND#)  
IF YELLOW ON PHASE #5 IS ON

SCROLL DOWN

THEN:  
SET OUTPUT ASSIGNMENT #43 ON

NOTE: LOGIC FOR YELLOW ARROW CLEARANCE FROM PHASE 5 (HEAD 51).

LOGIC I/O PROCESSOR PROGRAMMING COMPLETE

**OUTPUT REFERENCE SCHEDULE**

OUTPUT 42 = Overlap C Red  
OUTPUT 43 = Overlap C Yellow  
OUTPUT 44 = Overlap C Green

**EMERGENCY VEHICLE PREEMPTION PROGRAMMING DETAIL**

(program controller as shown below)

From Main Menu press 'A' (Preemption), then '1' (Standard Preemptions). Press 'NEXT' as needed to advance to Preempts 3, 4 and 5.

PREEMPTION #3 SETTINGS (NEXT:1-10)  
INTERVAL/TIMING CLEAR/DWELL PHASES  
GRN YEL RED 12345678910111213141516  
1 255 0.0\* 0.0\* X X  
2 0 0.0 0.0  
3 0 0.0 0.0  
4 0 0.0 0.0  
5 0 0.0 0.0

EXIT CALLS  
OPTIONS  
PRIORITY (Y/N TO SELECT) .....MED  
DELAY TIMER (0-255 SEC) .....0  
MIN GREEN BEFORE PRE (0= DEFAULT)....1  
PED CLEAR BEFORE PRE (0= DEFAULT)....0  
YELLOW CLEAR BEFORE PRE (0= DEFAULT)....0\*  
RED CLEAR BEFORE PRE (0= DEFAULT)....0\*  
DWELL MIN TIMER (0-255 SEC) .....7  
DWELL MAX TIMER (0=OFF,1-255MIN) ....0  
DWELL HOLD-OVER TIMER (0-255) .....0  
LATCH CALL? .....N  
LINK TO NEXT PREEMPT? .....N  
ENABLE BACKUP PROTECTION? .....N  
HOLD CLEAR 1 PHASES DURING DELAY? ..N  
FAST GREEN FLASH DWELL PHASES? .....N  
PED CLEARANCE THROUGH YELLOW? .....N  
INHIBIT OVERLAP GREEN EXTENSION? ..N  
SERVICE DURING SOFTWARE FLASH? .....N  
REST IN RED DURING DWELL INTERVAL? ..N  
FLASH DWELL INTERVAL? .....N  
ALLOW PEDS IN DWELL INTERVAL? .....N  
RE-TIME DWELL INTERVAL? .....N  
OVERLAPS: ABCDEFGHIJKLMNPO  
DWELL INT FLASH YELLOW  
OMIT OVERLAPS: X

PRESS 'NEXT'

\* TIME DEFAULTS TO TIME USED BY PHASE DURING NORMAL OPERATION

**NOTE!**

PROGRAM EXTEND TIME ON ALL 'OPTICOM' DETECTOR UNITS FOR 2 SEC.

PREEMPTION #4 SETTINGS (NEXT:1-10)  
INTERVAL/TIMING CLEAR/DWELL PHASES  
GRN YEL RED 12345678910111213141516  
1 255 0.0\* 0.0\* X X  
2 0 0.0 0.0  
3 0 0.0 0.0  
4 0 0.0 0.0  
5 0 0.0 0.0

EXIT CALLS  
OPTIONS  
PRIORITY (Y/N TO SELECT) .....MED  
DELAY TIMER (0-255 SEC) .....0  
MIN GREEN BEFORE PRE (0= DEFAULT)....1  
PED CLEAR BEFORE PRE (0= DEFAULT)....0  
YELLOW CLEAR BEFORE PRE (0= DEFAULT)....0\*  
RED CLEAR BEFORE PRE (0= DEFAULT)....0\*  
DWELL MIN TIMER (0-255 SEC) .....12  
DWELL MAX TIMER (0=OFF,1-255MIN) ....0  
DWELL HOLD-OVER TIMER (0-255) .....0  
LATCH CALL? .....N  
LINK TO NEXT PREEMPT? .....N  
ENABLE BACKUP PROTECTION? .....N  
HOLD CLEAR 1 PHASES DURING DELAY? ..N  
FAST GREEN FLASH DWELL PHASES? .....N  
PED CLEARANCE THROUGH YELLOW? .....N  
INHIBIT OVERLAP GREEN EXTENSION? ..N  
SERVICE DURING SOFTWARE FLASH? .....N  
REST IN RED DURING DWELL INTERVAL? ..N  
FLASH DWELL INTERVAL? .....N  
ALLOW PEDS IN DWELL INTERVAL? .....N  
RE-TIME DWELL INTERVAL? .....N  
OVERLAPS: ABCDEFGHIJKLMNPO  
DWELL INT FLASH YELLOW  
OMIT OVERLAPS:

PRESS 'NEXT'

PREEMPTION #5 SETTINGS (NEXT:1-10)  
INTERVAL/TIMING CLEAR/DWELL PHASES  
GRN YEL RED 12345678910111213141516  
1 255 0.0\* 0.0\* X  
2 0 0.0 0.0  
3 0 0.0 0.0  
4 0 0.0 0.0  
5 0 0.0 0.0

EXIT CALLS  
OPTIONS  
PRIORITY (Y/N TO SELECT) .....MED  
DELAY TIMER (0-255 SEC) .....0  
MIN GREEN BEFORE PRE (0= DEFAULT)....1  
PED CLEAR BEFORE PRE (0= DEFAULT)....0  
YELLOW CLEAR BEFORE PRE (0= DEFAULT)....0\*  
RED CLEAR BEFORE PRE (0= DEFAULT)....0\*  
DWELL MIN TIMER (0-255 SEC) .....7  
DWELL MAX TIMER (0=OFF,1-255MIN) ....0  
DWELL HOLD-OVER TIMER (0-255) .....0  
LATCH CALL? .....N  
LINK TO NEXT PREEMPT? .....N  
ENABLE BACKUP PROTECTION? .....N  
HOLD CLEAR 1 PHASES DURING DELAY? ..N  
FAST GREEN FLASH DWELL PHASES? .....N  
PED CLEARANCE THROUGH YELLOW? .....N  
INHIBIT OVERLAP GREEN EXTENSION? ..N  
SERVICE DURING SOFTWARE FLASH? .....N  
REST IN RED DURING DWELL INTERVAL? ..N  
FLASH DWELL INTERVAL? .....N  
ALLOW PEDS IN DWELL INTERVAL? .....N  
RE-TIME DWELL INTERVAL? .....N  
OVERLAPS: ABCDEFGHIJKLMNPO  
DWELL INT FLASH YELLOW  
OMIT OVERLAPS:

PROGRAMMING COMPLETE

**OVERLAP PROGRAMMING DETAIL**

(program controller as shown below)

FROM MAIN MENU PRESS '8' (OVERLAPS), THEN '1' (VEHICLE OVERLAP SETTINGS).

PRESS '+' OR '-' TO POSITION ON OVERLAP B

PAGE 1: VEHICLE OVERLAP 'B' SETTINGS  
PHASE: 12345678910111213141516  
VEH OVL PARENTS: X  
VEH OVL NOT VEH:  
VEH OVL NOT PED:  
VEH OVL GRN EXT:  
STARTUP COLOR: - RED - YELLOW - GREEN  
FLASH COLORS: - RED - YELLOW - GREEN  
SELECT VEHICLE OVERLAP OPTIONS: (Y/N)  
FLASH YELLOW IN CONTROLLER FLASH?...N  
GREEN EXTENSION (0-255 SEC).....0  
YELLOW CLEAR (0=PARENT,3-25.5 SEC)...0.0  
RED CLEAR (0=PARENT,0.1-25.5 SEC)...0.0  
OUTPUT AS PHASE # (0=NONE, 1-16)....0

PRESS '+'

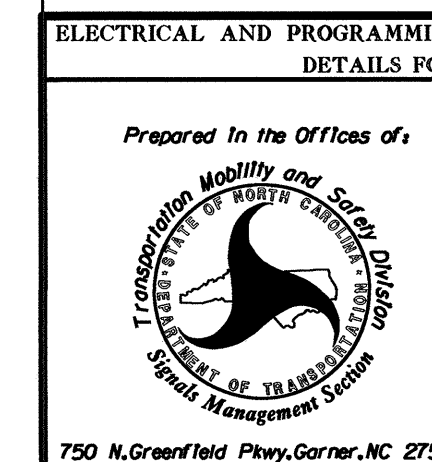
PAGE 1: VEHICLE OVERLAP 'C' SETTINGS  
PHASE: 12345678910111213141516  
VEH OVL PARENTS: XX  
VEH OVL NOT VEH:  
VEH OVL NOT PED:  
VEH OVL GRN EXT:  
STARTUP COLOR: - RED - YELLOW - GREEN  
FLASH COLORS: - RED - YELLOW X GREEN  
SELECT VEHICLE OVERLAP OPTIONS: (Y/N)  
FLASH YELLOW IN CONTROLLER FLASH?...Y  
GREEN EXTENSION (0-255 SEC).....0  
YELLOW CLEAR (0=PARENT,3-25.5 SEC)...0.0  
RED CLEAR (0=PARENT,0.1-25.5 SEC)...0.0  
OUTPUT AS PHASE # (0=NONE, 1-16)....0

OVERLAP PROGRAMMING COMPLETE

← NOTICE GREEN FLASH

THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 01-0221T1  
DESIGNED: March 2010  
SEALED: 08/24/10  
REVISED:

ELECTRICAL DETAIL - TEMP 1 - SHEET 2 OF 2



Division 1		Camden County		Belcross	
PLAN DATE: August 2010	REVIEWED BY: T. Jaffe				
PREPARED BY: C. Strickland	REVIEWED BY:				
REVISIONS	INIT.	DATE			

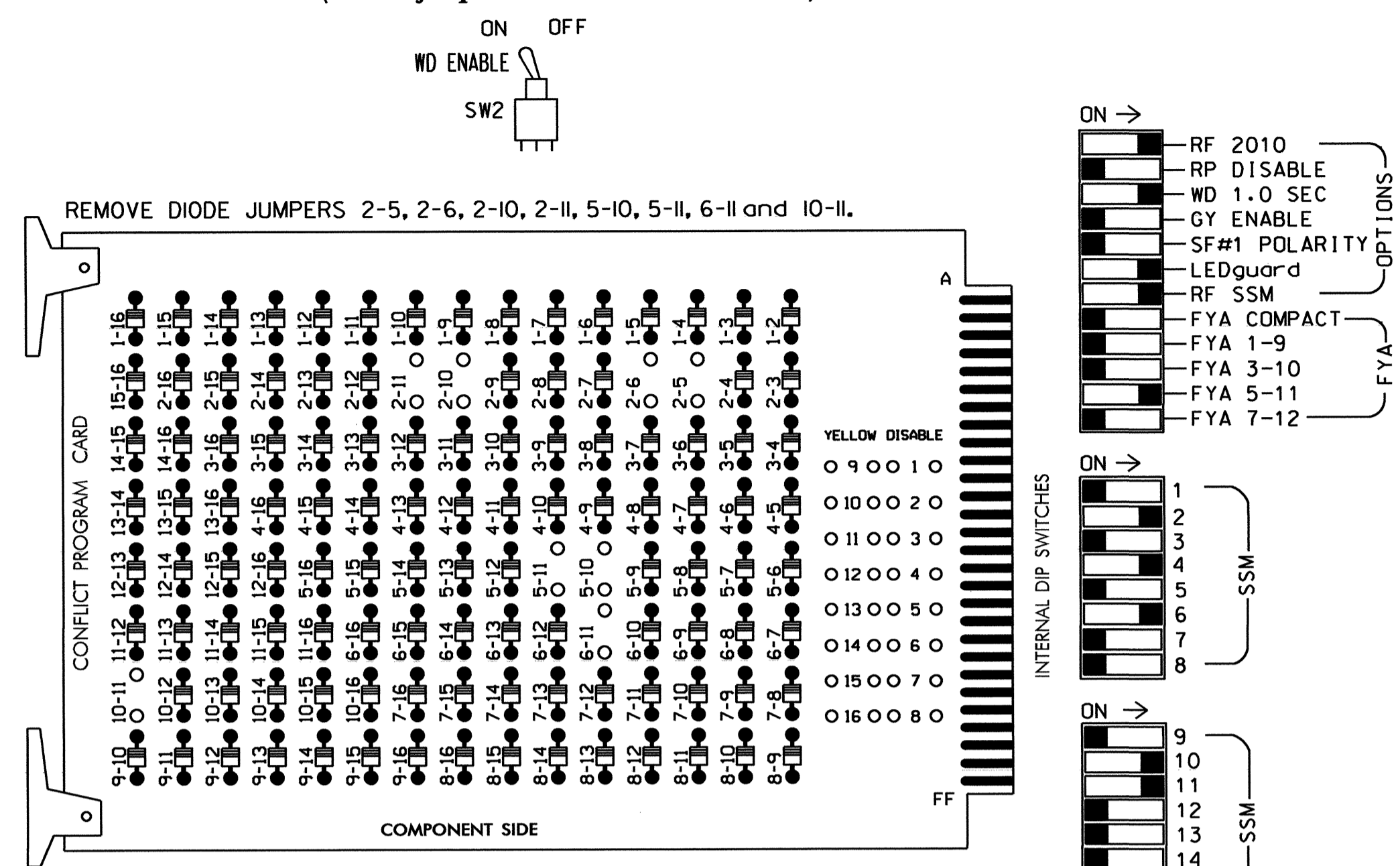
SEAL  
NORTH CAROLINA  
PROFESSIONAL ENGINEER  
SEAL 022013  
GEORGE C. BROWN  
SIGNATURE DATE 8/26/10

SIG. INVENTORY NO. 01-0221T1



**EDI MODEL 2010ECL-NC CONFLICT MONITOR PROGRAMMING DETAIL**

(remove jumpers and set switches as shown)

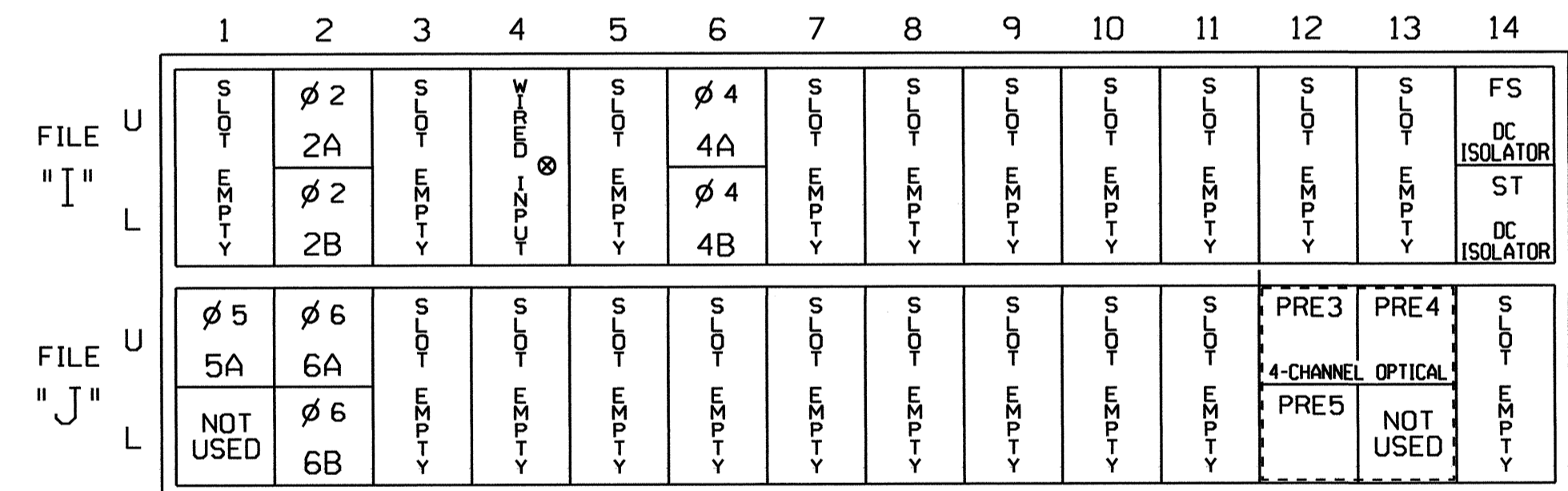


**NOTES:**

- Card is provided with all diode jumpers in place. Removal of any jumper allows its channels to run concurrently.
- Make sure jumpers SEL2-SEL5 are present on the monitor board.

**INPUT FILE POSITION LAYOUT**

(front view)

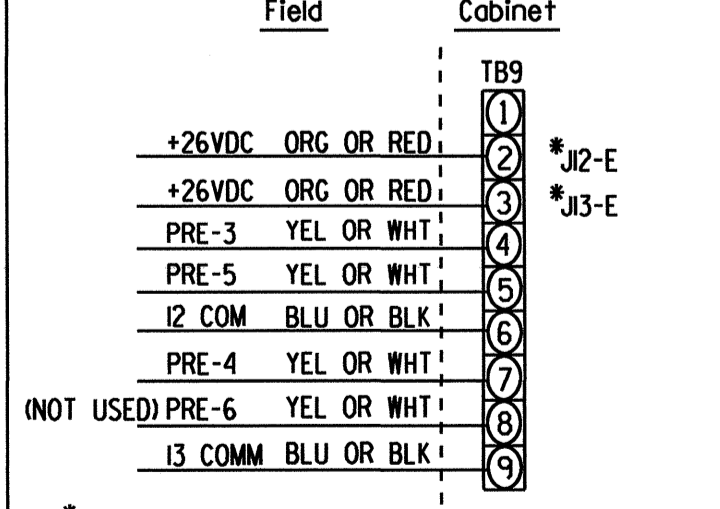


EX.: 1A, 2A, ETC. = LOOP NO.'S

FS = FLASH SENSE  
ST = STOP TIME

⊗ Wired Input - Do not populate slot with detector card

**OPTICOM FIELD WIRE DETAIL**



\* Assuming TB9-2 & TB9-3 are unused on the J File, move wires on JII-J & JII-K (Twisted Pair) to JI2-E & JI3-E Respectively.

**NOTES**

- To prevent "flash-conflict" problems, insert red flash program blocks for all unused vehicle load switches in the output file. The installer shall verify that signal heads flash in accordance with the Signal Plans.
- Ensure that Red Enable is active at all times during normal operation. To prevent Red Failures on unused monitor channels, tie unused red monitor inputs 1,3,5,7,8,9,12,13,14,15 & 16 to load switch AC+ per the cabinet manufacturer's instructions.
- Enable Simultaneous Gap-Out for all phases.
- Program phases 2 and 6 for Variable Initial and Gap Reduction.
- Program phases 2 and 6 for Start Up In Green.
- Program phases 2 and 6 for Yellow Flash.

**EQUIPMENT INFORMATION**

CONTROLLER.....2070L  
CABINET.....332 /W/ AUX  
SOFTWARE.....ECONOLITE OASIS  
CABINET MOUNT.....BASE  
OUTPUT FILE POSITIONS...18 WITH AUX. OUTPUT FILE  
LOAD SWITCHES USED.....S2,S4,S5,S6,S10,S12  
PHASES USED.....2,4,5,6  
OVERLAP "A".....NOT USED  
OVERLAP "B".....5  
OVERLAP "C".....5+6  
OVERLAP "D".....NOT USED

**SIGNAL HEAD HOOK-UP CHART**

LOAD SWITCH NO.	S1	S2	S2P	S3	S4	S4P	S5	S6	S6P	S7	S8	S8P	S9	S10	S11	S12	S13	S14
PHASE	1	2	2 PED	3	4	4 PED	5	6	6 PED	7	8	8 PED	OLA	OLB	SPARE	OLC	OLD	SPARE
SIGNAL HEAD NO.	NU	21,22	NU	NU	41,42	NU	51*	61,62	NU	NU	NU	NU	NU	42	NU	51*	NU	NU
RED		128			101			134						*				
YELLOW		129			102		*	135										
GREEN		130			103			136										
RED ARROW																		A114
YELLOW ARROW																		A125
FLASHING YELLOW ARROW																		A115
GREEN ARROW																		A116
																		A126

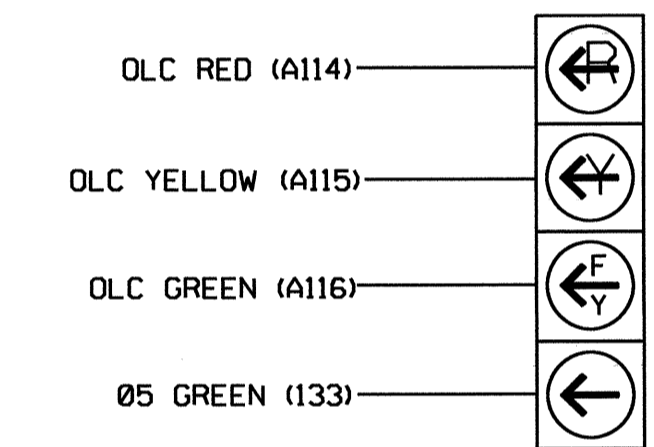
NU = Not Used

\* Denotes install load resistor. See load resistor installation detail this sheet.

★ See pictorial of head wiring in detail below.

**4 SECTION FYA PPLT SIGNAL WIRING DETAIL**

(wire signal heads as shown)



51

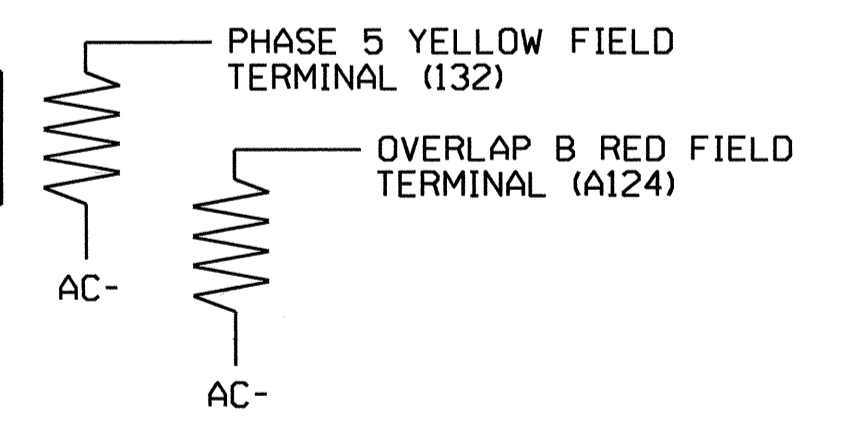
**NOTE**

- The sequence display for signal head 51 requires special logic programming. See sheet 2 of 2 for programming instructions.

**LOAD RESISTOR INSTALLATION DETAIL**

(install resistors as shown below)

VALUE (ohms)	WATTAGE
1.5K - 1.9K	25W (min)
2.0K - 3.0K	10W (min)

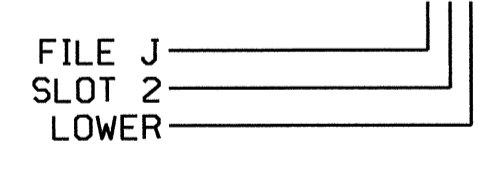


**INPUT FILE CONNECTION & PROGRAMMING CHART**

LOOP NO.	LOOP TERMINAL	INPUT FILE POS.	PIN NO.	INPUT ASSIGNMENT NO.	DETECTOR NO.	NEMA PHASE	CALL	EXTEND	FULL TIME DELAY	STRETCH TIME	DELAY TIME
2A	TB2-5,6	I2U	39	1	2	2	Y	Y			
2B	TB2-7,8	I2L	43	5	12	2	Y	Y			
4A	TB4-9,10	I6U	41	3	4	4	Y	Y			3
4B	TB4-11,12	I6L	45	7	14	4	Y	Y			15
5A <sup>1</sup>	TB3-1,2	J1U	55	17	5	5	Y	Y			15
		I4U	47	9	22	2	Y	Y	Y		3
6A	TB3-5,6	J2U	40	2	6	6	Y	Y			
6B	TB3-7,8	J2L	44	6	16	6	Y	Y			

<sup>1</sup>Add jumper from J1-W to I4-W, on rear of input file.

INPUT FILE POSITION LEGEND: J2L



THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 01-0221  
DESIGNED: March 2010  
SEALED: 08/24/10  
REVISED:

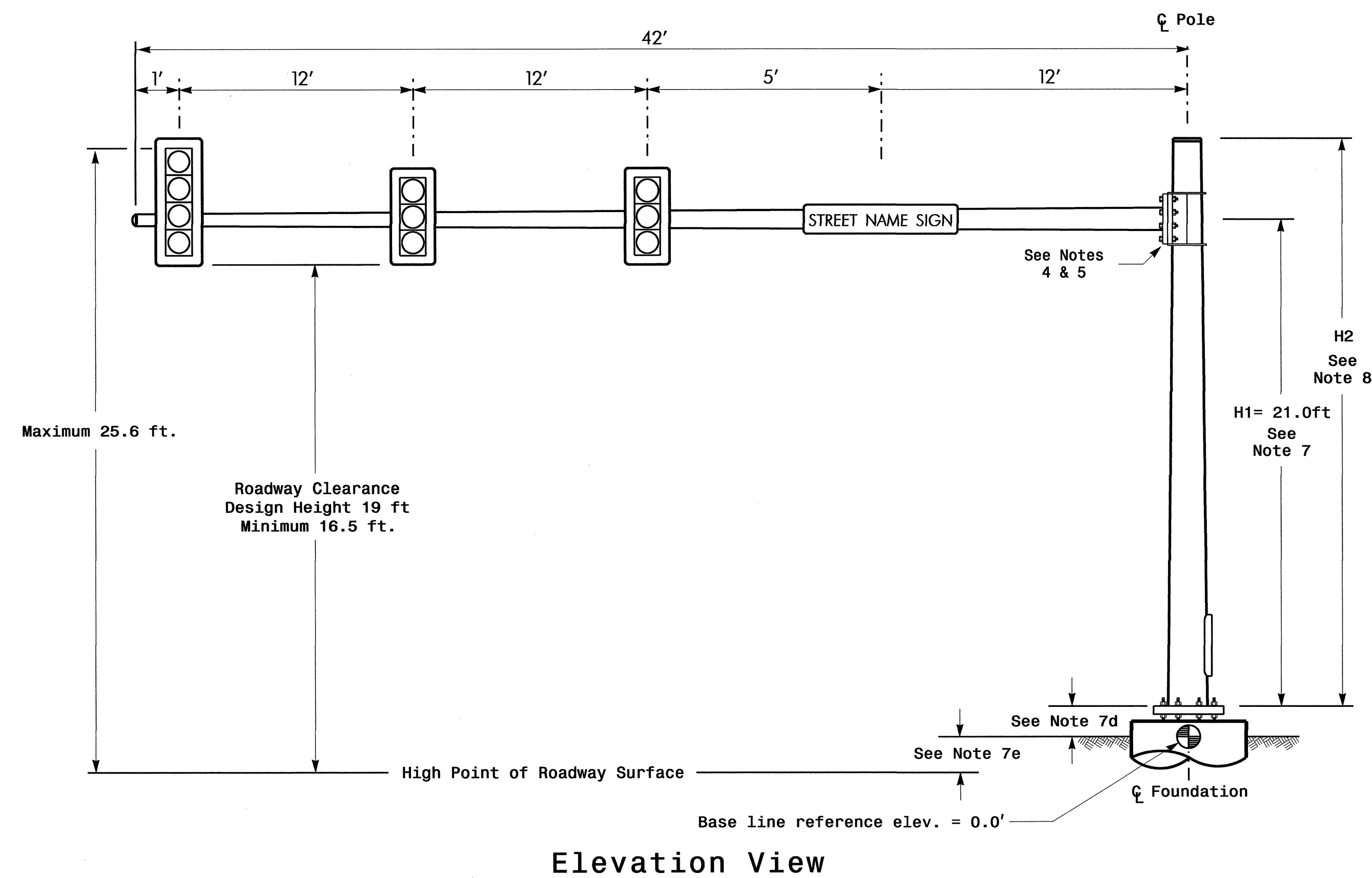
**ELECTRICAL DETAIL - FINAL - SHEET 1 OF 2**

	ELECTRICAL AND PROGRAMMING DETAILS FOR:		US 158 at NC 34		SEAL NORTH CAROLINA PROFESSIONAL ENGINEER SEAL 022013 GEORGE C. BROWN	
	Division 1		Camden County			Beicross
	PLAN DATE: August 2010		REVIEWED BY: T. Jopek			ENGINEER
	PREPARED BY: C. Strickland		REVIEWED BY:			SIGNATURE
REVISIONS		INIT. DATE		DATE		
750 N. Greenfield Pkwy, Garner, NC 27529		Signature: <i>George C. Brown</i>		DATE		
SIG. INVENTORY NO. 01-0221						





Design Loading for METAL POLE NO. 7



Elevation View

**SPECIAL NOTE**  
The contractor is responsible for verifying that the mast arm attachment height (H1) will provide the "Design Height" clearance from the roadway before submitting final shop drawings for approval. Verify elevation data below which was obtained by field measurement or from available project survey data.

Elevation Data for Mast Arm Attachment (H1)

Elevation Differences for:	Pole 7	
Baseline reference point at $\phi$ Foundation @ ground level	0.0 ft.	
Elevation difference at High point of roadway surface	+/-0.0ft.	
Elevation difference at Edge of travelway or face of curb	NA	

MAST ARM LOADING SCHEDULE

LOADING SYMBOL	DESCRIPTION	AREA	SIZE	WEIGHT
	SIGNAL HEAD 12"-5 SECTION-WITH BACKPLATE AND ASTRO-BRAC	16.3 S.F.	42.0" W X 56.0" L	103 LBS
	SIGNAL HEAD 12"-4 SECTION-WITH BACKPLATE AND ASTRO-BRAC	11.5 S.F.	25.5" W X 66.0" L	74 LBS
	SIGNAL HEAD 12"-3 SECTION-WITH BACKPLATE AND ASTRO-BRAC	9.3 S.F.	25.5" W X 52.5" L	60 LBS
	SIGN RIGID MOUNTED WITH ASTRO-SIGN-BRAC	5.0 S.F.	24.0" W X 30.0" L	11 LBS
	STREET NAME SIGN RIGID MOUNTED WITH ASTRO-SIGN-BRAC	12.0 S.F.	18.0" W X 96.0" L	27 LBS

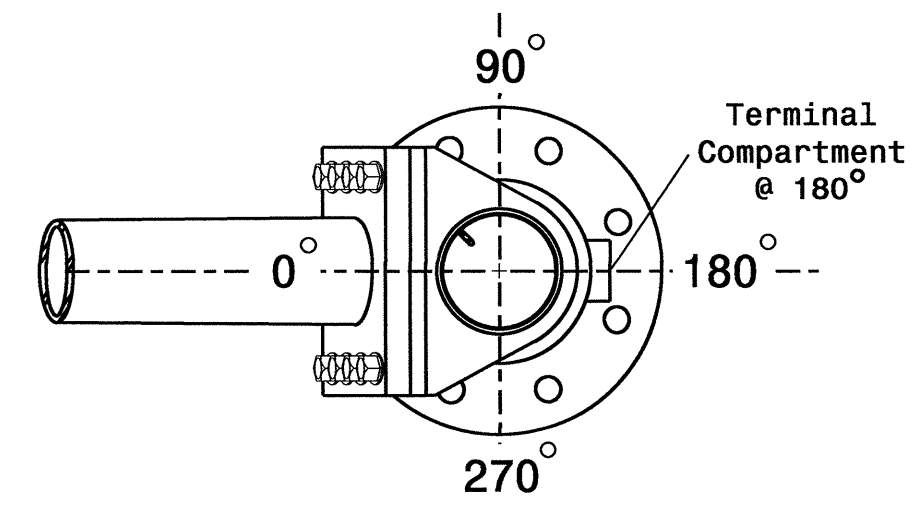
NOTES

Design Reference Material

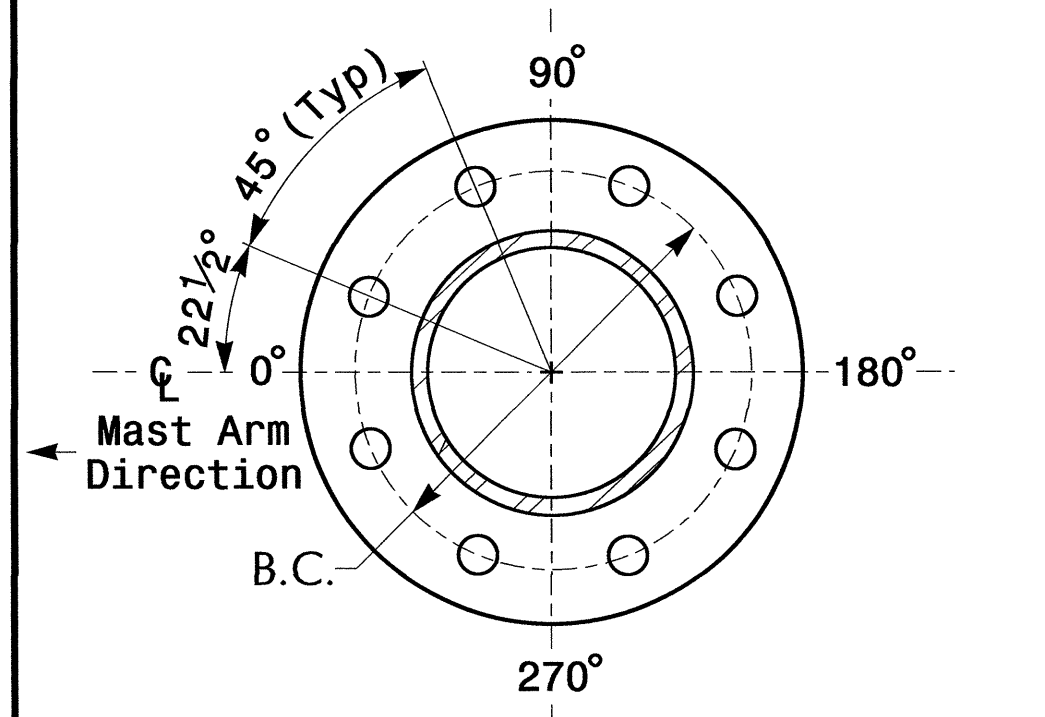
- Design the traffic signal structure and foundation in accordance with:
  - The 4th Edition 2001 AASHTO "Standard Specifications for Structural Supports for Highway Signs, Luminaires, and Traffic Signals, including all of the latest interim revisions.
  - The 2006 NCDOT "Standard Specifications for Roads and Structures". The latest addenda to these specifications can be found in the traffic signal project special provisions.
  - The 2006 NCDOT Roadway Standard Drawings.
  - The traffic signal project plans and special provisions.
  - The NCDOT "Metal Pole Standards" located at the following NCDOT website: <http://www.ncdot.org/doh/preconstruct/traffic/ITSS/ws/mpoles/poles.html>

Design Requirements

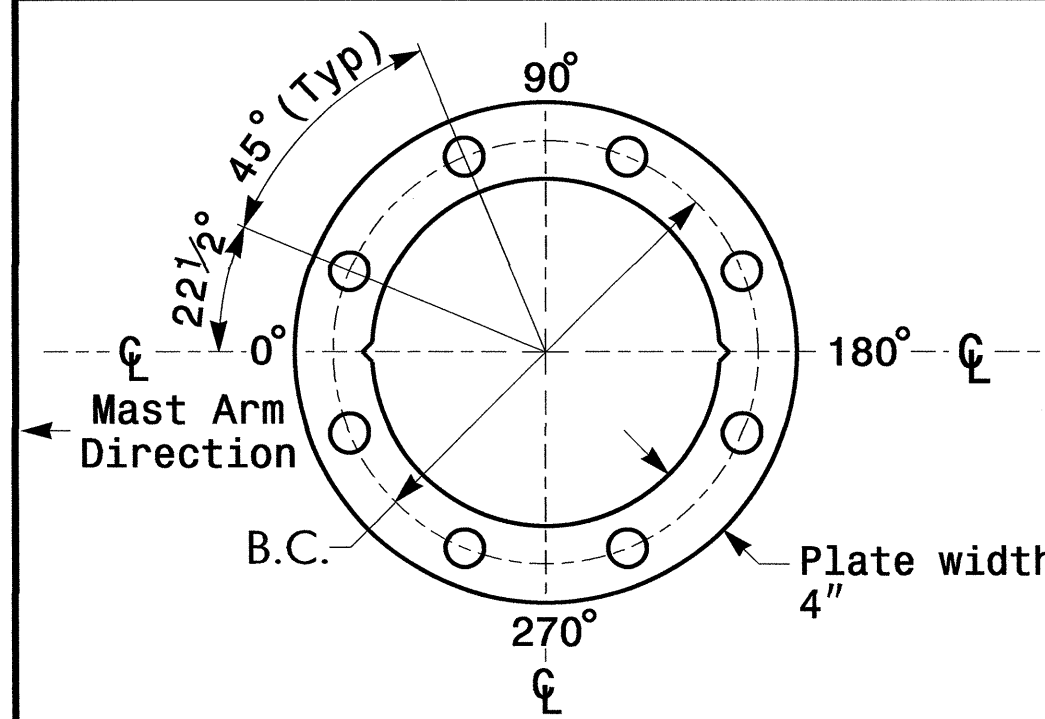
- Design the traffic signal structure using the loading conditions shown in the elevation views. These are anticipated worst case "Design loads" and may not represent the actual loads that will be applied at the time of the installation. The contractor should refer to the traffic signal plans for the actual loads that will be applied at the time of the installation.
- Design all signal supports using stress ratios that do not exceed 0.9.
- The camber design for mast arm deflection should provide an appearance of a low pitched arch where the tip or the free end of the mast arm does not deflect below horizontal when fully loaded.
- A clamp-type bolted mast arm-to-pole connection may be used instead of the welded ring stiffened box connection shown as long as the connection meets all of the design requirements.
- Design base plate with 8 anchor bolt holes. Provide 2 inch x 60 inch anchor bolts.
- The mast arm attachment height (H1) shown is based on the following design assumptions:
  - Mast arm slope and deflection are not considered in determining the arm attachment height as they are assumed to offset each other.
  - Signal heads attached to the mast arm are rigid mounted and vertically centered on the arm.
  - The roadway clearance height for design is as shown in the elevation views.
  - The top of the pole base plate is .75 feet above the ground elevation.
  - Refer to the Elevation Data chart for elevation differences between the proposed foundation ground level and the high point on the roadway.
- The pole manufacturer will determine the total height (H2) of each pole using the greater of the following:
  - Mast arm attachment height (H1) plus 2 feet, or
  - H1 plus 1/2 of the total height of the mast arm attachment assembly plus 1 foot.
- If pole location adjustments are required, the contractor must gain approval from the engineer as this may affect the mast arm lengths and arm attachment heights. The contractor may contact the Signals & Geometrics Structural Engineer for assistance at (919) 773-2800.
- The contractor is responsible for verifying that the mast arm length shown will allow proper positioning of the signal heads over the roadway.
- The contractor is responsible for providing soil penetration testing data (SPT) to the pole manufacturer so site specific foundations can be designed.



POLE RADIAL ORIENTATION



8 BOLT BASE PLATE DETAIL  
See Note 6



BASE PLATE TEMPLATE & ANCHOR BOLT LOCK PLATE DETAIL  
For 8 Bolt Base Plate

NCDOT Wind Zone 2 (130 mph)

Prepared in the Offices of:

750 N. Greenfield Pkwy, Garner, NC 27529

US 158  
At  
NC 34

Division 1 Camden County Belcross

PLAN DATE: May 2010 REVIEWED BY: PLA

PREPARED BY: JPG REVIEWED BY:

SCALE: N/A

0 N/A

SEAL

PROFESSIONAL ENGINEER

SEAL 29904

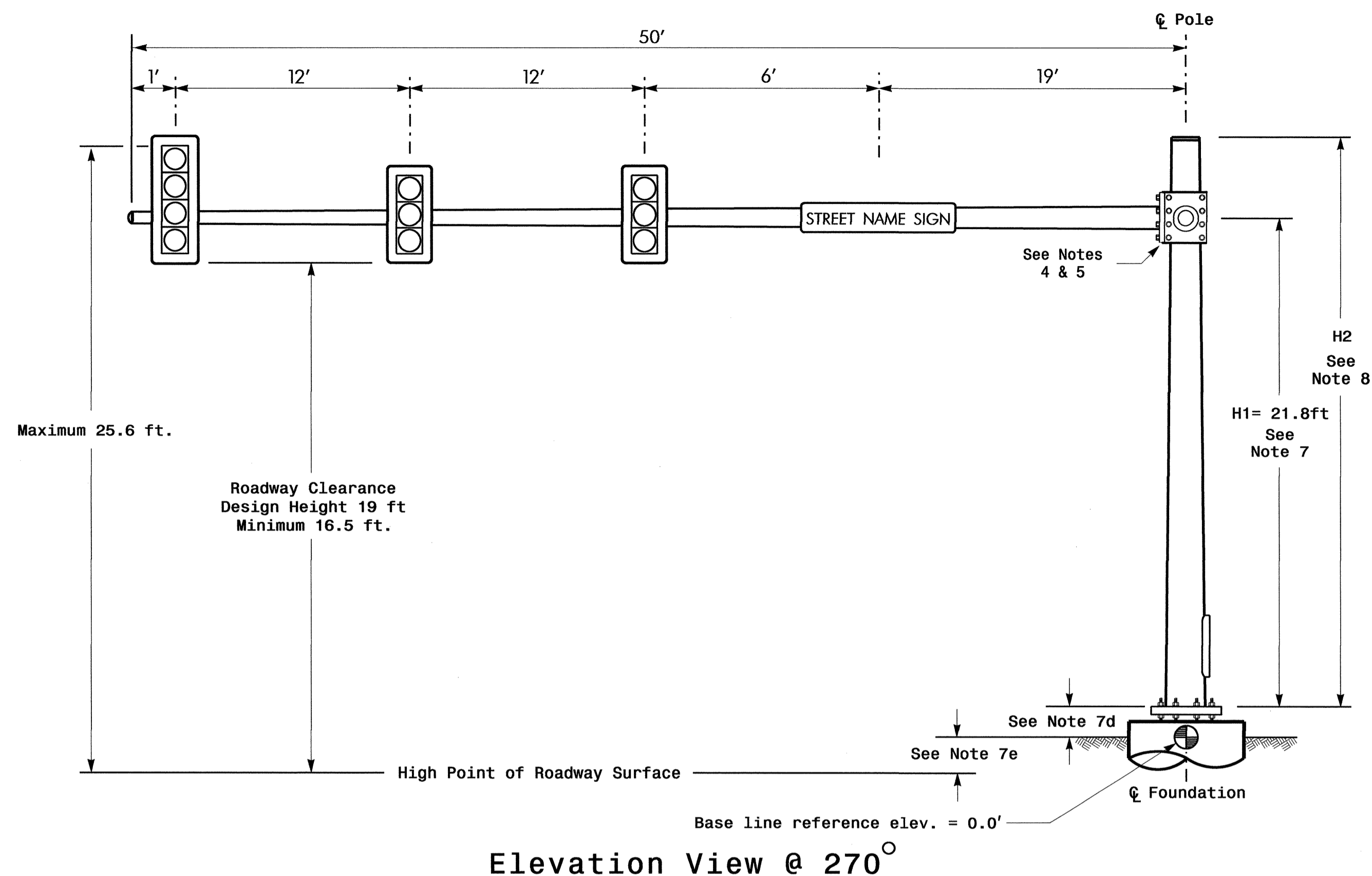
9/3/10

SIGNATURE DATE

SIG. INVENTORY NO. 01-0221

02-SEP-2010 11:26 S:\ITSS\ITS\Sigs\12\Signal\work\poupe\TIP Projects\4148\MS\gnal\sig\01-0221\mpo.dgn

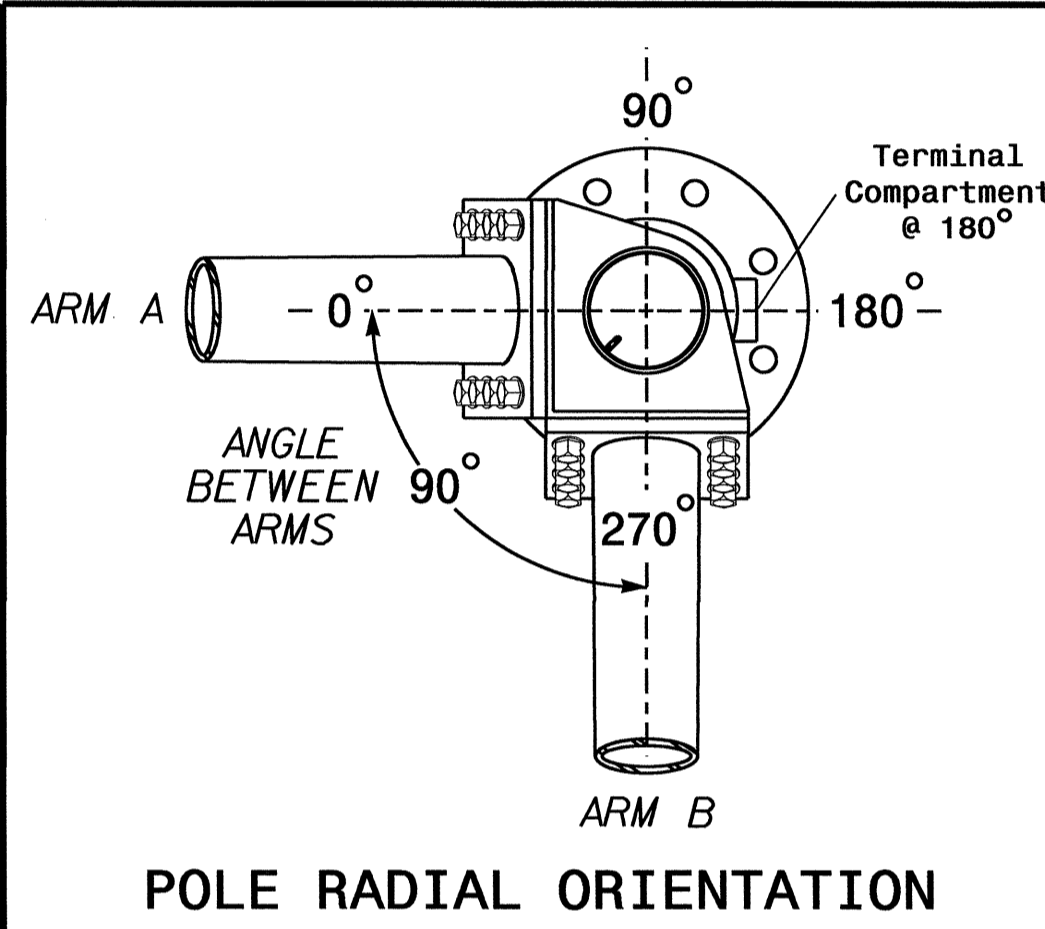
Design Loading for METAL POLE NO. 8, MAST ARM A



**SPECIAL NOTE**  
 The contractor is responsible for verifying that the mast arm attachment height (H1) will provide the "Design Height" clearance from the roadway before submitting final shop drawings for approval. Verify elevation data below which was obtained by field measurement or from available project survey data.

**Elevation Data for Mast Arm Attachment (H1)**

Elevation Differences for:	Arm "A"	Arm "B"
Baseline reference point at $\phi$ Foundation @ ground level	0.0 ft.	0.0 ft.
Elevation difference at High point of roadway surface	+0.8 ft.	+0.8 ft.
Elevation difference at Edge of travelway or face of curb	NA	NA



**MAST ARM LOADING SCHEDULE**

LOADING SYMBOL	DESCRIPTION	AREA	SIZE	WEIGHT
[Symbol]	SIGNAL HEAD 12"-5 SECTION-WITH BACKPLATE AND ASTRO-BRAC	16.3 S.F.	42.0" W X 56.0" L	103 LBS
[Symbol]	SIGNAL HEAD 12"-4 SECTION-WITH BACKPLATE AND ASTRO-BRAC	11.5 S.F.	25.5" W X 66.0" L	74 LBS
[Symbol]	SIGNAL HEAD 12"-3 SECTION-WITH BACKPLATE AND ASTRO-BRAC	9.3 S.F.	25.5" W X 52.5" L	60 LBS
[Symbol]	SIGN RIGID MOUNTED WITH ASTRO-SIGN-BRAC	5.0 S.F.	24.0" W X 30.0" L	11 LBS
[Symbol]	STREET NAME SIGN RIGID MOUNTED WITH ASTRO-SIGN-BRAC	12.0 S.F.	18.0" W X 96.0" L	27 LBS

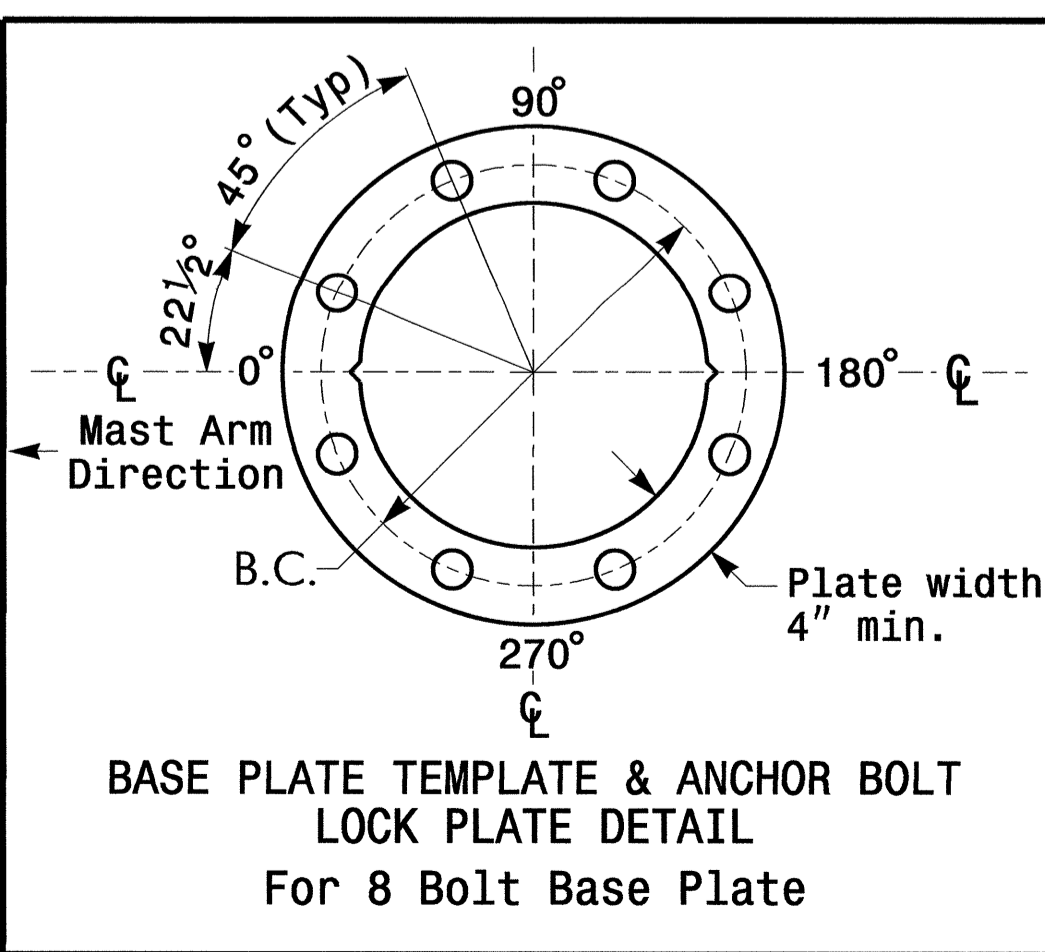
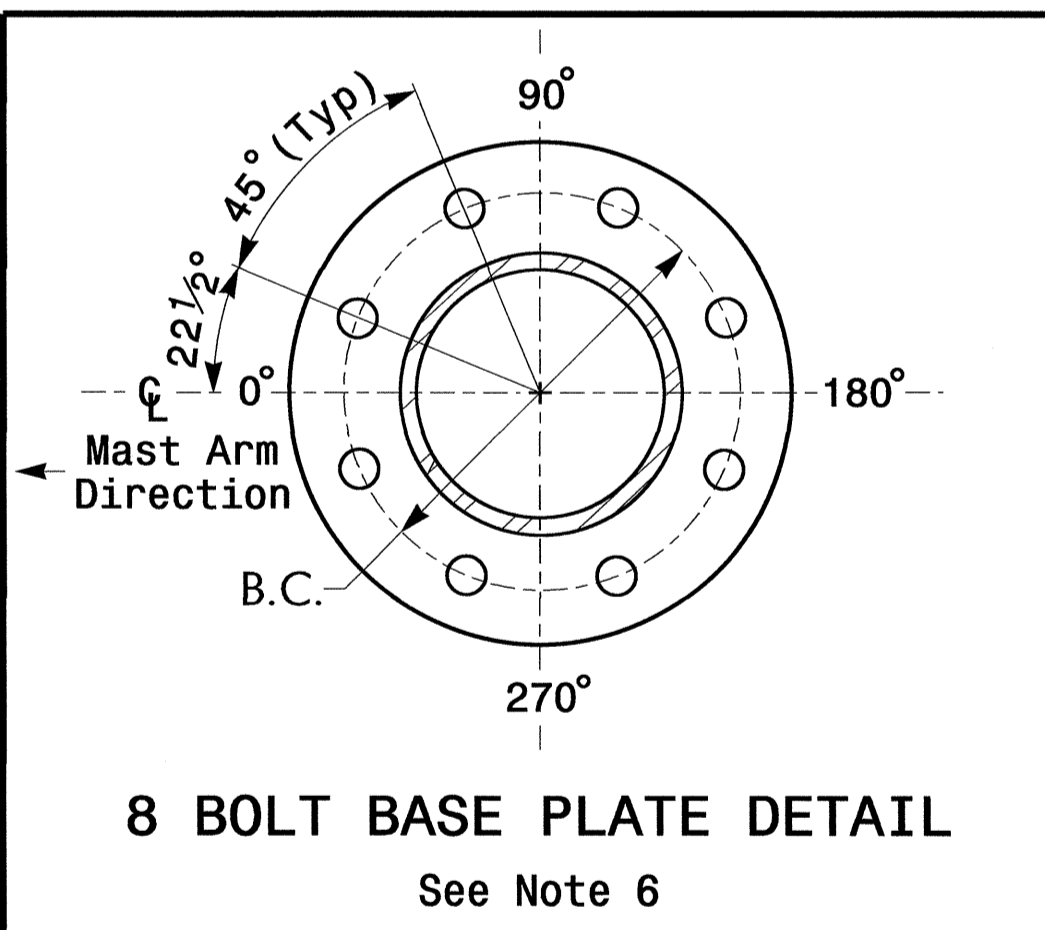
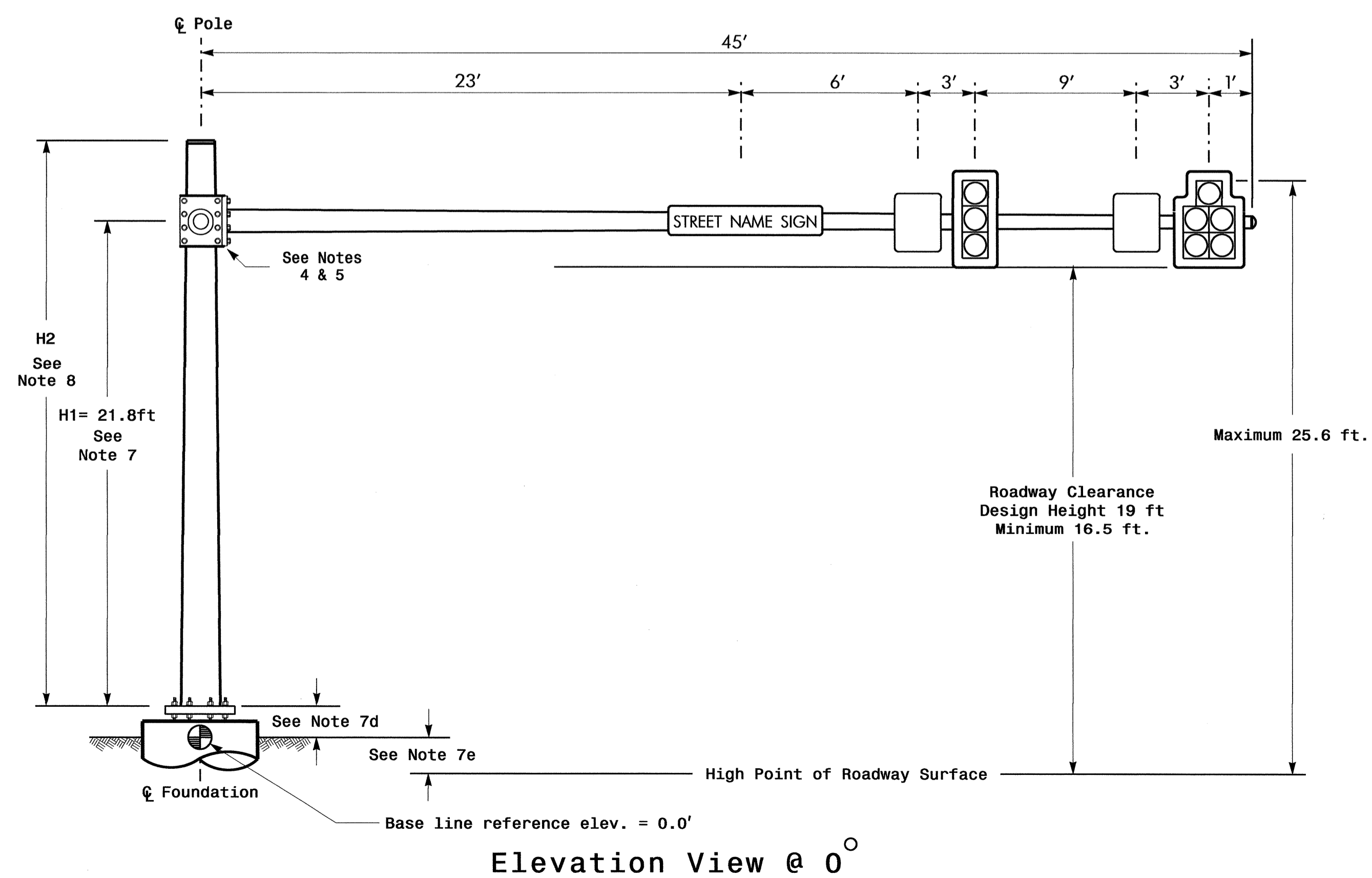
Design Reference Material

- NOTES**
- Design the traffic signal structure and foundation in accordance with:
    - The 4th Edition 2001 AASHTO "Standard Specifications for Structural Supports for Highway Signs, Luminaires, and Traffic Signals, including all of the latest interim revisions.
    - The 2006 NCDOT "Standard Specifications for Roads and Structures". The latest addenda to these specifications can be found in the traffic signal project special provisions.
    - The 2006 NCDOT Roadway Standard Drawings.
    - The traffic signal project plans and special provisions.
    - The NCDOT "Metal Pole Standards" located at the following NCDOT website: <http://www.ncdot.org/doh/preconstruct/traffic/ITSS/ws/mpoles/poles.html>

Design Requirements

- Design the traffic signal structure using the loading conditions shown in the elevation views. These are anticipated worst case "Design loads" and may not represent the actual loads that will be applied at the time of the installation. The contractor should refer to the traffic signal plans for the actual loads that will be applied at the time of the installation.
- Design all signal supports using stress ratios that do not exceed 0.9.
- The camber design for mast arm deflection should provide an appearance of a low pitched arch where the tip or the free end of the mast arm does not deflect below horizontal when fully loaded.
- A clamp-type bolted mast arm-to-pole connection may be used instead of the welded ring stiffened box connection shown as long as the connection meets all of the design requirements. This requires staggering the connections. Use elevation data for each arm to determine appropriate arm connection points.
- Design base plate with 8 anchor bolt holes. Provide 2 inch x 60 inch anchor bolts.
- The mast arm attachment height (H1) shown is based on the following design assumptions:
  - Mast arm slope and deflection are not considered in determining the arm attachment height as they are assumed to offset each other.
  - Signal heads attached to the mast arm are rigid mounted and vertically centered on the arm.
  - The roadway clearance height for design is as shown in the elevation views.
  - The top of the pole base plate is .75 feet above the ground elevation.
  - Refer to the Elevation Data chart for elevation differences between the proposed foundation ground level and the high point on the roadway.
- The pole manufacturer will determine the total height (H2) of the pole using the greater of the following:
  - Mast arm attachment height (H1) plus 2 feet, or
  - H1 plus 1/2 of the total height of the mast arm attachment assembly plus 1 foot.
- If pole location adjustments are required, the contractor must gain approval from the engineer as this may affect the mast arm lengths and arm attachment heights. The contractor may contact the Signals & Geometrics Structural Engineer for assistance at (919) 773-2800.
- The contractor is responsible for verifying that the mast arm lengths shown will allow proper positioning of the signal heads over the roadway.
- The contractor is responsible for providing soil penetration testing data (SPT) to the pole manufacturer so site specific foundations can be designed.

Design Loading for METAL POLE NO. 8, MAST ARM B



NCDOT Wind Zone 2 (130 mph)

09-SEP-2010 11:25 S:\IT\AS\1415 S:\IT\AS\1415\SIG\WORK\DRG\09-2414BAS\gnd\sa01-010\mpb.dgn Topilowdy

Prepared in the Offices of:

**US 158 At NC 34**

Division 1 Camden County Belcross

PLAN DATE: May 2010 REVIEWED BY: PLA

PREPARED BY: JPG REVIEWED BY:

SCALE: 0 N/A

REVISIONS: INIT. DATE

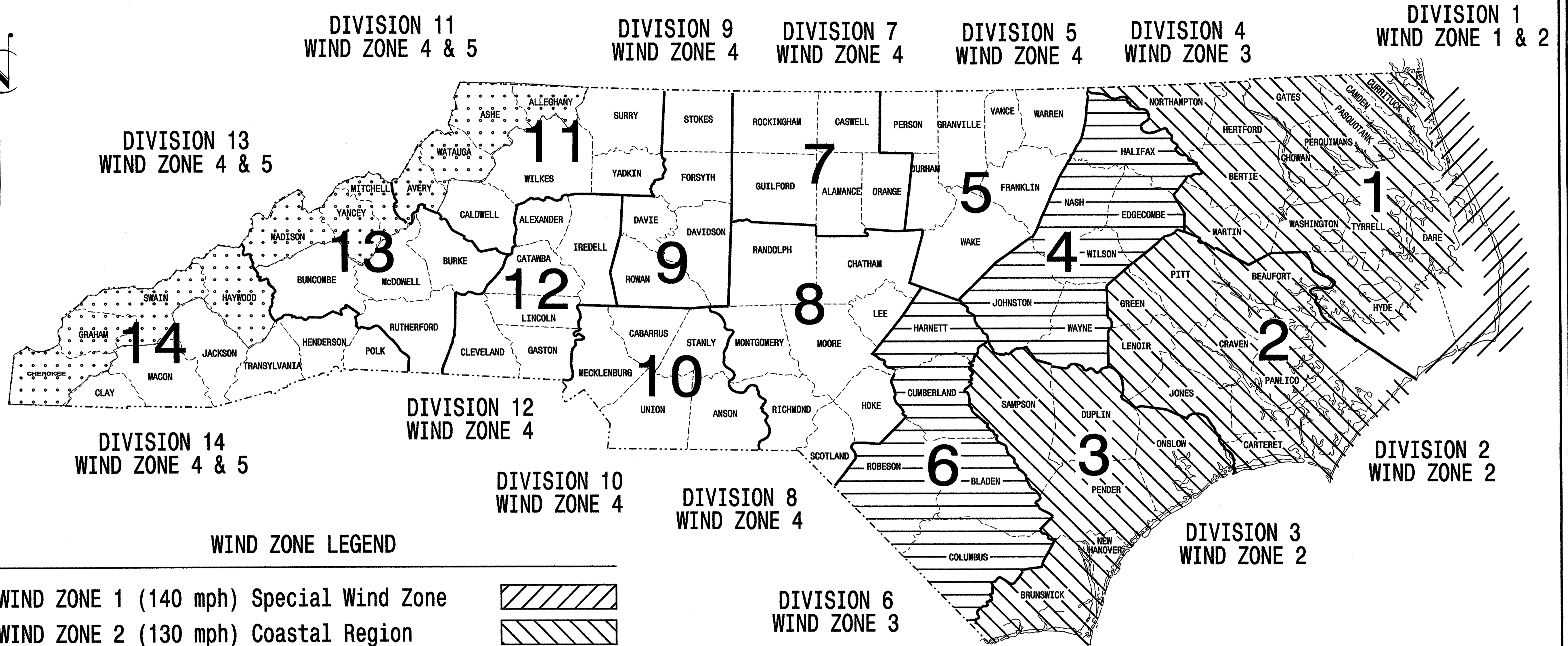
9/2/10

SIG. INVENTORY NO. 01-0221

# STATE OF NORTH CAROLINA DIVISION OF HIGHWAYS

STATE	PROJECT NO.	SHEET NO.
N.C.	R-2414B	Sig. 44
F. A. PROJ. NO.	M 1	
PROJECT ID. NO.		

## STANDARD DRAWINGS FOR METAL POLES



### WIND ZONE LEGEND

WIND ZONE 1 (140 mph)	Special Wind Zone	
WIND ZONE 2 (130 mph)	Coastal Region	
WIND ZONE 3 (110 mph)	Eastern Region	
WIND ZONE 4 (90 mph)	Central & Mtn. Region	
WIND ZONE 5 (120 mph)	Special Wind Zone	

<http://www.ncdot.org/doh/preconstruct/traffic/ITSS/ws/mpoles/poles.html>

Prepared In the Offices of:



750 N. Greenfield Pkwy, Garner, NC 27529

Designed in conformance  
with the  
2002 Interim to the  
4th Edition 2001

### AASHTO

Standard Specifications for  
Structural Supports for  
Highway Signs, Luminaires,  
and Traffic Signals

### INDEX OF PLANS

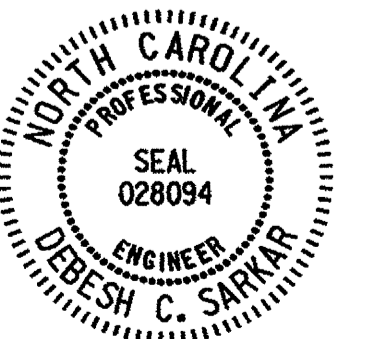
DRAWING NUMBER	DESCRIPTION
M 1	Title Sheet
M 2	Fabrication Details - All Poles
M 3	Fabrication Details - Strain Poles
M 4,5	Fabrication Details - Mast Arm Poles
M 6	Construction Details - Strain Poles
M 7	Construction Details - Foundations
M 8	Standard Strain Poles

### NCDOT CONTACTS:

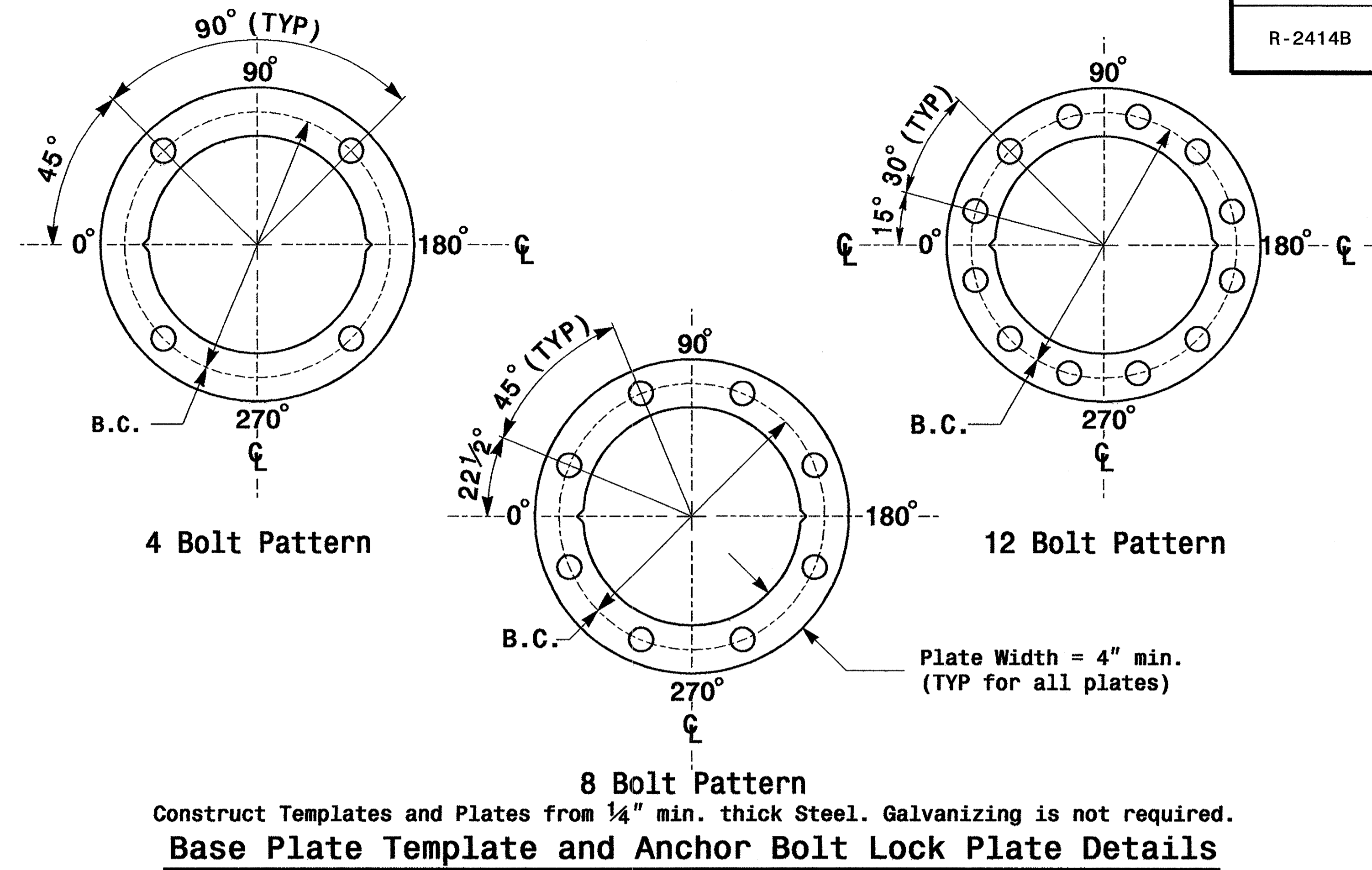
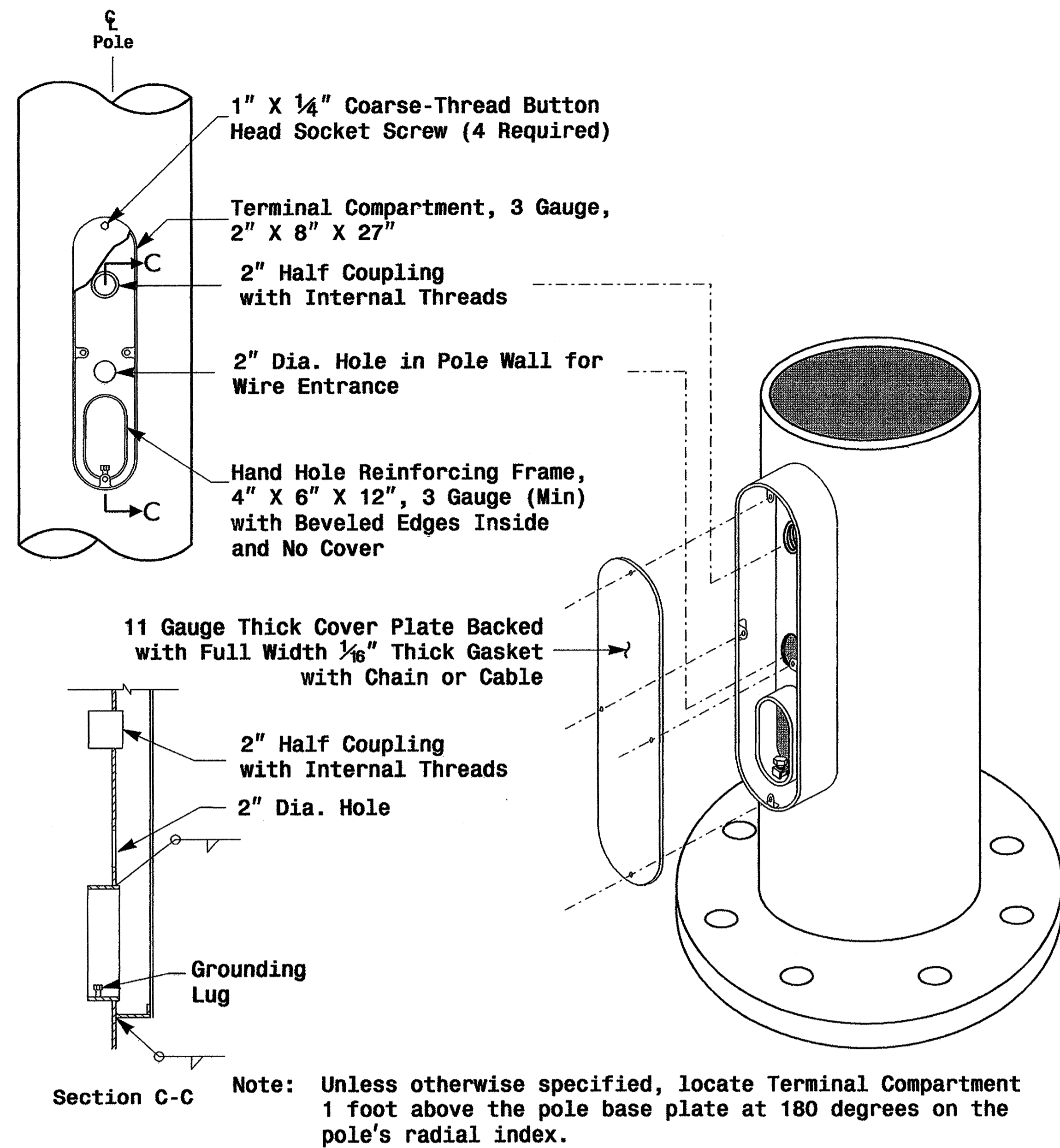
#### MOBILITY AND SAFETY DIVISION - ITS and SIGNALS UNIT

- G. A. Fuller, P.E. - State ITS and Signals Engineer
- G. G. Murr, Jr., P.E. - State Signals Engineer
- D. C. Sarkar, P.E. - ITS and Signals Senior Structural Engineer
- C. F. Andrews, Jr. - ITS and Signals Structural Project Engineer
- M. Aslam - ITS and Signals Structural Project Engineer
- N. Bitting, P.E. - ITS and Signals Structural Project Engineer

SEAL



SIGNATURE      7.21.2009  
 DATE



**Shaft I.D. Tag**  
(Provide on Strain Poles and Mast Arm Poles)

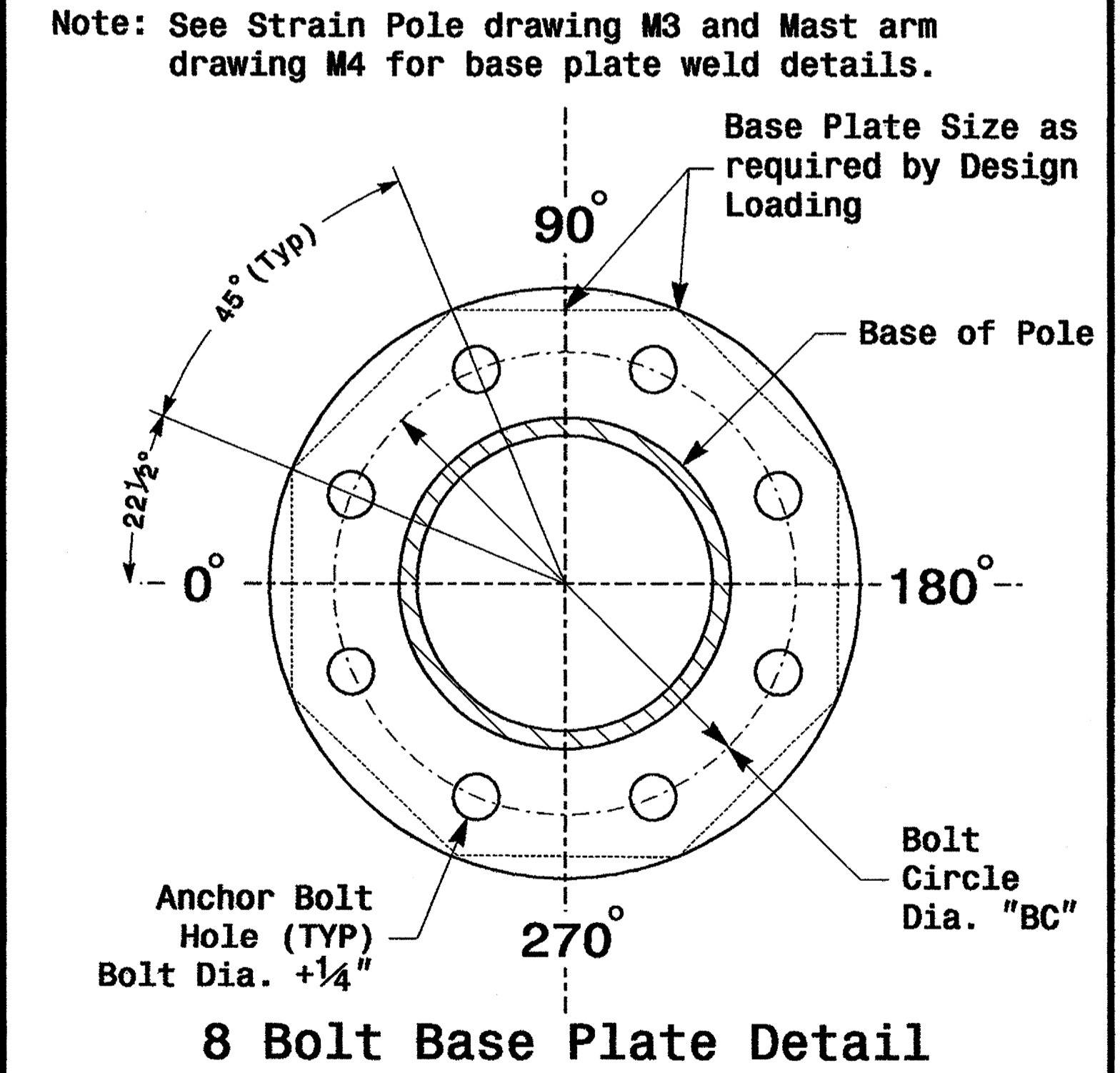
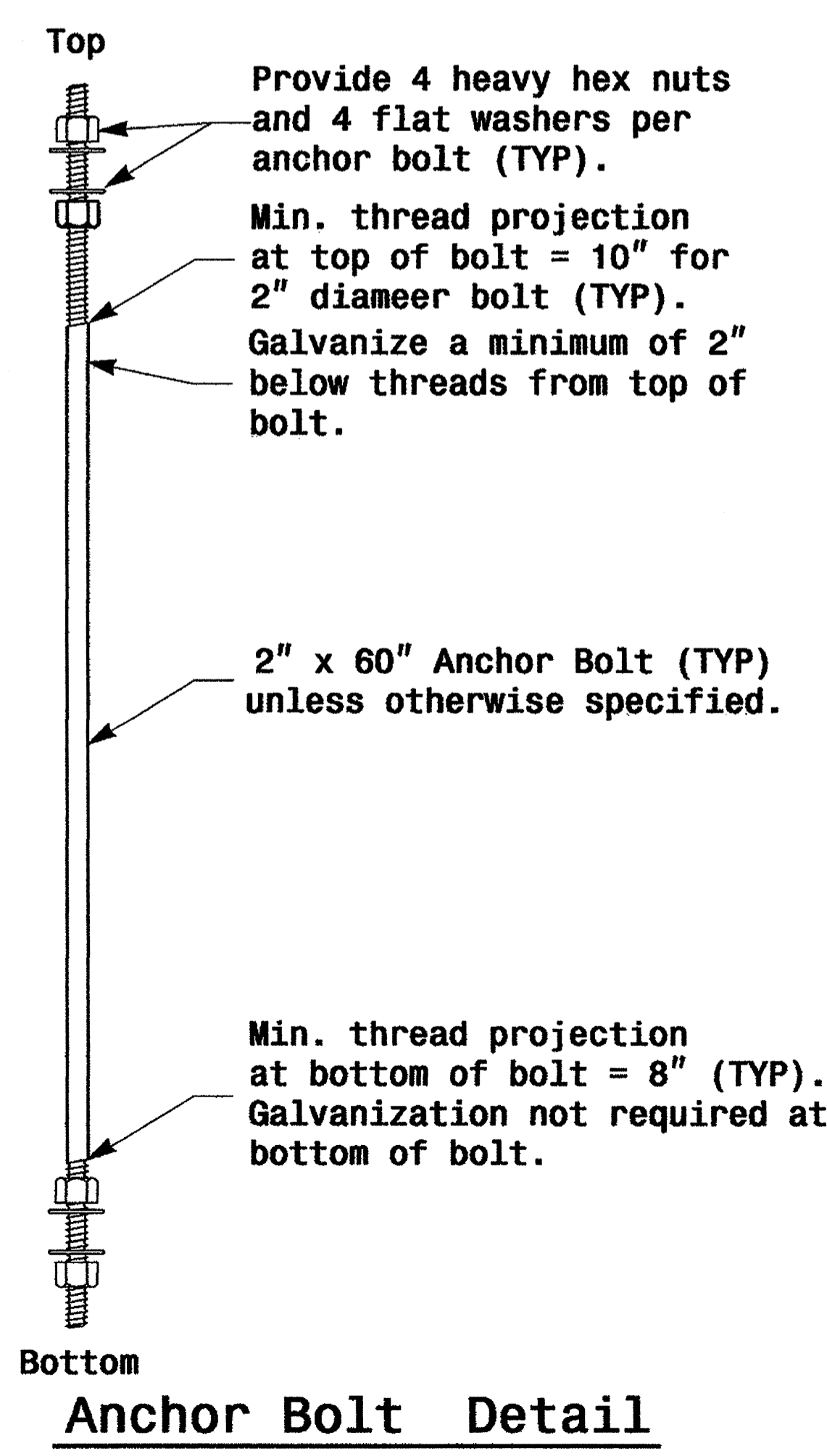
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SHAFT D/T/L/Y	-----/-----/-----/-----
ARM-A D/T/L/Y	-----/-----/-----/-----
ARM-B D/T/L/Y	-----/-----/-----/-----
A.B. DIA./B.C./L/Y	-----/-----/-----/-----
NCDOT STANDARD	-----

**Arm I.D. Tag**  
(Provide on each section of a multi-section mast arm)

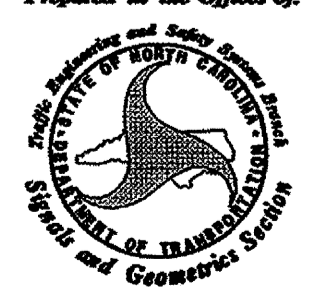
MFG	MFG. DATE: MM/YY
SECTION D/T/L/Y	-----/-----/-----/-----
NCDOT STANDARD	-----

- Notes:**
- 1) D= Diameter, T= Thickness, L= Length, Y= Yield Strength
  - 2) A.B. = Anchor Bolt
  - 3) B.C. = Bolt Circle of Anchor Bolts
  - 4) If Custom Design, use "NCDOT STANDARD" line for plan pole I.D.
  - 5) See drawing M4 for mounting positions of I.D. tags.

**Identification Tag Details**



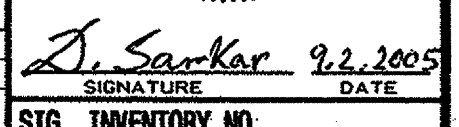
**Typical Fabrication Details Common To All Metal Poles**

Prepared in the Office of:  NORTH CAROLINA PROFESSIONAL SEAL 028094 ENGINEER DEEESH C. SARKAR

PLAN DATE: May 2005 REVIEWED BY: C.F. Andrews  
 PREPARED BY: P.L. Alexander REVIEWED BY: A.M. Esposito

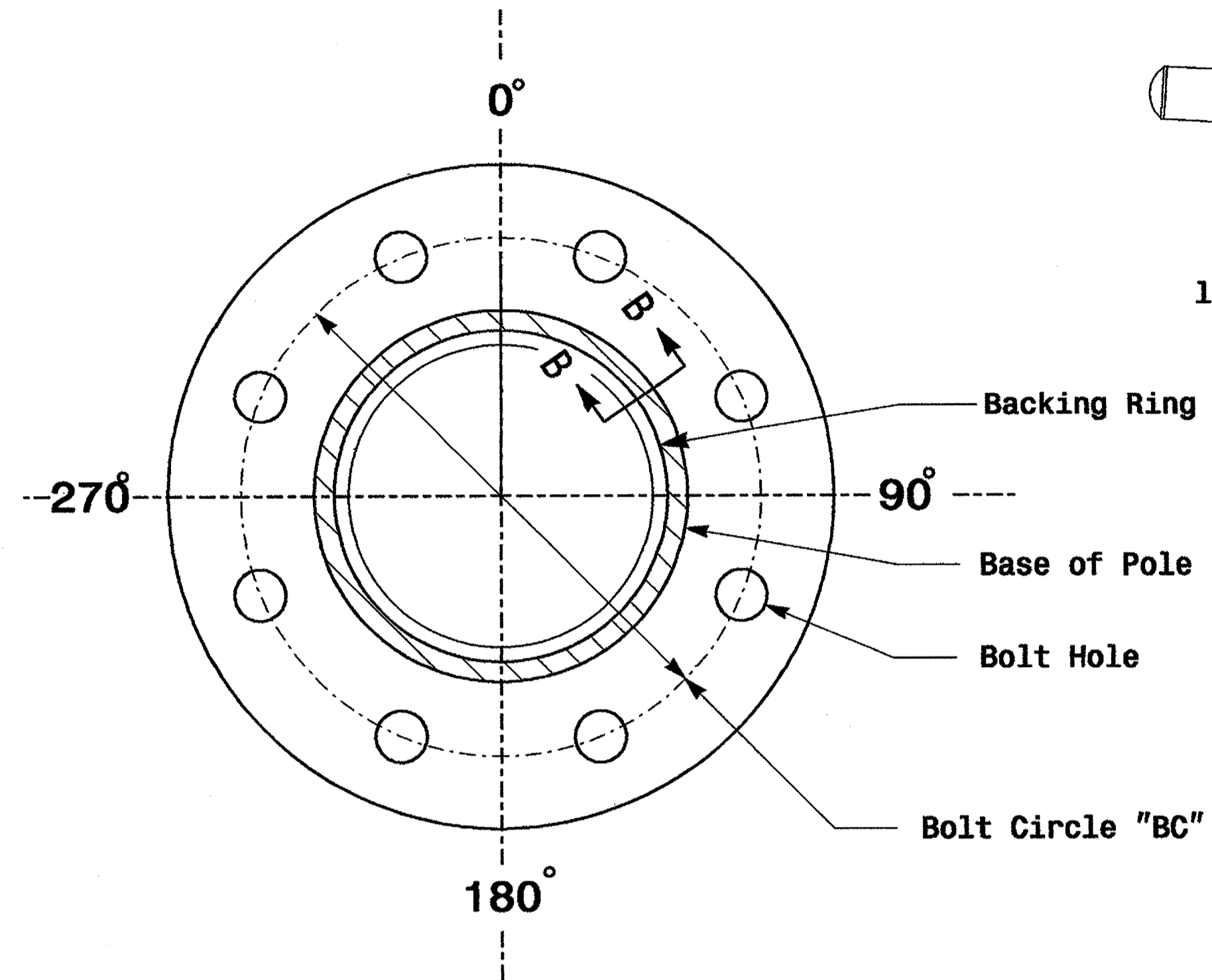
REVISIONS	INIT.	DATE

SCALE: 0 NA NONE

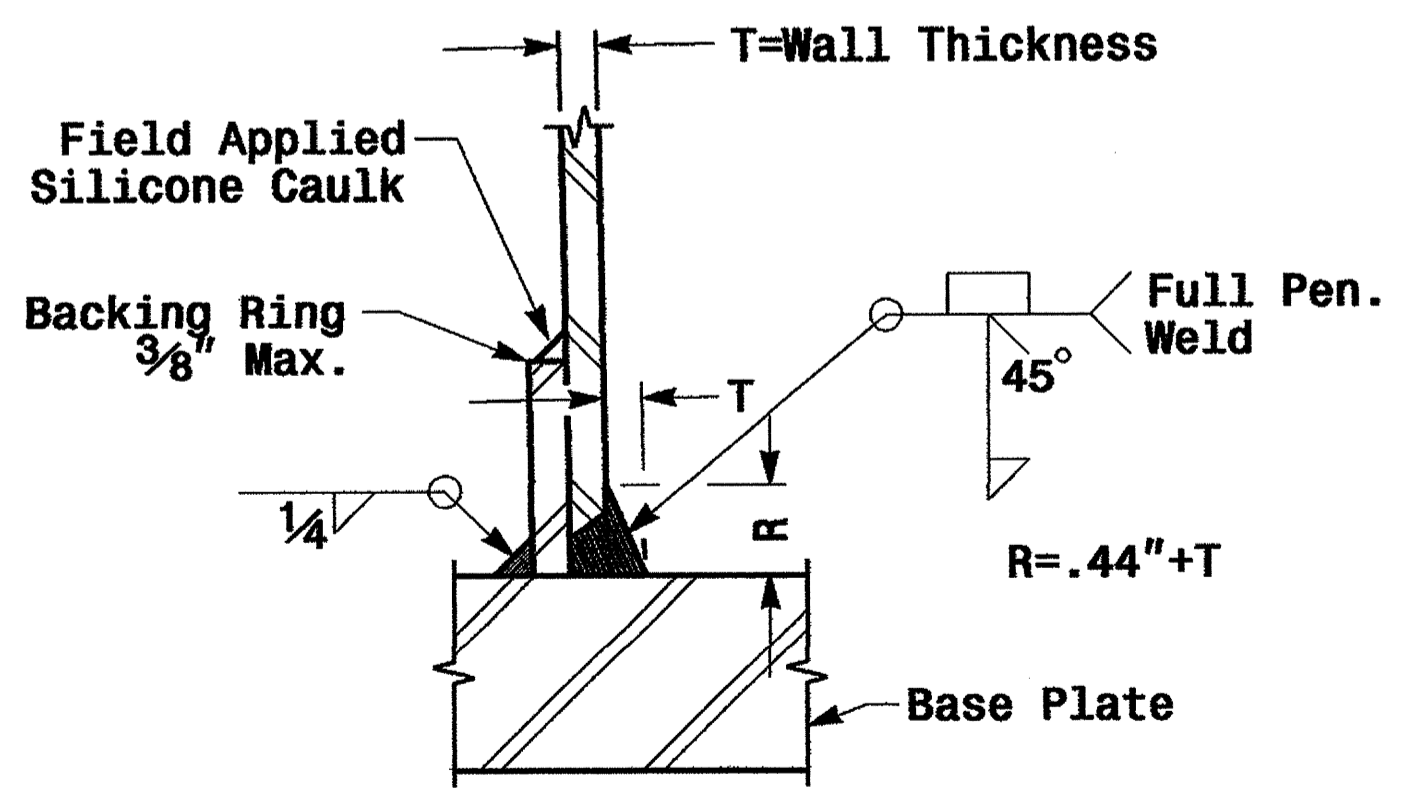
SIG. INVENTORY NO.  9.2.2005 DATE

**Fabrication Details - All Poles**

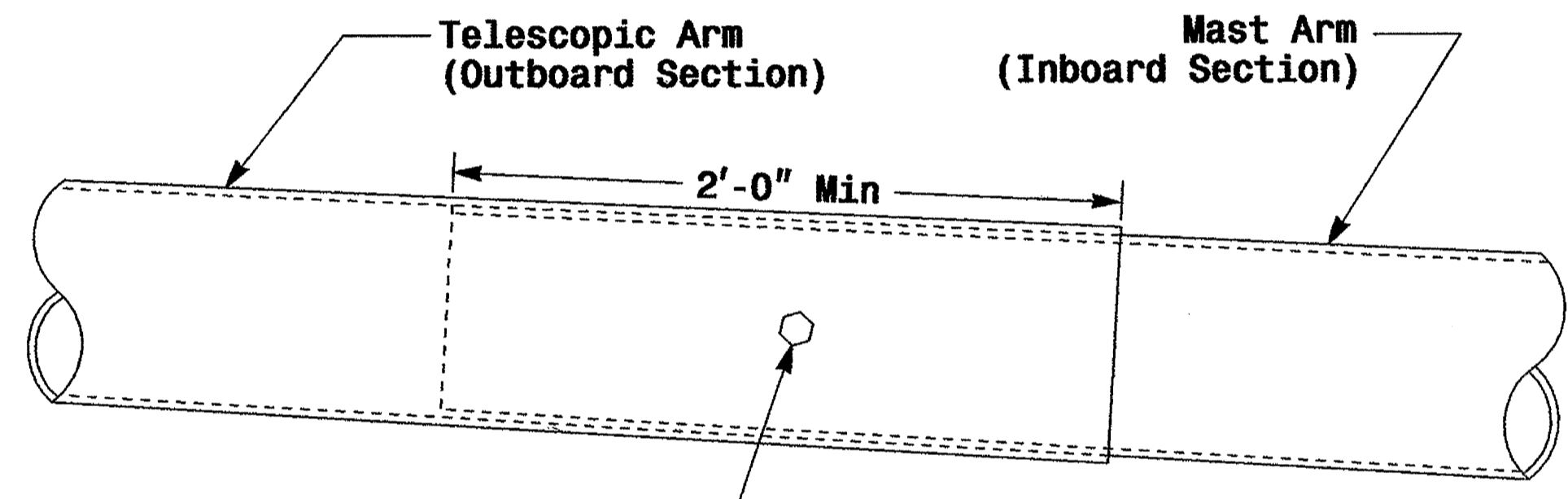
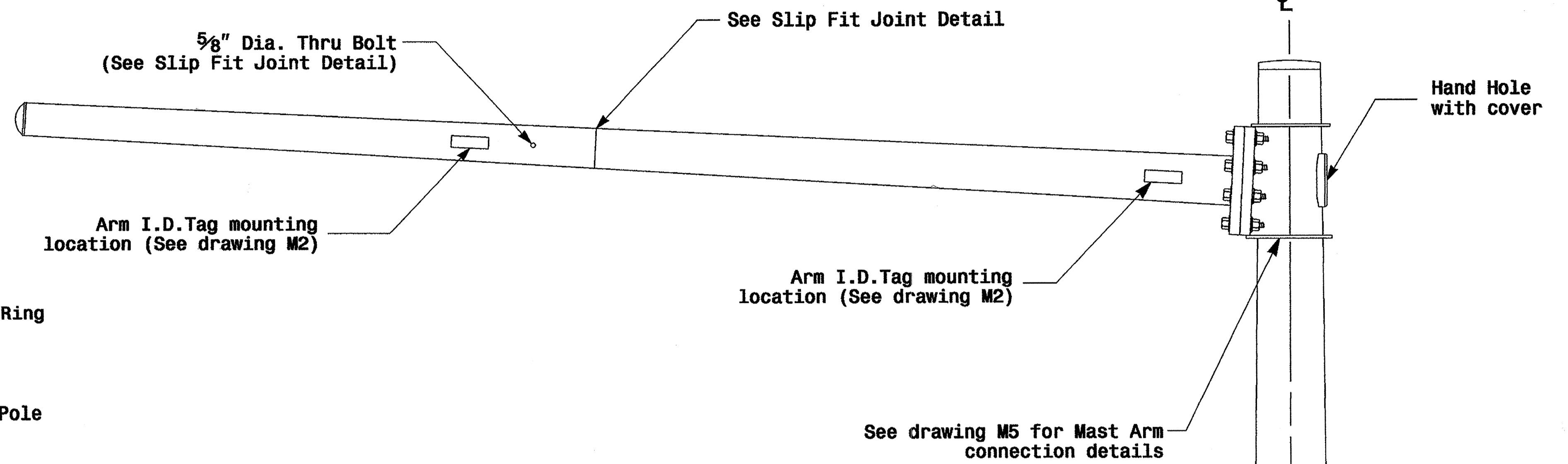
01-SEP-2005 18:22 D:\2004 Metal Pole Standards\2004.m2 thru m6.dgn candra



Section A-A  
(See drawing M 2)  
**Pole Base Plate**

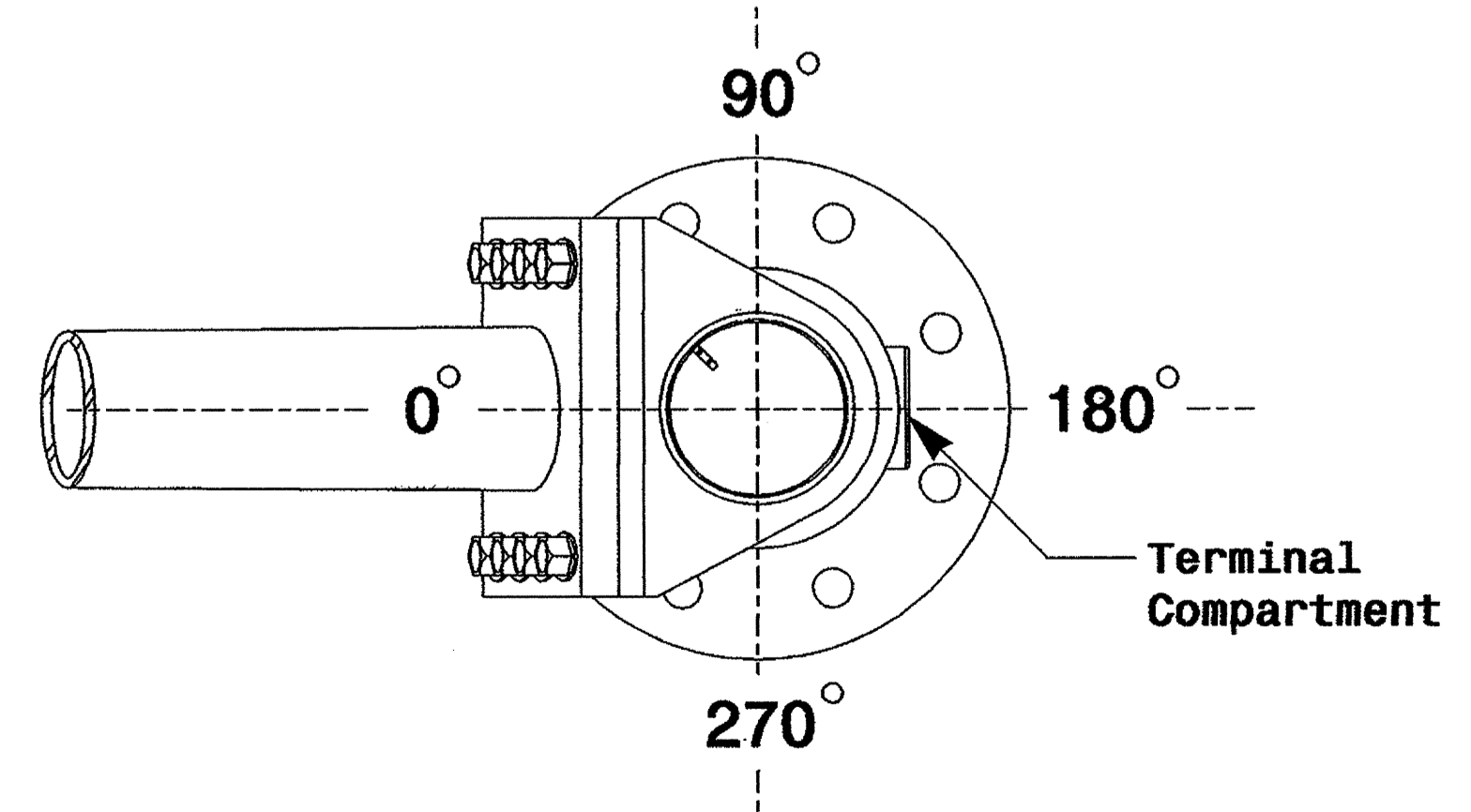


Section B-B  
(Pole Attachment to Base Plate)  
**Full-Penetration Groove Weld Detail**

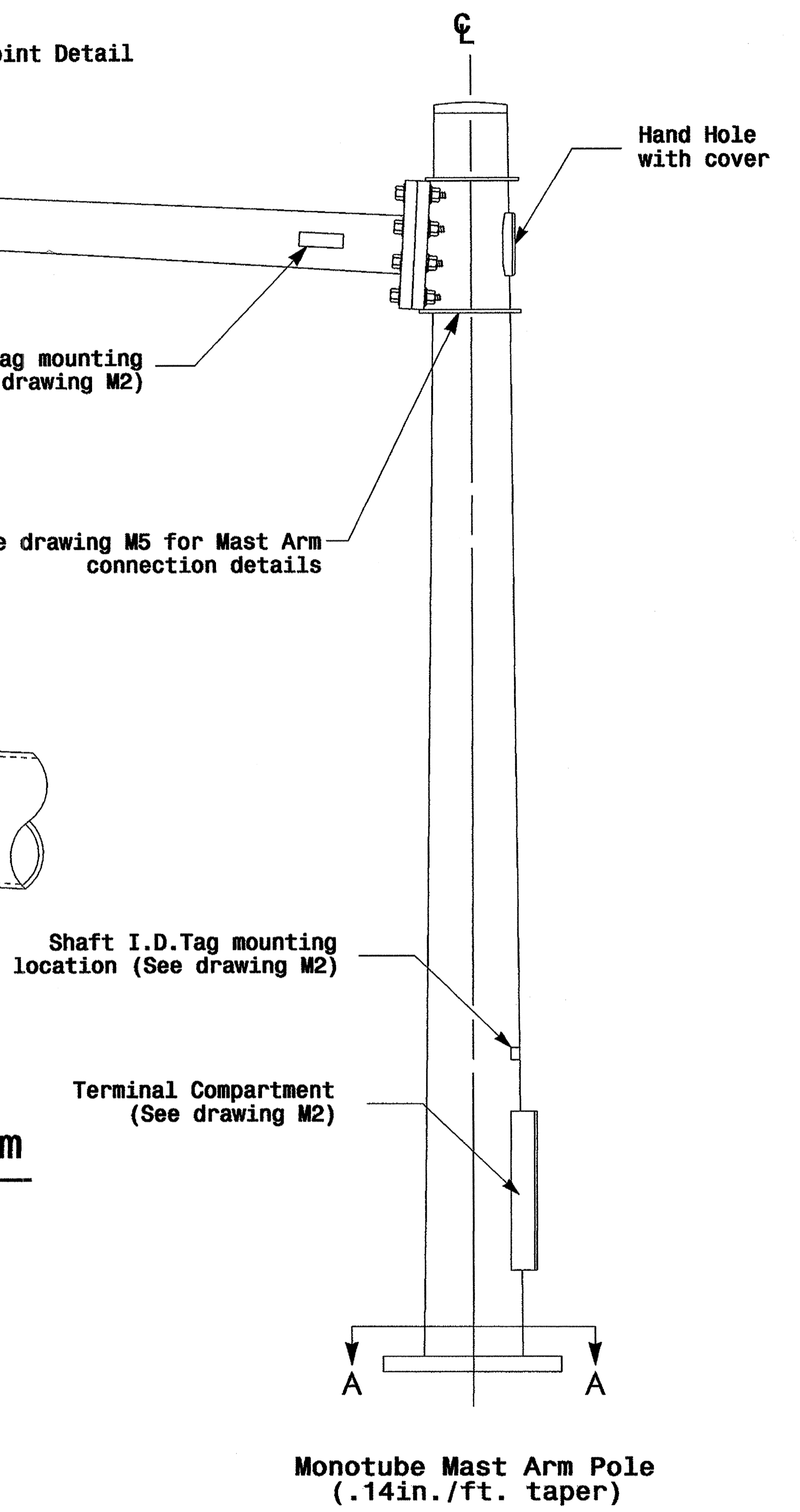


3/4" Factory Drilled Hole in Outboard Tube.  
Field Drill Inboard Tube.  
5/8" Galvanized Thru Stud with (2) Hex. Locknuts Ea.

**Slip Fit Joint Detail for Mast Arm**



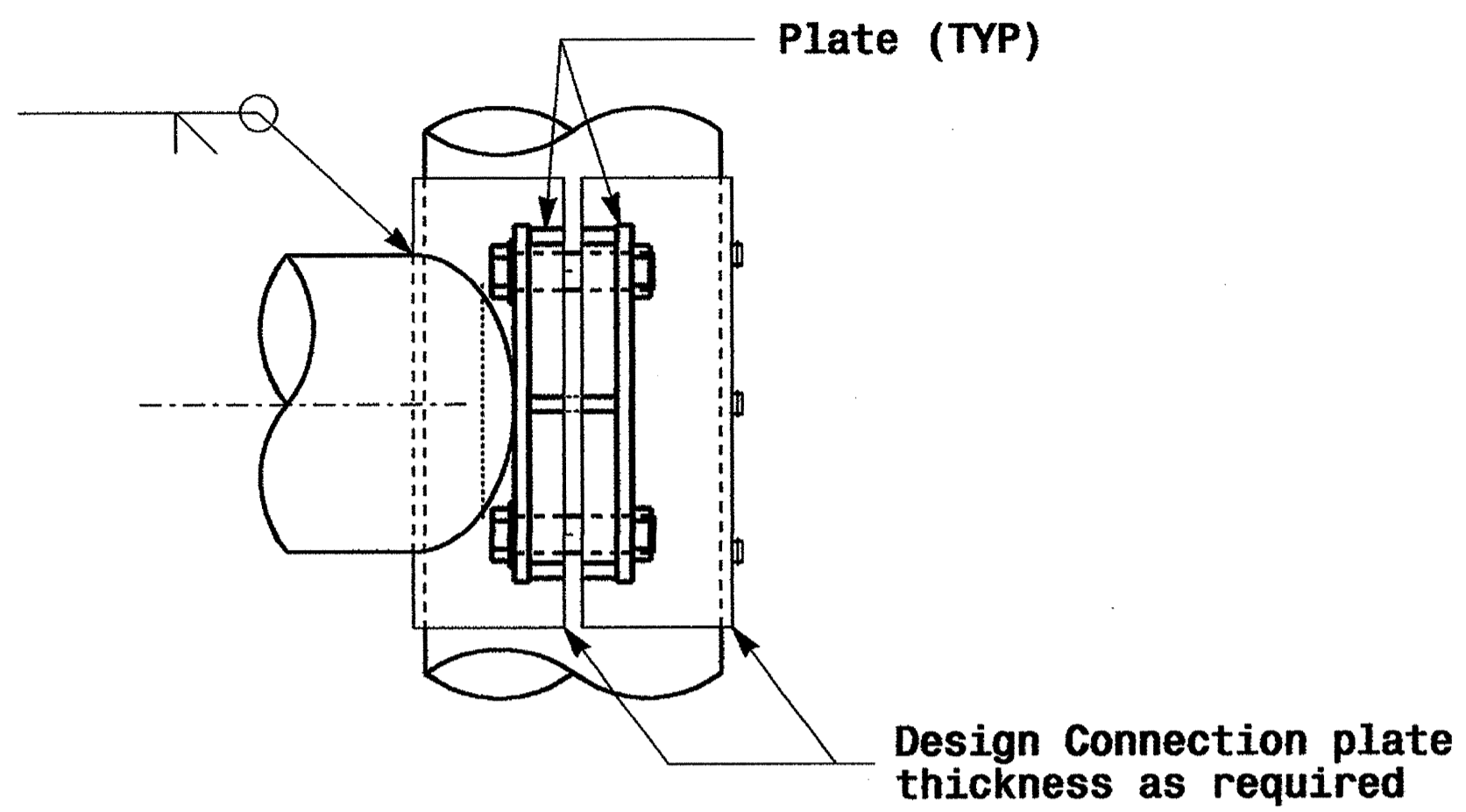
**Mast Arm Radial Orientation**



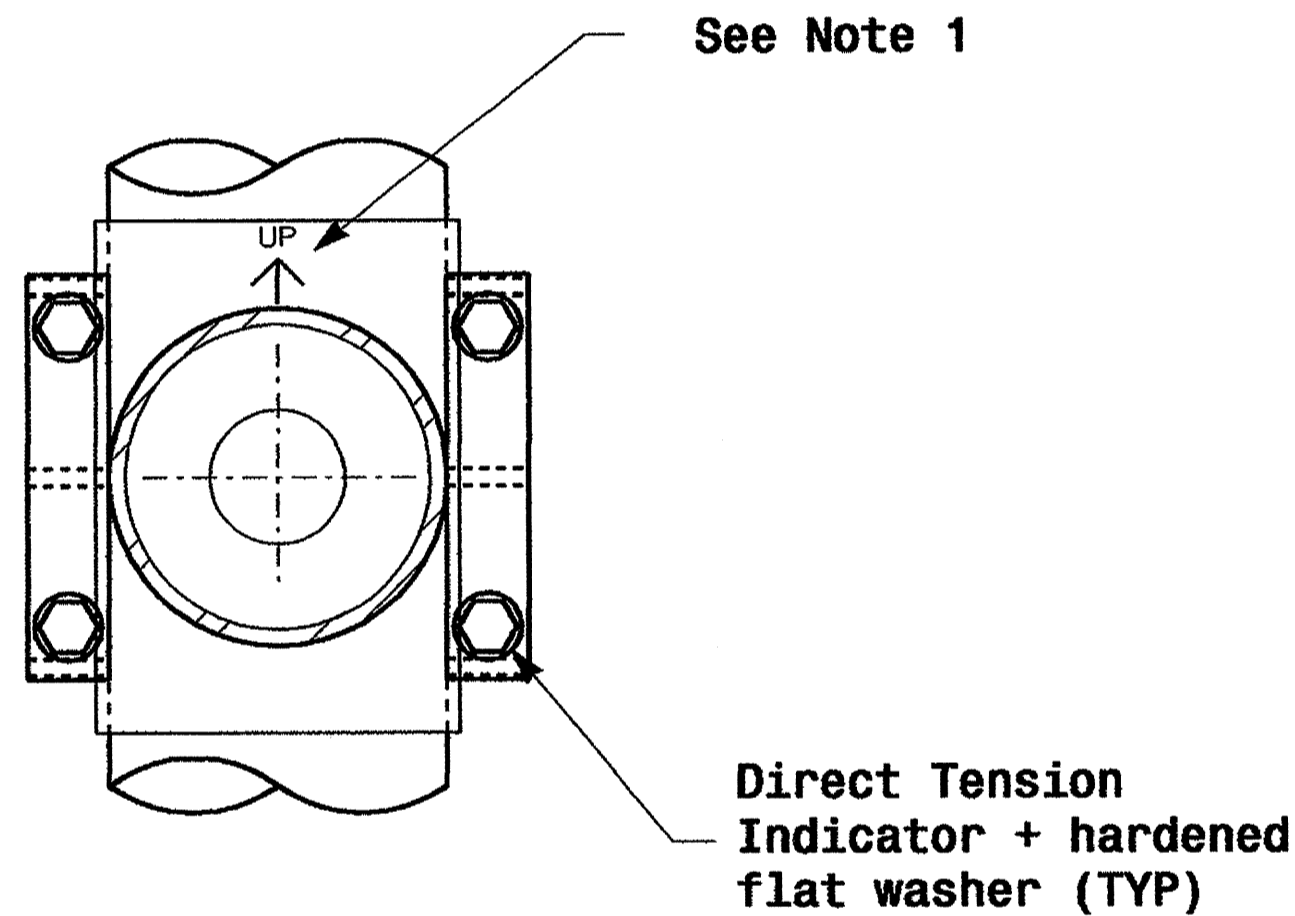
	<b>Typical Fabrication Details for Mast Arm Poles</b>			
	PLAN DATE: May 2005 PREPARED BY: P.L. Alexander SCALE: 0 NA NONE	REVIEWED BY: C.F. Andrews REVIEWED BY: A.W. Esposito REVISIONS: _____ INIT. DATE		SEAL NORTH CAROLINA PROFESSIONAL ENGINEER SEAL 028094 D. SACKER 9.2.2005 SIGNATURE DATE SIG. INVENTORY NO.
	Prepared to the Office of:  222 N. McDowell St., Raleigh, NC 27603			

01-SEP-2005 14:08 v1:rev01:14e-unit:hw:typ:poles004 metal pole standard:ds004 int. dgp p.l.alexander

# Adjustable Clamp Type Bolted Mast Arm Connection

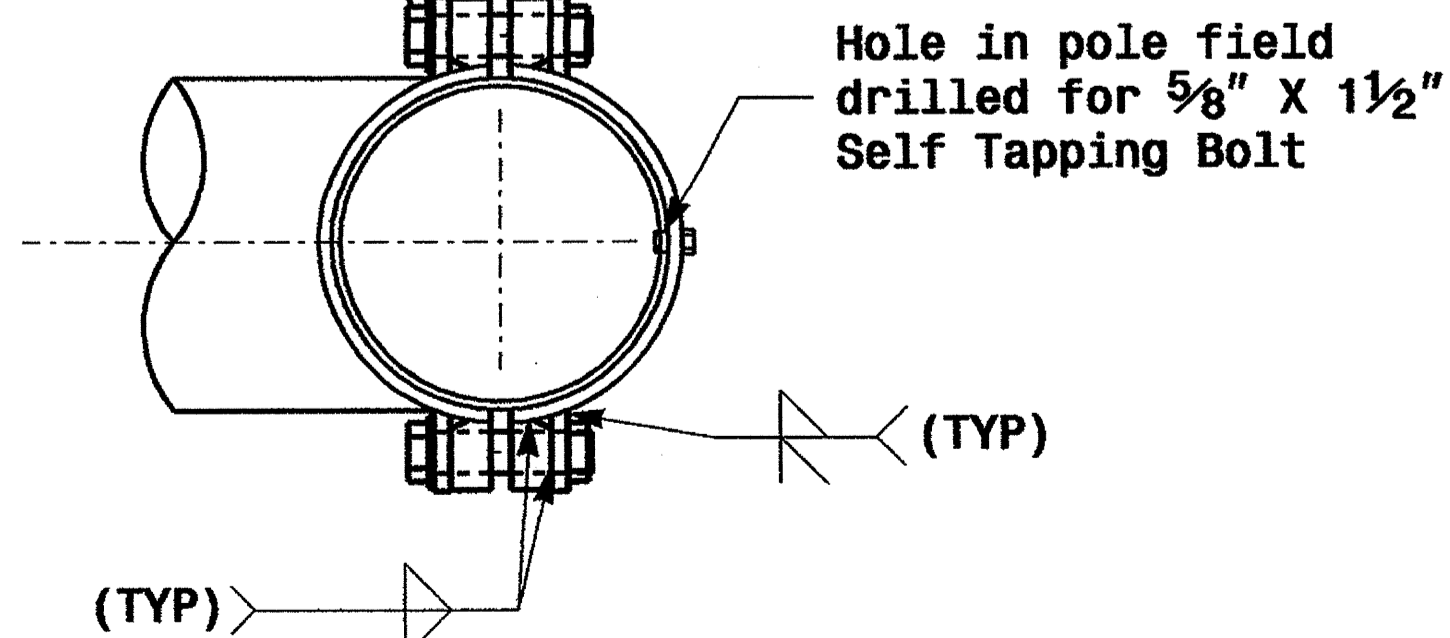


Side Elevation View



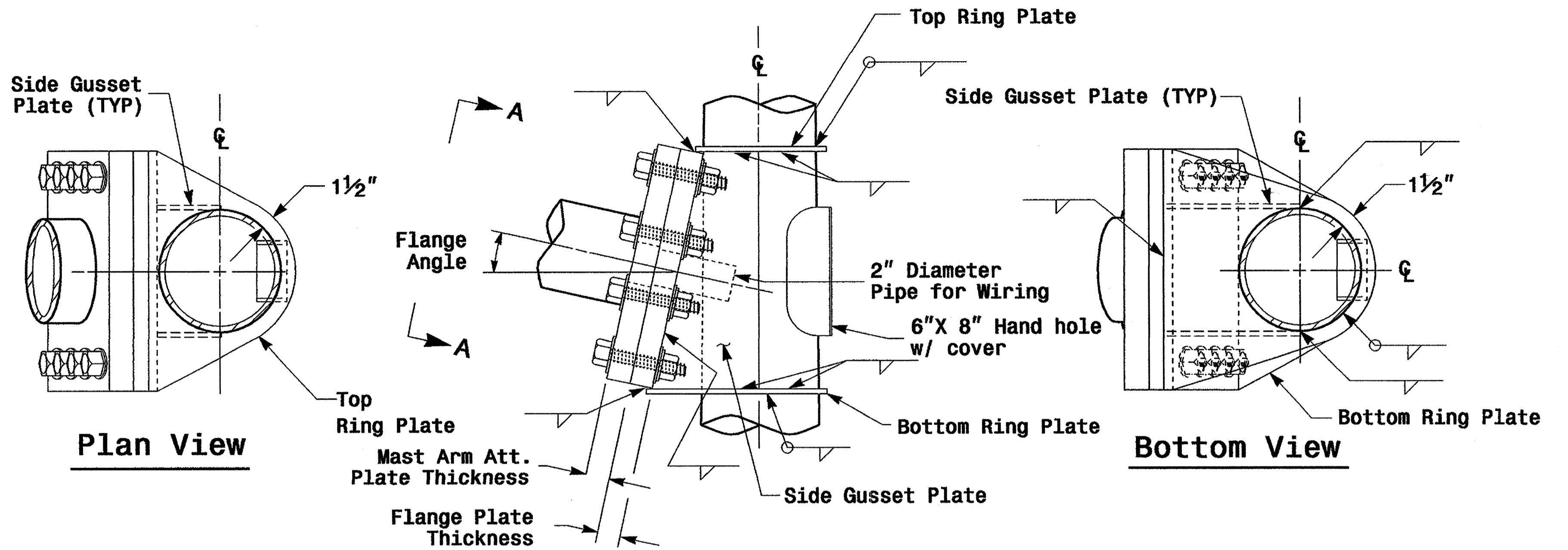
Front Elevation View

(4) - Size "E" Hex Head Bolts with (1) Hex Nuts & Washers

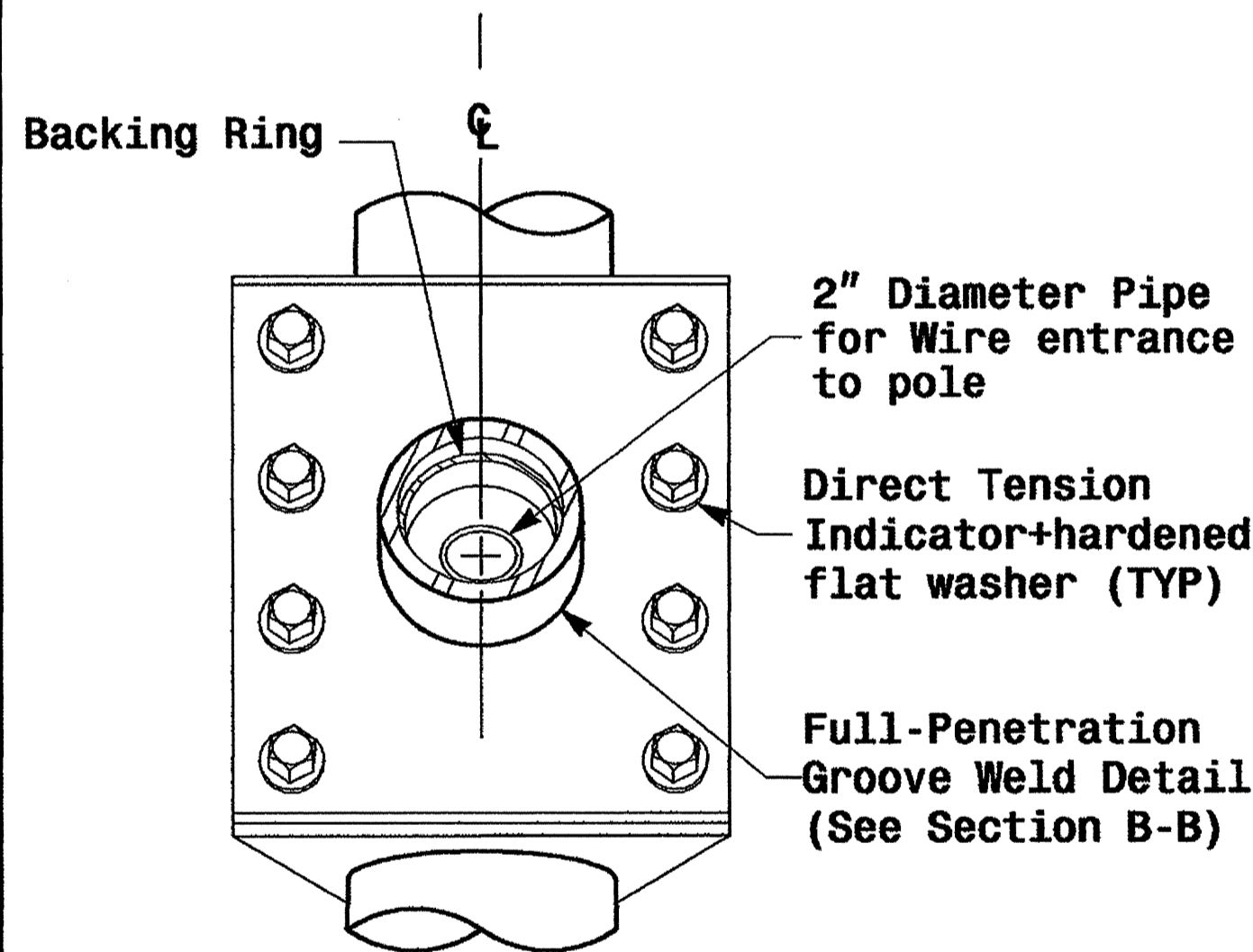


Plan View

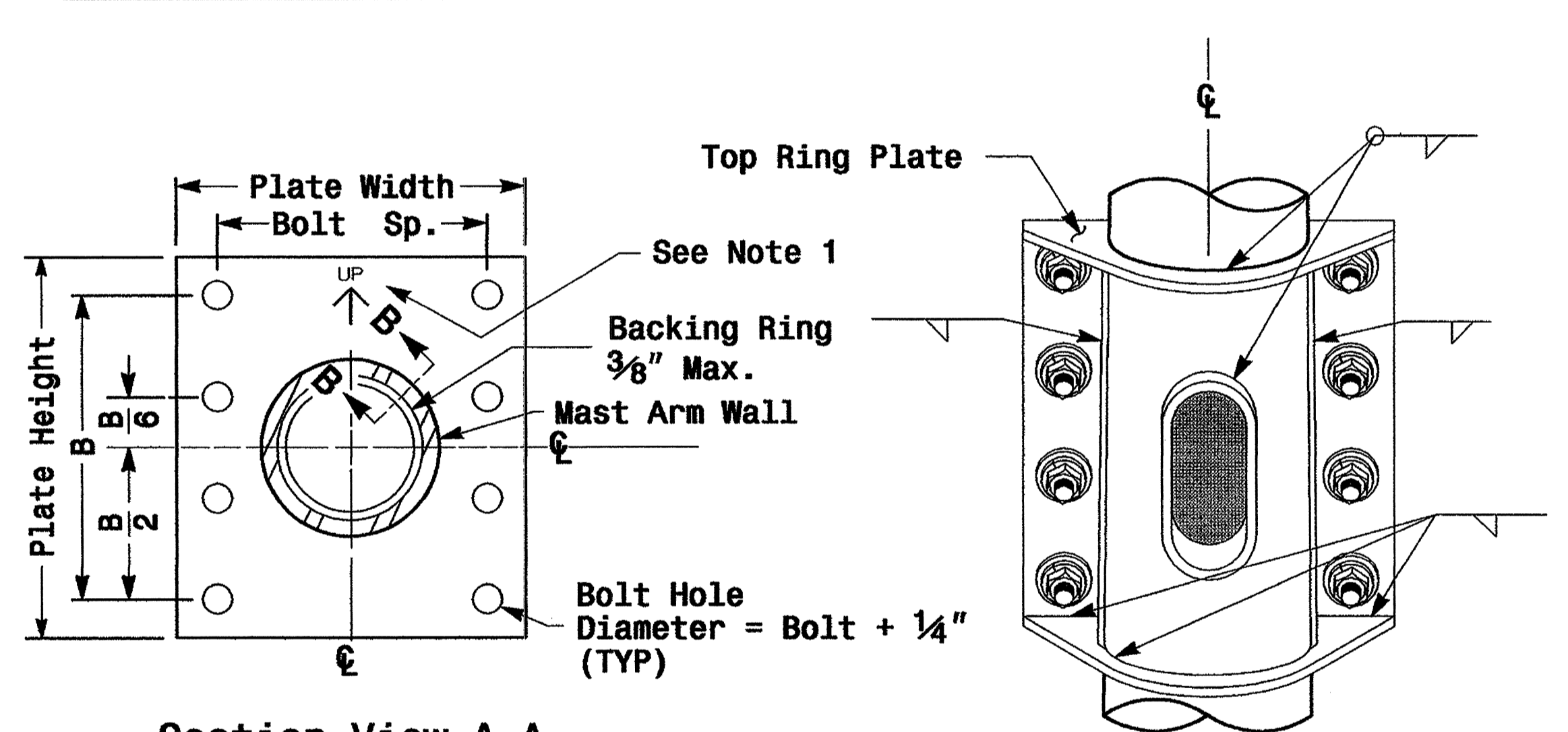
# Welded Ring Stiffened Mast Arm Connection



Side Elevation View

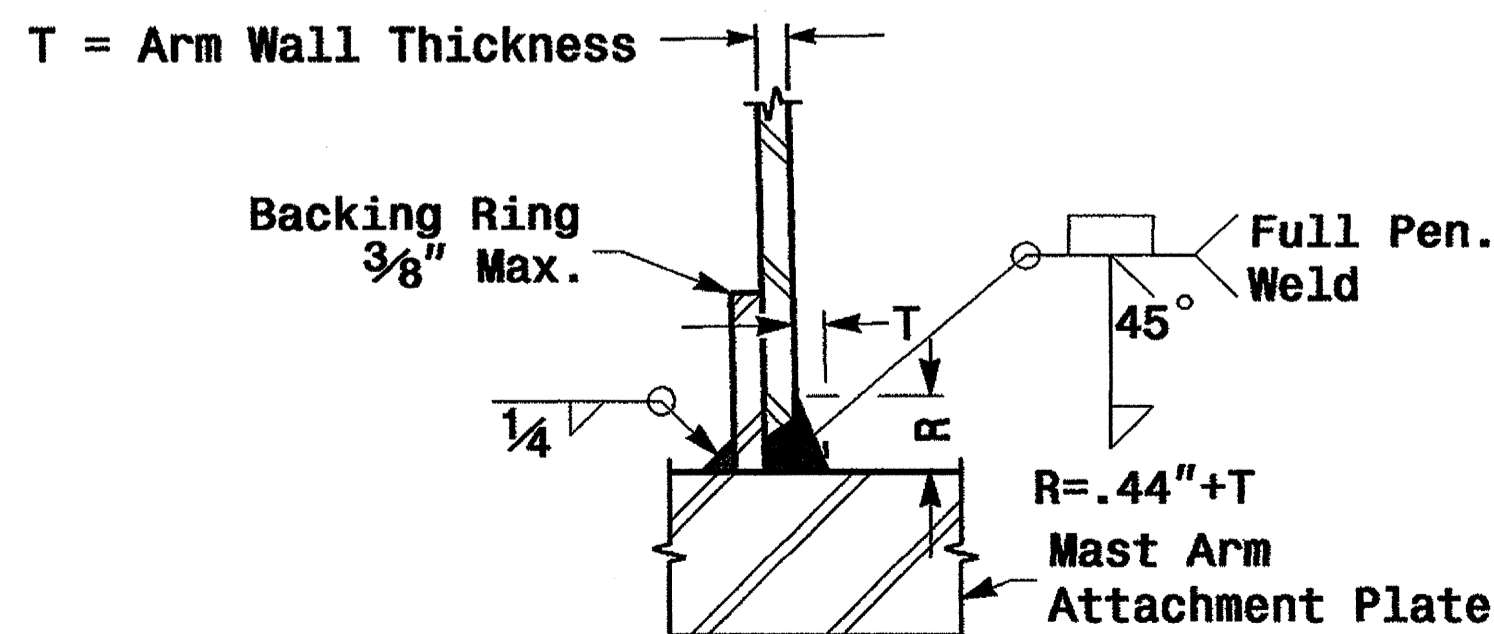


Front Elevation View



Section View A-A Mast Arm Attachment Plate

Back Elevation View



Section B-B Full-Penetration Groove Weld Detail

Notes:

1. Provide a permanent means of identification above the mast arm to indicate proper attachment orientation of the mast arm.
2. Designer will determine the size of all structural components, plates, fasteners, and welds shown unless they are already specified.
3. Designer is responsible for providing appropriate drainage points.

Fabrication Details - Mast Arm Poles

01-SEP-2005 14:11:11 w:\pocp\ee-un1\work\groups\2004 metal pole stand\ds42004.m5.dgn p01alexander

<p>222 N. McDowell St., Raleigh, NC 27603</p>	<p align="center"><b>Fabrication Details For Mast Arm Connection To Pole</b></p>		
	<p>PLAN DATE: May 2005</p>	<p>REVIEWED BY: C.F. Andrews</p>	
<p>PREPARED BY: P.L. Alexander</p>	<p>REVIEWED BY: A.M. Esposito</p>	<p>REVISIONS</p>	<p>SIGNATURE: <i>D. Sarkar</i> 9.2.2005</p>
<p>SCALE: 0 NA NONE</p>	<p>INIT. DATE</p>	<p>DATE</p>	<p>SIG. INVENTORY NO.</p>





STATE OF NORTH CAROLINA  
DEPT. OF TRANSPORTATION  
DIVISION OF HIGHWAYS  
RALEIGH, N.C.

11-08

ENGLISH DETAIL DRAWING FOR  
**INDUCTIVE DETECTION LOOPS**

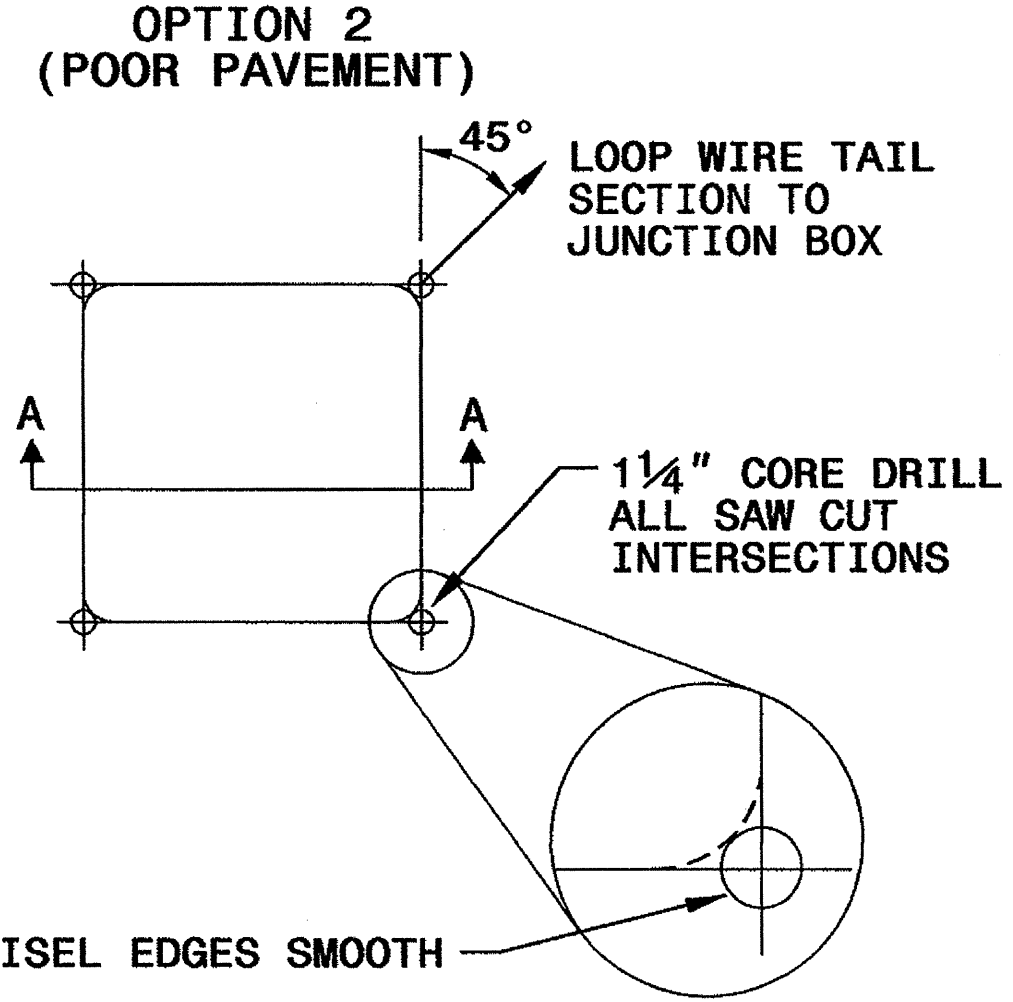
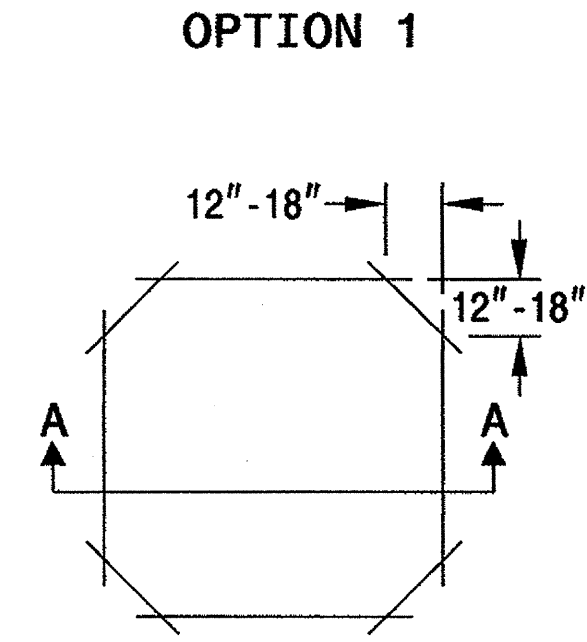
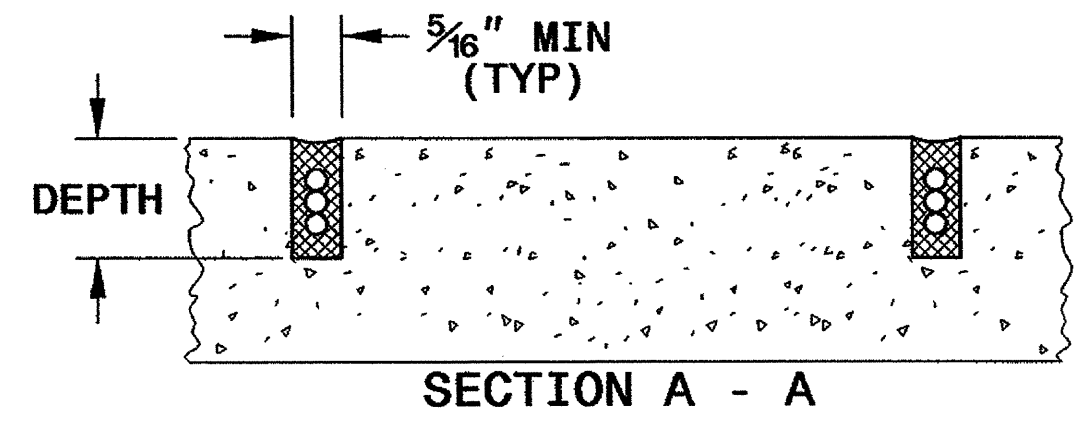
SHEET 1 OF 3  
**1725D01**

**CONVENTIONAL 4-SIDED LOOP**

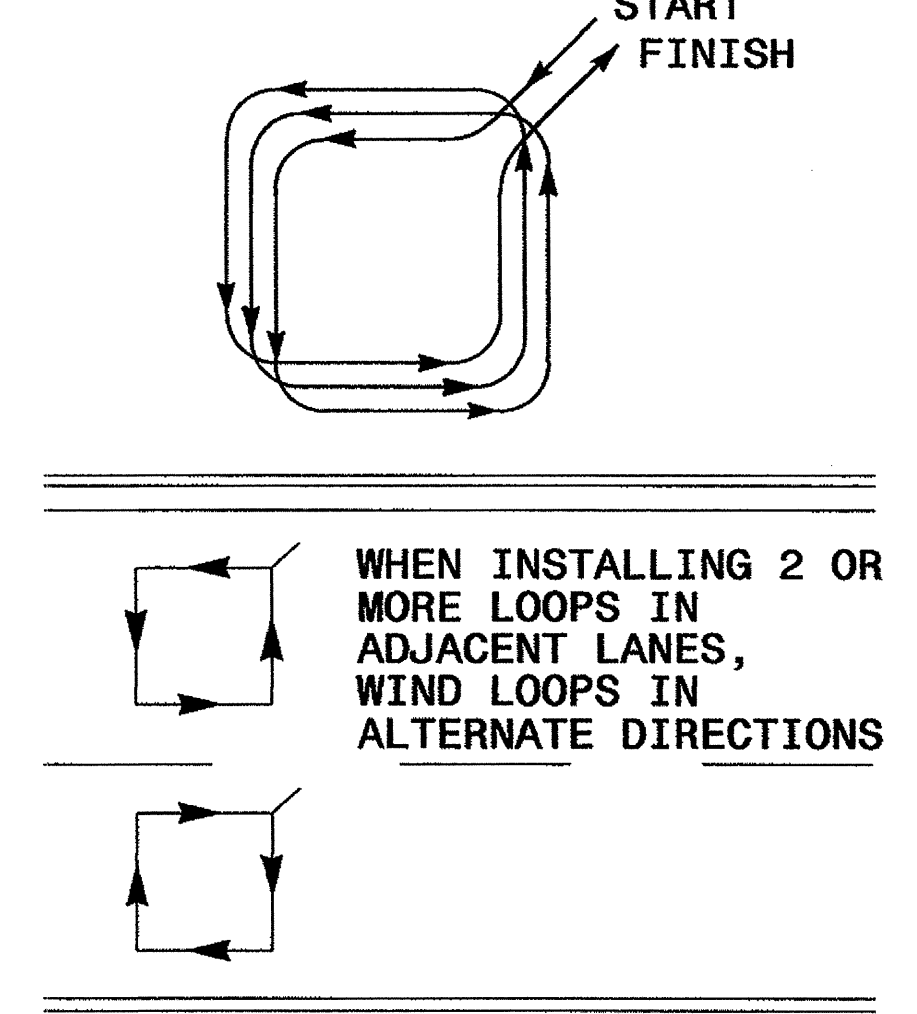
**SAW CUT OPTIONS**

**SAW SLOT DEPTH CHART**

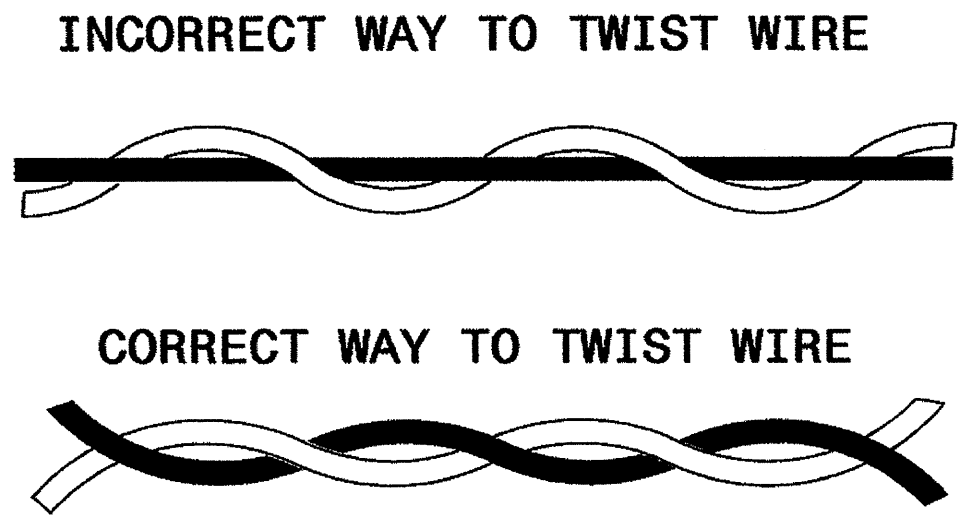
DEPTH (IN)	NO. OF WIRE TURNS				
	2	3	4	5	6
CONCRETE	2.0	2.0	2.5	2.5	3.0
ASPHALT	2.0	2.5	3.0	3.0	3.0



**LOOP WINDING METHOD**



**LOOP WIRE TWISTING METHOD**

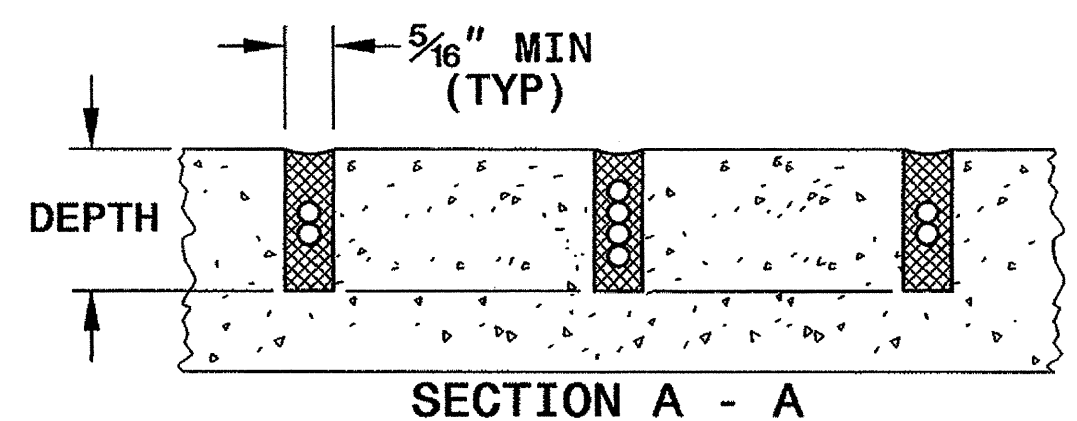
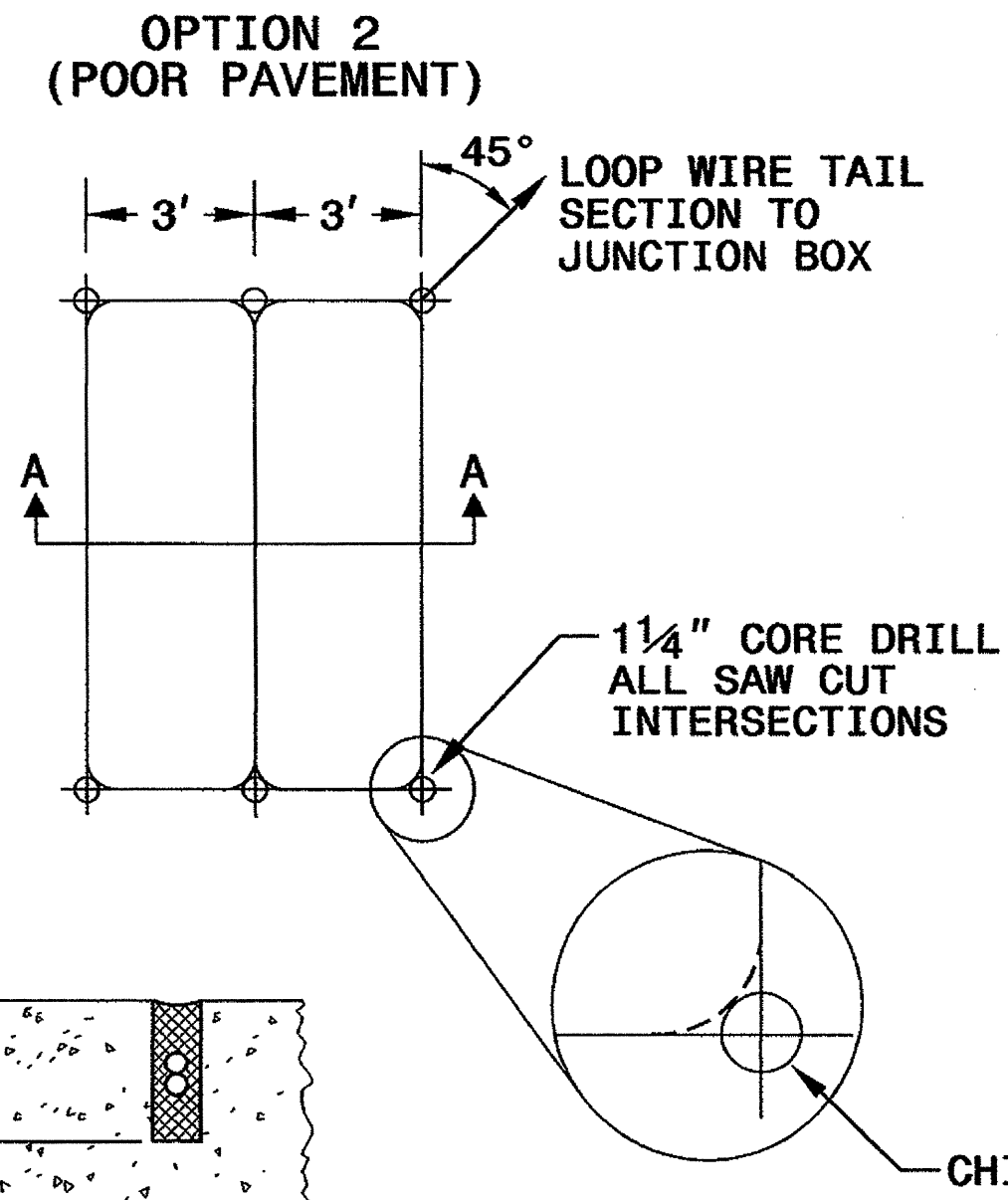
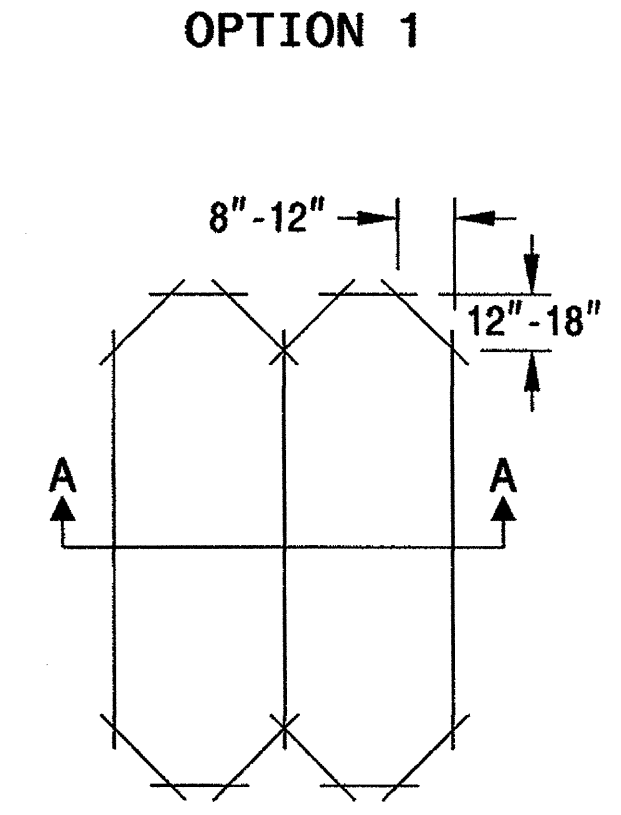


**NOTES**

1. OVERLAP SAW CUTS AT CORNERS AND INTERSECTION POINTS TO ENSURE UNIFORM SAW SLOT DEPTH.
2. MAINTAIN 12" SPACING BETWEEN LOOP WIRE TAIL SECTIONS.
3. WIRE LOOPS CONNECTED TO THE SAME DETECTOR CHANNEL IN SERIES.
4. LOCATE LOOPS IN CENTER OF LANES UNLESS OTHERWISE SHOWN ON PLANS OR APPROVED BY ENGINEER.

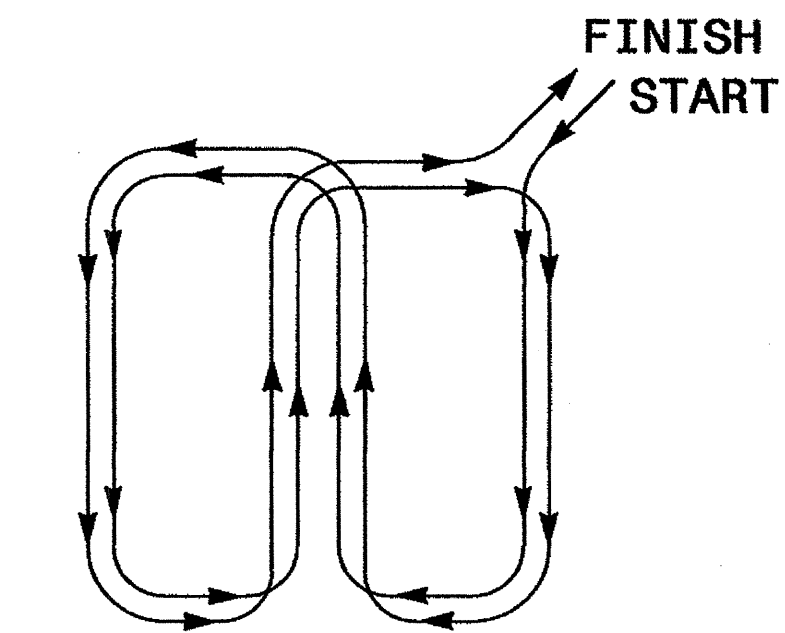
**QUADRUPOLE LOOP**

**SAW CUT OPTIONS**



DEPTH IS 2.5" FOR CONCRETE AND 3.0" FOR ASPHALT

**LOOP WINDING METHOD**



STATE OF NORTH CAROLINA  
DEPT. OF TRANSPORTATION  
DIVISION OF HIGHWAYS  
RALEIGH, N.C.

11-08

ENGLISH DETAIL DRAWING FOR  
**INDUCTIVE DETECTION LOOPS**

SHEET 1 OF 3  
**1725D01**

See Plate for Title

Prepared in the Offices of:

750 N. Greenfield Parkway  
Garner, NC 27529

SEAL

SIGNATURE: *Milton Dean* DATE: 11/24/08

24-Nov-2008 09:28 C:\work\1725D01\1725D01.dwg zml:tlf

STATE OF NORTH CAROLINA  
DEPT. OF TRANSPORTATION  
DIVISION OF HIGHWAYS  
RALEIGH, N.C.

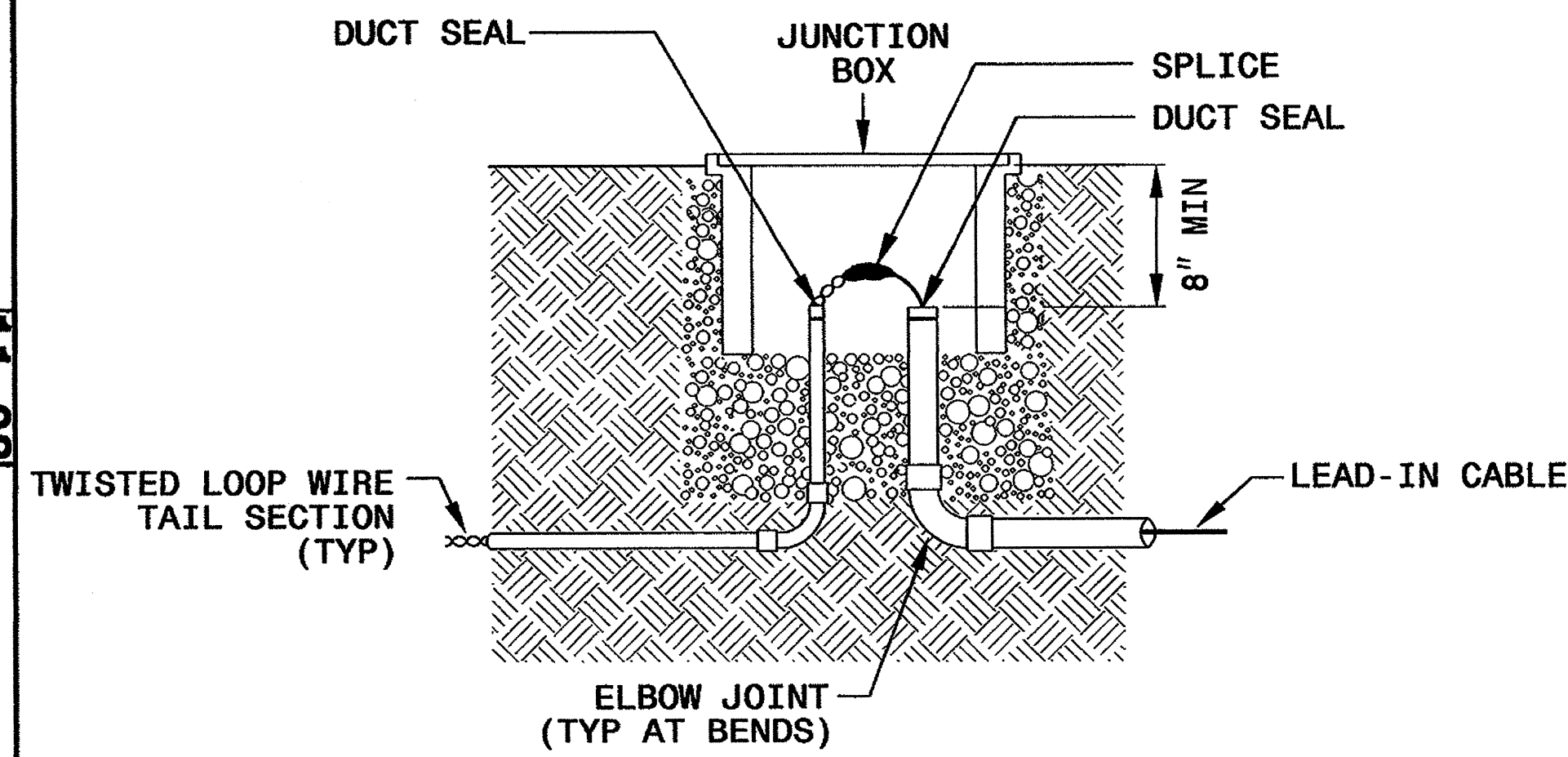
11-08

ENGLISH DETAIL DRAWING FOR  
**INDUCTIVE DETECTION LOOPS**  
LOOP WIRE DETAILS

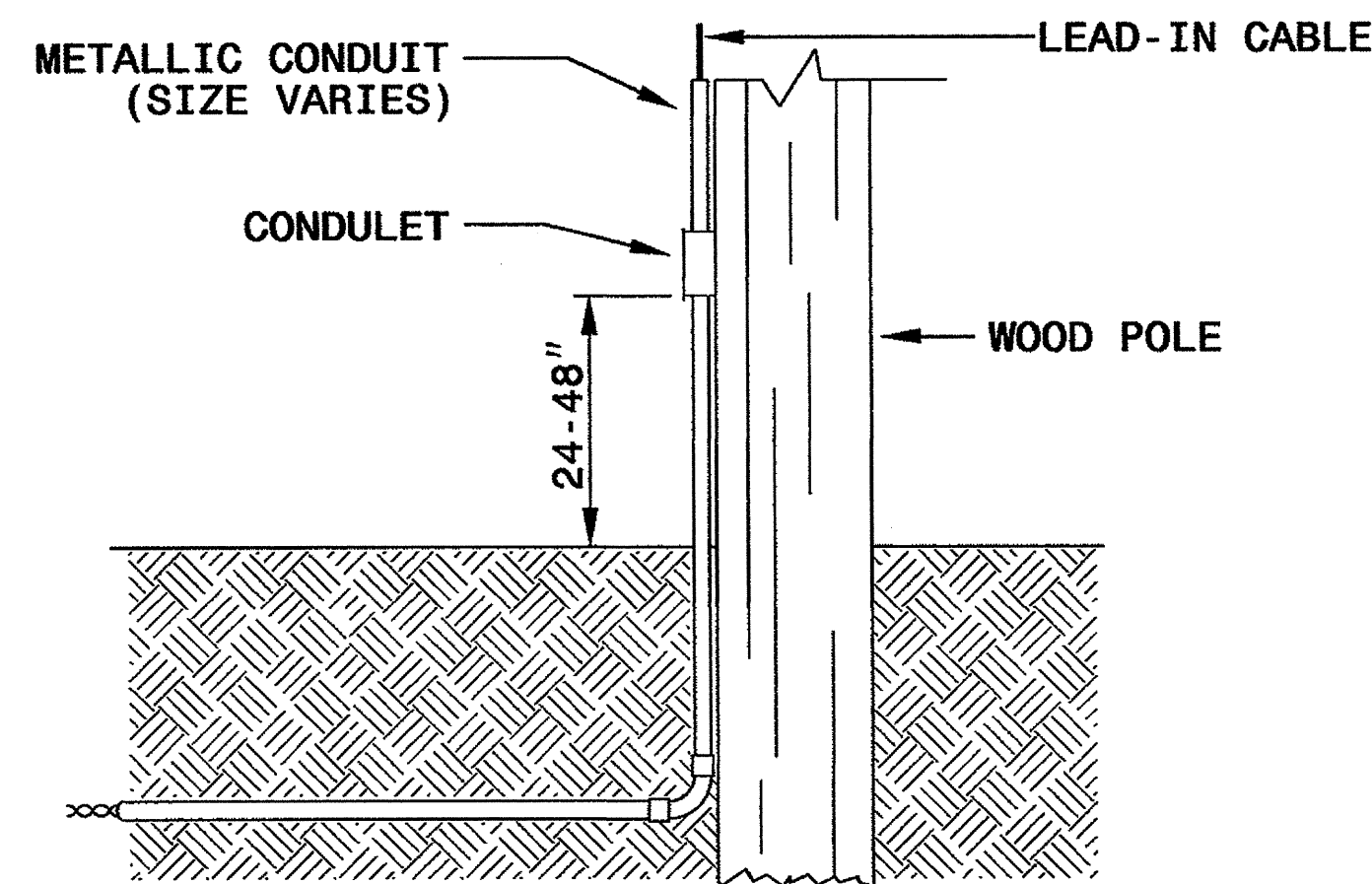
SHEET 2 OF 3  
**1725D01**

**LOOP WIRE SPLICE POINT DETAILS**

**LOOP WIRE AT JUNCTION BOX**



**LOOP WIRE AT POLE**

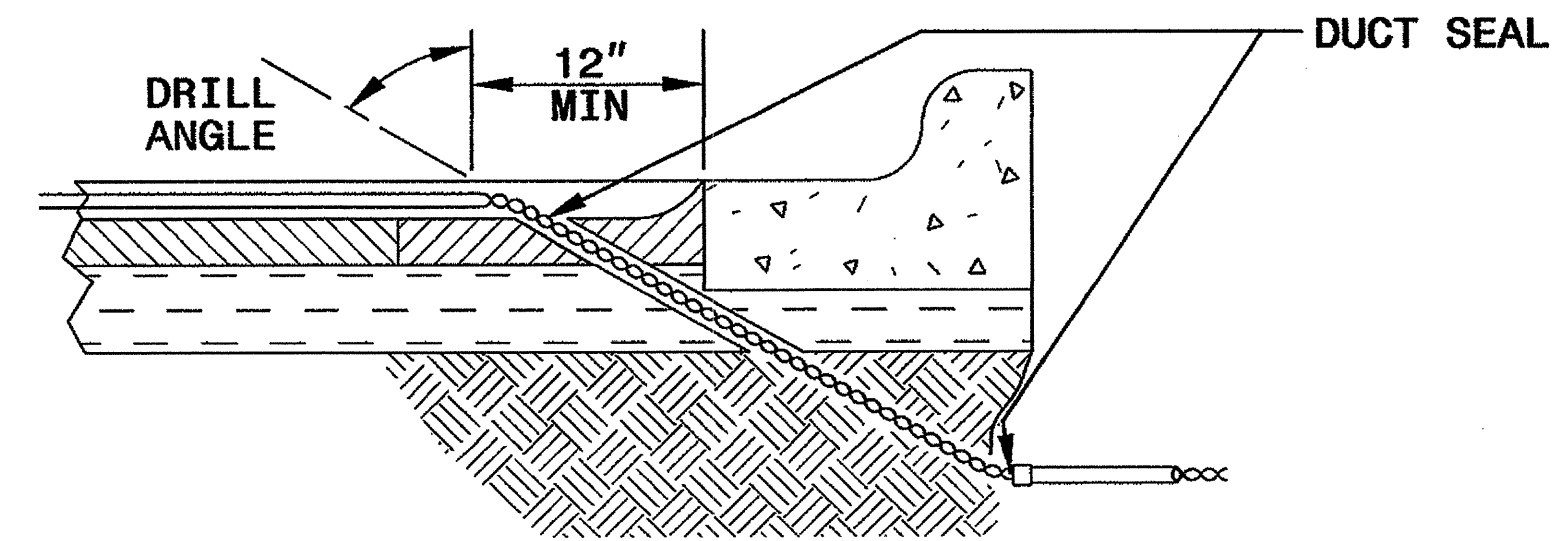


**NOTE**

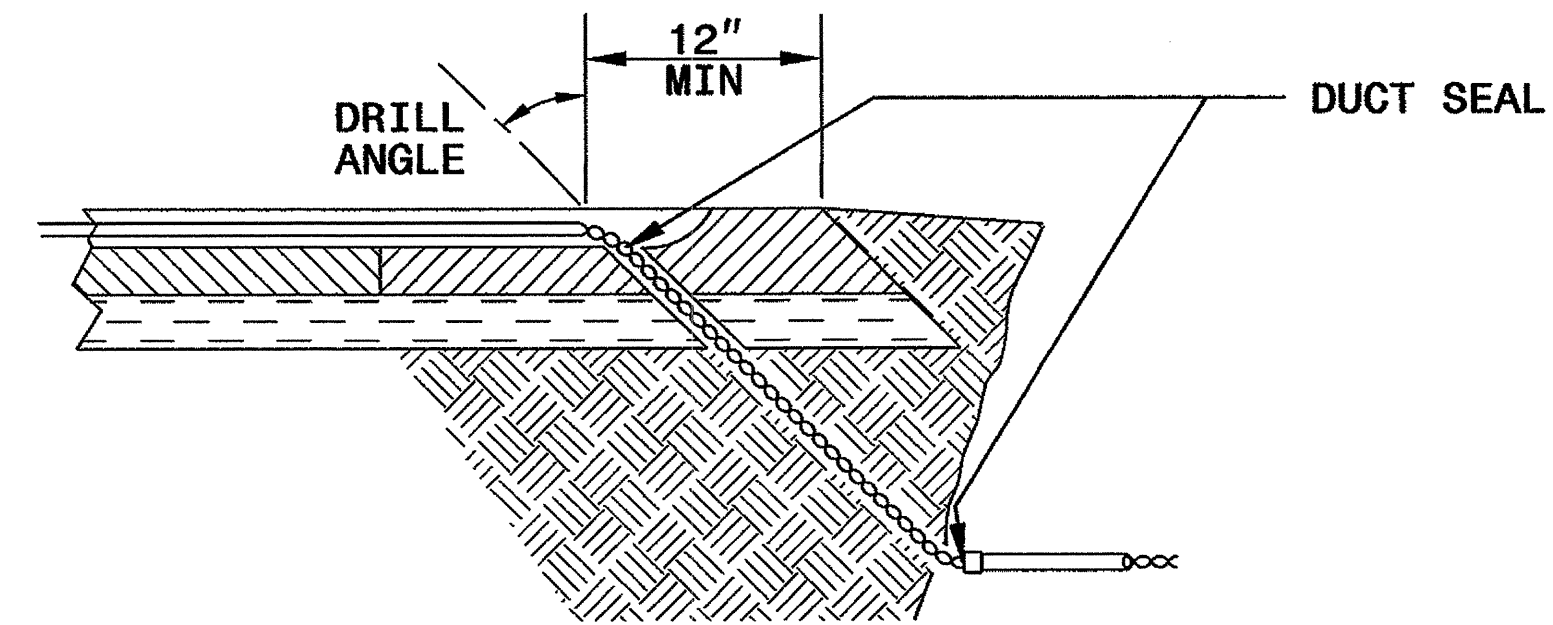
SPLICE ALL LOOP WIRE TAIL SECTIONS/LEAD-IN CABLE IN JUNCTION BOXES OR APPROVED CONDULETS.

**LOOP WIRE PAVEMENT EDGE DETAILS**

**LOOP WIRE AT CURB & GUTTER SECTION**



**LOOP WIRE AT PAVEMENT SECTION**



**NOTES**

- DO NOT EXCAVATE UNDER CURB AND GUTTER SECTIONS FOR CONDUIT INSTALLATION.
- TWIST LOOP WIRE TAIL SECTIONS FROM WHERE LOOP WIRE TAIL LEAVES SAW CUT TO JUNCTION BOX, INCLUDING THROUGH CONDUIT.
- BEFORE SEALING LOOPS, INSTALL DUCT SEAL WHERE LOOP WIRE TAIL SECTION LEAVES SAW CUT IN PAVEMENT AND AT ENTRANCE OF CONDUIT TO JUNCTION BOX.

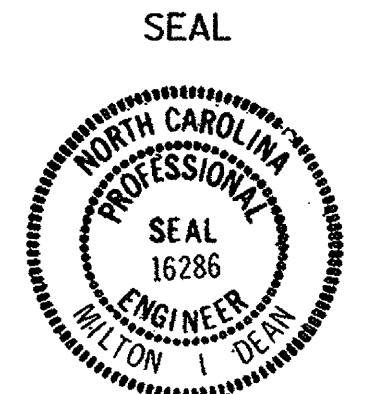
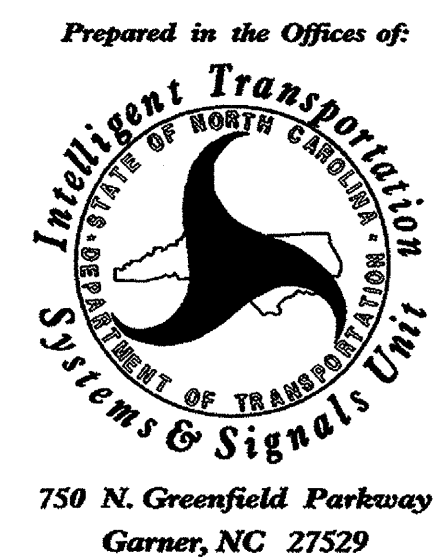
STATE OF NORTH CAROLINA  
DEPT. OF TRANSPORTATION  
DIVISION OF HIGHWAYS  
RALEIGH, N.C.

11-08

ENGLISH DETAIL DRAWING FOR  
**INDUCTIVE DETECTION LOOPS**  
LOOP WIRE DETAILS

SHEET 2 OF 3  
**1725D01**

See Plate for Title



Milton I. Dean 11/24/08  
SIGNATURE DATE

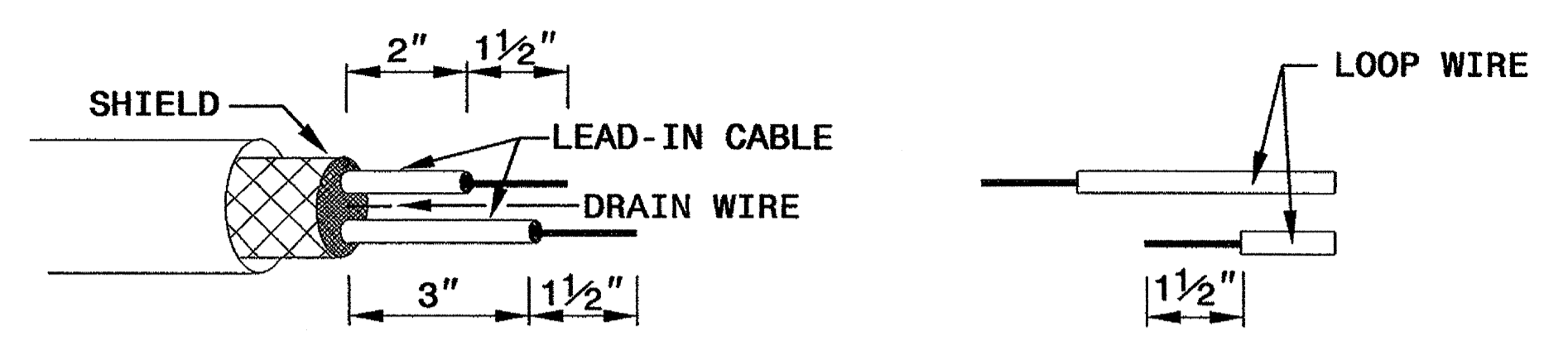
STATE OF  
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DEPT. OF TRANSPORTATION  
DIVISION OF HIGHWAYS  
RALEIGH, N.C.

11-08

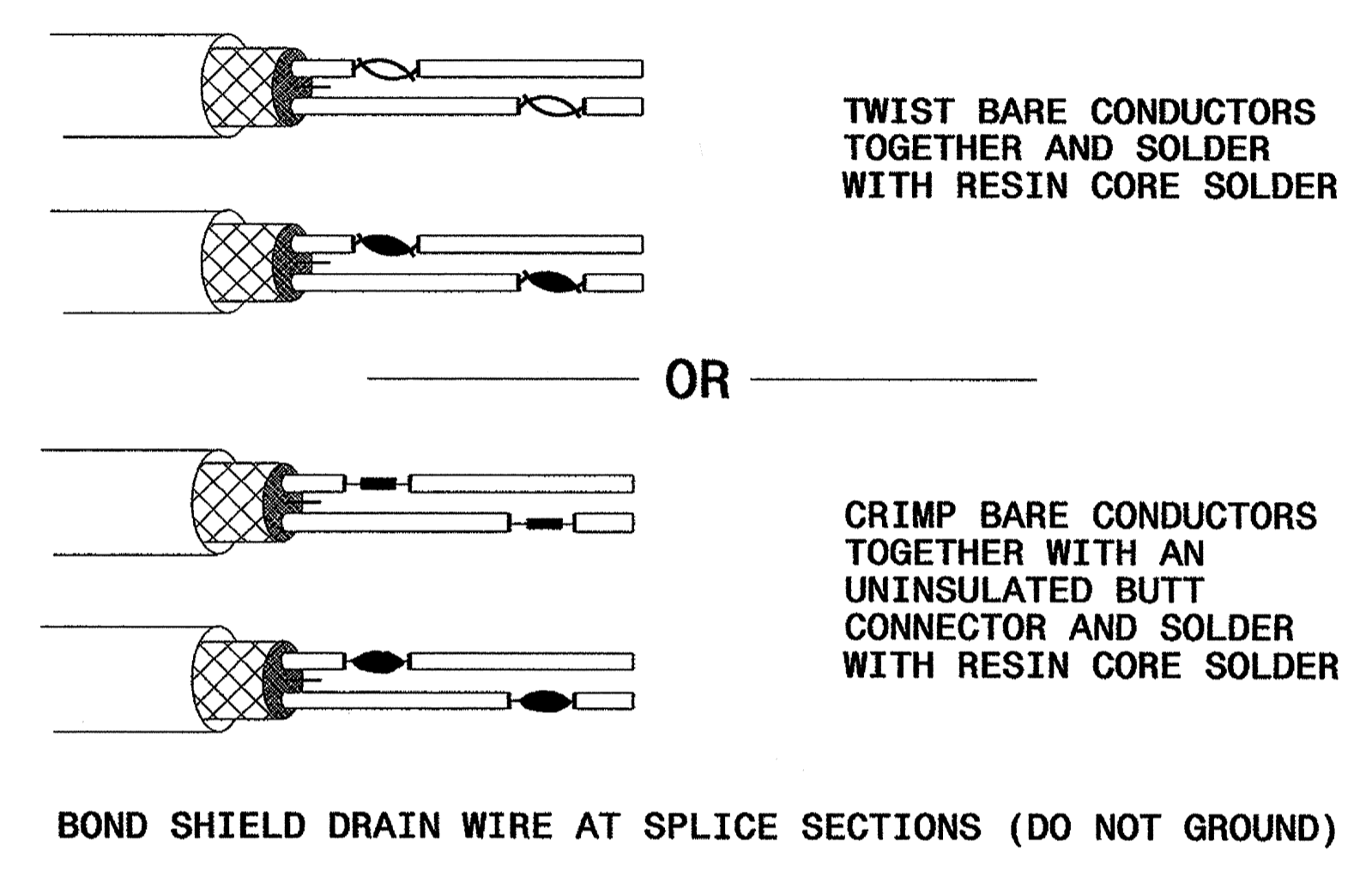
ENGLISH DETAIL DRAWING FOR  
**INDUCTIVE DETECTION LOOPS**  
SPlicing FOR LEAD-IN CABLE AND LOOP WIRE

SHEET 3 OF 3  
**1725D01**

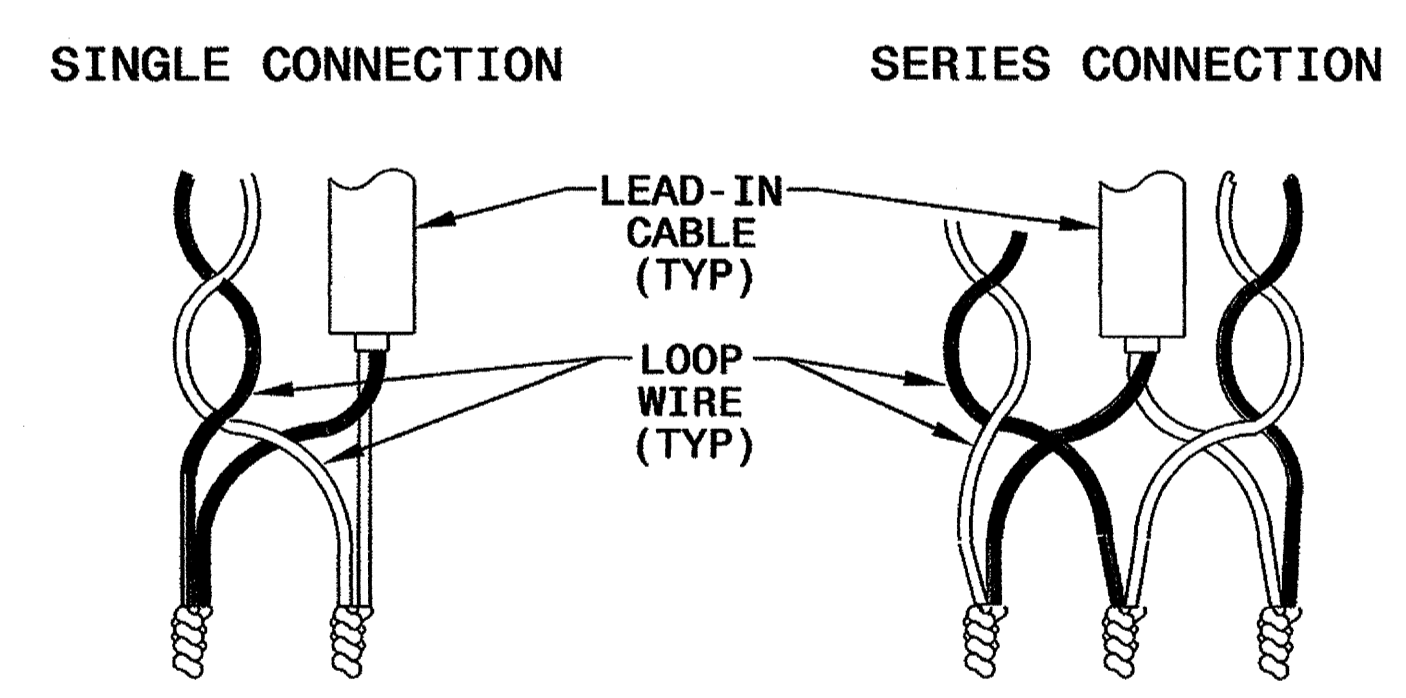
**STEP 1. STRIP LOOP WIRE AND LEAD-IN CABLE**



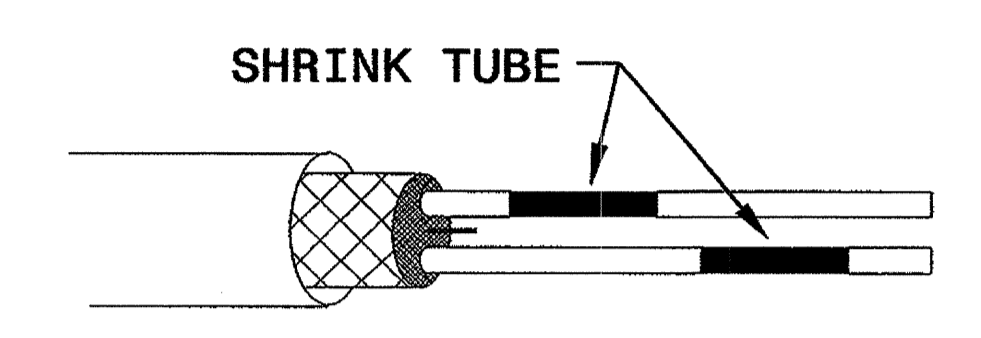
**STEP 2. CONNECT AND SOLDER**



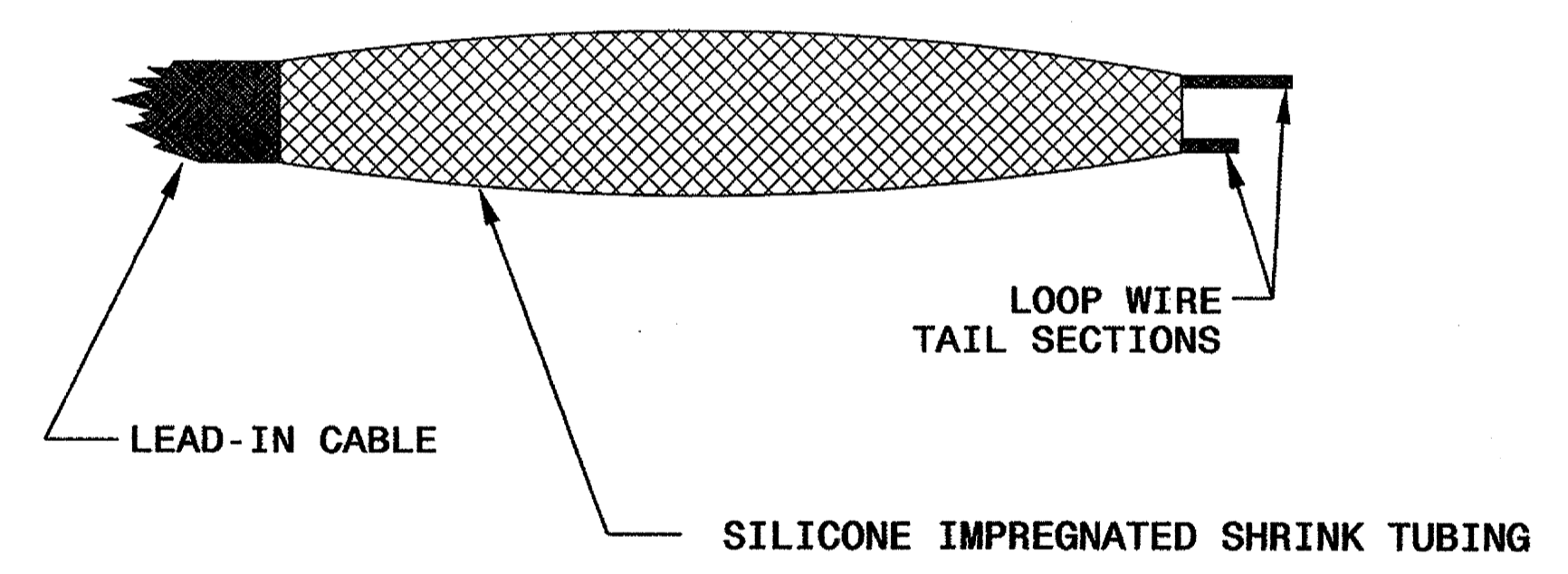
**LOOP WIRE AND LEAD-IN CABLE CONNECTION DETAILS**



**STEP 3. INSULATE EACH SOLDER JOINT SEPARATELY**



**STEP 4. ENVIRONMENTALLY PROTECT SPLICE**



STATE OF  
NORTH CAROLINA  
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DIVISION OF HIGHWAYS  
RALEIGH, N.C.

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ENGLISH DETAIL DRAWING FOR  
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SPlicing FOR LEAD-IN CABLE AND LOOP WIRE

SHEET 3 OF 3  
**1725D01**

See Plate for Title

Prepared in the Offices of:

750 N. Greenfield Parkway  
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SEAL

Milton I. Dean 11/24/08  
SIGNATURE DATE

24-NOV-2008 09:16  
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