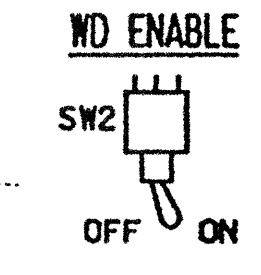
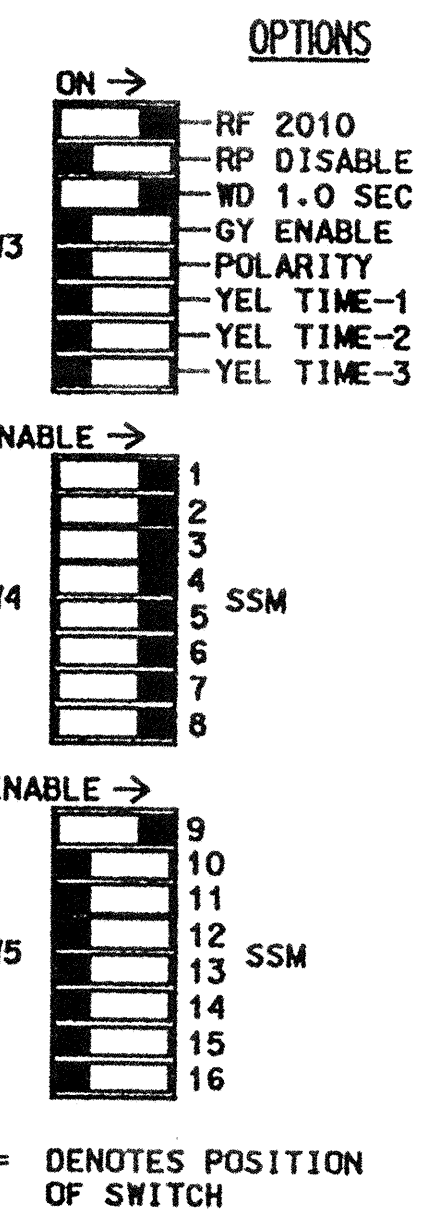
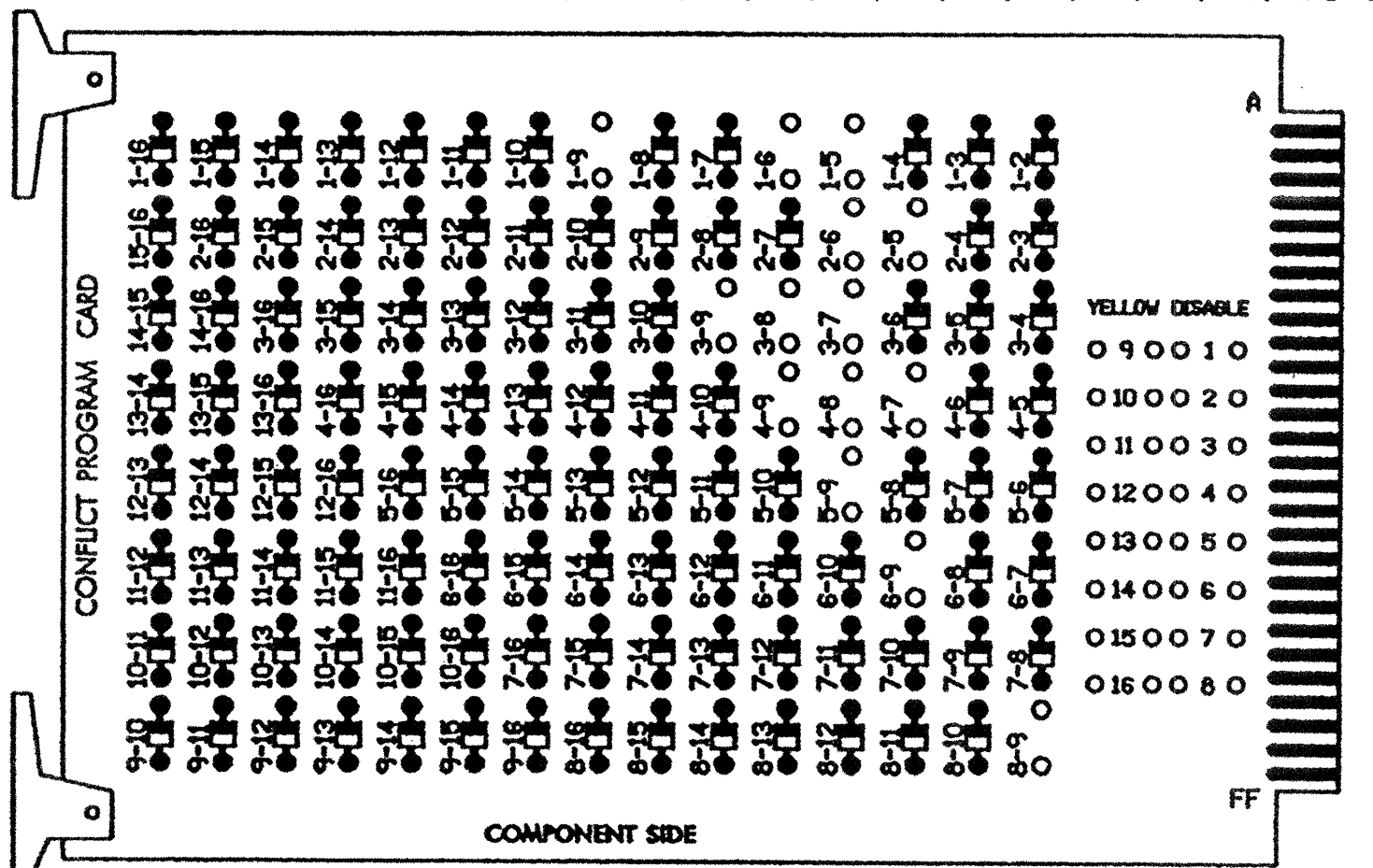


EDI MODEL 2010ECL CONFLICT MONITOR PROGRAMMING DETAIL



(Remove jumpers and set switches as shown)

REMOVE DIODE JUMPERS 1-5,1-6,1-9,2-5,2-6,3-7,3-8,3-9,4-7,4-8,4-9,5-6,6-9, AND 8-9.



REMOVE JUMPERS AS SHOWN

NOTES:

- Card is provided with all diode jumpers in place. Removal of any jumper allows its channels to run concurrently.
- Make sure jumpers SEL1-SEL5 are present on the monitor board.

NOTES

- To prevent "flash-conflict" problems, insert red flash program blocks for all unused vehicle load switches in the output file. The installer shall verify that signal heads flash in accordance with the Signal Plans.
- Ensure that Red Enable is active at all times during normal operation. To prevent Red Failures on unused monitor channels, tie unused red monitor inputs 10,11,12,13,14,15 & 16 to load switch AC+ per the cabinet manufacturer's instructions.
- Program phases 2 and 6, on the controller unit, for Start Up in Green.
- Enable Simultaneous Gap-Out, on the controller unit, for all phases.
- Program phases 2 and 6, on the controller unit, for Variable Initial and Gap Reduction.
- Program phases 4 and 8, on the controller unit, for Gap Reduction.

EQUIPMENT INFORMATION

CONTROLLER.....CONTRACTOR SUPPLIED 2070L
 CABINETCONTRACTOR SUPPLIED 332
 SOFTWAREECONOLITE OASIS
 CABINET MOUNT.....BASE
 OUTPUT FILE POSITIONS..18 (12-STD, 6-AUX)
 LOAD SWITCHES USED.....S1,S2,S3,S4,S5,S6,S7,S8,S9
 PHASES USED.....1,2,3,4,5,6,7,8
 OVERLAP A.....1+8

SIGNAL HEAD HOOK-UP CHART

LOAD SWITCH NO.	S1	S2	S2P	S3	S4	S4P	S5	S6	S6P	S7	S8	S8P	S9	S10	S11	S12	S13	S14	
PHASE	1	2	2 PED	3	4	4 PED	5	6	6 PED	7	8	8 PED	OLA ¹	OLB	SPARE	OLC	OLD	SPARE	
SIGNAL HEAD NO.	11,12	21,22	NU	31,32	22	41,42	NU	51,52	42	61,62	NU	71,72	62	81,82	NU	83,84	NU	NU	
RED		128			101			134			107								
YELLOW		129			102			135			108								
GREEN		130			103			136			109								
RED ARROW	125			116			131			122								A121	
YELLOW ARROW	126			117	117		132	132		123	123								A122
GREEN ARROW	127			118	118		133	133		124	124								A123

NU = Not Used

¹ Wire OLA to flash on Flasher 2. Circuit #1

INPUT FILE POSITION LAYOUT

(front view)

FILE	1	2	3	4	5	6	7	8	9	10	11	12	13	14
U	φ 1	φ 1	φ 2	←-NOT USED	φ 3	φ 4	φ 4	←-NOT USED	←-NOT USED	←-NOT USED	←-NOT USED	←-NOT USED	←-NOT USED	←-NOT USED
I	1A	1C	2A		3A	4A	4C							FS
L	φ 1	φ 1	φ 2	←-NOT USED	φ 3	φ 4	φ 4	←-NOT USED	←-NOT USED	←-NOT USED	←-NOT USED	←-NOT USED	←-NOT USED	DC ISOLATOR
U	φ 5	φ 5	φ 6	←-NOT USED	φ 7	φ 8	φ 8	←-NOT USED	←-NOT USED	←-NOT USED	←-NOT USED	←-NOT USED	←-NOT USED	DC ISOLATOR
I	5A	5C	6A		7A	8A	8C							ST
L	φ 5	NOT USED	φ 6	←-NOT USED	φ 7	φ 8	φ 8	←-NOT USED	←-NOT USED	←-NOT USED	←-NOT USED	←-NOT USED	←-NOT USED	
U	5B		6B		7B	8B	8D							

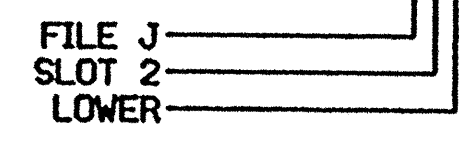
EX.: 1A, 2A, ETC. = LOOP NO.'S

FS = FLASH SENSE
 ST = STOP TIME

INPUT FILE CONNECTION & PROGRAMMING CHART

LOOP NO.	LOOP TERMINAL	INPUT FILE POS.	PIN NO.	INPUT ASSIGNMENT NO.	DETECTOR NO.	NEMA PHASE	CALL	EXTEND	FULL TIME DELAY	STRETCH TIME	DELAY TIME
1A	TB2-1,2	I1U	56	18	1	1	Y	Y			
1B	TB2-3,4	I1L	56	18	1	1	Y	Y			
1C	TB2-5,6	I2U	39	1	2	1	Y	Y			10
1D	TB2-7,8	I2L	43	5	12	1	Y	Y			15
2A	TB2-9,10	I3U	63	25	32	2	Y	Y			
2B	TB2-11,12	I3L	76	36	42	2	Y	Y			
3A	TB4-5,6	I6U	58	20	3	3	Y	Y			
3B	TB4-7,8	I6L	58	20	3	3	Y	Y			
4A	TB4-9,10	I6U	41	3	4	4		Y			
4B	TB4-11,12	I6L	45	7	14	4		Y			
4C	TB6-1,2	I7U	65	27	34	4	Y	Y	Y	2.0	5
4D	TB6-3,4	I7L	78	40	44	4	Y	Y	Y	2.0	5
5A	TB3-1,2	J1U	55	17	5	5	Y	Y			
5B	TB3-3,4	J1L	55	17	5	5	Y	Y			
5C	TB3-5,6	J2U	40	2	6	5	Y	Y			10
6A	TB3-9,10	J3U	64	26	36	6	Y	Y			
6B	TB3-11,12	J3L	77	39	46	6	Y	Y			
7A	TB5-5,6	J5U	57	19	7	7	Y	Y			
7B	TB5-7,8	J5L	57	19	7	7	Y	Y			
8A	TB5-9,10	J6U	42	4	8	8		Y			
8B	TB5-11,12	J6L	46	8	18	8		Y			
8C	TB7-1,2	J7U	66	28	38	8	Y	Y	Y	2.0	5
8D	TB7-3,4	J7L	79	41	48	8	Y	Y	Y	2.0	5

INPUT FILE POSITION LEGEND: J2L



THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 04-0478
 DESIGNED: May 2005
 SEALED: 5-27-05
 REVISED: N/A

OVERLAP PROGRAMMING DETAIL

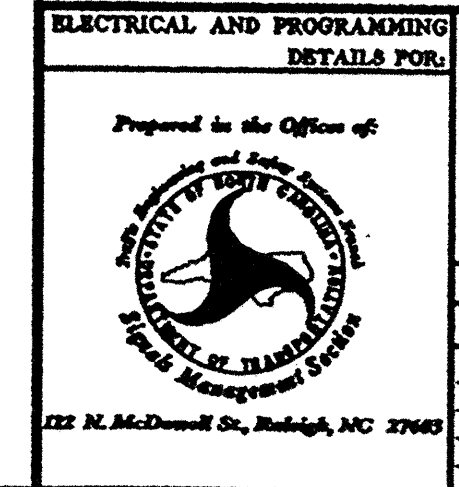
(program controller as shown below)

From Main Menu press '8' (Overlaps), then '1' (Vehicle Overlap Settings).

PAGE 1: VEHICLE OVERLAP 'A' SETTINGS
 PHASE: 12345678910111213141516
 VEH OVL PARENTS: X X
 VEH OVL NOT VEH: X
 VEH OVL NOT PED: X
 VEH OVL GRN EXT: X
 STARTUP COLOR: - RED - YELLOW - GREEN
 FLASH COLORS: - RED - YELLOW - GREEN
 SELECT VEHICLE OVERLAP OPTIONS: (Y/N)
 FLASH YELLOW IN CONTROLLER FLASH?...N
 GREEN EXTENSION (0-255 SEC)...0
 YELLOW CLEAR (0-PARENT,3-25.5 SEC)...0.0
 RED CLEAR (0-PARENT,0.1-25.5 SEC)...0.0
 OUTPUT AS PHASE # (0-NONE, 1-16)...0

OVERLAP PROGRAMMING COMPLETE

Signal Upgrade - Final



US 264A (Raleigh Road)
 at
 SR 1320 (Airport Blvd.)/
 SR 1158 (Airport Blvd.)

Division 4 Wilson County Wilson

PLAN DATE: 5-12-05 REVIEWED BY: D.T. Joyce

PREPARED BY: D.H. Spaulding REVIEWED BY:

REVISIONS	INIT.	DATE

SEAL
 NORTH CAROLINA
 PROFESSIONAL ENGINEER
 SEAL 022013
 GEORGE C. BROWN
 SIGNATURE: *George C. Brown*
 DATE: *5/27/05*
 SIG. INVENTORY NO. 04-0478

01-JUN-2005 09:02
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 02/20/05 10:02