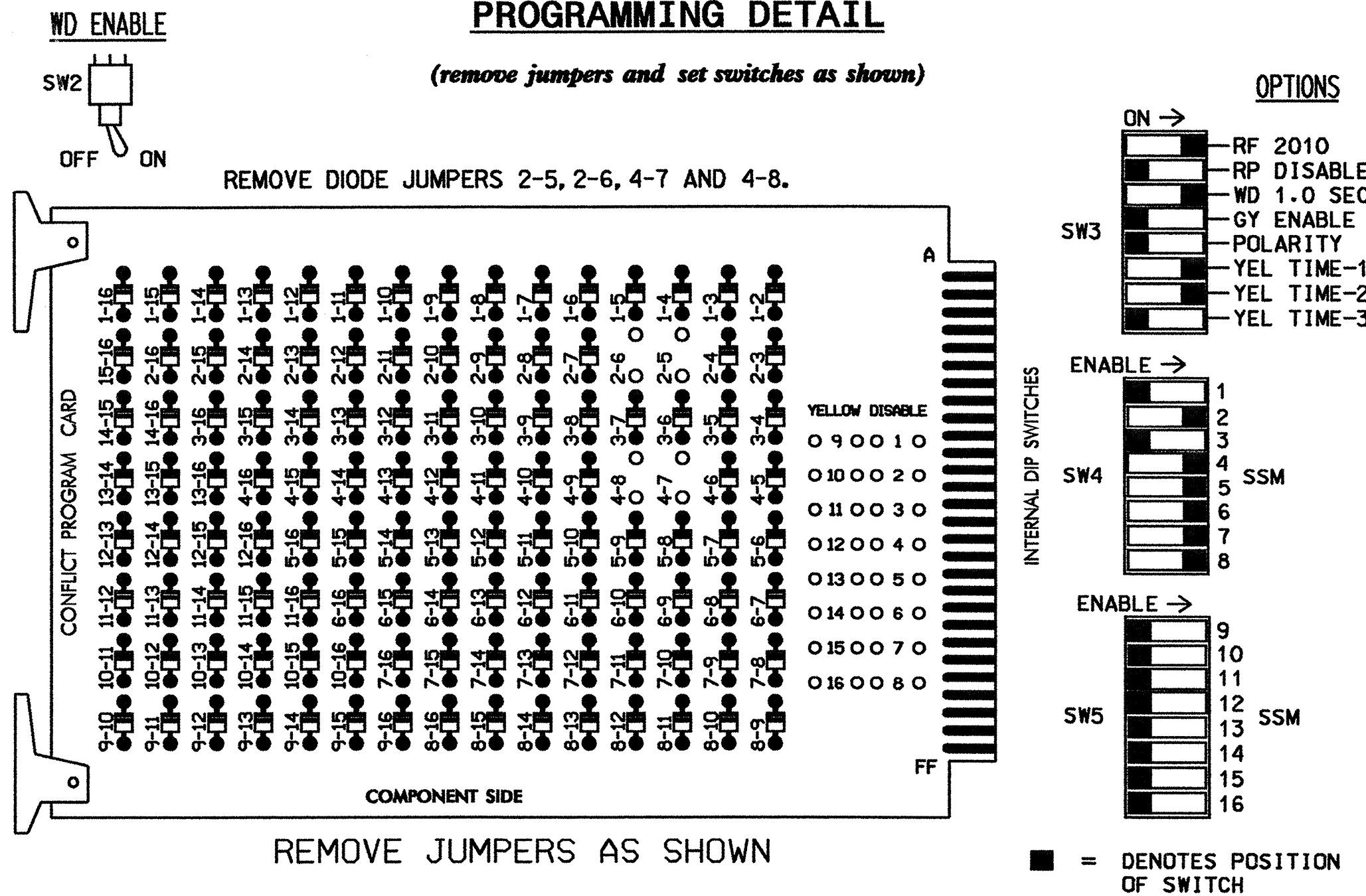


EDI MODEL 2010ECL CONFLICT MONITOR

PROGRAMMING DETAIL

(remove jumpers and set switches as shown)



NOTES:

- CARD IS PROVIDED WITH ALL DIODE JUMPERS IN PLACE. REMOVAL OF ANY JUMPER ALLOWS ITS CHANNELS TO RUN CONCURRENTLY.
- MAKE SURE JUMPERS SEL1-SEL5 ARE PRESENT ON THE MONITOR BOARD.

NOTES

- TO PREVENT "FLASH-CONFLICT" PROBLEMS, INSERT RED FLASH PROGRAM BLOCKS FOR ALL UNUSED VEHICLE LOAD SWITCHES IN THE OUTPUT FILE. THE INSTALLER SHALL VERIFY THAT SIGNAL HEADS FLASH IN ACCORDANCE WITH THE SIGNAL PLANS.
- ENSURE THAT RED ENABLE IS ACTIVE AT ALL TIMES DURING NORMAL OPERATION. TO PREVENT RED FAILURES ON UNUSED MONITOR CHANNELS, TIE UNUSED RED MONITOR INPUTS 1,3,9, 10,11,12,13,14,15 & 16 TO LOAD SWITCH AC+ PER THE CABINET MANUFACTURER'S INSTRUCTIONS.
- PROGRAM CONTROLLER TO START UP IN PHASES 2 AND 6 GREEN.
- ENABLE SIMULTANEOUS GAP-OUT FEATURE, ON CONTROLLER UNIT, FOR ALL PHASES.
- PROGRAM PHASES 4 AND 8, ON CONTROLLER UNIT, FOR DUAL ENTRY.
- THE CABINET AND CONTROLLER ARE PART OF THE CENTER STREET CLOSED LOOP SYSTEM.

FIELD CONNECTION HOOK-UP CHART

| LOAD SWITCH NO. | S1 | S2 | S2P | S3 | S4 | S4P | S5 | S6 | S6P | S7 | S8 | S8P |
|-----------------|----|-------|-------|----|-------|-------|-------|-------|-------|-------|-------|-------|
| PHASE | 1 | 2 | 2 PED | 3 | 4 | 4 PED | 5 | 6 | 6 PED | 7 | 8 | 8 PED |
| SIGNAL HEAD NO. | NU | 21,22 | NU | NU | 41,42 | NU | 21,42 | 61,62 | NU | 41,62 | 81,82 | NU |
| GREEN | | 130 | | | 103 | | | 136 | | | 109 | |
| YELLOW | | 129 | | | 102 | | | 135 | | | 108 | |
| RED | | 128 | | | 101 | | * | 134 | | * | 107 | |
| RED ARROW | | | | | | | | | | | | |
| YELLOW ARROW | | | | | | | | 132 | | | 123 | |
| GREEN ARROW | | | | | | | | 133 | | | 124 | |

NU = NOT USED
* DENOTES INSTALL LOAD RESISTOR. SEE LOAD RESISTOR INSTALLATION DETAIL THIS PAGE.

EQUIPMENT INFORMATION

CONTROLLER.....CONTRACTOR SUPPLIED 2070L
CABINETCONTRACTOR SUPPLIED 332
SOFTWAREECONOLITE OASIS
CABINET MOUNT.....BASE
OUTPUT FILE POSITIONS...12
LOAD SWITCHES USED.....S2,S4,S5,S6,S7,S8
PHASES USED.....2,4,5,6,7,8
OVERLAPS.....NONE

BACK-UP PROTECTION PROGRAMMING DETAIL

(program controller as shown below)

- FROM MAIN MENU PRESS '2' (PHASE CONTROL), THEN '1' (PHASE CONTROL FUNCTIONS). SCROLL TO THE BOTTOM OF THE MENU AND ENABLE DYNAMIC/BACKUP CONTROL FUNCTIONS 1 AND 2.
- FROM PHASE CONTROL FUNCTIONS MENU PRESS '2' (DYNAMIC/BACKUP CONTROL FUNCTIONS).

DYNAMIC/BACKUP CONTROL FUNCTION #01
OVERLAPS::ABCDEFGHIJKLMNPO
IF OVERLAPS ARE ACTIVE :
OR PHASES::12345678910111213141516
IF PHASES ARE ON : X
OMIT PHASES : X
CALL PHASES : X

PRESS 'NEXT'

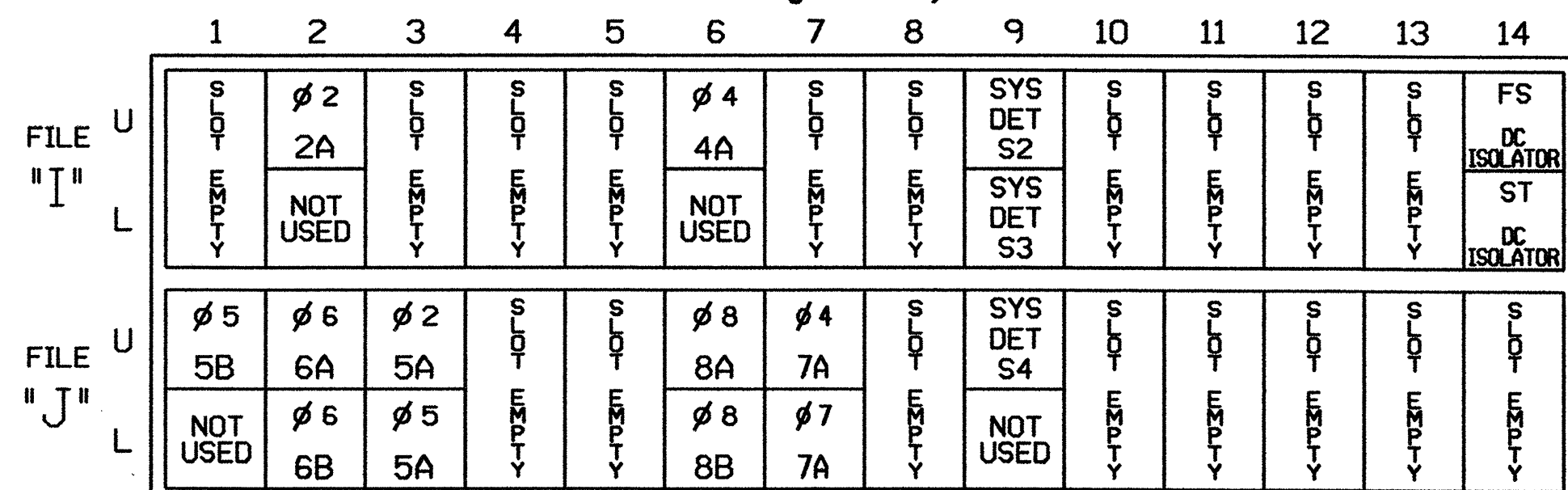
DYNAMIC/BACKUP CONTROL FUNCTION #02
OVERLAPS::ABCDEFGHIJKLMNPO
IF OVERLAPS ARE ACTIVE :
OR PHASES::12345678910111213141516
IF PHASES ARE ON : X
OMIT PHASES : X
CALL PHASES : X

BACKUP PROTECTION PROGRAMMING COMPLETE

THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 09-0411
DESIGNED: JUNE 2003
SEALED: 07/22/03
REVISED:

INPUT FILE POSITION LAYOUT

(front view)



EX. : 1A, 2A, ETC. = LOOP NO.'S

FS = FLASH SENSE
ST = STOP TIME

INPUT FILE CONNECTION & PROGRAMMING CHART

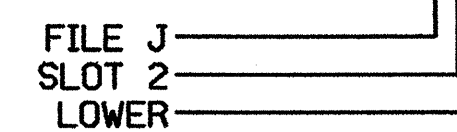
| LOOP NO. | LOOP TERMINAL | INPUT FILE POS. | PIN NO. | INPUT ASSIGNMENT NO. | DETECTOR NO. | NEMA PHASE | CALL | EXTEND | FULL TIME DELAY | STRETCH TIME | DELAY TIME |
|-----------------|---------------|-----------------|---------|----------------------|--------------|------------|------|--------|-----------------|--------------|------------|
| 2A | TB2-5,6 | I2U | 39 | 1 | 2 | 2 | Y | Y | | | |
| 4A | TB4-9,10 | I6U | 41 | 3 | 4 | 4 | Y | Y | | | |
| * S2 | TB6-9,10 | I9U | 60 | 22 | 11 | SYS | | | | | |
| * S3 | TB6-11,12 | I9L | 62 | 24 | 13 | SYS | | | | | |
| 5B | TB3-1,2 | J1U | 55 | 17 | 5 | 5 | Y | Y | | | 15 |
| 6A | TB3-5,6 | J2U | 40 | 2 | 6 | 6 | Y | Y | Y | | 3 |
| 6B | TB3-7,8 | J2L | 44 | 6 | 16 | 6 | Y | Y | | | |
| 5A ¹ | TB3-9,10 | J3U | 64 | 26 | 36 | 2 | Y | Y | | | |
| | TB3-11,12 | J3L | 77 | 39 | 46 | 5 | Y | Y | | | 15 |
| 8A | TB5-9,10 | J6U | 42 | 4 | 8 | 8 | Y | Y | | | 3 |
| 8B | TB5-11,12 | J6L | 46 | 8 | 18 | 8 | Y | Y | | | 10 |
| 7A ² | TB7-1,2 | J7U | 66 | 28 | 38 | 4 | Y | Y | | | 3 |
| | TB7-3,4 | J7L | 79 | 41 | 48 | 7 | Y | Y | | | 15 |
| * S4 | TB7-9,10 | J9U | 59 | 21 | 15 | SYS | | | | | |

¹ ADD JUMPERS FROM TB3-9 TO TB3-11, AND FROM TB3-10 TO TB3-12.

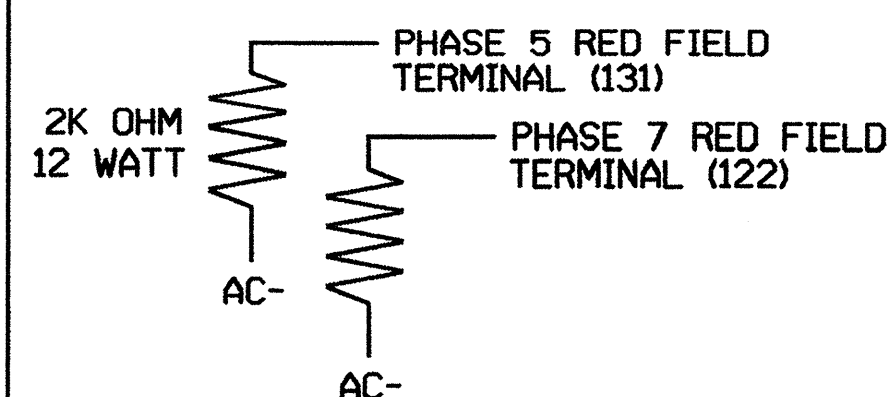
² ADD JUMPERS FROM TB7-1 TO TB7-3, AND FROM TB7-2 TO TB7-4.

* SYSTEM DETECTOR ONLY. REMOVE THE VEHICLE PHASE ASSIGNED TO THIS DETECTOR IN THE DEFAULT PROGRAMMING.

INPUT FILE POSITION LEGEND: J2L



LOAD RESISTOR INSTALLATION DETAIL



NOTE: THE PURPOSE OF THESE RESISTORS IS TO LOAD THE CHANNEL RED MONITOR INPUTS IN ORDER FOR THE SIGNAL SEQUENCE MONITOR TO USE THE FULL SIGNAL SEQUENCE MONITORING CAPABILITY ON CHANNELS THAT DO NOT USE THE RED DISPLAY IN THE FIELD.

SIGNAL UPGRADE - FINAL DESIGN

ELECTRICAL AND PROGRAMMING DETAILS FOR: SR 1243 (WEST CENTER STREET) AT SR 1242 (WEST CENTER STREET EXTENSION) / AND BURLER STREET

Prepared in the Office of:

222 N. McDowell St., Raleigh, NC 27603

DIVISION 09 DAVIDSON COUNTY LEXINGTON
PLAN DATE: JUNE 2003 REVIEWED BY: R. Vinslow
PREPARED BY: JAMES PETERSON REVIEWED BY:
REVISIONS INIT. DATE
SIGNATURE: DATE:

SEAL:

SIG. INVENTORY NO. 09-0411