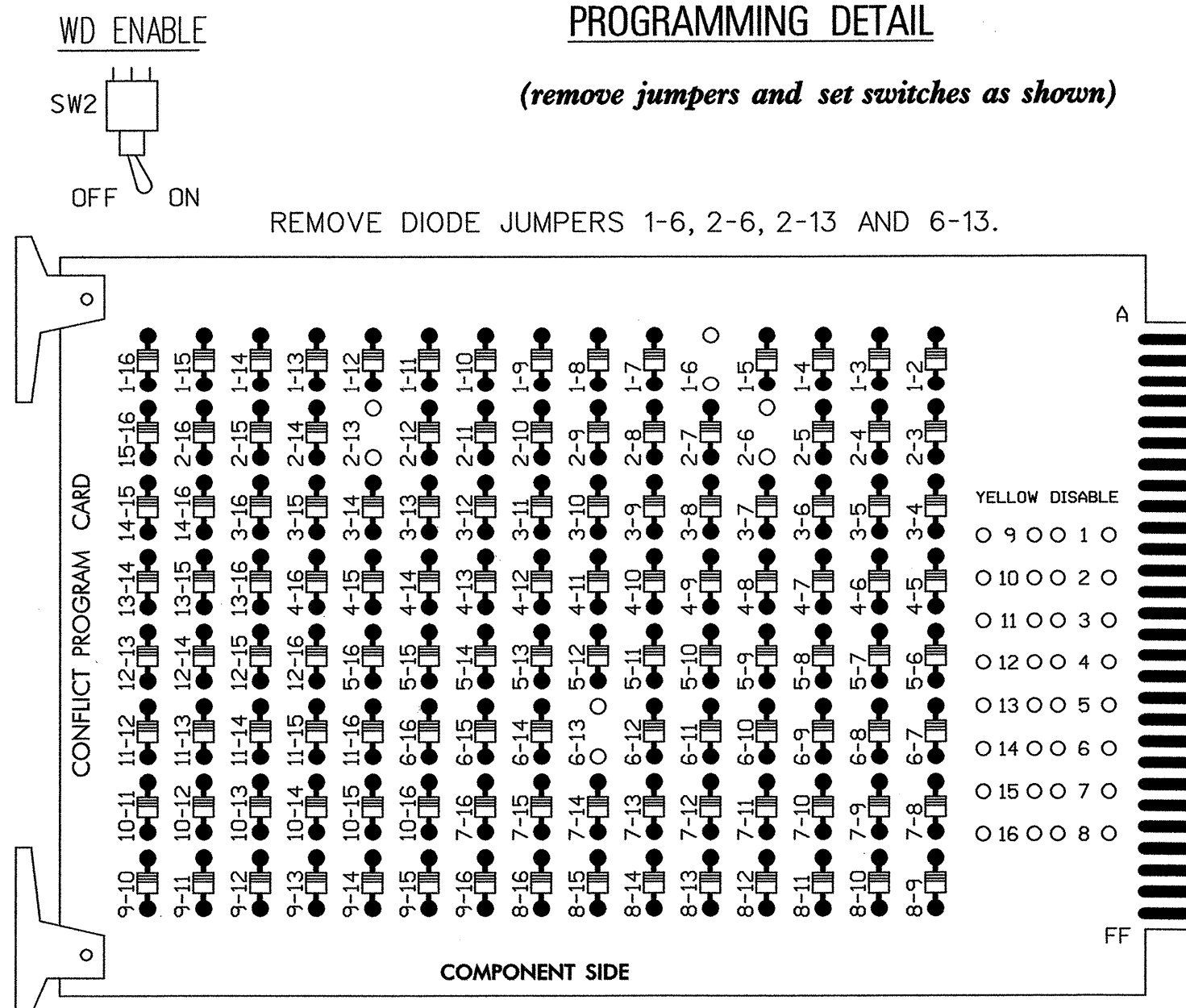


EDI MODEL 2010ECL CONFLICT MONITOR

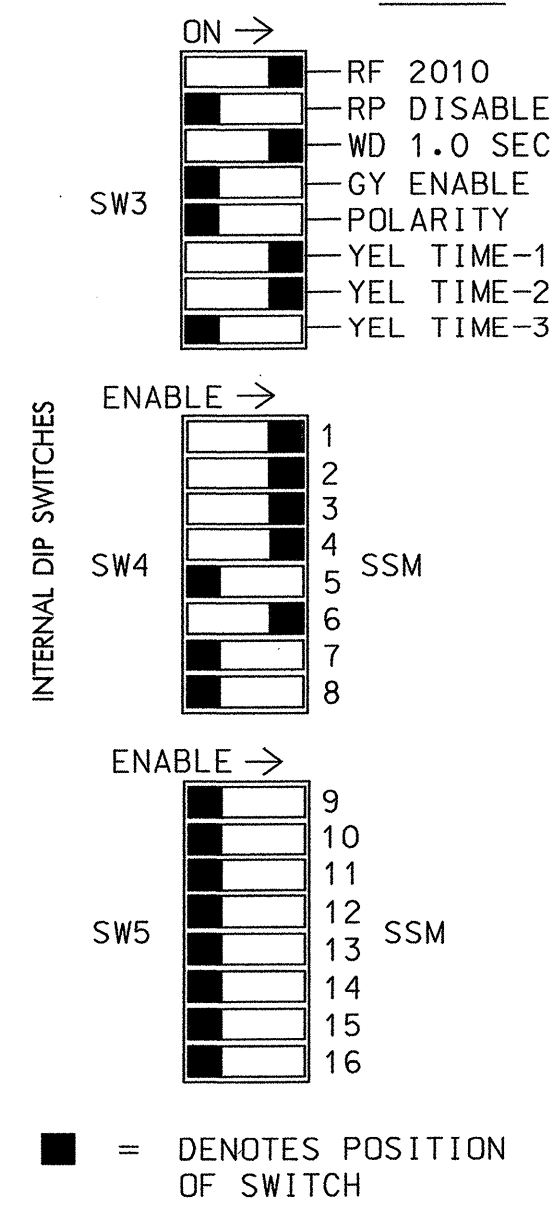
PROGRAMMING DETAIL

(remove jumpers and set switches as shown)



REMOVE JUMPERS AS SHOWN

OPTIONS



NOTES:

- CARD IS PROVIDED WITH ALL DIODE JUMPERS IN PLACE. REMOVAL OF ANY JUMPER ALLOWS ITS CHANNELS TO RUN CONCURRENTLY.
- MAKE SURE JUMPERS SEL1-SEL5 ARE PRESENT ON THE MONITOR BOARD.

- NOTES**
- TO PREVENT "FLASH-CONFLICT" PROBLEMS, INSERT RED FLASH PROGRAM BLOCKS FOR ALL UNUSED VEHICLE LOAD SWITCHES IN THE OUTPUT FILE. THE INSTALLER SHALL VERIFY THAT SIGNAL HEADS FLASH IN ACCORDANCE WITH THE SIGNAL PLANS.
  - ENSURE THAT RED ENABLE IS ACTIVE AT ALL TIMES DURING NORMAL OPERATION. TO PREVENT RED FAILURES ON UNUSED MONITOR CHANNELS, TIE UNUSED RED MONITOR INPUTS 5,7,8,9,10,11,12,13,14,15 & 16 TO LOAD SWITCH AC+ PER CABINET MANUFACTURER'S INSTRUCTIONS.
  - PROGRAM CONTROLLER TO START UP IN PHASES 2 AND 6 GREEN.
  - ENABLE SIMULTANEOUS GAP-OUT FEATURE, ON CONTROLLER UNIT, FOR ALL PHASES.
  - PROGRAM PHASES 2 AND 6, ON CONTROLLER UNIT, FOR VARIABLE INITIAL AND GAP REDUCTION.
  - THE CONTROLLER AND CABINET ARE TO BE PROGRAMMED AND WIRED TO BE PART OF A CLOSED LOOP SIGNAL SYSTEM. CONTROLLER ASSET: 0882
  - PROGRAM PHASE 2 FOR 'START-UP PED CALLS'.

EQUIPMENT INFORMATION

CONTROLLER.....CONTRACTOR SUPPLIED 2070L  
 CABINET .....CONTRACTOR SUPPLIED 332  
 SOFTWARE .....ECONOLITE OASIS  
 CABINET MOUNT.....BASE  
 OUTPUT FILE POSITIONS...12  
 LOAD SWITCHES USED.....S1,S2,S2P,S3,S4,S6  
 PHASES USED.....1,2,2PED,3,4,6  
 OVERLAPS.....NONE

BACK-UP PROTECTION PROGRAMMING DETAIL

(program controller as shown below)

- FROM MAIN MENU PRESS '2' (PHASE CONTROL), THEN '1' (PHASE CONTROL FUNCTIONS). SCROLL TO THE BOTTOM OF THE MENU AND ENABLE DYNAMIC/BACKUP CONTROL FUNCTION 1.
- FROM PHASE CONTROL FUNCTIONS MENU PRESS '2' (DYNAMIC/BACKUP CONTROL FUNCTIONS).

DYNAMIC/BACKUP CONTROL FUNCTION #01  
 OVERLAPS: ABCDEFGHIJKLMNPO  
 IF OVERLAPS ARE ACTIVE: |  
 OR PHASES: 12345678910111213141516  
 IF PHASES ARE ON: X  
 OMIT PHASES: X  
 CALL PHASES: X

BACKUP PROTECTION PROGRAMMING COMPLETE

THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 03-0882  
 DESIGNED: SEPTEMBER 2003  
 SEALED: 12/11/2003  
 REVISED: TBD

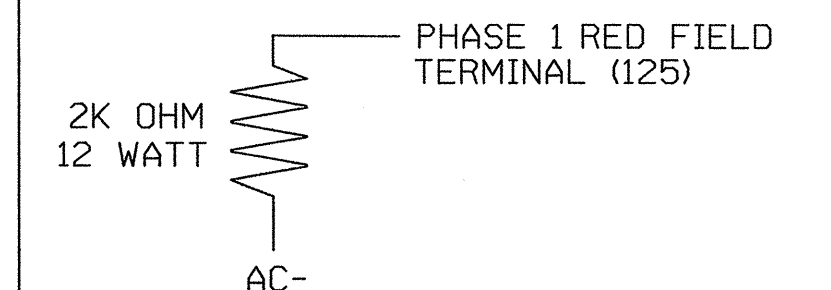
FIELD CONNECTION HOOK-UP CHART

LOAD SWITCH NO.	S1	S2	S2P	S3	S4	S4P	S5	S6	S6P	S7	S8	S8P
PHASE	1	2	2 PED	3	4	4 PED	5	6	6 PED	7	8	8 PED
SIGNAL HEAD NO.	32,61	21,22, 23	P21, P22	23	31	32	41	42	NU	NU	61,62, 63	NU
GREEN		130		118	118	103	103				136	
YELLOW		129		117	117	102	102				135	
RED	*	128		116	116	101	101				134	
RED ARROW												
YELLOW ARROW	126			117								
GREEN ARROW	127			118	118		103					
				115								
				113								

NU = NOT USED

\* DENOTES INSTALL LOAD RESISTOR. SEE LOAD RESISTOR INSTALLATION DETAIL THIS PAGE.

LOAD RESISTOR INSTALLATION DETAIL



NOTE: THE PURPOSE OF THESE RESISTORS IS TO LOAD THE CHANNEL RED MONITOR INPUTS IN ORDER FOR THE SIGNAL SEQUENCE MONITOR TO USE THE FULL SIGNAL SEQUENCE MONITORING CAPABILITY ON CHANNELS THAT DO NOT USE THE RED DISPLAY IN THE FIELD.

INPUT FILE POSITION LAYOUT

(from view)

FILE "I"	1	2	3	4	5	6	7	8	9	10	11	12	13	14
	∅ 1	∅ 1	∅ 2	∅ 2	SLOT	∅ 3	∅ 4	SLOT	SLOT	SLOT	SLOT	∅ 2 PED	SLOT	FS
	1B	1A	2A	2C	NOT USED	3A	4A	NOT USED	NOT USED	NOT USED	NOT USED	NOT USED	NOT USED	DC ISOLATOR
FILE "J"		∅ 6	∅ 2	NOT USED	NOT USED	∅ 3	NOT USED	NOT USED	NOT USED	NOT USED	NOT USED	NOT USED	NOT USED	ST
		1A	2B			3B								DC ISOLATOR
	SLOT	∅ 6	NOT USED	NOT USED	NOT USED	NOT USED	NOT USED	NOT USED	SYS	SLOT	SLOT	SLOT	SLOT	SLOT
		6A							S21					
		∅ 6							SYS					
		6B							S22					

EX. : 1A, 2A, ETC. = LOOP NO.'S

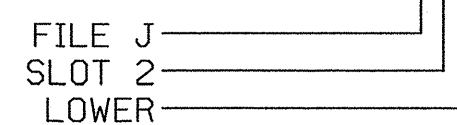
FS = FLASH SENSE  
 ST = STOP TIME

INPUT FILE CONNECTION & PROGRAMMING CHART

LOOP NO.	LOOP TERMINAL	INPUT FILE POS.	PIN NO.	INPUT ASSIGNMENT NO.	DETECTOR NO.	NEMA PHASE	CALL	EXTEND	FULL TIME DELAY	STRETCH TIME	DELAY TIME
1B	TB2-1,2	I1U	56	18	1	1	Y	Y	-	---	15
1A <sup>1</sup>	TB2-5,6	I2U	39	1	2	1	Y	Y	-	---	15
	TB2-7,8	I2L	43	5	12	6	Y	Y	Y	---	3
2A	TB2-9,10	I3U	63	25	32	2	Y	Y	-	---	--
2B	TB2-11,12	I3L	76	38	42	2	Y	Y	-	---	--
2C	TB4-1,2	I4U	47	9	22	2	Y	Y	Y	---	3
3A	TB4-9,10	I6U	41	3	4	3	Y	Y	-	---	3
3B	TB4-11,12	I6L	45	7	14	3	Y	Y	-	---	--
4A	TB6-1,2	I7U	65	27	34	4	Y	Y	-	---	10
6A	TB3-5,6	J2U	40	2	6	6	Y	Y	-	---	--
6B	TB3-7,8	J2L	44	6	16	6	Y	Y	-	---	--
* S21	TB7-9,10	J9U	59	21	15	SYS	-	-	-	---	--
* S22	TB7-11,12	J9L	61	23	17	SYS	-	-	-	---	--
PED PUSH BUTTONS											
P21,P22	TB8-4,6	I12U	67	29	PED 2	2 PED					

NOTE: INSTALL DC ISOLATORS IN INPUT FILE SLOT 112.

INPUT FILE POSITION LEGEND: J2L



<sup>1</sup> DENOTES ADD JUMPERS FOR LOOP 1A FROM TB2-5 TO TB2-7, AND FROM TB2-6 TO TB2-8.

\* SYSTEM DETECTOR ONLY. REMOVE THE VEHICLE PHASE ASSIGNED TO THIS DETECTOR IN THE DEFAULT PROGRAMMING.

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ELECTRICAL AND PROGRAMMING DETAILS FOR: SR 1409 (MILITARY CUTOFF ROAD) AT SR 1940 (COVIL FARM ROAD) AND CHURCH

Prepared for the Offices of: NORTH CAROLINA PROFESSIONAL ENGINEER JAMES O. DEATON

PLAN DATE: NOVEMBER 2003 REVIEWED BY: J O DEATON  
 PREPARED BY: M W YALCH REVIEWED BY: [Signature]

REVISIONS: [Table with columns for REVISIONS, INIT., DATE]

DATE: 12/16/03  
 SIGNATURE: [Signature]  
 DATE: [Date]

SIG. INVENTORY NO. 03-0882