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PROJECT REFERENCE NO.	SHEET NO.
U-3422B	Sig-3.2

MAXTIME ALTERNATE PHASING ACTIVATION DETAIL

To run alternate phasing, select a Pattern that is programmed to run Overlap Plan 2 and Detector Plan 2.
A Pattern can be selected through the scheduler or manually by changing the Operational Mode.

PHASING	OVERLAP PLAN	VEH DET PLAN
ACTIVE PLAN REQUIRED TO RUN DEFAULT PHASING	1	1
ACTIVE PLAN REQUIRED TO RUN ALTERNATE PHASING	2	2

ALTERNATE PHASING CHANGE SUMMARY

THE FOLLOWING IS A SUMMARY OF WHAT TAKES PLACE WHEN OVERLAP PLAN 2 AND VEHICLE DETECTOR PLAN 2 ACTIVATE TO CALL THE "ALTERNATE PHASING":

OVERLAP PLAN 2: Modifies overlap included phases for heads 11, 31, 32, 51, 71 and 72 to run protected turns only.

VEH DET PLAN 2: Disables phase 6 call on loop 1A and reduces delay time for phase 1 call on loop 1A to 0 seconds.

Disables phase 8 call on loop 3A and reduces delay time for phase 3 call on loop 3A to 0 seconds.

Disables phase 2 call on loop 5A and reduces delay time for phase 5 call on loop 5A to 0 seconds.

Disables phase 4 call on loop 7A and reduces delay time for phase 7 call on loop 7A to 0 seconds.

OUTPUT CHANNEL CONFIGURATION

Front Panel
Main Menu >Controller >More>Channels>Channels Config

Web Interface
Home >Controller >Advanced IO>Channels>Channel Configuration

Channel Configuration

Channel	Control Type	Control Source	Flash Yellow	Flash Red	Flash Alt	MMU Channel
1	Phase Vehicle	1		X	X	1
2	Phase Vehicle	2		X		2
3	Phase Vehicle	3		X	X	3
4	Phase Vehicle	4		X		4
5	Phase Vehicle	5		X		5
6	Phase Vehicle	6		X	X	6
7	Phase Vehicle	7		X		7
8	Phase Vehicle	8		X	X	8
9	Overlap	1		X	X	9
10	Overlap	2		X	X	10
11	Overlap	3		X		11
12	Overlap	4		X		12
13	Phase Ped	2				13
14	Phase Ped	4				14
15	Phase Ped	6				15
16	Phase Ped	8				16
17	Overlap	5		X	X	17
18	Overlap	6		X		18

MAXTIME OVERLAP PROGRAMMING DETAIL FOR DEFAULT PHASING

Front Panel
Main Menu >Controller >Overlap >Overlap Parameters/Overlap Timings

Web Interface
Home >Controller >Overlap Configuration >Overlaps

Overlap Plan 1

Overlap	1	2	3	4
Type	FYA 4 - Section	FYA 4 - Section	FYA 4 - Section	FYA 4 - Section
Included Phases	2	4	6	8
Modifier Phases	1	3	5	7
Trail Green	0	0	0	0
Trail Yellow	0.0	0.0	0.0	0.0
Trail Red	0.0	0.0	0.0	0.0

MAXTIME OVERLAP PROGRAMMING DETAIL FOR ALTERNATE PHASING

Front Panel
Main Menu >Controller >Overlap >Overlap Parameters/Overlap Timings

Web Interface
Home >Controller >Overlap Configuration >Overlaps

In the table view of the web interface, right click on "Overlap" in the top left corner of the table. Copy the entire contents of Overlap Plan 1. Paste Overlap Plan 1 into Overlap Plan 2. Modify Overlap Plan 2 as shown below and save changes.

Overlap Plan 2

Overlap	1	2	3	4
Type	FYA 4 - Section	FYA 4 - Section	FYA 4 - Section	FYA 4 - Section
Included Phases	-	-	-	-
Modifier Phases	1	3	5	7
Trail Green	0	0	0	0
Trail Yellow	0.0	0.0	0.0	0.0
Trail Red	0.0	0.0	0.0	0.0

MAXTIME STARTUP AND SOFTWARE FLASH PROGRAMMING DETAIL

Front Panel
Main Menu >Controller >Unit

Web Interface
Home >Controller >Unit

Modify parameters as shown below and save changes.

Start Up Parameters

StartUp Clearance Hold
6

Unit Flash Parameters

All Red Flash Exit Time
6

Plans Prepared By:

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MAXTIME DETECTOR PROGRAMMING DETAIL FOR ALTERNATE PHASING ZONES 1A, 3A, 5A & 7A

Front Panel
Main Menu >Controller >Detector >Veh Det Plans

Web Interface
Home >Controller >Detector Configuration >Vehicle Detectors

In the table view of web interface right click on "Detector" in the top left corner of the table. Copy the entire contents of Detector Plan 1. Paste Detector Plan 1 into Detector Plan 2. Modify Detector Plan 2 as shown below and save changes.

Plan 2

1A

Detector	Call Phase	Delay
33	1	-
125	0	-

3A

Detector	Call Phase	Delay
34	3	-
126	0	-

5A

Detector	Call Phase	Delay
35	5	-
127	0	-

7A

Detector	Call Phase	Delay
36	7	-
128	0	-

FLASHER CIRCUIT MODIFICATION DETAIL

IN ORDER TO INSURE THAT SIGNALS FLASH CONCURRENTLY ON THE SAME APPROACH, MAKE THE FOLLOWING FLASHER CIRCUIT CHANGES:

1. ON REAR OF PDA - REMOVE WIRE FROM TERM. T2-4 AND TERMINATE ON T2-2.
2. ON REAR OF PDA - REMOVE WIRE FROM TERM. T2-5 AND TERMINATE ON T2-3.
3. REMOVE FLASHER UNIT 2.

THE CHANGES LISTED ABOVE TIES ALL PHASES AND OVERLAPS TO FLASHER UNIT 1.

THIS PLAN SUPERSEDES THE PLAN SEALED ON 12/22/2023

THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 06-0470T1
DESIGNED: Sep 2026
SEALED: 09/11/2026
REVISED: N/A

Electrical Detail - Sheet 2 of 3
Temporary Design 1 (TMP Phase I)

DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED

ELECTRICAL AND PROGRAMMING DETAILS FOR:

SR 1003 (Camden Road)
at
SR 1112 (Rockfish Road)

Division 6 Cumberland County Hope Mills

PLAN DATE: September 2026 REVIEWED BY: BN Groome
PREPARED BY: DS Griffith DRMP PROJ. NO.: 24941

REVISIONS	INIT.	DATE

Prepared for:
NC Department of Transportation Mobility and Safety Division Traffic Engineering Branch Signals Management Section
750 N.Greenfield Pkwy,Garner,NC 27529

SEAL
NORTH CAROLINA PROFESSIONAL SEAL 052936 ENGINEER BRITTANY N GROOME

Signed by: Brittany Groome E0E0340E1044484...
09/11/2026
SIGNATURE DATE

SIG. INVENTORY NO. 06-0470TI