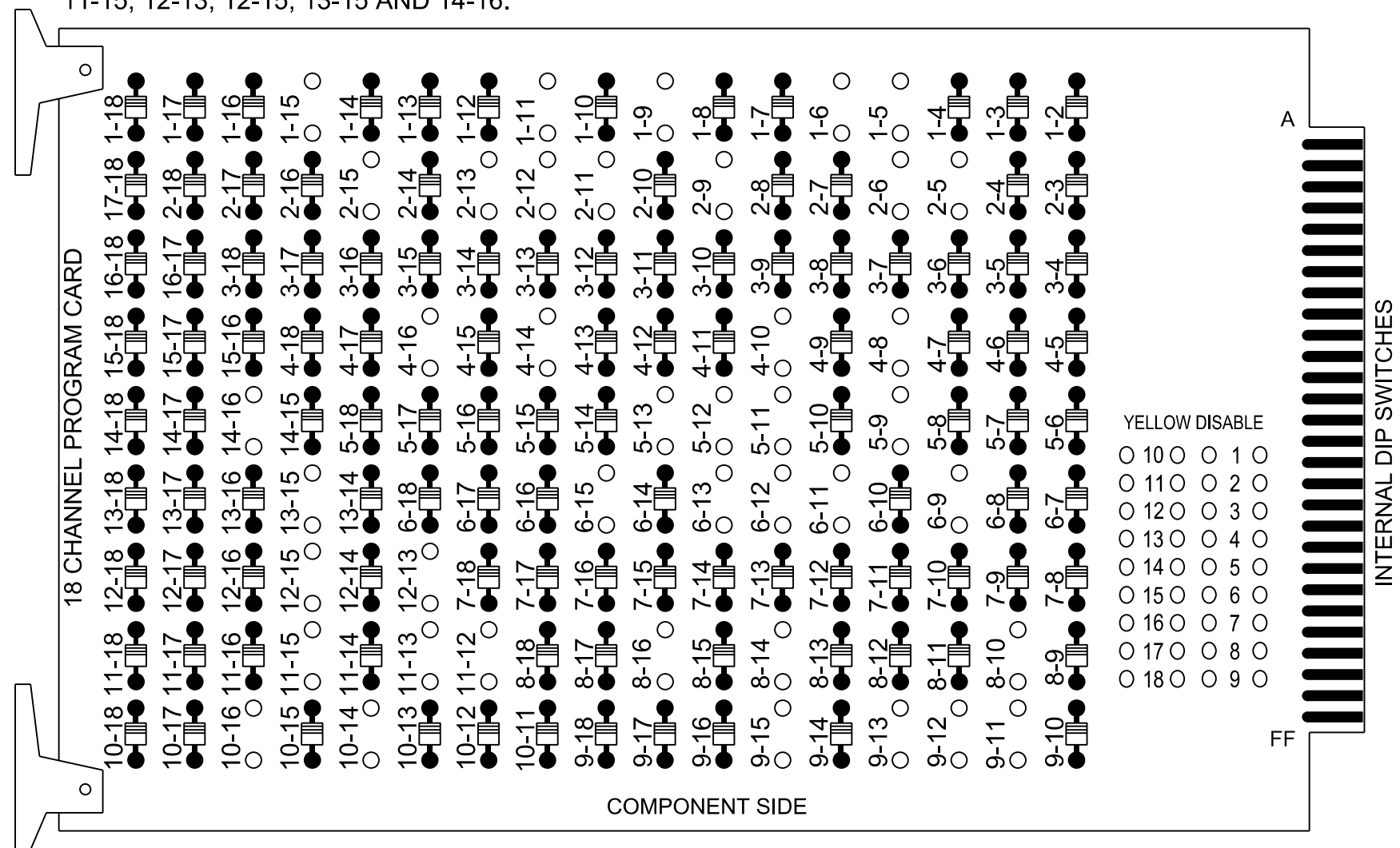


18 CHANNEL CONFLICT MONITOR PROGRAMMING DETAIL

(remove jumpers and set switches as shown)

REMOVE DIODE JUMPERS 1-5, 1-6, 1-9, 1-11, 1-15, 2-5, 2-6, 2-9, 2-11, 2-12, 2-13, 2-15, 4-8, 4-10, 4-14, 4-16, 5-9, 5-11, 5-12, 5-13, 6-9, 6-11, 6-12, 6-13, 6-15, 8-10, 8-14, 8-16, 9-11, 9-12, 9-13, 9-15, 10-14, 10-16, 11-12, 11-13, 11-15, 12-13, 12-15, 13-15 AND 14-16.



NOTES:

REMOVE JUMPERS AS SHOWN

- Card is provided with all diode jumpers in place. Removal of any jumper allows its channels to run concurrently.
- Ensure jumpers SEL2-SEL5 and SEL9 are present on the monitor board.
- Ensure that the Red Enable is active at all times during normal operation.
- Connect serial cable from conflict monitor to comm. port 1 of 2070 controller. Ensure conflict monitor communicates with 2070.

NOTES

- To prevent "flash-conflict" problems, insert red flash program blocks for all unused vehicle load switches in the output file. The installer shall verify that signal heads flash in accordance with the signal plan.
- Program phases 4 and 8 for Dual Entry.
- Program controller to start up in phase 2 Green No Walk and 6 Green No Walk.
- Program phases 4 and 8 for Simultaneous Start.
- If this signal will be managed by an ATMS software, enable controller and detector logging for all detectors used at this location.
- The cabinet and controller are part of the Wake Forest Signal System.

EQUIPMENT INFORMATION

Controller.....2070LX
Cabinet.....332 w/ Aux
Software.....Q-Free MAXTIME
Cabinet Mount.....Base
Output File Positions.....18 With Aux. Output File
Load Switches Used.....S1, S2, S3, S5, S6, S7, S8, S9, S11
S12, AUX S1, AUX S2, AUX S4, AUX S5
Phases Used.....1, 2, 2PED, 4, 4PED, 5, 6, 6PED, 8, 8PED
Overlap "1".....*
Overlap "2".....*
Overlap "3".....*
Overlap "4".....*

*See overlap programming detail on sheet 2

SIGNAL HEAD HOOK-UP CHART

LOAD SWITCH NO.	S1	S2	S3	S4	S5	S6	S7	S8	S9	S10	S11	S12	AUX S1	AUX S2	AUX S3	AUX S4	AUX S5	AUX S6
CMU CHANNEL NO.	1	2	13	3	4	14	5	6	15	7	8	16	9	10	17	11	12	18
PHASE	1	2	2 PED	3	4	4 PED	5	6	6 PED	7	8	8 PED	OL1	OL2	SPARE	OL3	OL4	SPARE
SIGNAL HEAD NO.	11★	21,22	P21, P22	NU	41,42	P41, P42	51★	61,62	P61, P62	NU	82,83, 84	P81, P82	11★	81★	NU	51★	23★	NU
RED		128			101			134			107						A101	
YELLOW	★	129			102		★	135			108							
GREEN		130			103			136			109							
RED ARROW													A121	A124		A114		
YELLOW ARROW													A122	A125		A115	A102	
FLASHING YELLOW ARROW													A123	A126		A116	A103	
GREEN ARROW	127						133											
Hand				113		104			119		110							
Walking Person				115		106			121		112							

NU = Not Used

★ Denotes install load resistor. See load resistor installation detail this sheet.

★ See pictorial of head wiring in detail this sheet.

INPUT FILE CONNECTION & PROGRAMMING CHART

LOOP NO.	LOOP TERMINAL	INPUT FILE POS.	PIN NO.	INPUT POINT	DETECTOR NO.	CALL PHASE	DELAY TIME	EXTEND TIME	EXTEND	ADDED INITIAL	CALL	DELAY DURING GREEN
1A	TB2-1,2	I1U	56	18	1★	1	15.0		X		X	
2A	TB2-5,6	I2U	39	1	29★	6	3.0		X		X	X
2B	TB2-7,8	I2L	43	5	3	2			X	X	X	
4A	TB4-9,10	I6U	41	3	8	4	10.0		X		X	
5A	TB3-1,2	J1U	55	17	15★	5	15.0		X		X	
6A	TB3-5,6	J2U	40	2	16	6			X	X	X	X
6B	TB3-7,8	J2L	44	6	17	6			X	X	X	
8A	TB5-9,10	J6U	42	4	22	8	3.0		X		X	
8B	TB5-11,12	J6L	46	8	23	8	10.0		X		X	
PED PUSH BUTTONS												
P21,P22	TB8-4,6	I12U	67	33	2	PED 2						
P41,P42	TB8-5,6	I12L	69	35	4	PED 4						
P61,P62	TB8-7,9	I13U	68	34	6	PED 6						
P81,P82	TB8-8,9	I13L	70	36	8	PED 8						

NOTE:
INSTALL DC ISOLATORS
IN INPUT FILE SLOTS
112 AND 113.

★ For the detectors to work as shown on the signal design plan, see the Vehicle Detector Setup Programming Detail for Alternate Phasing on Sheet 2.

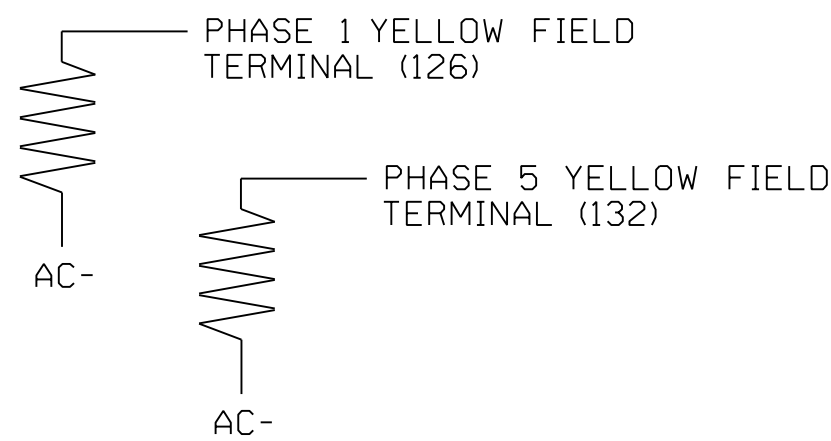
INPUT FILE POSITION LEGEND: J2L

FILE J
SLOT 2
LOWER

LOAD RESISTOR INSTALLATION DETAIL

(install resistors as shown)

VALUE (ohms)	WATTAGE
1.5K - 1.9K	25W (min)
2.0K - 3.0K	10W (min)

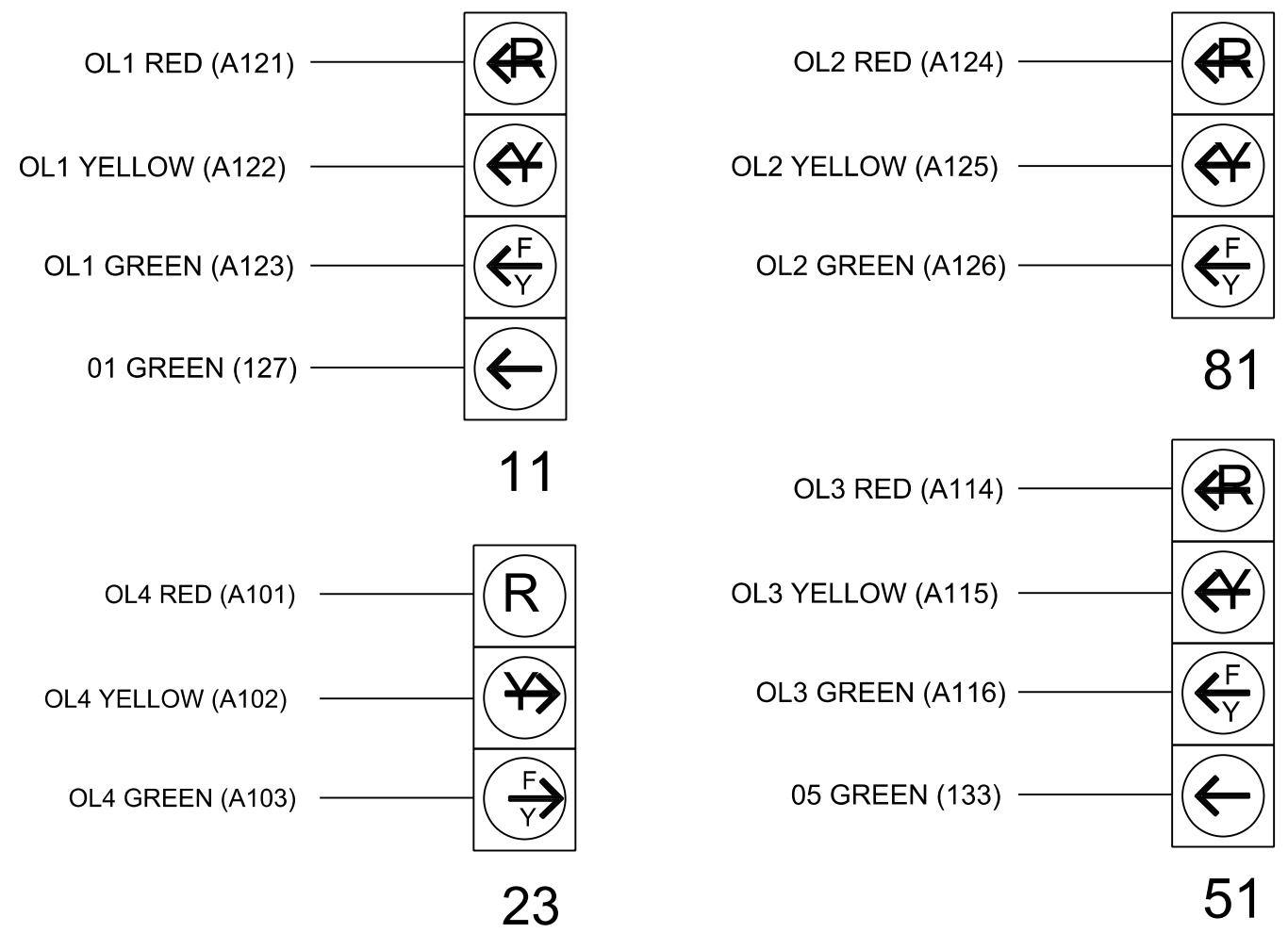


COUNTDOWN PEDESTRIAN SIGNAL OPERATION

Countdown Ped Signals are required to display timing only during Ped Clearance Interval. Consult Ped Signal Module user's manual for instructions on selecting this feature.

FYA SIGNAL WIRING DETAIL

(wire signal heads as shown)



THIS ELECTRICAL DETAIL IS FOR
THE SIGNAL DESIGN: 05-1916
DESIGNED: June 2026
SEALED: June 9, 2026
REVISED:

SIGNAL UPGRADE - FINAL DESIGN
ELECTRICAL DETAIL - SHEET 1 OF 2

DOCUMENT NOT CONSIDERED
FINAL UNLESS ALL
SIGNATURES COMPLETED

ELECTRICAL AND PROGRAMMING
DETAILS FOR:

SR 2052 (ROGER RD.)

AT
HERITAGE BRANCH RD
AND HERITAGE POINTE SHOPPES

DIVISION 5 WAKE COUNTY WAKE FOREST

PLAN DATE: JUNE 2026 REVIEWED BY: DT SEARS

PREPARED BY: WP JONES REVIEWED BY:

REVISIONS INIT. DATE

SEAL
NORTH CAROLINA
PROFESSIONAL
SEAL
044558
DAVID T. SEARS
DAVID T. SEARS
6/9/2026

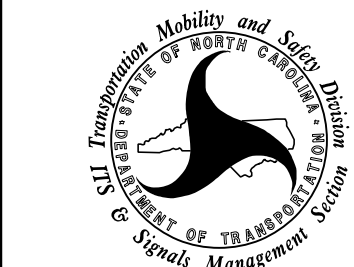
SIG. INVENTORY NO. 05-1916

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