

6/9/2026  
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jstiff

MAXTIME OVERLAP PROGRAMMING DETAIL  
FOR DEFAULT PHASING

Front Panel  
Main Menu >Controller >Overlap >Overlap Parameters/Overlap Timings

Web Interface  
Home >Controller >Overlap Configuration >Overlaps

Overlap Plan 1

| Overlap           | 2               | 3               | 4               |
|-------------------|-----------------|-----------------|-----------------|
| Type              | FYA 4 - Section | FYA 4 - Section | FYA 4 - Section |
| Included Phases   | 6               | 6               | 4               |
| Modifier Phases   | -               | 5               | -               |
| Modifier Overlaps | -               | -               | -               |
| Trail Green       | 0               | 0               | 0               |
| Trail Yellow      | 0.0             | 0.0             | 0.0             |
| Trail Red         | 0.0             | 0.0             | 0.0             |
| FYA Ped Delay     | 5.0             | 5.0             | 0.0             |

MAXTIME OVERLAP PROGRAMMING DETAIL  
FOR ALTERNATE PHASING

Front Panel  
Main Menu >Controller >Overlap >Overlap Parameters/Overlap Timings

Web Interface  
Home >Controller >Overlap Configuration >Overlaps

In the table view of the web interface, right click on "Overlap" in the top left corner of the table. Copy the entire contents of Overlap Plan 1. Paste Overlap Plan 1 into Overlap Plan 2. Modify Overlap Plan 2 as shown below and save changes.

Overlap Plan 2

| Overlap           | 2               | 3               | 4               |
|-------------------|-----------------|-----------------|-----------------|
| Type              | FYA 4 - Section | FYA 4 - Section | FYA 4 - Section |
| Included Phases   | 6               | -               | 4               |
| Modifier Phases   | -               | 5               | -               |
| Modifier Overlaps | -               | -               | -               |
| Trail Green       | 0               | 0               | 0               |
| Trail Yellow      | 0.0             | 0.0             | 0.0             |
| Trail Red         | 0.0             | 0.0             | 0.0             |
| FYA Ped Delay     | 5.0             | 5.0             | 0.0             |

← NOTICE INCLUDED PHASE

MAXTIME STARTUP AND SOFTWARE FLASH  
PROGRAMMING DETAIL

Front Panel  
Main Menu >Controller >Unit

Web Interface  
Home >Controller >Unit

Modify parameters as shown below and save changes.

Start Up Parameters

|                        |
|------------------------|
| StartUp Clearance Hold |
| 6                      |

Unit Flash Parameters

|                         |
|-------------------------|
| All Red Flash Exit Time |
| 6                       |

MAXTIME DETECTOR PROGRAMMING DETAIL  
FOR ALTERNATE PHASING LOOP 5A

Front Panel  
Main Menu >Controller >Detector >Veh Det Plans

Web Interface  
Home >Controller >Detector Configuration >Vehicle Detectors

In the table view of web interface right click on "Detector" in the top left corner of the table. Copy the entire contents of Detector Plan 1. Paste Detector Plan 1 into Detector Plan 2. Modify Detector Plan 2 as shown below and save changes.

Plan 2

| Detector | Call Phase | Delay |
|----------|------------|-------|
| 15       | 5          | 0.0   |
| 31       | 0          | 3.0   |

5A

MAXTIME ALTERNATE PHASING ACTIVATION DETAIL

To run alternate phasing, select a Pattern that is programmed to run Overlap Plan 2 and Detector Plan 2. A Pattern can be selected through the scheduler or manually by changing the Operational Mode.

| PHASING                                       | OVERLAP PLAN | VEH DET PLAN |
|-----------------------------------------------|--------------|--------------|
| ACTIVE PLAN REQUIRED TO RUN DEFAULT PHASING   | 1            | 1            |
| ACTIVE PLAN REQUIRED TO RUN ALTERNATE PHASING | 2            | 2            |

ALTERNATE PHASING CHANGE SUMMARY

THE FOLLOWING IS A SUMMARY OF WHAT TAKES PLACE WHEN OVERLAP PLAN 2 AND VEHICLE DETECTOR PLAN 2 ACTIVATE TO CALL THE "ALTERNATE PHASING":

OVERLAP PLAN 2: Modifies overlap included phases for head 51 to run protected turns only.

VEH DET PLAN 2: Disables phase 2 call on loop 5A and reduces delay time for phase 5 call on loop 5A to 0 seconds.

MAXTIME ALTERNATE PHASING PATTERN  
PROGRAMMING DETAIL

Front Panel  
Main Menu >Controller >Coordination >Patterns

Web Interface  
Home >Controller >Coordination >Patterns

Pattern Parameters

| Pattern | Veh Det Plan | Overlap Plan |
|---------|--------------|--------------|
| *       | 2            | 2            |

\* The Pattern number(s) are to be determined by the Division and/or Town Traffic Engineer.

THIS ELECTRICAL DETAIL IS FOR  
THE SIGNAL DESIGN: 05-2103T2  
DESIGNED: June 2026  
SEALED: June 9, 2026  
REVISED:

SIGNAL UPGRADE - TEMPORARY DESIGN 2  
(CONSTRUCTION PHASE 1 STEPS 7-8)  
ELECTRICAL DETAIL - SHEET 2 OF 2

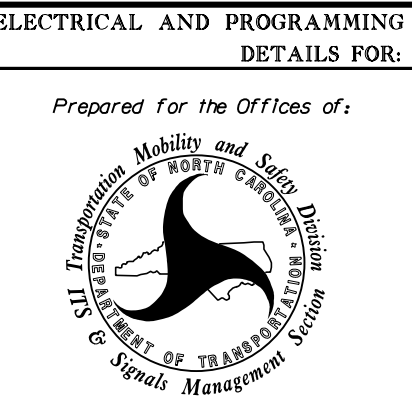
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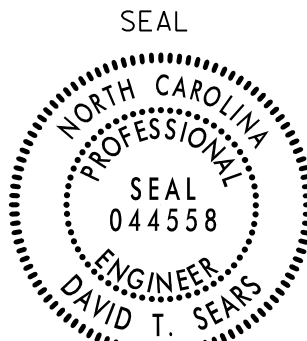
SR 2052 (ROGERS RD)  
AT  
HERITAGE BRANCH RD

DIVISION 5 WAKE COUNTY WAKE FOREST

PLAN DATE: JUNE 2026 REVIEWED BY: DSEARS

PREPARED BY: WPJONES REVIEWED BY:

REVISIONS INIT. DATE



DocuSigned by  
David T. Sears 6/9/2026

SIGNATURE DATE

SIG. INVENTORY NO. 05-2103T2