

OVERLAP PROGRAMMING

Front Panel
Main Menu >Controller >Overlap >Overlap Parameters/Overlap Timings

Web Interface
Home >Controller >Overlap Configuration >Overlaps
Overlap Plan 1

Overlap	1	2	3	7	8	9
Type	FYA 4 - Section	FYA 4 - Section	FYA 4 - Section	Normal	Normal	Normal
Included Phases	2	4	2	1,3	3,4	3,4
Modifier Phases	1	1,3	3,4	-	-	-
Modifier Overlaps	-	-	-	-	-	-
Min Green	0	7	0	0	0	0
Trail Green	0	0	0	0	0	0
Trail Yellow	0.0	0.0	0.0	0.0	0.0	0.0
Trail Red	0.0	0.0	0.0	0.0	0.0	0.0
FYA Ped Delay	7.0	7.0	7.0	0.0	0.0	0.0

LOGIC PROCESSOR PROGRAMMING

Front Panel
Main Menu >Controller >More >User Programs >Definition

Web Interface
Home >Controller >User Programs Configuration >User Programs Definition

Program 1

Statement	Result	Index	Operation	Parameter A	Index	Parameter B	Index	Delay	Ext
1	Phase Phase Omit	3	Result=Lach(A,B)	Phase Green	4	Phase Green	6	0.0	0.0

LOGIC STATEMENT DESCRIPTION

Statement 1 Description: Omit Phase 3 if Phase 4 is present.

OUTPUT CHANNEL CONFIGURATION

Front Panel
Main Menu >Controller >More>Channels>Channels Config

Web Interface
Home >Controller >Advanced IO>Channels>Channels Configuration

Channel Configuration

NOTICE CONTROL TYPE & SOURCE →
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Channel	Control Type	Control Source	Flash Yellow	Flash Red	Flash Alt	MMU Channel
1	Vehicle Phase	1		X	X	1
2	Phase Vehicle	2		X		2
3	Overlap	7		X	X	3
4	Phase Vehicle	4		X		4
5	Overlap	8		X		5
6	Phase Vehicle	6		X	X	6
7	Phase Vehicle	7		X		7
8	Overlap	9		X	X	8
9	Overlap	1		X	X	9
10	Overlap	2		X	X	10
11	Overlap	3		X		11
12	Overlap	4		X		12
13	Phase Ped	2				13
14	Phase Ped	4				14
15	Phase Ped	6				15
16	Phase Ped	8				16
17	Overlap	5		X	X	17
18	Overlap	6		X		18

COUNTDOWN PEDESTRIAN SIGNAL OPERATION

Countdown Ped Signals are required to display timing only during Ped Clearance Interval. Consult Ped Signal Module user's manual for instructions on selecting this feature.

SEQUENCE DETAIL

Front Panel
Main Menu >Controller >Sequence & Phs Config>Sequences

Web Interface
Home >Controller >Sequence

Sequence 1

Ring	Sequence Data
1	1,2,a,4,3,b
2	6,a,b

FLASHER CIRCUIT MODIFICATION DETAIL

IN ORDER TO INSURE THAT SIGNALS FLASH CONCURRENTLY ON THE SAME APPROACH, MAKE THE FOLLOWING FLASHER CIRCUIT CHANGES:

- ON REAR OF PDA - REMOVE WIRE FROM TERM. T2-4 AND TERMINATE ON T2-2.
- ON REAR OF PDA - REMOVE WIRE FROM TERM. T2-5 AND TERMINATE ON T2-3.
- REMOVE FLASHER UNIT 2.

THE CHANGES LISTED ABOVE TIES ALL PHASES AND OVERLAPS TO FLASHER UNIT 1.

MAXTIME STARTUP AND SOFTWARE FLASH PROGRAMMING DETAIL

Front Panel
Main Menu >Controller >Unit

Web Interface
Home >Controller >Unit

Modify parameters as shown below and save changes.

Start Up Parameters

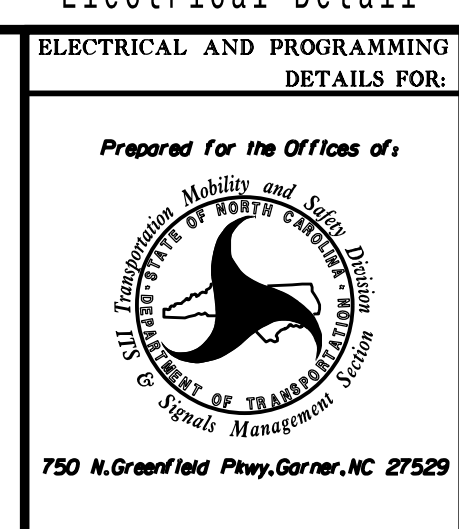
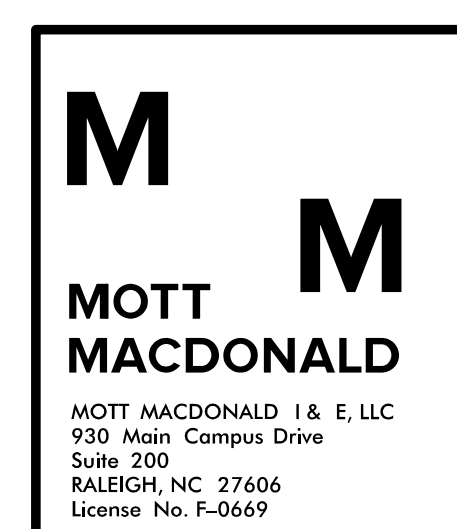
StartUp Clearance Hold
6

Unit Flash Parameters

All Red Flash Exit Time
6

Electrical Detail - Sheet 2 of 2

THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 08-0661
DESIGNED: February 2026
SEALED: March 11, 2026
REVISED: N/A



NC 42 at SR 2197 (Dublin Rd)	
Division 8 Randolph County	Asheboro
PLAN DATE: February 2026	REVIEWED BY: LD STOUCHKO
PREPARED BY: S O'Farrell	REVIEWED BY:
REVISIONS	INIT. DATE

DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED

SIGNATURE: Louis Stouchko
DATE: 11-Mar-2026
SIG. INVENTORY NO. 08-0661