PROJECT REFERENCE NO.	SHEET NO.
U-5302	Sig. 19.2

OVERLAP PROGRAMMING DETAIL

(program controller as shown below)

1. From Main Menu select `4-UNIT DATA`
2. From UNIT DATA Submenu select `3-OVERLAP DATA`

Use Up/Dn/Left/Right keys to position cursor on the desired Overlap. Use the NEXT key to select the overlap type. Press the ENT key and then program as per the Overlap screen(s) shown.

```
OVERLAP DATA

   A: FYA   E: ---   I: ---   M: ---
   B: FYA   F: ---   J: ---   N: ---
   C: FYA   G: ---   K: ---   O: ---
   D: FYA   H: ---   L: ---   P: ---

 PREV/NEXT TO CYCLE
```

OVERLAP A

Use Up/Dn/Left/Right keys to position cursor on Overlap 'A', use the NEXT key to select 'FYA', then press ENT

```
FYA OVERLAP - A            MIN PERM:  1
     PHASES..12345678 90123456
PROT PHASES: 10000000 00000000  DELAY/10
PERM PHASES: 01000000 00000000   FYA:  0
-PED PHASES: 00000000 00000000  -PED: 30
   OVERLAPS..ABCDEFGH IJKLMNOP
PROT O-LAPS: x0000000 00000000
PERM O-LAPS: x0000000 00000000
```

← NOTICE FYA DELAY/10 = 0

Press ESC

OVERLAP B

Use Up/Dn/Left/Right keys to position cursor on Overlap 'B', use the NEXT key to select 'FYA', then press ENT

```
FYA OVERLAP - B            MIN PERM:  1
     PHASES..12345678 90123456
PROT PHASES: 00100000 00000000  DELAY/10
PERM PHASES: 00010000 00000000   FYA:  0
-PED PHASES: 00000000 00000000  -PED: 30
   OVERLAPS..ABCDEFGH IJKLMNOP
PROT O-LAPS: 0x000000 00000000
PERM O-LAPS: 0x000000 00000000
```

← NOTICE FYA DELAY/10 = 0

Press ESC

OVERLAP C

Use Up/Dn/Left/Right keys to position cursor on Overlap 'C', use the NEXT key to select 'FYA', then press ENT

```
FYA OVERLAP - C            MIN PERM:  1
     PHASES..12345678 90123456
PROT PHASES: 00001000 00000000  DELAY/10
PERM PHASES: 00000100 00000000   FYA:  0
-PED PHASES: 00000000 00000000  -PED: 30
   OVERLAPS..ABCDEFGH IJKLMNOP
PROT O-LAPS: 00x00000 00000000
PERM O-LAPS: 00x00000 00000000
```

← NOTICE FYA DELAY/10 = 0

Press ESC

OVERLAP D

Use Up/Dn/Left/Right keys to position cursor on Overlap 'D', use the NEXT key to select 'FYA', then press ENT

```
FYA OVERLAP - D            MIN PERM:  1
     PHASES..12345678 90123456
PROT PHASES: 00000010 00000000  DELAY/10
PERM PHASES: 00000001 00000000   FYA:  0
-PED PHASES: 00000000 00000000  -PED: 30
   OVERLAPS..ABCDEFGH IJKLMNOP
PROT O-LAPS: 000x0000 00000000
PERM O-LAPS: 000x0000 00000000
```

← NOTICE FYA DELAY/10 = 0

OVERLAP PROGRAMMING COMPLETE

REMOTE FLASH PROGRAMMING DETAIL

(program controller as shown below)

1. From Main Menu select `4-UNIT DATA`
2. From UNIT DATA Submenu select `2-FLASH`
3. From FLASH Submenu select `1-REMOTE FLASH SETTINGS`

```
REMOTE FLASH SETTINGS    TEST-A FLASH: 0

LDSW:123456789 0123456789 0123456789 012
FLSH:110010110 0101101100 0000000000 000
 ALT:010010100 0000001100 0000000000 000

0-DARK  1-RED  2-YELLOW  3-STEADY YELLOW
```

PRESS 'F' TO RETURN TO FLASH

4. From REMOTE FLASH Submenu select `2-REMOTE FLASH ENTRY/EXIT PHASES`

```
REMOTE FLASH  ENTRY/EXIT PHASES

     PHASES:  12345678 90123456
      ENTRY:  01000100 00000000
       EXIT:  01000100 00000000
```

REMOTE FLASH PROGRAMMING COMPLETE

ALL RED FLASH STARTUP PROGRAMMING DETAIL

(program controller as shown below)

1. From Main Menu select `4-UNIT DATA`
2. From UNIT DATA Submenu select `1-STARTUP & MISC`

```
STARTUP & MISC
STARTUP TIME.:  6 (SEC) STATE: 2 (0-FL
RED REV/10...: 40 (TSEC)          1-RED
AUTO PED CLR.:  0 (0-NO 1-YES)    2-RAF)
STOP T RESET.:  0 (0-NO 1-YES)
SEQUENCE.....:  1 (1-19)
SPECIAL SEQ  :  0 (SEE HELP)
```

STARTUP PROGRAMMING COMPLETE

PRESS 'F' TO RETURN TO UNIT DATA

FLASHER CIRCUIT MODIFICATION DETAIL

IN ORDER TO INSURE THAT SIGNALS FLASH CONCURRENTLY ON THE SAME APPROACH, MAKE THE FOLLOWING FLASHER CIRCUIT CHANGES:

1. ON REAR OF PDA - REMOVE WIRE FROM TERM. T2-4 AND TERMINATE ON T2-2.
2. ON REAR OF PDA - REMOVE WIRE FROM TERM. T2-5 AND TERMINATE ON T2-3.
3. REMOVE FLASHER UNIT 2.

THE CHANGES LISTED ABOVE TIES ALL PHASES AND OVERLAPS TO FLASHER UNIT 1.

THIS ELECTRICAL DETAIL IS FOR
THE SIGNAL DESIGN: 05-1714T2
DESIGNED: February 2025
SEALED: 3/27/2025
REVISED: N/A

Electrical Detail - Temporary Design 2 (TMP Phases III & IV)
Sheet 2 of 3

DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED

Electrical and Programming Details For:

US 401 (Fayetteville Road)
at
Annaron Court

Division 5 Wake County Raleigh

Prepared in the Offices of:

Transportation Mobility and Safety Division
STATE OF NORTH CAROLINA DEPARTMENT OF TRANSPORTATION
ITS & Signals Management Section

750 N. Greenfield Pkwy, Garner, NC 27529

PLAN DATE:	March 2025	REVIEWED BY:	
PREPARED BY:	Tim Langston	REVIEWED BY:	

REVISIONS	INIT.	DATE

SEAL

NORTH CAROLINA PROFESSIONAL SEAL 031001 ENGINEER D. TODD JOYCE

DocuSigned by: D. Todd Joyce 03/28/2025
A90CADEF8D4D41D...
DATE

SIG. INVENTORY NO. 05-1714T2

28-MAR-2025 08:56
pw:\\pwcentfiley.contracdot*pw:01\Documents\NCDOT TSMO\Signal Design Section\Division_05\05-1714\Signals Management\05171412_sm_ele_yyyymmdd.dgn
dgn:\\pwcentfiley.contracdot*pw:01\Documents\NCDOT TSMO\Signal Design Section\Division_05\05-1714\Signals Management\05171412_sm_ele_yyyymmdd.dgn
tslangston