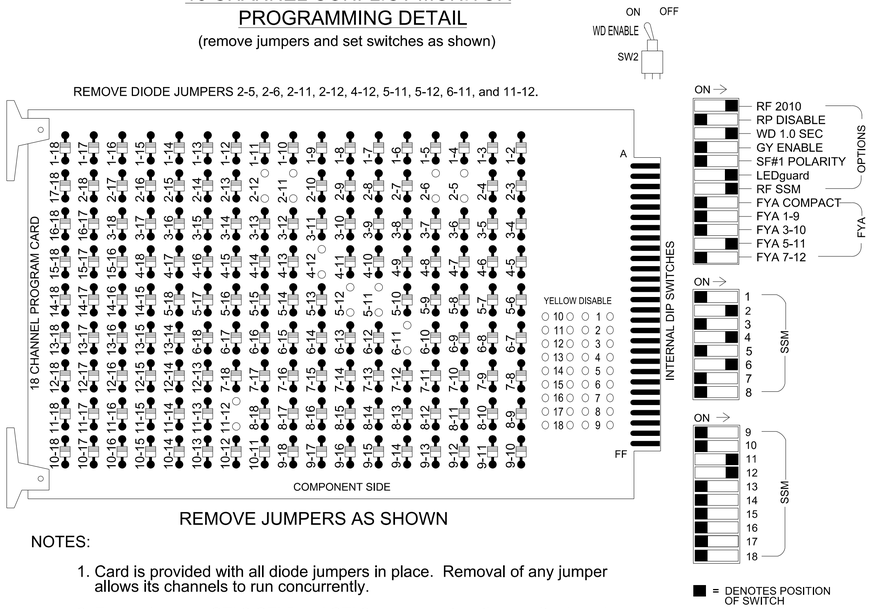
Docusign Envelope ID: CEA204B1-8994-4503-B71A-292DC63130FF

PROJECT REFERENCE NO.	SHEET NO.
U-3609B	SIG-11.1

18 CHANNEL CONFLICT MONITOR PROGRAMMING DETAIL

(remove jumpers and set switches as shown)



REMOVE JUMPERS AS SHOWN

NOTES:

1. Card is provided with all diode jumpers in place. Removal of any jumper allows its channels to run concurrently.
2. Ensure jumpers SEL2-SEL5 and SEL9 are present on the monitor board.
3. Ensure that the Red Enable is active at all times during normal operation.
4. Connect serial cable from conflict monitor to comm. port 1 of 2070 controller. Ensure conflict monitor communicates with 2070.

NOTES

1. To prevent "flash-conflict" problems, insert red flash program blocks for all vehicle load switches in the output file. The installer shall verify that signal heads flash in accordance with the signal plan.
2. Program controller to start up in phase 2 Green No Walk and 6 Green No Walk.
3. If this signal will be managed by an ATMS software, enable controller and detector logging for all detectors used at this location.

EQUIPMENT INFORMATION

Controller.....2070LX
Cabinet.....332 w/ Aux
Software.....Q-Free MAXTIME
Cabinet Mount.....Base
Output File Positions.....18 With Aux. Output File
Load Switches Used.....S2, S5, S7, S8, AUX S4, AUX S5
Phases Used.....2, 4, 5, 6
Overlap "1".....NOT USED
Overlap "2".....NOT USED
Overlap "3".....*
Overlap "4".....*

*See overlap programming detail this sheet

SIGNAL HEAD HOOK-UP CHART

LOAD SWITCH NO.	S1	S2	S3	S4	S5	S6	S7	S8	S9	S10	S11	S12	AUX S1	AUX S2	AUX S3	AUX S4	AUX S5	AUX S6
CMU CHANNEL NO.	1	2	13	3	4	14	5	6	15	7	8	16	9	10	17	11	12	18
PHASE	1	2	2 PED	3	4	4 PED	5	6	6 PED	7	8	8 PED	OL1	OL2	SPARE	OL3	OL4	SPARE
SIGNAL HEAD NO.	NU	21.22	NU	NU	41	NU	51★	61,62	NU	NU	NU	NU	NU	NU	NU	51★	42,43	NU
RED		128			101			134									A101	
YELLOW		129					*	135										
GREEN		130						136										
RED ARROW																A114		
YELLOW ARROW					102											A115	A102	
FLASHING YELLOW ARROW																A116		
GREEN ARROW					103		133										A103	

NU = Not Used
*Denotes install load resistor. See load resistor installation detail this sheet.
★See pictorial of head wiring in detail this sheet.

MAXTIME STARTUP AND SOFTWARE FLASH PROGRAMMING DETAIL

Front Panel
Main Menu >Controller >Unit

Web Interface
Home >Controller >Unit

Modify parameters as shown below and save changes.

Start Up Parameters

StartUp Clearance Hold
6

Unit Flash Parameters

All Red Flash Exit Time
6

FYA SIGNAL WIRING DETAIL

(wire signal heads as shown)

INPUT FILE POSITION LAYOUT

(front view)

		1	2	3	4	5	6	7	8	9	1O	11	12	13	14
FILE "I"	U	SLOT EMPTY	Ø2 2A	SLOT EMPTY	SLOT EMPTY	SLOT EMPTY	Ø4 4A	SLOT EMPTY	SLOT EMPTY	SLOT EMPTY	SLOT EMPTY	SLOT EMPTY	SLOT EMPTY	SLOT EMPTY	FS DC ISOLATOR
	L		NOT USED				NOT USED								ST DC ISOLATOR
FILE "J"	U	Ø5 5A	Ø5 5B	Ø6 6A	SLOT EMPTY	SLOT EMPTY	SLOT EMPTY	SLOT EMPTY	SLOT EMPTY	SLOT EMPTY	SLOT EMPTY	SLOT EMPTY	SLOT EMPTY	SLOT EMPTY	SLOT EMPTY
	L	NOT USED	NOT USED	NOT USED											

EX.: 1A, 2A, ETC. = LOOP NO.'S

FS = FLASH SENSE
ST = STOP TIME

INPUT FILE CONNECTION & PROGRAMMING CHART

LOOP NO.	LOOP TERMINAL	INPUT FILE POS.	PIN NO.	INPUT POINT	DETECTOR NO.	CALL PHASE	DELAY TIME	EXTEND TIME	EXTEND	ADDED INITIAL	CALL	DELAY DURING GREEN
2A	TB2-5,6	I2U	39	1	2	2			X	X	X	
4A	TB4-9,10	I6U	41	3	8	4			X		X	
5A	TB3-1,2	J1U	55	17	15	5	15.0		X		X	
				-	31	2	3.0		X		X	X
5B	TB3-5,6	J2U	40	2	16	5	15.0		X		X	
6A	TB3-9,10	J3U	64	30	18	6			X	X	X	

INPUT FILE POSITION LEGEND: J2L
FILE J
SLOT 2
LOWER

OVERLAP PROGRAMMING

Front Panel
Main Menu >Controller >Overlap >Overlap Parameters/Overlap Timings

Web Interface
Home >Controller >Overlap Configuration >Overlaps

Overlap Plan 1

Overlap	3	4
Type	FYA 4 - Section	Normal
Included Phases	6	4,5
Modifier Phases	5	-
Modifier Overlaps	-	-
Trail Green	0	0
Trail Yellow	0.0	0.0
Trail Red	0.0	0.0

FLASHER CIRCUIT MODIFICATION DETAIL

IN ORDER TO INSURE THAT SIGNALS FLASH CONCURRENTLY ON THE SAME APPROACH, MAKE THE FOLLOWING FLASHER CIRCUIT CHANGES:

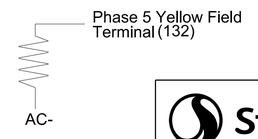
1. ON REAR OF PDA - REMOVE WIRE FROM TERM. T2-4 AND TERMINATE ON T2-2.
2. ON REAR OF PDA - REMOVE WIRE FROM TERM. T2-5 AND TERMINATE ON T2-3.
3. REMOVE FLASHER UNIT 2.

THE CHANGES LISTED ABOVE TIES ALL PHASES AND OVERLAPS TO FLASHER UNIT 1.

LOAD RESISTOR INSTALLATION DETAIL

(install resistors as shown)

ACCEPTABLE VALUES	
Value (ohms)	Wattage
1.5K - 1.9K	25W (min)
2.0K - 3.0K	10W (min)



THIS ELECTRICAL DETAIL IS FOR
THE SIGNAL DESIGN: 04-1468
DESIGNED: March 2025
SEALED: 8/18/2025
REVISED:

Stantec
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License No. F-0672

Electrical Detail

DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED

Electrical and Programming Details For:

Prepared for the Offices of:
Transportation Mobility and Safety Division
STATE OF NORTH CAROLINA
DEPARTMENT OF TRANSPORTATION
ITS & Signals Management Section
750 N. Greenfield Pkwy, Garner, NC 27529

US 13 (Berkeley Boulevard) at SR 1572 (Saulston Road)

Division 4 Wayne County Goldsboro

PLAN DATE: March 2025	REVIEWED BY: D A Waller
PREPARED BY: J Galloway	REVIEWED BY: R M Muncey

REVISIONS	INIT.	DATE

NORTH CAROLINA PROFESSIONAL SEAL 029904 ENGINEER JASON P. GALLOWAY

Signed by: Jason Galloway 100E2B4084846E... 8/18/2025
SIGNATURE DATE

SIG. INVENTORY NO. 04-1468

11:26:18 AM
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User:jgalloway