

### ECONOLITE ASC/3-2070 OVERLAP PROGRAMMING DETAIL (program controller as shown)

- From Main Menu select **2. CONTROLLER**
- From CONTROLLER Submenu select **2. VEHICLE OVERLAPS**

Toggle to 'Overlap G'  
**OVERLAP G**  
Select TMG VEH OVLP [G] and 'NORMAL'

```

TMG VEH OVLP...[G] TYPE: .....NORMAL
PHASES 1 2 3 4 5 6 7 8 9 0 1 2 3 4 5 6
INCLUDED X . X . . . . .
LAG GRN 0.0 YEL 0.0 RED 0.0
  
```

Toggle Once

#### OVERLAP H

Select TMG VEH OVLP [H] and 'NORMAL'

```

TMG VEH OVLP...[H] TYPE: .....NORMAL
PHASES 1 2 3 4 5 6 7 8 9 0 1 2 3 4 5 6
INCLUDED . . X X . . . . .
LAG GRN 0.0 YEL 0.0 RED 0.0
  
```

Toggle Once

#### OVERLAP I

Select TMG VEH OVLP [I] and 'NORMAL'

```

TMG VEH OVLP...[I] TYPE: .....NORMAL
PHASES 1 2 3 4 5 6 7 8 9 0 1 2 3 4 5 6
INCLUDED . . X X . . . . .
LAG GRN 0.0 YEL 0.0 RED 0.0
  
```

Toggle to 'Overlap A'

#### OVERLAP A

Select TMG VEH OVLP [A] and 'PPLT FYA'

```

TMG VEH OVLP...[A] TYPE: .....PPLT FYA
PROTECTED LEFT TURN.... PHASE 1
OPPOSING THROUGH..... PHASE 2

FLASHING ARROW OUTPUT.....CH9 ISOLATE
DELAY START OF: FYA..0.0 CLEARANCE..0.0
ACTION PLAN SF BIT DISABLE..... 1
  
```

NOTICE ACTION PLAN SF BIT "1"

Toggle Once

#### OVERLAP B

Select TMG VEH OVLP [B] and 'PPLT FYA'

```

TMG VEH OVLP...[B] TYPE: .....PPLT FYA
PROTECTED LEFT TURN.... OVERLAP G
OPPOSING THROUGH..... PHASE 4

FLASHING ARROW OUTPUT.....CH10 ISOLATE
DELAY START OF: FYA..0.0 CLEARANCE..0.0
ACTION PLAN SF BIT DISABLE..... 0
  
```

Toggle Once

**OVERLAP C**  
Select TMG VEH OVLP [C] and 'PPLT FYA'

```

TMG VEH OVLP...[C] TYPE: .....PPLT FYA
PROTECTED LEFT TURN.... OVERLAP H
OPPOSING THROUGH..... PHASE 2

FLASHING ARROW OUTPUT.....CH11 ISOLATE
DELAY START OF: FYA..0.0 CLEARANCE..0.0
ACTION PLAN SF BIT DISABLE..... 0
  
```

Toggle Once

#### OVERLAP D

Select TMG VEH OVLP [D] and 'OTHER/ECONOLITE'

```

TMG VEH OVLP...[D] TYPE: OTHER/ECONOLITE
PHASES 1 2 3 4 5 6 7 8 9 0 1 2 3 4 5 6
INCLUDED . . . . . X . . . . .
PROTECT . . . . .
PED PRTC . . . . .
NOT OVLP . . . . .
FLSH GRN . . . . . 1 . . . . .
LAG X PH . . . . .
LAG 2 PH . . . . .
LAG GRN 0.0 YEL 0.0 RED 0.0 ADV GRN 0.0
  
```

END PROGRAMMING

### ECONOLITE ASC/3-2070 VEHICLE DETECTOR SETUP PROGRAMMING DETAIL FOR ALTERNATE PHASING LOOP 1A (program controller as shown)

## IMPORTANT!

Program detectors per the input file connection and programming chart shown on sheet 1 before proceeding.

- From Main Menu select **8. UTILITIES**
- From UTILITIES Submenu select **1. COPY/CLEAR**
- Copy from DETECTOR PLAN "1" to DETECTOR PLAN "2".

```

COPY / CLEAR UTILITY
FROM          TO
PHASE TIMING... > PHASE TIMING...
TIMING PLAN... > TIMING PLAN...
PH DET OPT PLAN... > PH DET OPT PLAN...
DETECTOR PLAN... 1 > DETECTOR PLAN... 2
TOGGLE TO SELECT A "FROM" AND A "TO"
THEN PRESS ENTER
  
```

- From Main Menu select **6. DETECTORS**
- From DETECTOR Submenu select **2. VEHICLE DETECTOR SETUP**
- Place cursor in VEH DET PLAN [ ] position and enter "2".

- Place cursor in VEH DETECTOR [ ] position and enter "1".  
- Set delay time to "0".

```

VEH DETECTOR [ 1]  VEH DET PLAN [ 2]
TYPE: N-NTCIP
TS2 DETECTOR..... ECPI LOG..... NO
DET PH - 1 2 3 4 5 6 7 8 9 0 1 2 3 4 5 6
1 1 . . . . .
CALL OPTION... YES DELAY TIME... 0.0
EXT OPTION. PASSAGE EXTENSION TIME. 0.0
USE ADDED INITIAL . CROSS SWITCH PH.. 0
LOCK IN..... NONE NTCIP VOL . OR OCC .
PMT QUEUE DELAY- NO
  
```

← NOTICE VEH DET PLAN 2

← ENSURE DELAY IS SET TO '0'

- Place cursor in VEH DETECTOR [ ] position and enter "26".  
- Set assigned phase to "0".

```

VEH DETECTOR [26]  VEH DET PLAN [ 2]
TYPE: G-GREEN EXTENSION/DELAY
TS2 DETECTOR..... ECPI LOG..... NO
DET PH - 1 2 3 4 5 6 7 8 9 0 1 2 3 4 5 6
26 0 . . . . .
EXTEND TIME... 0.0 DELAY TIME... 3.0
USE ADDED INITIAL . CROSS SWITCH PH.. 0
LOCK IN..... NONE NTCIP VOL . OR OCC .
PMT QUEUE DELAY- NO
  
```

← NOTICE VEH DET PLAN 2

ENSURE PHASE IS SET TO "0"

END PROGRAMMING

### ECONOLITE ASC/3-2070 LOAD SWITCH ASSIGNMENT DETAIL (program controller as shown)

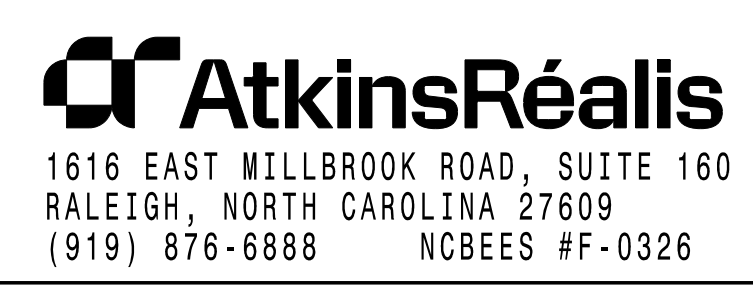
To assign load switches S4 as OLG, S7 as OLH, and S11 as OLI, program LD SWITCH 3 as OVLP '7' TYPE '0', LD SWITCH 5 as OVLP '8' TYPE '0', LD SWITCH 8 as OVLP '9' TYPE '0' as shown below.

- From Main Menu select **1. CONFIGURATION**
- From CONFIGURATION Submenu select **3. LOAD SW ASSIGN**

	LD SWITCH ASSIGN									
	PHASE	DIMMING	---	FLASH	---	TGR				
	/OVLP	TYPE	R	Y	G	D	PWR	AUT		
NOTICE OVERLAP G ASSIGNED TO LD SWITCH 3	1	1	V	.	.	.	+	A	R	X
	2	2	V	.	.	.	+	A	R	.
	3	7	0	.	.	.	+	A	R	X
NOTICE OVERLAP H ASSIGNED TO LD SWITCH 5	4	4	V	.	.	.	+	A	R	.
	5	8	0	.	.	.	-	A	R	.
	6	6	V	.	.	.	-	A	R	X
	7	7	V	.	.	.	-	A	R	.
NOTICE OVERLAP I ASSIGNED TO LD SWITCH 8	8	9	0	.	.	.	-	A	R	X
	9	2	P	.	.	.	+	A	.	.
	10	4	P	.	.	.	+	A	.	.
	11	6	P	.	.	.	-	A	.	.
	12	8	P	.	.	.	-	A	.	.
	13	1	0	.	.	.	+	A	R	X
	14	2	0	.	.	.	-	A	R	X
	15	3	0	.	.	.	+	A	R	.
	16	4	0	.	.	.	-	A	R	.

THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 07-1116  
DESIGNED: January 2025  
SEALED: 03-14-2025  
REVISED: N/A

13-MAR-2025 17:11 PW:///SUD0036433\_worhtk.ris.com:ATKMANCO1/Documents/Roads and Bridges/Projects/100059632\_USR S19 and ITS/Task 05-11\_Signals/071116\_sme\_e\_2023mtdat.dgn S1F4669 - AT L05491089



Electrical Detail - Sheet 2 of 4

DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED

SEAL

PROFESSIONAL ENGINEER SEAL 044476

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PLAN DATE: January 2025 REVIEWED BY: AM Encarnacion  
 PREPARED BY: JT Stiff REVIEWED BY: PL Alexander

REVISIONS	INIT.	DATE

Signature: Anthony Encarnacion DATE: 3/14/2025  
 Signature: \_\_\_\_\_ DATE: \_\_\_\_\_  
 SIG. INVENTORY NO. 07-1116