

ECONOLITE ASC/3-2070 LOGIC PROCESSOR PROGRAMMING DETAIL FOR LEADING PED INTERVAL (DELAYED GREEN)

(program controller as shown)

The following logic processor configuration holds the FYA's on signal heads 11, 23, 51 and 63 red for the duration of the delayed green time (leading ped interval) when serving a ped call on the opposing through phase.

- From Main Menu select **1. CONFIGURATION**
- From CONFIGURATION Submenu select **8. LOGIC PROCESSOR**
- From the LOGIC PROCESSOR Submenu select **2. LOGIC STATEMENTS**

ENTER A "1" IN THE LP# FIELD, PRESS 'ENTER', AND PROGRAM AS SHOWN.

```

LP#: 1 COPY FROM: 1 ACTIVE: M (T/F)
IF PED ON PH WALK 2 IS ON
AND VEH GREEN ON PH 2 IS OFF

THEN SIG SET OLP RED 1 ON
SIG SET OLP YELLOW 1 OFF
SIG SET OVLP GREEN 1 OFF

ELSE

```

HOLD SIGNAL HEAD 11 FYA RED DURING THE PHASE 2 DELAYED GREEN TIME (LEADING PED INTERVAL)

- From Main Menu select **1. CONFIGURATION**
- From CONFIGURATION Submenu select **8. LOGIC PROCESSOR**
- From the LOGIC PROCESSOR Submenu select **1. LOGIC STATEMENT CONTROL**

ENABLE LOGIC PROCESSOR STATEMENTS 1-4 BY POSITIONING THE CURSOR OVER THE FIELDS SHOWN BELOW AND USING THE TOGGLE KEY TO ENABLE THEM .

```

LOGIC STATEMENT CONTROL
1 2 3 4 5 6 7 8 9 0 1 2 3 4 5 6
LP 1-15 E E E E . . . . .
LP 16-30 . . . . .
LP 31-45 . . . . .
LP 46-60 . . . . .
LP 61-75 . . . . .
LP 76-90 . . . . .

```

END PROGRAMMING

ENTER A "2" IN THE LP# FIELD, PRESS 'ENTER', AND PROGRAM AS SHOWN.

```

LP#: 2 COPY FROM: 2 ACTIVE: M (T/F)
IF PED ON PH WALK 6 IS ON
AND VEH GREEN ON PH 6 IS OFF

THEN SIG SET OLP RED 3 ON
SIG SET OLP YELLOW 3 OFF
SIG SET OVLP GREEN 3 OFF

ELSE

```

HOLD SIGNAL HEAD 51 FYA RED DURING THE PHASE 6 DELAYED GREEN TIME (LEADING PED INTERVAL)

ENTER A "3" IN THE LP# FIELD, PRESS 'ENTER', AND PROGRAM AS SHOWN.

```

LP#: 3 COPY FROM: 3 ACTIVE: M (T/F)
IF PED ON PH WALK 6 IS ON
AND VEH GREEN ON PH 6 IS OFF

THEN SIG SET OLP RED 5 ON
SIG SET OLP YELLOW 5 OFF
SIG SET OVLP GREEN 5 OFF

ELSE

```

HOLD SIGNAL HEAD 63 FYA RED DURING THE PHASE 6 DELAYED GREEN TIME (LEADING PED INTERVAL)

ENTER A "4" IN THE LP# FIELD, PRESS 'ENTER', AND PROGRAM AS SHOWN.

```

LP#: 4 COPY FROM: 4 ACTIVE: M (T/F)
IF PED ON PH WALK 2 IS ON
AND VEH GREEN ON PH 2 IS OFF

THEN SIG SET OLP RED 6 ON
SIG SET OLP YELLOW 6 OFF
SIG SET OVLP GREEN 6 OFF

ELSE

```

HOLD SIGNAL HEAD 23 FYA RED DURING THE PHASE 2 DELAYED GREEN TIME (LEADING PED INTERVAL)

ASC/3 FLASH SENSE INPUT CONTROL FOR RED-RED FLASH

*The NCDOT default database is programmed to addresss Yellow-Red flash. Logic Statement 100 must be modified as shown when running Red-Red flash.

- From Main Menu select **1. CONFIGURATION**
- From CONFIGURATION Submenu select **8. LOGIC PROCESSOR**
- From LOGIC PROCESSOR Submenu select **2. LOGIC STATEMENTS**

Change the "LP" to 100 and move the cursor down. Delete the two "CTR-SET" statements by moving the cursor over them and hitting the "c" key. then hit "ENTER". select "LP SET CIB ON", hit "ENT", and then set the number to 427.

```

LP#:100 COPY FROM:100 ACTIVE: M FALSE
IF LP CIB CODE ON 331 F

THEN LP DELAY FOR 1.0 SECONDS
LP SET CIB ON 427

ELSE

```

THIS STATEMENT IS USED TO CONTROL THE FLASH SENSE INPUT WHEN RUNNING RED-RED FLASH OPERATION.

Hit "ESC", then 1 for "LOGIC STATEMENT CONTROL", next verify that LP#100 is ENABLED.

END PROGRAMMING

ECONOLITE ASC/3-2070 STARTUP AND SOFTWARE FLASH PROGRAMMING DETAIL

(program controller as shown)

- From Main Menu select **2. CONTROLLER**
- From CONTROLLER Submenu select **5. START/FLASH**

```

START/FLASH DATA
-----START UP-----
1 2 3 4 5 6 7 8 9 0 1 2 3 4 5 6
PHASE G G
A B C D E F G H I J K L M N O P
OVERLAP X X X X X X X X X X X X X X X X
FLASH>MON. NO FL TIME.. 0 ALL RED... 6
PWR START SEQ.. 1 MUTCD> YES Y- G: NO

```

Scroll down on this screen and set "Exit Fl" to Green "G"

COUNTDOWN PEDESTRIAN SIGNAL OPERATION

Countdown Ped Signals are required to display timing only during Ped Clearance Interval. Consult Ped Signal Module user's manual for instructions on selecting this feature.

FLASHER CIRCUIT MODIFICATION DETAIL

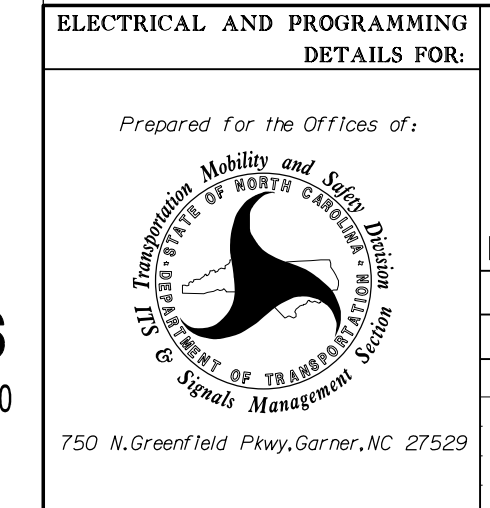
IN ORDER TO INSURE THAT SIGNALS FLASH CONCURRENTLY ON THE SAME APPROACH, MAKE THE FOLLOWING FLASHER CIRCUIT CHANGES:

- ON REAR OF PDA - REMOVE WIRE FROM TERM. T2-4 AND TERMINATE ON T2-2.
- ON REAR OF PDA - REMOVE WIRE FROM TERM. T2-5 AND TERMINATE ON T2-3.
- REMOVE FLASHER UNIT 2.

THE CHANGES LISTED ABOVE TIES ALL PHASES AND OVERLAPS TO FLASHER UNIT 1.

THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 07-1115
DESIGNED: January 2025
SEALED: 03-14-2025
REVISED: N/A

Electrical Detail - Sheet 4 of 4
Final Design

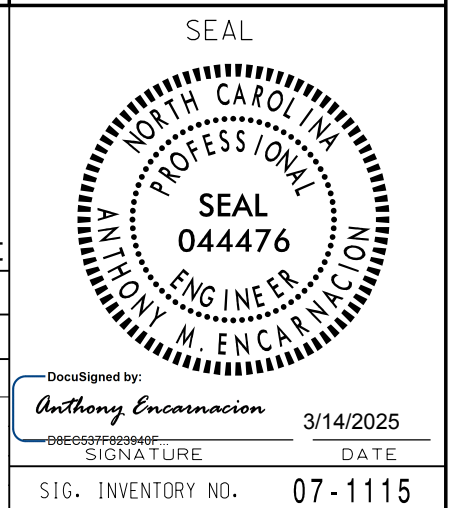


SR 1850 (Sandy Ridge Road) at SR 1556 (Gallimore Dairy Road)

Division 7 Guilford County High Point

PLAN DATE: January 2025	REVIEWED BY: AM Encarnacion
PREPARED BY: JT Stiff	REVIEWED BY: PL Alexander
REVISIONS	INIT. DATE

DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED



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