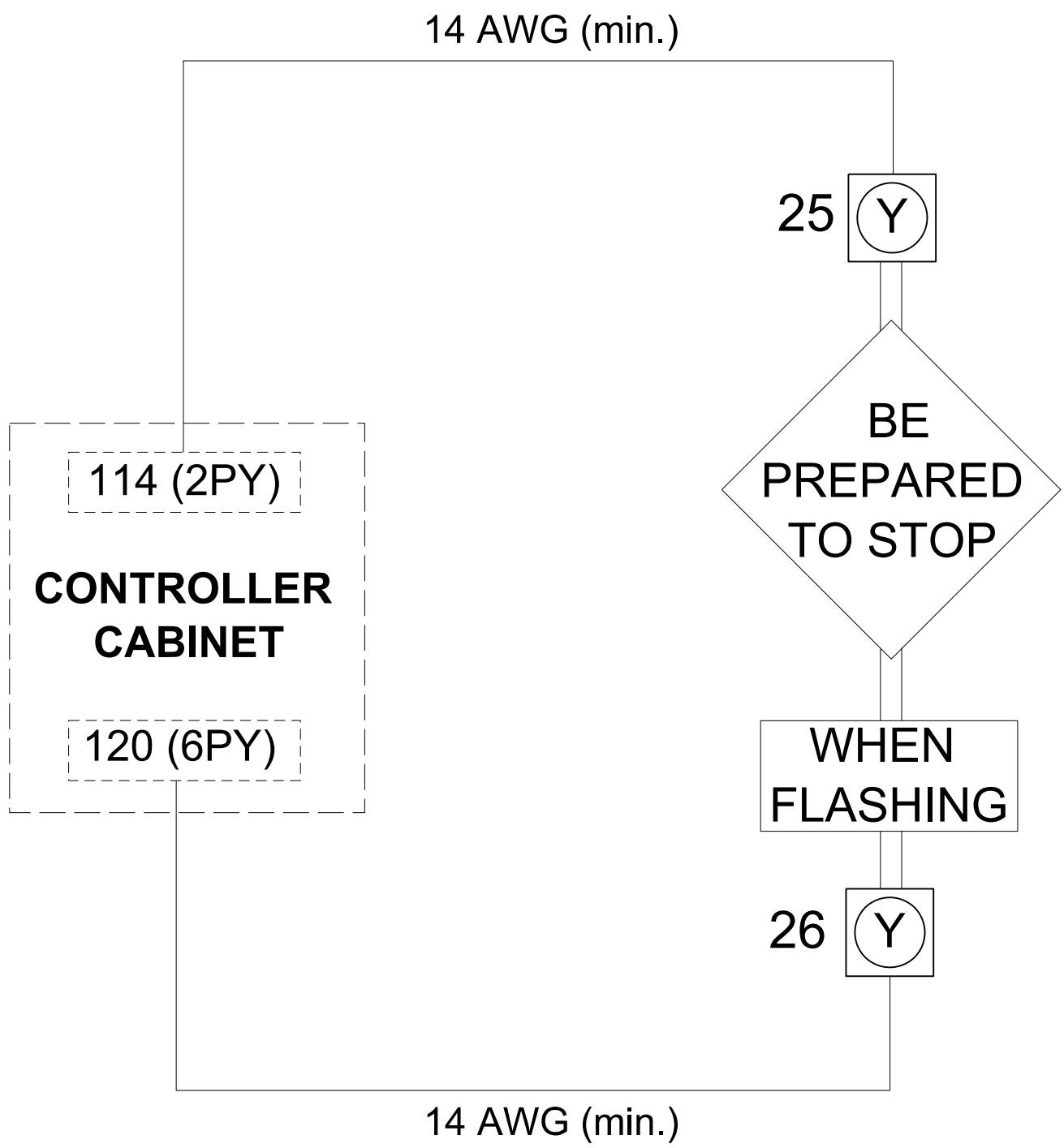


ADVANCE BEACON WIRING DETAIL

(wire flashers as shown below)

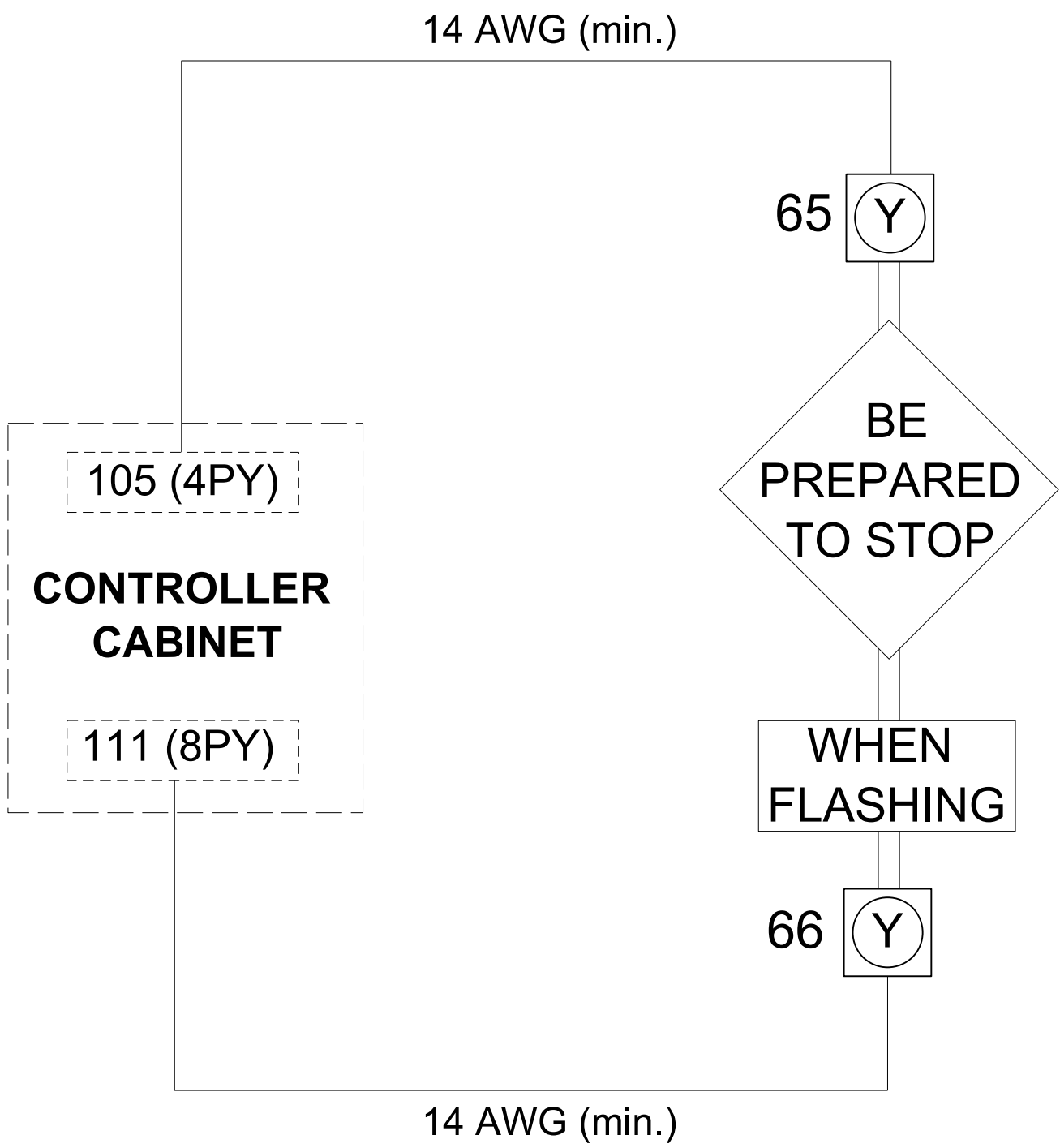


IMPORTANT

1. IF CONNECTED REMOVE, TAPE, AND LABEL CONFLICT MONITOR WIRE ATTACHED TO THE REAR OF TERMINAL 114 (2PY) AND TERMINAL 120 (6PY).
2. INSET LOADSWITCHES FOR S3 AND S9.
3. MAKE SURE LOAD RESISTORS ARE IN PLACE AS SHOWN IN LOAD RESISTOR INSTALLATION DETAIL ON SHEET 1.
4. TO ACTIVATE SIGN OPERATION AS INDICATED ON THE SIGNAL PLANS, REASSIGN OUTPUTS 33 AND 34 AS SHOWN ON THIS SHEET.

ADVANCE BEACON WIRING DETAIL

(wire flashers as shown below)



IMPORTANT

1. IF CONNECTED REMOVE, TAPE, AND LABEL CONFLICT MONITOR WIRE ATTACHED TO THE REAR OF TERMINAL 105 (4PY) AND TERMINAL 111 (8PY).
2. INSET LOADSWITCHES FOR S6 AND S12.
3. MAKE SURE LOAD RESISTORS ARE IN PLACE AS SHOWN IN LOAD RESISTOR INSTALLATION DETAIL ON SHEET 1.
4. TO ACTIVATE SIGN OPERATION AS INDICATED ON THE SIGNAL PLANS, REASSIGN OUTPUTS 35 AND 36 AS SHOWN ON THIS SHEET.

MAXTIME ALTERNATE PHASING ACTIVATION DETAIL

To run alternate phasing, select a Pattern that is programmed to run Overlap Plan 2 and Detector Plan 2. A Pattern can be selected through the scheduler or manually by changing the Operational Mode.

PHASING	OVERLAP PLAN	VEH DET PLAN
ACTIVE PLAN REQUIRED TO RUN DEFAULT PHASING	1	1
ACTIVE PLAN REQUIRED TO RUN ALTERNATE PHASING	2	2

ALTERNATE PHASING CHANGE SUMMARY

THE FOLLOWING IS A SUMMARY OF WHAT TAKES PLACE WHEN OVERLAP PLAN 2 AND VEHICLE DETECTOR PLAN 2 ACTIVATE TO CALL THE "ALTERNATE PHASING":

OVERLAP PLAN 2: Modifies overlap included phases for heads 11 and 51 to run protected turns only.

VEH DET PLAN 2: Disables phase 6 call on loop 1A and reduces delay time for phase 1 call on loop 1A to 0 seconds.

Disables phase 2 call on loop 5A and reduces delay time for phase 5 call on loop 5A to 0 seconds.

OUTPUT REMAPPING ASSIGNMENT
FOR SIGNAL HEADS 25, 26, 65, &66

Front Panel
Main Menu >Controller >More >Advanced IO >Output Points

Web Interface
Home >Controller >Advanced IO >Cabinet Configuration >Output Points

Modify IO Module 1 as shown below and save changes.

IO Module 1

Output Point	Descripton	Output Control Type	Index
33	C1-35	Channel Green Walk Driver	19
34	C1-36	Channel Red Do Not Walk Driver	19
35	C1-37	Channel Green Walk Driver	20
36	C1-38	Channel Red Do Not Walk Driver	20

THIS ELECTRICAL DETAIL IS FOR
THE SIGNAL DESIGN: 03-1244T1
DESIGNED: March 2025
SEALED: 3-31-2025
REVISED: N/A

Electrical Detail - Sheet 3 of 3

Electrical and Programming
Details For:

Prepared in the Offices of:

SEAL

TRANSPORTATION MOBILITY AND SAFETY DESIGN

SEAL

DEPARTMENT OF TRANSPORTATION

SEAL

Signal Management Section

US 17 (Ocean Highway West)
at
US 17 Bus (Main Street SW) /
SR 1316 (Old Shallotte Rd NW)

Division 3Brunswick CountyShallotte

PLAN DATE: March 2025REVIEWED BY: GG Murr, Jr.

PREPARED BY: JT RoweREVIEWED BY:

REVISIONSINIT.DATE

750 N. Greenfield Pkwy, Garner, NC 27529

DOCUMENT NOT CONSIDERED
FINAL UNLESS ALL
SIGNATURES COMPLETED

SEAL

NORTH CAROLINA
PROFESSIONAL
SEAL
008453
JOHN T. ROWE, JR.
ENGINEER

Signed by:
John T. Rowe, Jr.
DATE
4-1-2025
REFERENCE NO. 03-1244T1

TRANSYSTEMS

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4/1/2025
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