

ECONOLITE ASC/3-2070 LOAD SWITCH ASSIGNMENT DETAIL

(program controller as shown)

All channels must be programmed to flash red in controller for red-red flash operation as shown below. Notice "AUT" for all channels is programmed for RED.

1. From Main Menu select 1. CONFIGURATION
2. From CONFIGURATION Submenu select 3. LOAD SW ASSIGN

LD SWITCH ASSIGN									
PHASE	DIMMING	---FLASH---							
/OVLP	TYPE	R	Y	G	D	PWR	AUT	TGR	
1	1	V	.	.	.	+	A	R	X
2	2	V	.	.	.	+	A	R	.
3	3	V	.	.	.	+	A	R	X
4	4	V	.	.	.	+	A	R	.
5	5	V	.	.	.	-	A	R	.
6	6	V	.	.	.	-	A	R	X
7	7	V	.	.	.	-	A	R	.
8	8	V	.	.	.	-	A	R	X
9	1	O	.	.	.	+	A	R	X
10	2	O	.	.	.	+	A	R	X
11	3	O	.	.	.	-	A	R	.
12	4	O	.	.	.	-	A	R	.
13	2	P	.	.	.	+	A	.	.
14	4	P	.	.	.	-	A	.	.
15	6	P	.	.	.	+	A	.	.
16	8	P	.	.	.	-	A	.	.

ALTERNATE PHASING ACTIVATION DETAIL

TO RUN ALT. PHASING DURING FREE RUN - PROGRAM CHANGES (SHOWN BELOW) IN A TIME BASED ACTION PLAN. SCHEDULE A DAY PLAN THAT INCLUDES THE ACTION PLAN PROGRAMMED TO SELECT VEH DET PLAN 2 AND ENABLE SF BIT 1.

TO RUN ALT. PHASING DURING COORDINATION - SELECT THE TIME BASED ACTION PLAN THAT IS PROGRAMMED TO SELECT VEH DET PLAN 2 AND ENABLE SF BIT 1.

PHASING	VEH DET PLAN	SF BITS ENABLED
ACTIONS REQUIRED TO RUN <u>DEFAULT PHASING</u>	1	NONE
ACTIONS REQUIRED TO RUN <u>ALTERNATE PHASING</u>	2	1

IMPORTANT: IF ALT. PHASING IS USED DURING FREE RUN AND COORDINATION, DO NOT OPERATE TIME OF DAY EVENTS CONCURRENTLY WITH COORDINATION PLAN EVENTS IN THE EVENT SCHEDULER. (EX. FREE RUN EVENT SHOULD END BEFORE COORDINATION PLAN EVENT STARTS AND VICE-VERSA).

ECONOLITE ASC/3-2070 STARTUP AND SOFTWARE FLASH PROGRAMMING DETAIL

(program controller as shown)

1. From Main Menu select 2. CONTROLLER
2. From CONTROLLER Submenu select 5. START/FLASH

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START/FLASH DATA
-----START UP-----
 1 2 3 4 5 6 7 8 9 0 1 2 3 4 5 6
PHASE      R      R
          A B C D E F G H I J K L M N O P
OVERLAP  X X X X X X X X X X X X X X X X
FLASH>MON. NO FL TIME.. 0 ALL RED... 6
PWR START SEQ.. 1 MUTCD> YES Y- G: NO
    
```

Scroll down on this screen and set "Exit Fl" to Green "G"

ALTERNATE PHASING CHANGE SUMMARY

THE FOLLOWING IS A SUMMARY OF WHAT TAKES PLACE WHEN SF BIT 1 AND VEH DET PLAN 2 ACTIVATE TO CALL THE "ALTERNATE PHASING":

SF BIT 1: Modifies overlap parent phases for head 11 to run protected turns only.

VEH DET PLAN 2: Disables phase 6 call on loop 1A and reduces delay time for phase 1 call on loop 1A to 0 seconds.

ASC/3 FLASH SENSE INPUT CONTROL FOR RED-RED FLASH

*The NCDOT default database is programmed to address Yellow-Red flash. Logic Statement 100 must be modified as shown when running Red-Red flash.

1. From Main Menu select 1. CONFIGURATION
2. From CONFIGURATION Submenu select 8. LOGIC PROCESSOR
3. From LOGIC PROCESSOR Submenu select 2. LOGIC STATEMENTS

Change the "LP" to 100 and move the cursor down. Delete the two "CTR-SET" statements by moving the cursor over them and hitting the "C" key. then hit "ENTER", select "LP SET CIB ON", hit "ENT", and then set the number to 427.

```

LP#:100 COPY FROM:100 ACTIVE: M FALSE
IF LP CIB CODE ON 331 F

THEN LP DELAY FOR 1.0 SECONDS
LP SET CIB ON 427

ELSE
    
```

THIS STATEMENT IS USED TO CONTROL THE FLASH SENSE INPUT WHEN RUNNING RED-RED FLASH OPERATION.

Hit "ESC", then 1 for "LOGIC STATEMENT CONTROL", next verify that LP#100 is ENABLED.

END PROGRAMMING

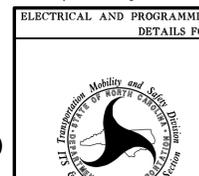
THIS ELECTRICAL DETAIL IS FOR
THE SIGNAL DESIGN: 07-2090T3
DESIGNED: Mar 2025
SEALED: 3/17/2025
REVISED: N/A

Electrical Detail - Sheet 3 of 3
Temporary Design 3 - (TMP Phase III)

DOCUMENT NOT CONSIDERED
FINAL UNLESS ALL
SIGNATURES COMPLETED



DRMP, INC.
8210 UNIVERSITY EXECUTIVE PARK DR.
SUITE 200
CHARLOTTE, NC 28262
PHONE: 704-543-4260



750 N. Greenfield Pkwy, Garner, NC 27529

ELECTRICAL AND PROGRAMMING DETAILS FOR:		NC 62 (Liberty Road) at I-85 Southbound Ramps	
Division 7		Guilford County Archdale	
PLAN DATE: March 2025	REVIEWED BY: ZM Esposito		
PREPARED BY: AW Poole	RKA PROJ. NO.: 17380 (040)		
REVISIONS	INIT.	DATE	



Signed by: *Zachary M. Esposito* 3/17/2025
DATE: _____
SIGNATURE: _____
DATE: _____
SIG. INVENTORY NO. 07-2090T3