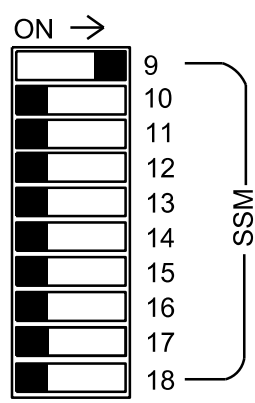
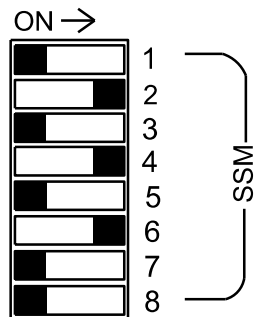
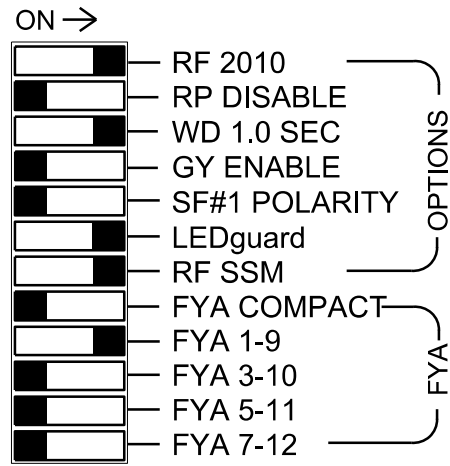
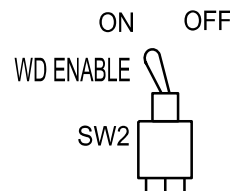


(remove jumpers and set switches as shown)

The diagram shows the back of the 18 Channel Program Card. It features two rows of 18 channels each, labeled 10-18 and 9-10. Each channel has a set of four pins (two on the left, two on the right) and a corresponding frequency value. The top row of pins is labeled 'COMPONENT SIDE' and the bottom row is labeled 'INTERNAL DIP SWITCHES'. The frequency values for the top row are: 10-18, 11-18, 12-18, 13-18, 14-18, 15-18, 16-18, 17-18, 1-18, 2-18, 3-18, 4-18, 5-18, 6-18, 7-18, 8-18, 9-18. The frequency values for the bottom row are: 10-17, 11-17, 12-17, 13-17, 14-17, 15-17, 16-17, 1-17, 2-17, 3-17, 4-17, 5-17, 6-17, 7-17, 8-17, 9-17, 10-16, 11-16, 12-16, 13-16, 14-16, 15-16, 1-16, 2-16, 3-16, 4-16, 5-16, 6-16, 7-16, 8-16, 9-16, 10-15, 11-15, 12-15, 13-15, 14-15, 1-15, 2-15, 3-15, 4-15, 5-15, 6-15, 7-15, 8-15, 9-15, 10-14, 11-14, 12-14, 13-14, 1-14, 2-14, 3-14, 4-14, 5-14, 6-14, 7-14, 8-14, 9-14, 10-13, 11-13, 12-13, 1-13, 2-13, 3-13, 4-13, 5-13, 6-13, 7-13, 8-13, 9-13, 10-12, 11-12, 1-12, 2-12, 3-12, 4-12, 5-12, 6-12, 7-12, 8-12, 9-12, 10-11, 1-11, 2-11, 3-11, 4-11, 5-11, 6-11, 7-11, 8-11, 9-11, 10-10, 1-10, 2-10, 3-10, 4-10, 5-10, 6-10, 7-10, 8-10, 9-10, 10-9, 1-9, 2-9, 3-9, 4-9, 5-9, 6-9, 7-9, 8-9, 9-9, 10-8, 1-8, 2-8, 3-8, 4-8, 5-8, 6-8, 7-8, 8-8, 9-8, 10-7, 1-7, 2-7, 3-7, 4-7, 5-7, 6-7, 7-7, 8-7, 9-7, 10-6, 1-6, 2-6, 3-6, 4-6, 5-6, 6-6, 7-6, 8-6, 9-6, 10-5, 1-5, 2-5, 3-5, 4-5, 5-5, 6-5, 7-5, 8-5, 9-5, 10-4, 1-4, 2-4, 3-4, 4-4, 5-4, 6-4, 7-4, 8-4, 9-4, 10-3, 1-3, 2-3, 3-3, 4-3, 5-3, 6-3, 7-3, 8-3, 9-3, 10-2, 1-2, 2-2, 3-2, 4-2, 5-2, 6-2, 7-2, 8-2, 9-2, 10-1, 1-1, 2-1, 3-1, 4-1, 5-1, 6-1, 7-1, 8-1, 9-1, 10-0, 1-0, 2-0, 3-0, 4-0, 5-0, 6-0, 7-0, 8-0, 9-0, 10-0.



■ = DENOTES POSITION OF SWITCH

1. To prevent "flash-conflict" problems, insert red flash program blocks for all vehicle load switches in the output file. The installer shall verify that signal heads flash in accordance with the Signa Plans.

2. Program controller to start up in phase 2 Green and 6 Green.

3. The cabinet and controller are part of the High Point Signal System.

```

CONTROLLER.....2070LX
CABINET.....332 W/ AUX
SOFTWARE.....ECONOLITE ASC/3-2070
CABINET MOUNT.....BASE
OUTPUT FILE POSITIONS...18 WITH AUX. OUTPUT FILE
LOAD SWITCHES USED.....S1,S2,S5,S8,AUX S1
PHASES USED.....1,2,4,6
OVERLAP "A".....*
OVERLAP "B".....NOT USED
OVERLAP "C".....NOT USED
OVERLAP "D".....NOT USED

```

* See overlap programming on this sheet.

For all loops, install a multizone microwave detection system for vehicle detection. Perform installation according to the manufacturer's directions and NCDOT engineer approved mounting locations to accomplish the detection schemes shown on the Signal Design Plans.

For Detection Zone 1A, the equipment and slot reserved for wired input is typical for an NCDOT installation.

LOOP NO.	LOOP TERMINAL	INPUT FILE POS.	PIN NO.	DETECTOR NO.	NEMA PHASE	CALL	EXTEND TIME	DELAY TIME	ADDED INITIAL	DETECTOR TYPE
1A ¹	TB2-1,2	I1U	56	1 ★	1	YES		15		N
	-	J4U	48	26 ★	6	YES		3		G

¹Add jumper from I1-W to J4-W, on rear of input file.

★ See vehicle detector setup programming detail for alternate phasing on sheet 2.

1. From Main Menu select **2. CONTROLLER**
2. From CONTROLLER Submenu select **2. VEHICLE OVERLAPS**

```

OVERLAP A
Select TMG VEH OVLP [A] and 'PPLT FYA'
  TMG VEH OVLP...[A] TYPE: ....PPLT FYA
  PROTECTED LEFT TURN...    PHASE    1
  OPPOSING THROUGH.....    PHASE    2

  FLASHING ARROW OUTPUT....CH9 ISOLATE

  DELAY START OF: FYA..0.0 CLEARANCE..0.0
  ACTION PLAN SF BIT DISABLE.....
END PROGRAMMING

```

NOTICE ACTION
PLAN SF BIT 1

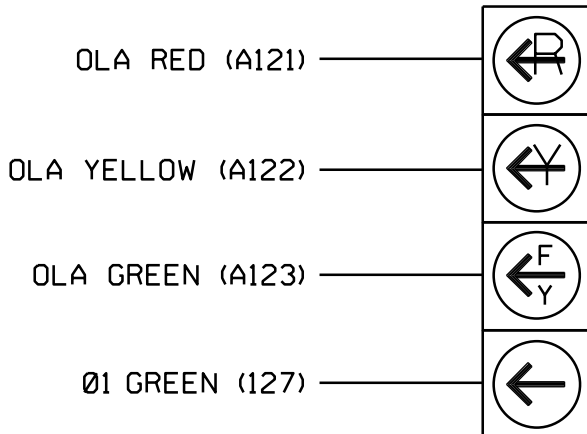
[illegible]

NU = Not Used

★ See pictorial of head wiring in detail this sheet.

* Denotes install load resistor. See load resistor installation detail this sheet.

(wire signal head as shown)



11

(front view)

[illegible]

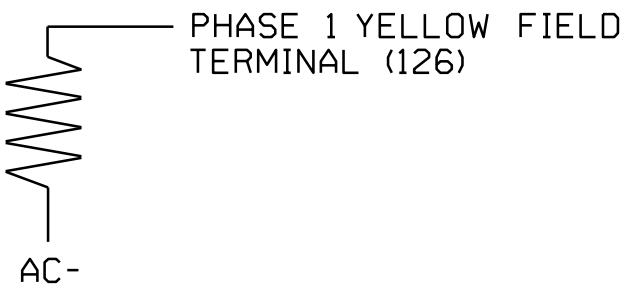
EX.: 1A, 2A, ETC. = LOOP NO.'S

FS = FLASH SENSE
ST = STOP TIME

⊗ Wired Input - Do not populate slot with detector card

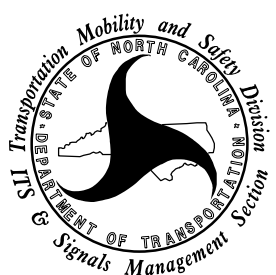
(install resistor as shown)

ACCEPTABLE VALUES	
VALUE (ohms)	WATTAGE
1.5K - 1.9K	25W (min)
2.0K - 3.0K	10W (min)



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Electrical Detail - Sheet 1 of 3
Temporary Design 2 - (TMP Phase II)

NC 62 (Liberty Road)
at
I-85 Southbound Ramps

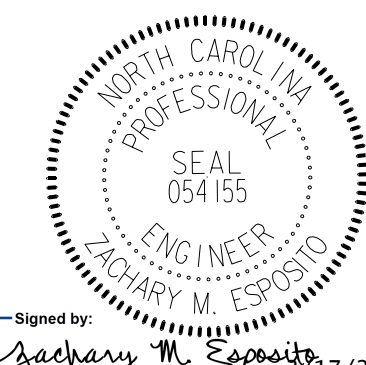
Division 7 Guilford County Archdale

PLAN DATE: March 2025	REVIEWED BY: ZM Esposito
PREPARED BY: AW Poole	RKA PROJ. NO.: 17380 (040)

REVISIONS	INIT.	DATE
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DOCUMENT NOT CONSIDERED
FINAL UNLESS ALL
SIGNATURES COMPLETED

SEA



Signed by: Zachary M. Esposito 3/17/2021

SIG. INVENTORY NO. 07-2090T2