

# MAXTIME ALTERNATE PHASING ACTIVATION DETAIL

To run alternate phasing, select a Pattern that is programmed to run Overlap Plan 2 and Detector Plan 2. A Pattern can be selected through the scheduler or manually by changing the Operational Mode.

#### PHASING

ACTIVE PLAN REQUIRED TO RUN DEFAULT PHASING ACTIVE PLAN REQUIRED TO RUN ALTERNATE PHASING

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ALTER	NATE PHASING CH
THE FOLLOWING IS A SU OVERLAP PLAN 2 AND V TO CALL THE "ALTERNA	EHICLE DETECTOR
	fies overlap included   ad 71 to run protecte
	ces delay time for pha n loop 7A to 0 second

# MAXTIME DETECTOR PROGRAMMING DETAIL FOR ALTERNATE PHASING LOOPS 7A

Front Panel Main Menu >Controller >Detector >Veh Det Plans

Web Interface Home >Controller >Detector Configuration >Vehicle Detectors

In the table view of web interface right click on "Detector" in the top left corner of the table. Copy the entire contents of Detector Plan 1. Paste Detector Plan 1 into Detector Plan 2. Modify Detector Plan 2 as shown below and save changes.

	Plan 2		
	Detector	Call Phase	Delay
7A	21	7	0

Signa 8601 Six Forks Road Suite 700 | Raleigh, North Carolina 27615-2965 750 N. Gree

PROJECT REFERENCE NO.	SHEET NO.
R-2577A	Sig. 27.2

OVERLAP PLAN	VEH DET PLAN
1	1
2	2
2	2

#### HANGE SUMMARY

TAKES PLACE WHEN R PLAN 2 ACTIVATE

phases ed turns only.

ase 7 ds.

> THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 09-0966 DESIGNED: February 2024 SEALED: February 12, 2024 **REVISED:**

nal Upgrade - Final	0	Electrical Detail	- Sheet 2 (	of 2		DOCUMENT NOT C FINAL UNLES SIGNATURES CO	SS ALL
Electrical and Programming Details For: Prepared for the Offices of:		US 158 EB (R a SR 2385 (Da		SEAL PRIH CAR OFESS/C SEAL 05614			
Step 1	Division 9	Forsyth	ertown	Z . NGINFER . X			
	PLAN DATE:	February 2024	REVIEWED BY:	DT Sears		P	· O in
	PREPARED BY:	WP Erickson-Jones	REVIEWED BY:			DocuSigned by:	minit
Onal OF TRANSPORT		REVISIONS		INIT. C	DATE	Purtue Aurice	
<sup>o</sup> Manaye.				Porter Jones	2/12/2024		
Greenfield Pkwy, Garner, NC 27529						D06E13E3A0E0498	DATE
						SIG. INVENTORY NO.	09-0996

DEFAULT PHASING DIAGRAM ALTERNATE PHASING DIAGRAM 73+8 PHASING DIAGRAM DETECTION LEGEND DETECTED MOVEMENT **---**UNDETECTED MOVEMENT (OVERLAP) -UNSIGNALIZED MOVEMENT ◀----<----> PEDESTRIAN MOVEMENT 31 R/W \_\_\_\_\_\_\_ R/W 45 MPH -2% Grade SR 2385 [Darrow Rd.] MAXTIME TIMING CHART PHASE FEATURE 8 3 6 RIM Walk \* -\_ \_ Ped Clear \* \_ \_ \_ 7 Min Green 7 12 Passage \* 2.0 6.0 2.0 Max 1 \* 30 90 30 3.0 Yellow Change 3.0 4.4 Red Clear 3.2 3.2 1.2 Added Initial \* 2.5 \_ — Maximum Initial \* 34 \_ \_ Time Before Reduction 15 -\_ Time To Reduce 30 \_ \_ 3.0 Minimum Gap -\_ Advance Walk \_ \_\_\_\_ \_ Non Lock Detector Х Х \_ MIN RECALL Vehicle Recall \_ \_ Х Dual Entry Х \* These values may be field adjusted. Do not adjust Min Green and Extension times for phase 6 lower than what is shown. Min Green

for all other phases should not be lower than 4 seconds.

DEFAULT F TABLE OF O			
	Ρ	HAS	E
SIGNAL FACE	Ø 6	Ø 3+8	FLANT
31	₽	-	<b>-</b> ¥
61, 62	ł	R	Y
81, 82	R		R

DEFAULT PHASING TABLE OF OPERATION									
	Р	HAS	E						
SIGNAL FACE	Ø 6	Ø 3+ 8	LUANI						
31	<del>≺R</del>	-	<b>-</b> ¥-						
61,62	1	R	Y						
81, 82	R		R						

## MAXTIME DETECTOR INSTALLATION CHART

	DETI	PROGRAMMING								
ZONE	SIZE (FT)	DISTANCE FROM STOPBAR (FT)	TURNS	NEW LOOP	CALL PHASE	DELAY TIME	EXTEND TIME	EXTEND	ADDED INITIAL	CALL
3A *	6X40	0	*	*	3	15#	<u>+</u>	Х	-	Х
6A *	6X6	300	*	*	6	÷	<u>+</u>	Х	Х	Х
8:A *	6X40	0	*	*	8	15	÷	Х	-	Х
				•	•		•			

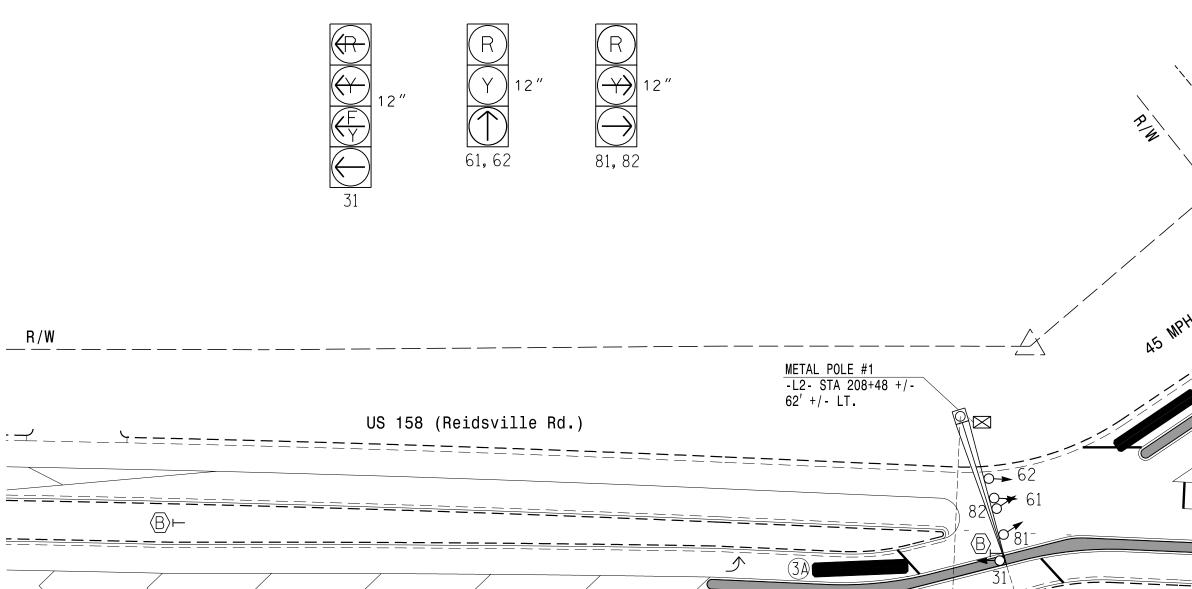
\* Video Detection Zone

< 20°00′ →

# Disable Delay During Alternate Phasing Operation.

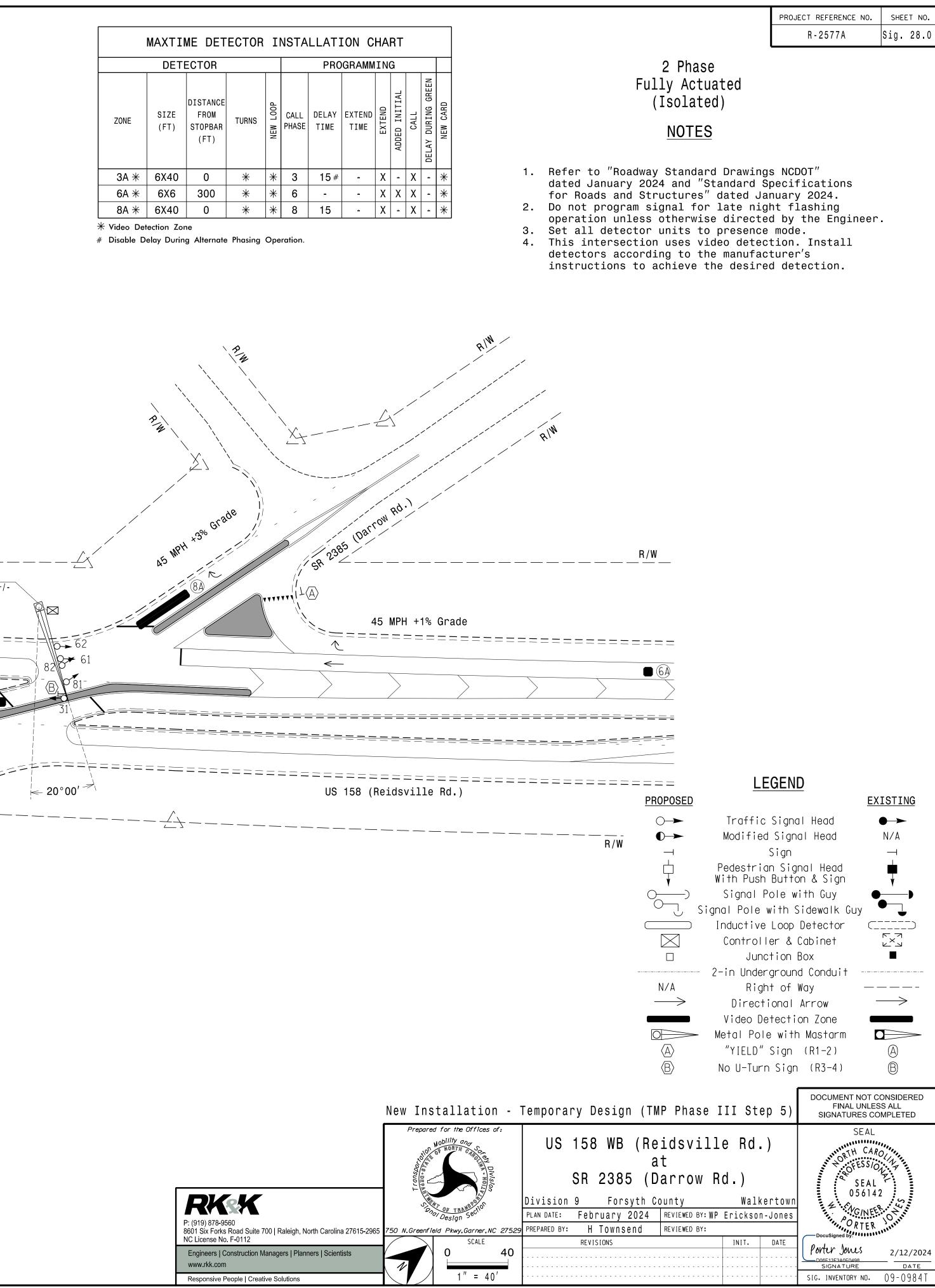
SIGNAL FACE I.D.

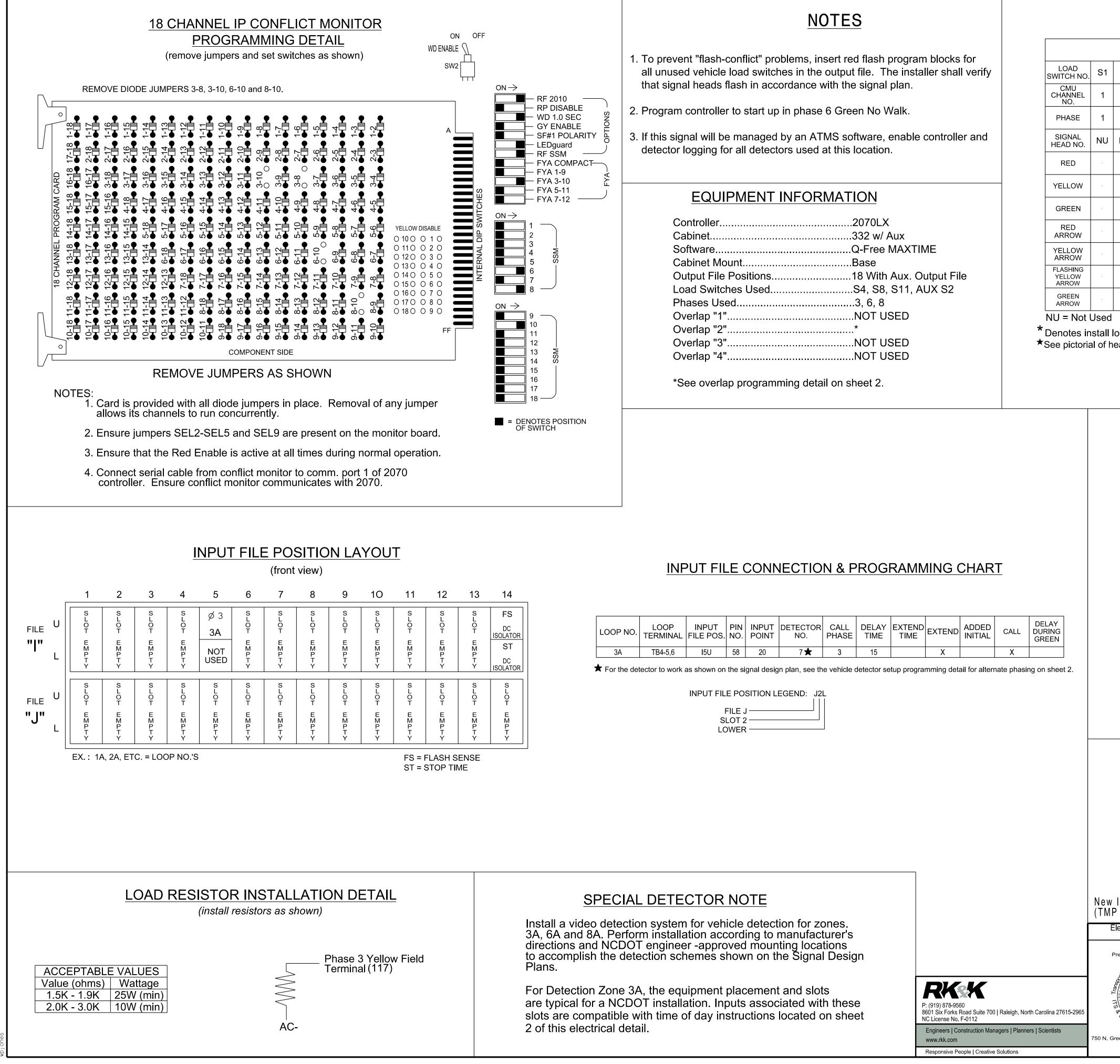
All Heads L.E.D.



`& & & & & A

MPH +1% Grade





LOOP NO.	LOOP TERMINAL	INPUT FILE POS.	PIN NO.	INPUT POINT	DETECTOR NO.	CALL PHASE	DELAY TIME	EXTEND TIME	EXTEND	ADDED INITIAL	CALL	DELAY DURING GREEN
3A	TB4-5,6	I5U	58	20	7★	3	15		Х		Х	

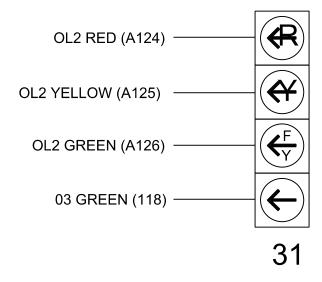
INPUT FILE POSITION LEGEND:	J2L
FILE J	

														PROJE	ECT REI	FERENC	E NO.	SHEET NO.
															R-25	577A		Sig. 28.1
		SIGNAL HEAD HOOK-UP CHART																
	·	<del></del>											l					
	S2	S3	S4	S5	S6	S7	S8	S9	S10	S11	S12	AUX S1	AUX S2	AUX S3	AŲX S4	AŲX S5	AŲX S6	
	2	13	3	4	14	5	6	15	7	8	16	9	10	17	11	12	18	
	2	2 PÉD	3	4	4 PÉD	5	6	6 PED	7	8	8 PÉD	OL1		SPARE	OL3	OL4	SPARE	
	NU	NU	<b>★</b> 31	NU	NU	NU	61,62	NU	NU	81,82	NU	NU	★ 31	NU	NU	NU	NU	
							134			107								
	·		*				135								·			
													A124					
										108			A125					
													A126					
			118				136			109								
-							·					•	•					

\* Denotes install load resistor. See load resistor installation detail this sheet. ★See pictorial of head wiring in detail this sheet.

# FYA SIGNAL WIRING DETAIL

(wire signal heads as shown)





/ Installation - Temp P Phase III Step 5)		tail - Sheet	1 of 2			DOCUMENT NOT FINAL UNLE SIGNATURES C	SS ALL
Electrical and Programming Details For: Prepared for the Offices of:	US	158 WB (R a R 2385 (Da	Ý I I OŠ			ROINA	
A Contraction of the second se	Division 9	Forsyth	,		alkertown		ER J
TS BAR	PLAN DATE:       February 2024       REVIEWED BY:       DT Sears         PREPARED BY:       WP Erickson-Jones       REVIEWED BY:						, in the
Contraction of the second			REVIEWED BY:		DATE	DocuSigned by:	inner
als Management	REVI	SIONS		INIT.	DATE	Porter Jones	2/12/2024
Greenfield Pkwy, Garner, NC 27529						D06E13E3A0E0498	DATE
						SIG. INVENTORY NO.	09-0984T

	MAXTIME OVERLAP PROGRAMMING DETAIL FOR DEFAULT PHASING
	Front Panel Main Menu >Controller >Overlap >Overlap Parameters/Overlap Timings
	Web Interface Home >Controller >Overlap Configuration >Overlaps
	Overlap Plan 1
	Overlap 2
	Type FYA 4 - Section
	Included Phases     6       Modifier Phases     3
	Modifier Overlaps     -       Trail Green     0
	Trail Yellow0.0Trail Red0.0
	FOR ALTERNATE PHASING         Front Panel         Main Menu >Controller >Overlap >Overlap Parameters/Overlap Timings         Web Interface         Home >Controller >Overlap Configuration >Overlaps         In the table view of the web interface, right click on         "Overlap" in the top left corner of the table. Copy the entire contents of Overlap Plan 1. Paste Overlap Plan 1 into Overlap Plan 2. Modify Overlap Plan 2 as shown below and save changes.         Overlap Plan 2         Overlap Plan 2         Modifier Phases         Modifier Phases         Modifier Overlaps         Included Phases         Modifier Overlaps         Modifier Overlaps
-	
ngb.XXX	MAXTIME ALTERNATE PHASING PATTERN PROGRAMMING DETAIL
sig_dsn_XXXXXX.dgn	Front Panel Main Menu >Controller >Coordination >Patterns
eT	Web Interface Home >Controller >Coordination >Patterns
1a   s*090984	Pattern Parameters
an¥S∶gn	PatternVeh Det PlanOverlap Plan*22
nals#Desi	* The Pattern number(s) are to be determined by
12/2024 *Traffic*Signals*Design*Sign jones	the Division and/or City Traffic Engineer.

# MAXTIME ALTERNATE PHASING ACTIVATION DETAIL

To run alternate phasing, select a Pattern that is programmed to run Overlap Plan 2 and Detector Plan 2. A Pattern can be selected through the scheduler or manually by changing the Operational Mode.

#### PHASING

ACTIVE PLAN REQUIRED TO RUN DEFAULT PHASING ACTIVE PLAN REQUIRED TO RUN ALTERNATE PHASING

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	ALTERNATE PHASING CH
OVERLAP PLAN 2	IS A SUMMARY OF WHAT AND VEHICLE DETECTOF FERNATE PHASING":
OVERLAP PLAN 2:	Modifies overlap included for head 31 to run protecte
VEH DET PLAN 2:	Reduces delay time for pha call on loop 3A to 0 second

# MAXTIME DETECTOR PROGRAMMING DETAIL FOR ALTERNATE PHASING LOOPS 3A

Front Panel Main Menu >Controller >Detector >Veh Det Plans

Web Interface Home >Controller >Detector Configuration >Vehicle Detectors

In the table view of web interface right click on "Detector" in the top left corner of the table. Copy the entire contents of Detector Plan 1. Paste Detector Plan 1 into Detector Plan 2. Modify Detector Plan 2 as shown below and save changes.

	Plan 2		
	Detector	Call Phase	Delay
3A	7	3	0

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PROJECT REFERENCE NO.	SHEET NO.
R-2577A	Sig. 28.2

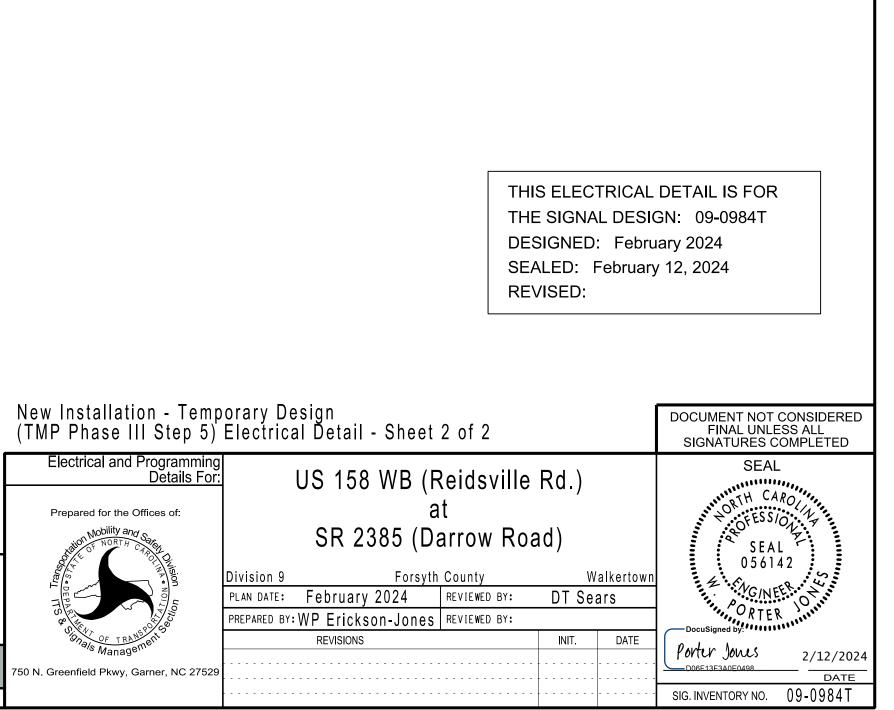
1
2

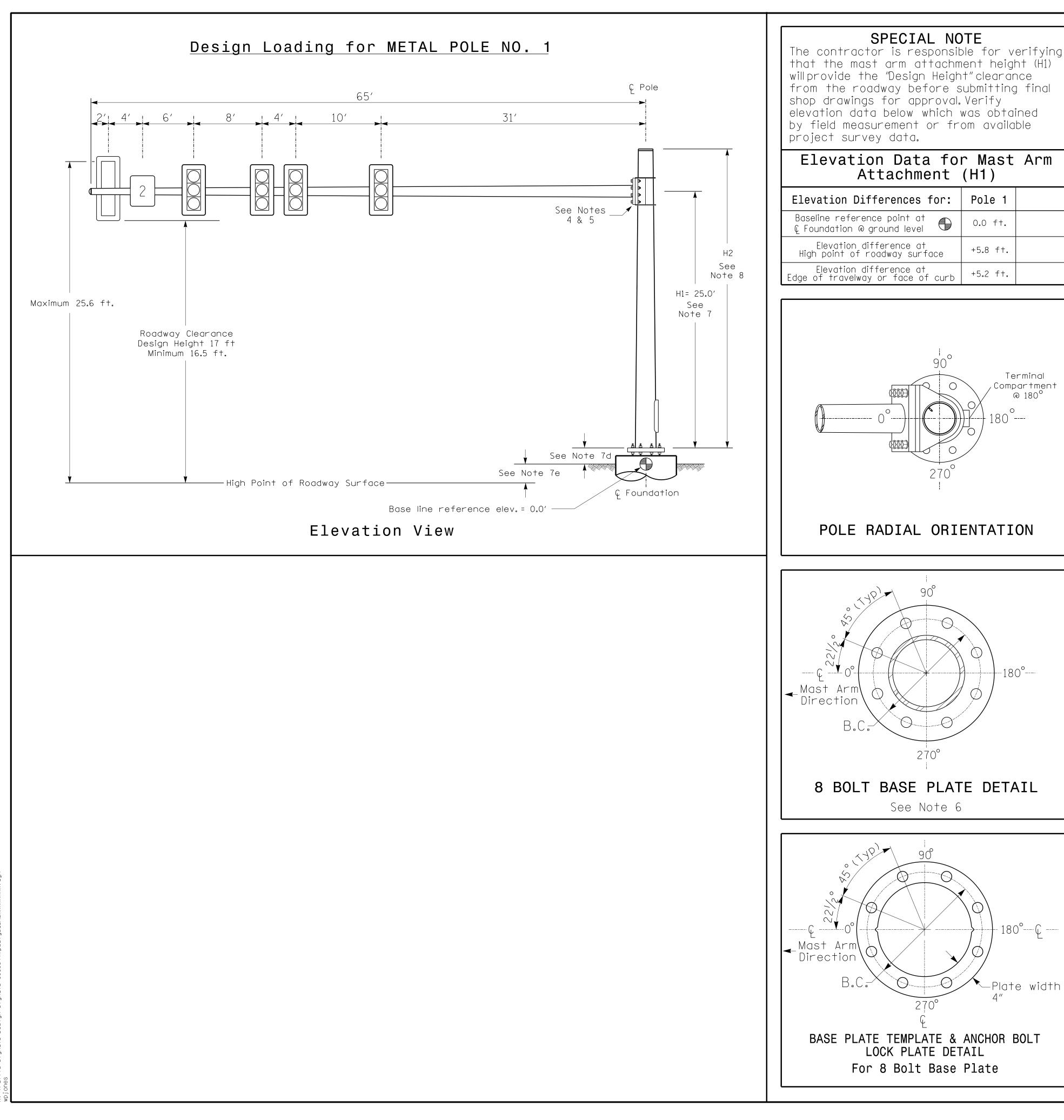
#### HANGE SUMMARY

T TAKES PLACE WHEN OR PLAN 2 ACTIVATE

l phases ed turns only.

nase 3 ds.





N R

# The contractor is responsible for verifying that the mast arm attachment height (H1) will provide the "Design Height" clearance from the roadway before submitting final

#### DESIGN REFERENCE

- . Design the • The 1st Ec
- Signs, Lumir
- The 2024
- the speci-• The 2024
- The traff
- The NCDOT
- https://co

### DESIGN REQUIREM

- 2. Design the views. These loads that w traffic sign
- 3. Design all sign 4. The camber
- pitched arch horizontalwh 5. A clamp-type
- stiffened bo requirements
- . Design base 7. The mast ar a.Mast arm
- height as b. Signalhead c. The roadw
- d. The top o
- e.Refer to foundatior
- 3. The pole man the following • Mast arm
- H1 plus 1/2 9. If pole loca-Engineer as
- contractor assistance a 0.The contract
- proper posit 11.The contract manufacture
  - NCDO R P: (919) 8 8601 Six NC Licen Enginee www.rkt Respon 750 N.Gre

N / A

	METAL POLE No. 1										
	IVI		L PUL		).				R - 25	77A	Sig. 28.3
MAST ARM LOADING SCHEDULE											
	LOADING	MAS	ST AR	M LO	ADIN	G SC	HEDU			_	
	SYMBOL			DESCRIPTIC			AREA	SIZE		-	
			IGID MOL -3 SECTI				9.3 S.F.	23.5 V X 52.5″L	60 LBS		
		RIGID MOUNTED SIGNAL HEAD 12"-4 SECTION-WITH BACKPLATE 11.5 S.F. 25.5" W X 66.0"L 74 LBS									
	2 SIGN RIGID MOUNTED 7.5 S.F. 30.0"W X 14 LBS 36.0"L										
tr di in N(	<u>CE MATERIAL</u> Traffic signal structure and foundation in accordance with: dition 2015 AASHTO LRFD "Standard Specifications for Structural Supports for Highway naires, and Traffic Signals, including all of the latest interim revisions. NCDOT "Standard Specifications for Roads and Structures." The latest addenda to fications can be found in the traffic signal project special provisions. NCDOT Roadway Standard Drawings.										
-ic	metalPole	oject p Stando	olans and ords″loco	d specie ated at	lprovisi the fo	ollowing					
M	ENTS										
traffic signal structure using the loading conditions shown in the elevation are anticipated worst case "design loads" and may not represent the actual vill be applied at the time of the installation. The contractor should refer to the al plans for the actual loads that will be applied at the time of the installation. In al supports using force ratios that do not exceed 0.9. design for the mast arm deflection should provide an appearance of a low in where the tip or the free end of the mast arm does not deflect below then fully loaded. The bolted mast arm-to-pole connection may be used instead of the welded ring box connection shown as long as the connection meets all of the design											
m s t ds vo f t n	-	nt heid deflect dssumed dy mour de heig base p on Data	ght (H1)s ion are d to off nted and ht for plate is a Chart the hig	shown is not co set ea vertic design 0.75 fee for th h point	s based nsidered ch othe cally cer is as sh et abov e eleva of the	on the d in de- er. ntered on nown in e the o tion dif e roadwo	e followi terminin on the the ele ground ference ay.	ng des g the mast evatior elevati es bet	sign assu arm at arm. views. ion. ween th	umptions tachmen e propo	t sed
): 2 +i +i +i +i +i	n ground leveland the high point of the roadway. hufacturer will determine the total height (H2) of each pole using the greater of attachment height (H1) plus 2 feet, or 2 of the total height of the mast arm attachment assembly plus 1 foot. tion adjustments are required, the contractor must gain approval from the this may affect the mast arm lengths and arm attachment heights. The may contact the Signal Design Section Senior Structural Engineer for 1 (919) 814-5000. tor is responsible for verifying that the mast arm length shown will allow rioning of the signal heads over the roadway. tor is responsible for providing soil penetration testing data (SPT) to the pole										
PT Wind Zone 4 (90 mph)         878-9560         x Forks Road Suite 700   Raleigh, North Carolina 27615-2965         nsvo F-0112         bers   Construction Managers   Planners   Scientists         kk.com         nsive People   Creative Solutions         repared for the Offices of:											
	Mobility ond NORTH CARONAL NORTH CARONAL CONTRACTOR	oly Division	Division PLAN DATE:	SR 23 9 F Februar	a 85 (Da orsyth Co y 2024	REVIEWED BY	Road) W	alkerto	WN NINNINN NINNINN	SEAL PRTH CAR POFESS/C SEAL 05614 CORTER	OVINA NAL 2 R. O
0	nfield Pkwy,Garner, SCALE N//		I NEFARED BY:	REVISIONS	5 0 11 - 0 0 11 e S	NEATEMED RA	: INIT	• DAT	E Porte	igned by: V JONLS	2/12/2024

- - SIGNATURE N/A

DATE

SIG. INVENTORY NO. 09-0984T

DEFAULT PHASING DIAGRAM ALTERNATE PHASING DIA PHASING DIAGRAM DETECTION LEGEND DETECTED MOVEMENT **---**UNDETECTED MOVEMENT (OVERLAP) UNSIGNALIZED MOVEMENT **4**----<----> PEDESTRIAN MOVEMENT 31 R/W US 158 (Reidsville Rd.) \_\_\_\_\_\_ -----R/W 45 MPH -2% Grade SR 2385 [Darrow Rd.] MAXTIME TIMING CHART PHASE FEATURE 8 3 6 RIM Walk \* -\_ \_ Ped Clear \* \_ \_ \_ 7 Min Green 7 12 Passage \* 2.0 6.0 2.0 Max 1 \* 30 90 30 3.0 Yellow Change 3.0 4.4 Red Clear 3.2 3.2 1.2 Added Initial \* 1.5 \_ — Maximum Initial \* 34 \_ \_ Time Before Reduction 15 -\_ Time To Reduce 30 \_ \_ 3.0 Minimum Gap \_ — Advance Walk \_ \_\_\_\_ \_ Non Lock Detector Х Х \_ MIN RECALL Vehicle Recall \_ \_ Х Dual Entry Х \* These values may be field adjusted. Do not adjust Min Green and Extension times for phase 6 lower than what is shown. Min Green

for all other phases should not be lower than 4 seconds.

																	_
IAGRAM	DEFAULT	PHASING OPERATION	ALTERNATE	ALTERNATE PHASING TABLE OF OPERATION			MAXTIME DETECTOR INSTALLATION CHART										
								DET	ECTOR				PRC	GRAMM	INC	;	
Ø3+8	SIGNAL FACE 31 61, 62	PHASE	SIGNAL FACE 31 61, 62	PH Ø 6 	HASE Ø F 3 L + f 8 F 		LOOP	SIZE (FT)	DISTANCE FROM STOPBAR (FT)	TURNS	NEW LOOP	CALL PHASE	DELAY TIME	EXTEND TIME		ADDED INITIAL	
			· · · ·				3A	6X40	0	2-4-2	X	3	15#	-	X	-	)
	81, 82	$R \rightarrow R$	81, 82	R	<b>→</b>		6A	6X6	300	5	X	6	-	<u> </u>	X	X	X
							6B	6X6	300	5	X	6	÷	<u>+</u>	X	X	X
							8A	6X40	0	2-4-2	X	8	15	<u>+</u>	X	-	X
							# Disable [	Delay Duri	ing Alterna	te Phasing	Ор	eration.			<u> </u>	L	

METAL POLE #1 -L2- STA 208+48 +/-

=====

62′+/-LT.

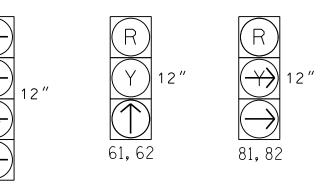
P

MPH +3% Grade

\_\_\_\_

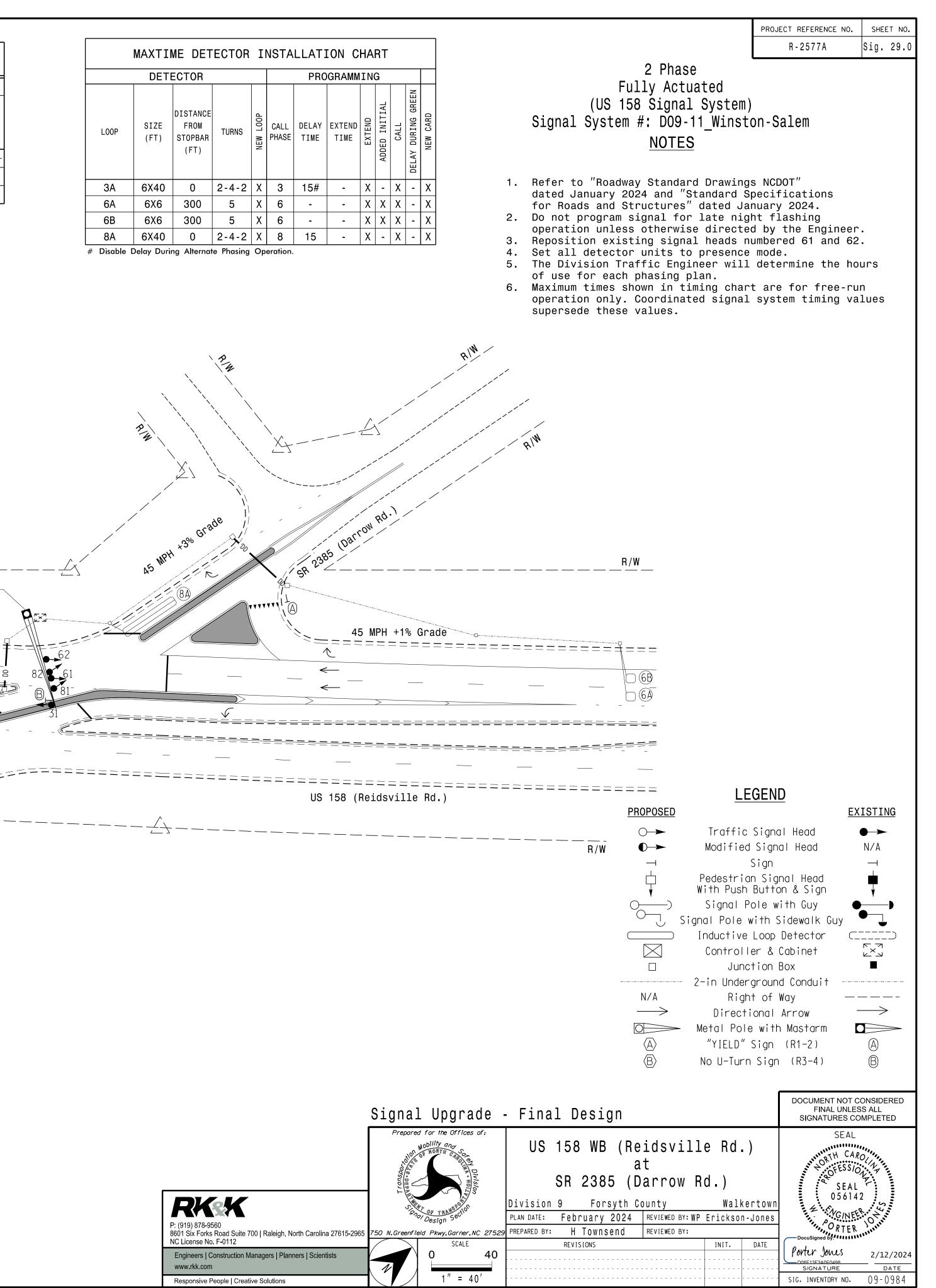
## SIGNAL FACE I.D.

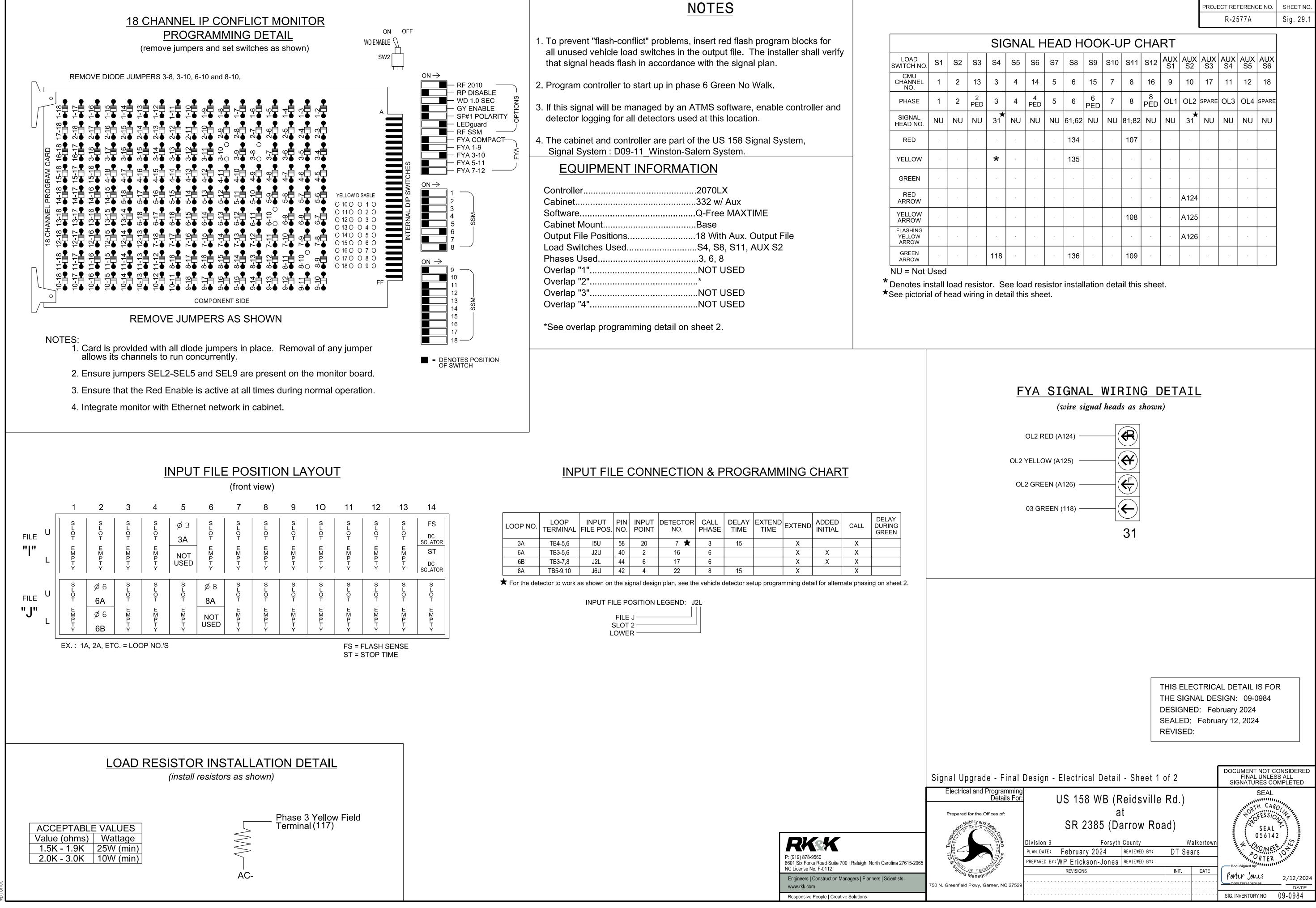
All Heads L.E.D.



MPH +1% Grade

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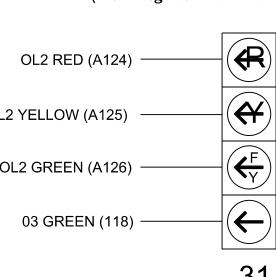


14							
	FS						
	DC ISOLATOR ST						
	DC ISOLATOR						
	S L OT						
	E M P T Y						
_							

	LOOP NO.	LOOP TERMINAL	INPUT FILE POS.	PIN NO.		DETECTOR NO.	CALL PHASE	DELAY TIME	EXTEND TIME	EXTEND	ADDED INITIAL	CALL	DELAY DURING GREEN
Γ	3A	TB4-5,6	I5U	58	20	7 ★	3	15		Х		Х	
Γ	6A	TB3-5,6	J2U	40	2	16	6			Х	Х	Х	
Γ	6B	TB3-7,8	J2L	44	6	1.7	6			Х	Х	Х	
	8A	TB5-9,10	J6U	42	4	22	8	15		Х		Х	

													PROJE	ECT REF	ERENG	CE NO.	SHEET NO.
														R-28	577A		Sig. 29.1
											пт						
		510	אוכ			DH	00	K-U		,HA	RI						
S2	S3	S4	S5	S6	S7	S8	S9	S10	S11	S12	AUX S1	AUX S2	AUX S3	AUX S4	AUX S5	AŲX S6	
2	13	З	4	14	5	6	15	7	8	16	9	10	17	11	12	18	
2	2 PÉD	3	4	4 PÉD	5	6	6 PED	7	8	8 PÉD	OL1	OL2	SPARE	OL3	OL4	SPARE	
NU	NU	<b>3</b> 1	NU	NU	NU	61,62	NU	NU	81,82	NU	NU	★ 31	NU	NU	NU	NU	
						134			107								
		*				135											
												A124					
									108			A125					
												A126					
		118				136			109								





# MAXTIME OVERLAP PROGRAMMING DETAIL FOR DEFAULT PHASING

Front Panel Main Menu >Controller >Overlap >Overlap Parameters/Overlap Timings

Web Interface Home >Controller >Overlap Configuration >Overlaps

Overlap Plan 1

Overlap	2				
Туре	FYA 4 - Section				
Included Phases	6				
Modifier Phases	3				
Modifier Overlaps	÷				
Trail Green	0				
Trail Yellow	0.0				
Trail Red	0.0				

# MAXTIME OVERLAP PROGRAMMING DETAIL FOR ALTERNATE PHASING

Front Panel Main Menu >Controller >Overlap >Overlap Parameters/Overlap Timings

Web Interface Home >Controller >Overlap Configuration >Overlaps

In the table view of the web interface, right click on "Overlap" in the top left corner of the table. Copy the entire contents of Overlap Plan 1. Paste Overlap Plan 1 into Overlap Plan 2. Modify Overlap Plan 2 as shown below and save changes.

Overlap Plan 2

Overlap	2	
Туре	FYA 4 - Section	
Included Phases	÷	NOTICE INCLUDED PHASE
Modifier Phases	3	
Modifier Overlaps	-	
Trail Green	0	
Trail Yellow	0.0	
Trail Red	0.0	

# MAXTIME ALTERNATE PHASING PATTERN **PROGRAMMING DETAIL**

Front Panel Main Menu >Controller >Coordination >Patterns

Web Interface

Home >Controller >Coordination >Patterns

Pattern Parameters										
Pattern Veh Det Plan Overlap Plan										
* 2 2										

\* The Pattern number(s) are to be determined by the Division and/or City Traffic Engineer.

# MAXTIME ALTERNATE PHASIN

To run alternate phasing, select a Pattern that is programmed to A Pattern can be selected through the scheduler or manually by

PHASING

ACTIVE PLAN REQUIRED TO RUN DEFAULT PHASING ACTIVE PLAN REQUIRED TO RUN ALTERNATE PHASING

#### ALTERNATE PHASING CHAN

THE FOLLOWING IS A SUMMARY OF WHAT TA OVERLAP PLAN 2 AND VEHICLE DETECTOR P TO CALL THE "ALTERNATE PHASING":

OVERLAP PLAN 2: Modifies overlap included pha for head 31 to run protected to VEH DET PLAN 2: Reduces delay time for phase call on loop 3A to 0 seconds.

# MAXTIME DETECTOR PROGRAMMING DETAIL FOR ALTERNATE PHASING LOOPS 3A

Front Panel Main Menu >Controller >Detector >Veh Det Plans

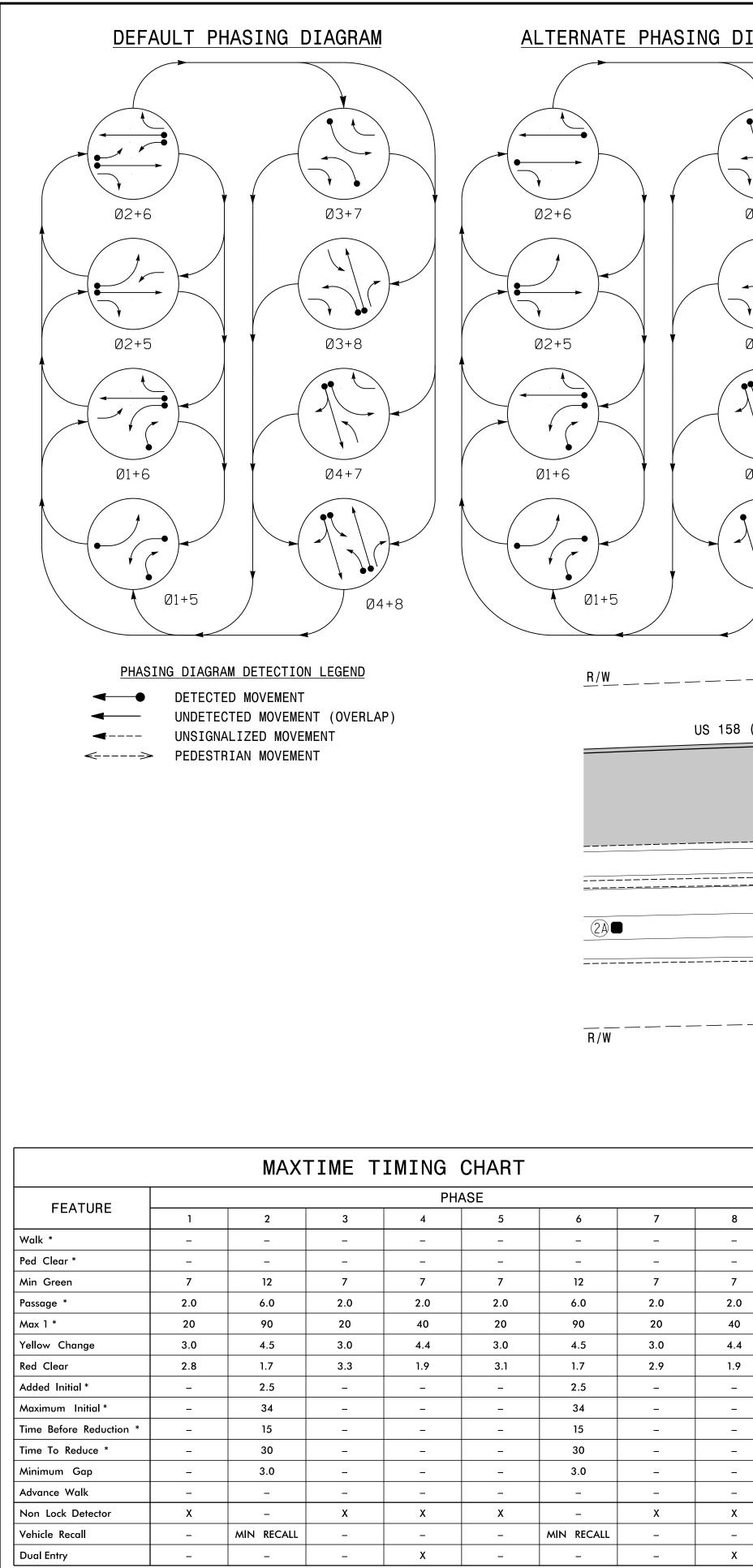
Web Interface Home >Controller >Detector Configuration >Vehicle Detectors

In the table view of web interface right click on "Detector" in the top left corner of the table. Copy the entire contents of Detector Plan 1. Paste Detector Plan 1 into Detector Plan 2. Modify Detector Plan 2 as shown below and save changes.

Plan 2

	Detector	Call Phase	Delay
3A	7	3	0

			PR	OJECT REFERENCE NO. R-2577A	sнеет no. Sig. 29.2
			L	1 2011 N	019. 20.2
LTERNATE PHASIN	<u>G ACTIVATIC</u>	<u>ON DETAIL</u>			
select a Pattern that is programmed to	run Overlan Plan 2 and D	etector Plan 2			
select a Pattern that is programmed to through the scheduler or manually by	changing the Operational	Mode.			
	OVERLAP PLAN	VEH DET PLAN			
RUN DEFAULT PHASING	1	1			
RUN ALTERNATE PHASING	2	2			
ALTERNATE PHASING CHANG	E SUMMARY				
OLLOWING IS A SUMMARY OF WHAT TAP	KES PLACE WHEN				
LAP PLAN 2 AND VEHICLE DETECTOR PL LL THE "ALTERNATE PHASING":					
AP PLAN 2: Modifies overlap included phase					
for head 31 to run protected tu	rns only.				
ET PLAN 2: Reduces delay time for phase	3				
call on loop 3A to 0 seconds.					
				CAL DETAIL IS FOI DESIGN: 09-0984	R
			DESIGNED: F	ebruary 2024	
			SEALED: Feb REVISED:	ruary 12, 2024	
		l			
	Signal Upgrade - Final C	Design - Electrical Detail - Sheet	2 of 2	DOCUMENT NOT C FINAL UNLES SIGNATURES CO	S ALL
	Electrical and Programming Details For:	US 158 WB (Reidsvi	lle Rd.)	SEAL	····,
	Prepared for the Offices of:	at		No FESSIO	
DVoV	CARDING NORTH CARDING	SR 2385 (Darrow F Division 9 Forsyth County	<b>VOAC)</b> Walkerto	wn SEAL	2
P: (919) 878-9560 8601 Six Forks Road Suite 700   Paleigh North Carolina 27615-2965	T CE P NOT L	PLAN DATE: February 2024 REVIEWED BY PREPARED BY: WP Erickson-Jones REVIEWED BY	: DT Sears	WIII CINES	10 AVIII
8601 Síx Forks Road Suite 700   Raleigh, North Carolina 27615-2965 NC License No. F-0112 Engineers   Construction Managers   Planners   Scientists	Grads Management	REVISIONS	INIT. DATE	Porter Jones	2/12/2024
	750 N. Greenfield Pkwy, Garner, NC 27529	· · · · · · · · · · · · · · · · · · ·		D06E13E3A0E0498	09-0984

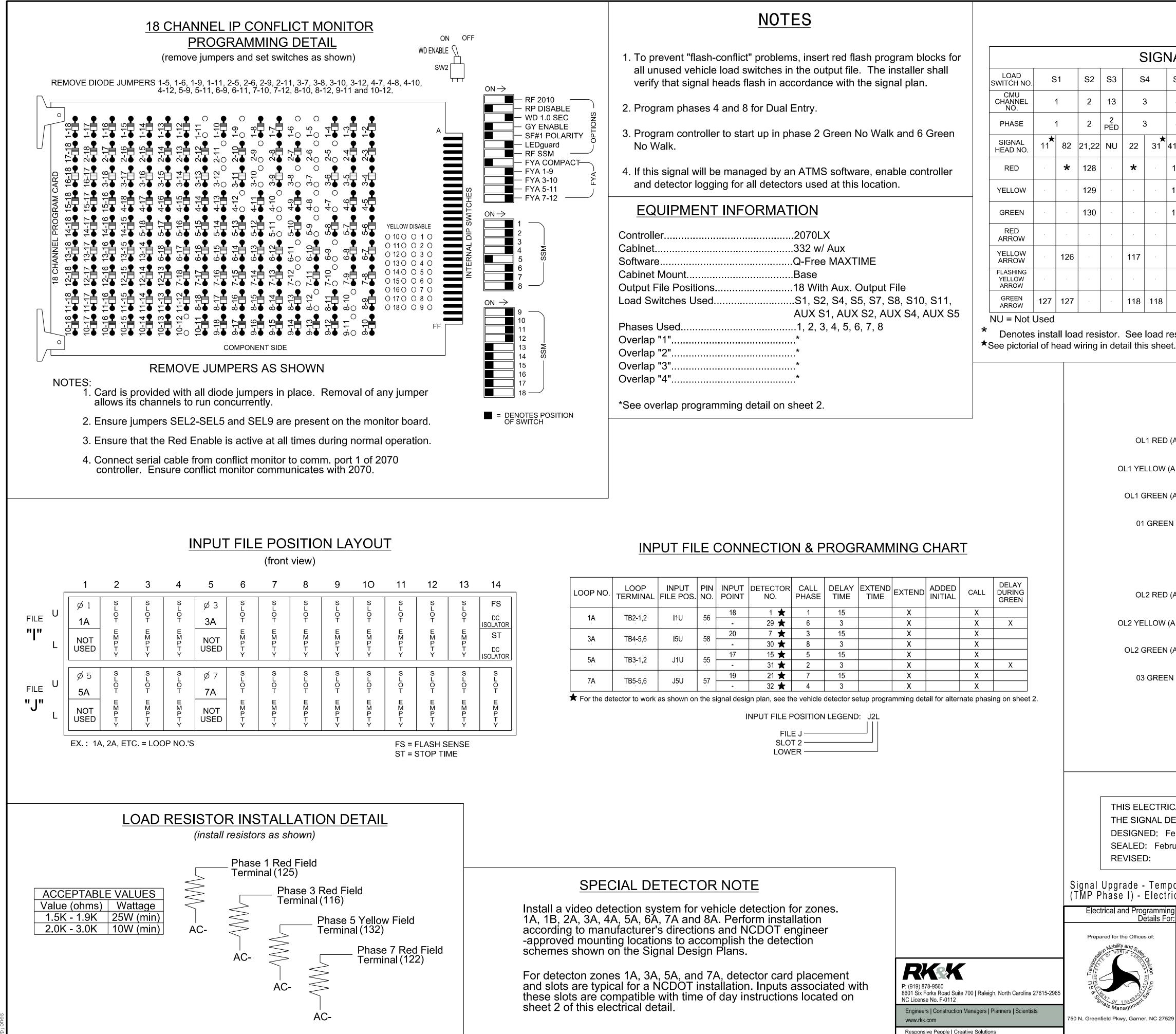


12/2024 \*Traffic\*Signals\*Design\*Signals\*090264T1\_sig\_dsn\_XXXXX

N N

\* These values may be field adjusted. Do not adjust Min Green and Extension times for phases 2 and 6 lower than what is shown. Min Green for other phases should not be lower than 4 seconds.

DIAGRAM	DEFAULT PHASING	ALTERNATE PHASING	PROJECT REFERENCE NO. SHEET NO. R-2577A Sig. 30.0
SIGNAL FACE I.D.	TABLE OF OPERATION	TABLE OF OPERATION	
All Heads L.E.D. All Heads L.E.D.	PHASE         SIGNAL       Ø       I </th <th>PHASE         SIGNAL       <math>\emptyset</math> <math>\emptyset</math></th> <th><ul> <li>8 Phase Fully Actuated (Isolated)</li> <li>NCTES</li> <li>1. Refer to "Roadway Standard Drawings NCDOT" dated January 2024 and "Standard Specifications for Roads and Structures" dated January 2024.</li> <li>2. Do not program signal for late night flashing operation unless otherwise directed by the Engineer.</li> <li>3. Phase 1 and/or phase 5 may be lagged.</li> <li>4. Phase 3 and/or phase 7 may be lagged.</li> <li>5. Set all detector units to presence mode.</li> <li>6. Locate new cabinet so as not to obstruct sight distance of vehicles turning right on red.</li> <li>7. This intersection uses video detection. Install detectors according to the manufacturer's instructions to achieve the desired detection.</li> <li>8. The Division Traffic Engineer will determine the hours of use for each phasing plan.</li> </ul></th>	PHASE         SIGNAL $\emptyset$	<ul> <li>8 Phase Fully Actuated (Isolated)</li> <li>NCTES</li> <li>1. Refer to "Roadway Standard Drawings NCDOT" dated January 2024 and "Standard Specifications for Roads and Structures" dated January 2024.</li> <li>2. Do not program signal for late night flashing operation unless otherwise directed by the Engineer.</li> <li>3. Phase 1 and/or phase 5 may be lagged.</li> <li>4. Phase 3 and/or phase 7 may be lagged.</li> <li>5. Set all detector units to presence mode.</li> <li>6. Locate new cabinet so as not to obstruct sight distance of vehicles turning right on red.</li> <li>7. This intersection uses video detection. Install detectors according to the manufacturer's instructions to achieve the desired detection.</li> <li>8. The Division Traffic Engineer will determine the hours of use for each phasing plan.</li> </ul>
Ø4+8 31 31 31 31 31 4 4 4 4 4 4 4 4 4 4 4 4 4	45 MPH +0% Grade	R/W	
45 MPH +0% Grade		<u>PR</u>	DEPOSED EXISTING → Traffic Signal Head → Modified Signal Head → Sign → Pedestrian Signal Head With Push Button & Sign → Signal Pole with Guy
MAXTIME DETECTOR INSTALLATION CHART     T       DETECTOR     PROGRAMMING	Grade 1		Inductive Loop Detector
			Controller & Cabinet <ul> <li>Junction Box</li> <li>Junction Box</li> <li>2-in Underground Conduit</li> <li>N/A</li> <li>Right of Way</li> <li>Directional Arrow</li> <li>Direction Zone</li> <li>Video Detection Zone</li> <li>Video Detection Zone</li> <li>Video Detection Zone</li> <li>No U-TURN YIELD TO RIGHT TURN"</li> <li>Sign (R10-16)</li> <li>No U-Turn Sign (R3-4)</li> <li>No U-Turn Sign (R3-4)</li> </ul>
1.9       2A *       6X6       300       *       *       2       -       -       X       X       -       *         -			
- 3A * 6X40 0 * * 8@ 3 - X - X - *	Signal	L Upgrade - Temporary Design 1	(TMP Phase I) DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED
$ \begin{array}{c ccccccccccccccccccccccccccccccccccc$		US 158 (Reids at NC 66 (Old H Division 9 Forsyth Cour PLAN DATE: February 2024 RE	SEAL Sville Rd.)
<ul><li>## Reduce Delay to 3 Seconds During Alternate Phasing Operation.</li><li>@ Disable Phase Call For Zone(s) During Alternate Phasing Operation.</li></ul>	Engineers   Construction Managers   Planners   Scientists www.rkk.com		Portur Jonus     2/12/2024       D06E13E3A0E0498     DATE
* Video Detection Zone	Responsive People   Creative Solutions	1 " = 40'	SIG. INVENTORY NO. 09-0264T



LOOP NO.	LOOP TERMINAL	INPUT FILE POS.	PIN NO.	INPUT POINT	DETECTOR NO.	CALL PHASE	DELAY TIME	EXTEND TIME	EXTEND	ADDED INITIAL	CALL	DEL DUR GRE			
1 Δ		1411	56	18	1 ★	1	15		Х		Х				
1 <u>A</u> T	TB2-1,2	1,2 I1U	00	÷	29 ★	6	3		Х		Х	×			
24	TB4-5,6				I5U	58	20	7 ★	3	15		Х		Х	
3A		150	50	÷	30 ★	8	3		Х		Х				
5A	TB3-1,2	1411	55	17	15 ★	5	15		Х		Х				
JA		,2 J <u>1</u> U	JŪ	55	-	31 ★	2	3		Х		Х	X		
7.\	TP556	1511	57	19	21 ★	7	15		Х		Х				
7A TB5-5,6	J5U	<u>.</u>	÷	32 ★	4	3		Х		Х					

50 N. Greenfield Pkwy, Garner, NC 27529

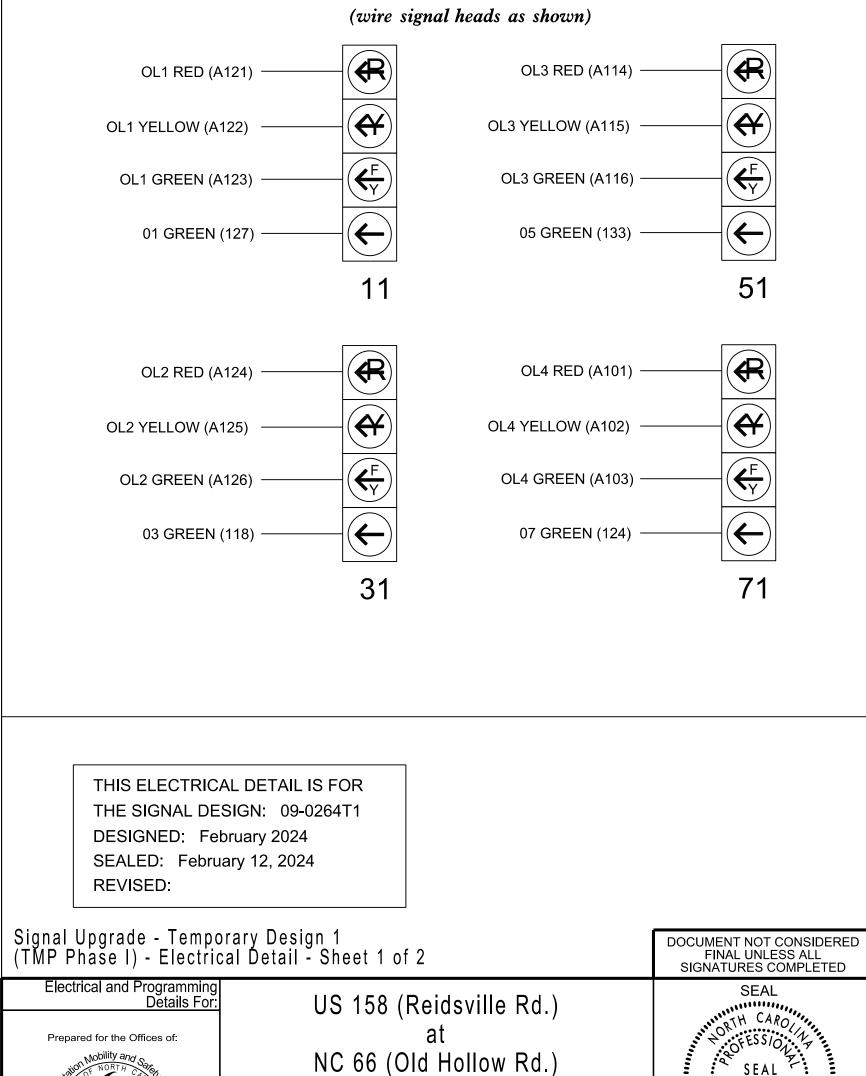
Division 9

PROJECT REFERENCE NO.	SHEET
R-2577A	Sig. 3

	SIGNAL HEAD HOOK-UP CHART																	
S2	S3	S	4	S5	S6	S7	S8	S9	S	10	S11	S12	AUX S1	AUX S2	AUX S3	AUX S4	AUX S5	AUX S6
2	13		3	4	14	5	6	15	7	7	8	16	9	10	17	11	12	18
2	2 PED	3	3	4	4 PÉD	5	6	6 PED	7	7	8	8 PÉD	OL1		SPARE	OL3	OL4	SPARE
1;22	NU	22	★ 31	41,42	NU	★ 51	61,62	NU	62	<b>★</b> 71	81,82	NU	★ 11	★ 31	NU	★ 51	★ 71	NU
28		*		101			134		*		107							
29	·		-	102		*	135		-		108					-		
30	·			103			136				109					-		
								-	-				A121	A124		A114	A101	
		117							123				A122	A125		A115	A102	
													A123	A126		A116	A103	
		118	118			133			124	124								

Denotes install load resistor. See load resistor installation detail this sheet

# FYA SIGNAL WIRING DETAIL



Forsyth County

PLAN DATE: February 2024 REVIEWED BY:

PREPARED BY: WP Erickson-Jones REVIEWED BY:

REVISIONS

056142

SIG. INVENTORY NO. 09-0264T1

2/12/2024

DATE

NGINEE

Porter Jones

Walkertown

DT Sears

INIT. DATE

# MAXTIME DETECTOR PROGRAMMING DETAIL FOR ALTERNATE PHASING LOOPS 1A, 3A, 5A & 7A

Front Panel Main Menu >Controller >Detector >Veh Det Plans

Web Interface Home >Controller >Detector Configuration >Vehicle Detectors

In the table view of web interface right click on "Detector" in the top left corner of the table. Copy the entire contents of Detector Plan 1. Paste Detector Plan 1 into Detector Plan 2. Modify Detector Plan 2 as shown below and save changes.

	Plan 2		
	Detector	Call Phase	Delay
1A	1	1	0
	29	0	3

	Detector	Call Phase	Delay
3A	7	3	0
	30	0	3

	Detector	Call Phase	Delay
5A	15	5	0
	31	0	3

	Detector	Call Phase	Delay
7A	21	7	3.0
	32	0	<u>-</u>

# MAXTIME OVERLAP PROGRAMMING DETAIL FOR DEFAULT PHASING

#### Front Panel

Main Menu >Controller >Overlap >Overlap Parameters/Overlap Timings

Web Interface Home >Controller >Overlap Configuration >Overlaps

#### Overlap Plan 1

Overlap	1	2	3	4
Туре	FYA 4 - Section			
Included Phases	2	4	6	8
Modifier Phases	1	3	5	7
Modifier Overlaps	<u>-</u>	÷	÷	<u>-</u>
Trail Green	0	0	0	0
Trail Yellow	0:0	0.0	0.0	0.0
Trail Red	0:0	0.0	0.0	0.0

# MAXTIME ALTERNATE PHASING ACTIVATION DETAIL

To run alternate phasing, select a Pattern that is programmed to run Overlap Plan 2 and Detector Plan 2. A Pattern can be selected through the scheduler or manually by changing the Operational Mode.

PHASING	OVERLAP PLAN	VEH DET PLAN
ACTIVE PLAN REQUIRED TO RUN DEFAULT PHASING	1	1
ACTIVE PLAN REQUIRED TO RUN ALTERNATE PHASING	2	2

1	ALTERNATE PHASING CHANGE SUMMARY	
THE FOLLOWING I	S A SUMMARY OF WHAT TAKES PLACE WHEN	
	AND VEHICLE DETECTOR PLAN 2 ACTIVATE	11
TO CALL THE "ALT	ERNATE PHASING":	S
OVERLAP PLAN 2:	Modifies overlap included phases	
	for heads 11, 31, 51, and 71 to	
	run protected turns only.	1. ON F
VEH DET PLAN 2:	Disables phase 6 call on loop 1A	2. ON F
	and reduces delay time for phase 1	
	call on loop 1A to 0 seconds.	3. REM
	Disables phase 8 call on loop 3A	
	and reduces delay time for phase 3	THE CH
	call on loop 3A to 0 seconds.	
	Disables phase 2 call on loop 5A	
	and reduces delay time for phase 5	
	call on loop 5A to 0 seconds.	
	Disables phase 4 call on loop 7A	
	and reduces delay time for phase 7	
	call on loop 7A to 3 seconds.	

# MAXTIME OVERLAP PROGRAMMING DETAIL FOR ALTERNATE PHASING

### Front Panel

Main Menu >Controller >Overlap >Overlap Parameters/Overlap Timings

### Web Interface

Home >Controller >Overlap Configuration >Overlaps

In the table view of the web interface, right click on "Overlap" in the top left corner of the table. Copy the entire contents of Overlap Plan 1. Paste Overlap Plan 1 into Overlap Plan 2. Modify Overlap Plan 2 as shown below and save changes.

#### Overlap Plan 2

Overlap	1	2	3	4		
Туре	FYA 4 - Section					
Included Phases	÷	÷	÷	<u>-</u>	NOTICE INCLUDED PHASE	
Modifier Phases	1	3	5	7		
Modifier Overlaps	<b>-</b>	<u>-</u>	÷	-		
Trail Green	0	0	0	0	RKK	
Trail Yellow	0.0	0.0	0.0	0.0		
Trail Red	0.0	0.0	0.0	0.0	P: (919) 878-9560 8601 Six Forks Road Suite 700 NC License No. F-0112	) R

P: (919) 878-9560 8601 Six Forks Road Suite 700 | Raleigh, North Carolina 27615-2965 NC License No. F-0112 Engineers | Construction Managers | Planners | Scientists www.rkk.com

Responsive People | Creative Solutions

PROJECT REFERENCE NO.	SHEET NO.
R-2577A	Sig. 30.2

# FLASHER CIRCUIT MODIFICATION DETAIL

IN ORDER TO INSURE THAT SIGNALS FLASH CONCURRENTLY ON THE SAME APPROACH, MAKE THE FOLLOWING FLASHER CIRCUIT CHANGES:

REAR OF PDA - REMOVE WIRE FROM TERM. T2-4 AND TERMINATE ON T2-2. REAR OF PDA - REMOVE WIRE FROM TERM. T2-5 AND TERMINATE ON T2-3. MOVE FLASHER UNIT 2.

HANGES LISTED ABOVE TIES ALL PHASES AND OVERLAPS TO FLASHER UNIT 1.

# MAXTIME ALTERNATE PHASING PATTERN PROGRAMMING DETAIL

Front Panel Main Menu >Controller >Coordination >Patterns

Web Interface

Home >Controller >Coordination >Patterns

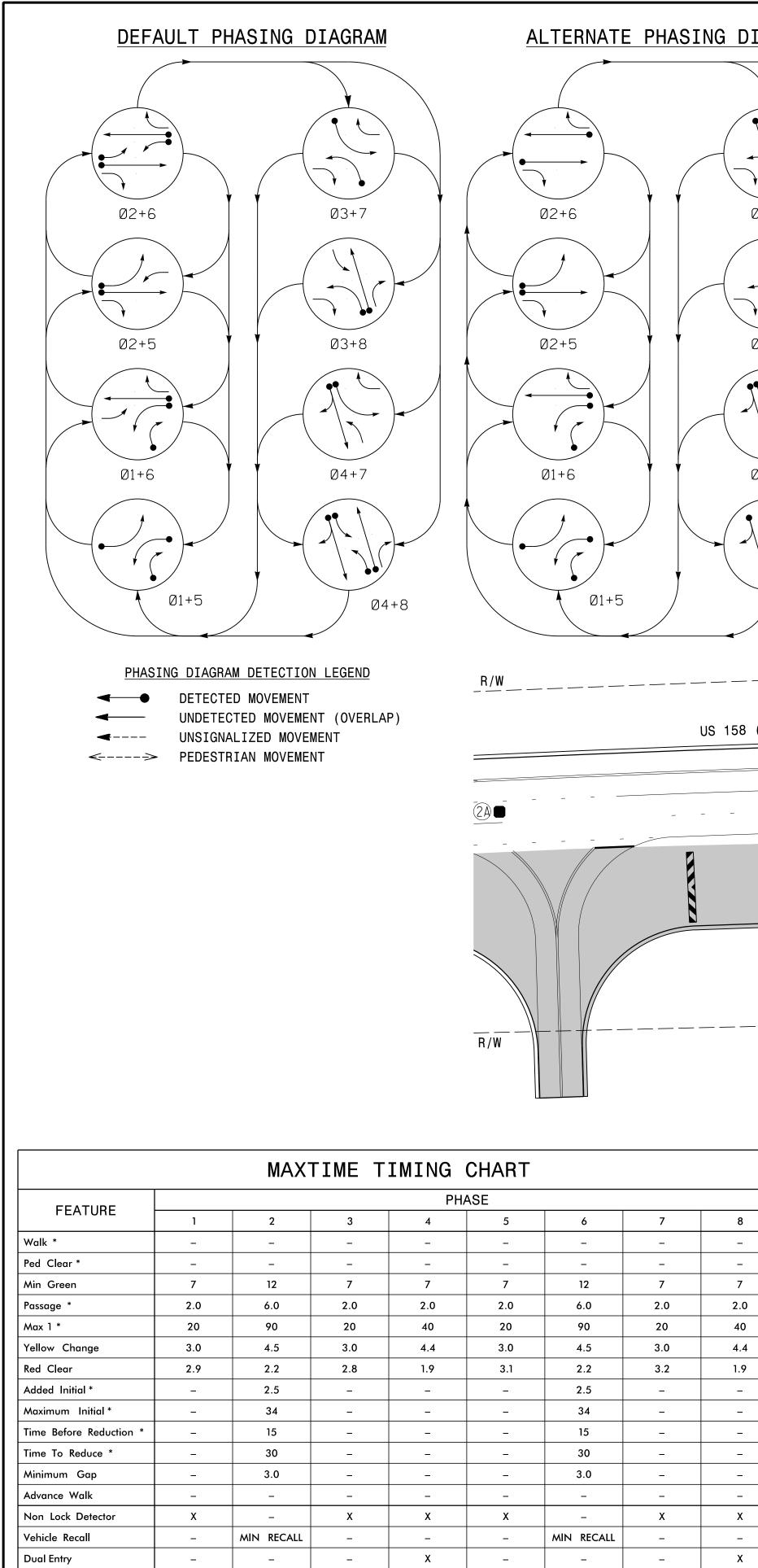
Pattern Parameters

Pattern Veh Det Plan Overlap Plan \* 2 2

\* The Pattern number(s) are to be determined by the Division and/or City Traffic Engineer.

> THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 09-0264T1 DESIGNED: February 2024 SEALED: February 12, 2024 REVISED:

<u> </u>	-						
Signal Upgrade - Tempo (TMP Phase I) - Electric	FINAL UNLE	ESS ALL					
Electrical and Programming Details For: Prepared for the Offices of:		US 158 (Rei a NC 66 (Old	t	,		DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED SEAL OFESSION SEAL OS6142 DOCUSIGNED DOCUSICINED DOCUSIGNED DOCUSIGNED DOCUSIGNED DOCUSIGNED DOCUSICINE	
A CONTRACT THE PARTY OF THE PAR	Division 9 PLAN DATE:	Forsyth February 2024		,	Valkertown ears	05614	
	PREPARED BY:	WP Erickson-Jones	REVIEWED BY:			DocuSigned by:	mini
Gnals Management	<u> </u>	REVISIONS		INIT.	DATE		2/12/2024
750 N. Greenfield Pkwy, Garner, NC 27529						D06E13E3A0E0498	DATE
					[	SIG. INVENTORY NO.	09-0264T1



2.5

\* These values may be field adjusted. Do not adjust Min Green and Extension times for phases 2 and 6 lower than what is shown. Min Green fo other phases should not be lower than 4 seconds.

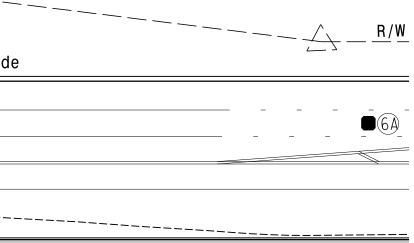
DIAGRAM	<u>SIGNAL FACE I.D.</u>	DEFAULT PHASING TABLE OF OPERATION	ALTERNATE PHASING TABLE OF OPERATION	
Ø3+7 Ø3+7 Ø3+8 Ø3+8 Ø4+7	All Heads L.E.D. 12'' $G$ $12''$ $G$ $12''$ $G$ $12''$ $G$ $12''$ $G$ $12''$ $G$ $22$ $82$ $12''$ $22$ $82$ $12''$ $1$	11 $\leftarrow$	PHASESIGNAL ASH $\emptyset$ 	1 2 3 4 5 6 7
58 (Reidsville Rd.)	Ha + 326 Graade + 43 + 43 + 31 81 82 + 62 + 61 + 61 + 62 + 61 + 61 + 61 + 6	45 MPH +0% Gr	ade	8
45 MPH +0% Grade				
MAXTIME DETECTOR       I         0       DETECTOR         0       I         2.0       1A *         4.4       1B *         1A *         6X40       0         0       *	PROGRAMMING       CALL     DELAY     EXTEND       PHASE     DELAY     EXTEND       TIME     TIME       I     15#       6@     3       X     X       X     X       X     X       X     X       X     X	Grade III	N/	
1.9       2A *       6X6       300       *       *         -       3A *       6X40       0       *       *         -       4A *       6X40       0       *       *         -       4A *       6X40       0       *       *         -       5A *       6X40       0       *       *         -       6A *       6X40       0       *       *         x       7A *       6X40       0       *       *         x       8A *       6X40       0       *       *         for all       # Reduce Delay to 3 Seconds During Alterna       @       Disable Phase Call For Zone(s) During Alterna         * Video Detection Zone       *       *       *       *	$ \begin{array}{c ccccccccccccccccccccccccccccccccccc$		nal Upgrade - Temporary Design 2 Prepared for the Offices of: US 158 (Reids at NC 66 (Old H Division 9 Forsyth Coun PLAN DATE: February 2024 REV SCALE 0 40 1" = 40'	s v : I o :

#### PROJECT REFERENCE NO. SHEET NO. R-2577A Sig 31

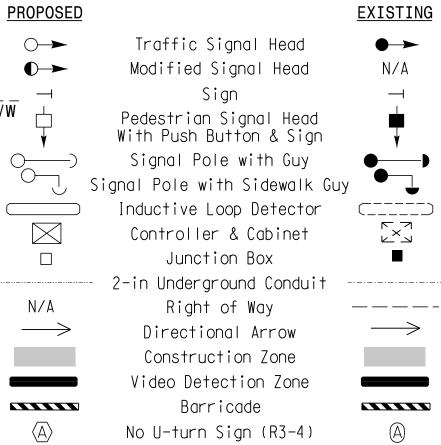
#### 8 Phase Fully Actuated (Isolated)

## NOTES

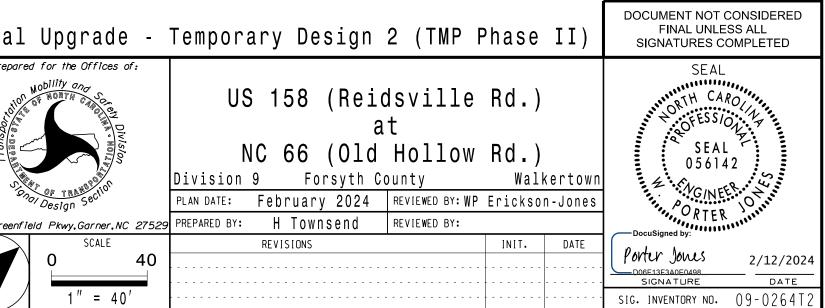
- 1. Refer to "Roadway Standard Drawings NCDOT" dated January 2024 and "Standard Specifications for Roads and Structures" dated January 2024. 2. Do not program signal for late night flashing operation unless otherwise noted by the Engineer. 3. Phase 1 and/or phase 5 may be
- lagged. 4. Phase 3 and/or phase 7 may be lagged.
- 5. Reposition existing signal heads numbered 11, 21, 22, 31, 51, 61, 62, 81, and 82.
- 6. Set all detector units to presence mode.
- 7. This intersection uses video detection. Install detectors according to the manufacturer's instructions to achieve the desired detection.
- 8. The Division Traffic Engineer will determine the hours of use for each phasing plan.

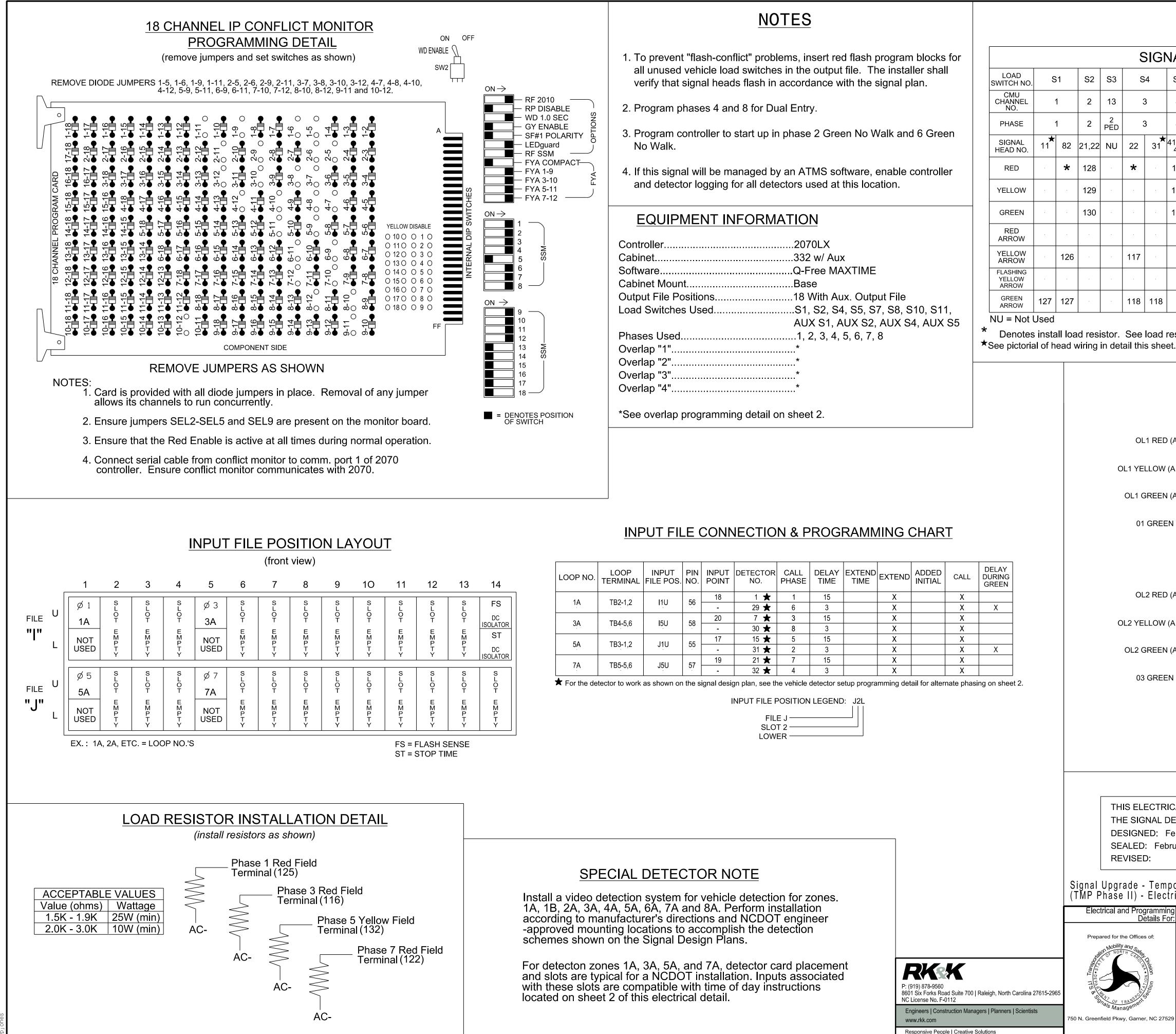


## <u>POSED</u>



LEGEND





LOOP NO.	LOOP TERMINAL	INPUT FILE POS.	PIN NO.	INPUT POINT	DETECTOR NO.	CALL PHASE	DELAY TIME	EXTEND TIME	EXTEND	ADDED INITIAL	CALL	DELA DURIN GREE
1 Δ		1411	56	18	1 ★	1	15		Х		Х	
1A	TB2-1,2	I1U	00	-	29 ★	6	3		Х		Х	X
2 A		1511	50	20	7 ★	3	15		Х		Х	
3A	TB4-5,6	15U	58	÷	30 ★	8	3		Х		Х	
E۸		141.1	EE	17	15 ★	5	15		Х		Х	
5A	TB3-1,2	J1U	55	÷	31 ★	2	3		Х		Х	X
7A TB5-5,6 J5U	1511	57	19	21 ★	7	15		Х		Х		
	100-0,0	190	<u> </u>	÷	32 ★	4	3		Х		Х	

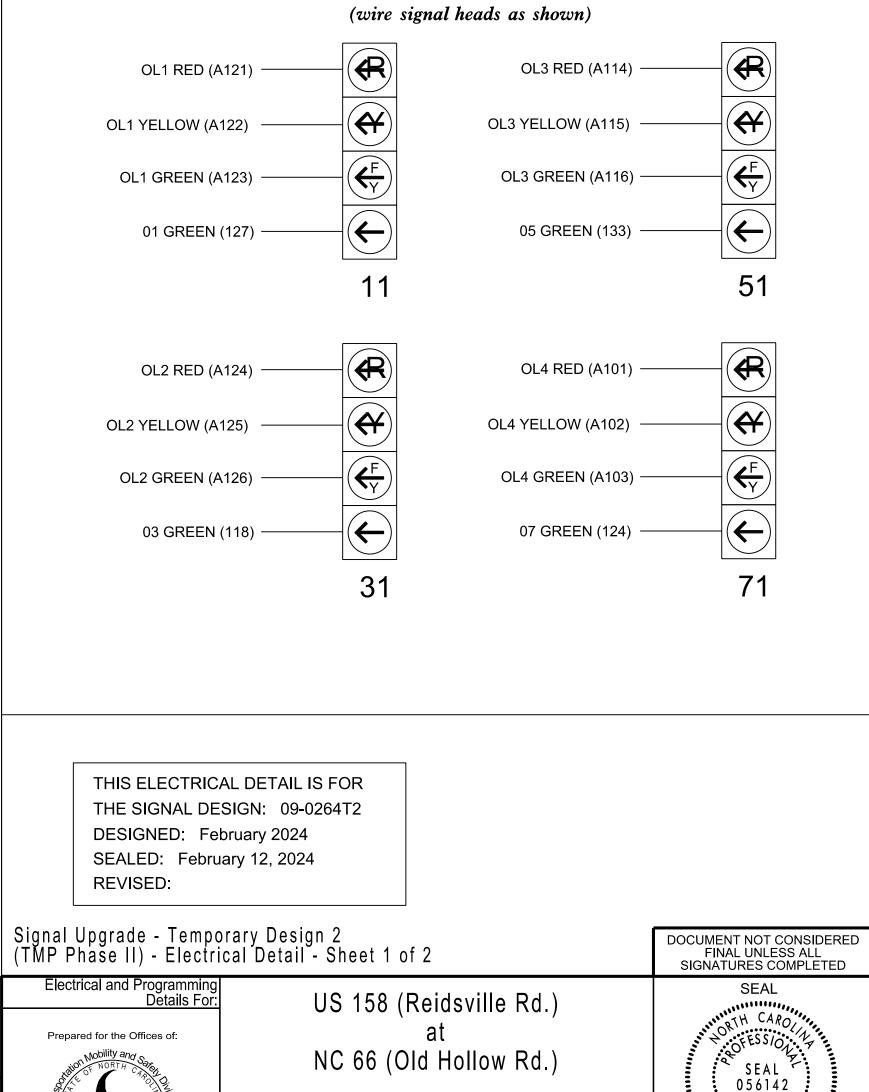
PROJECT REFERENCE NO.	SHEET
R-2577A	Sig. 3

			S	SIGI	NAL	HE	AD	HO	OK	-UP	CH	AR'	Т						
S	2	S3	S	4	S5	S6	S7	S8	S9	S	10	S11	S12	AUX S1	AUX S2	AUX S3	AUX S4	AUX S5	AŲX S6
2		13	ć	3	4	14	5	6	15	1	7	8	16	9	10	17	11	12	18
2		2 PÉD	3		4	4 PÉD	5	6	6 PED	7	7	8	8 PÉD	OL1	OL2	SPARE	OL3	OL4	SPARE
21;	22	NU	22	★ 31	41 <u>,</u> 42, 43	NU	★ 51	61,62	NU	62	★ 71	81,82	NU	<b>★</b> 11	★ 31	NU	★ 51	★ 71	NU
12	8		*		101			134		*		107	·						
12	9				102		*	135			-	108							
13	0				103			136				109						·	
														A121	A124		A114	A101	
			117							123				A122	A125		A115	A102	
														A123	A126		A116	A103	
			118	118			133			124	124								

Denotes install load resistor. See load resistor installation detail this sheet.

Division 9





Forsyth County

PLAN DATE: February 2024 REVIEWED BY:

PREPARED BY: WP Erickson-Jones REVIEWED BY:

REVISIONS

Walkertown

Porter Jones

SIG. INVENTORY NO. 09-0264T2

2/12/2024

DATE

DT Sears

INIT. DATE

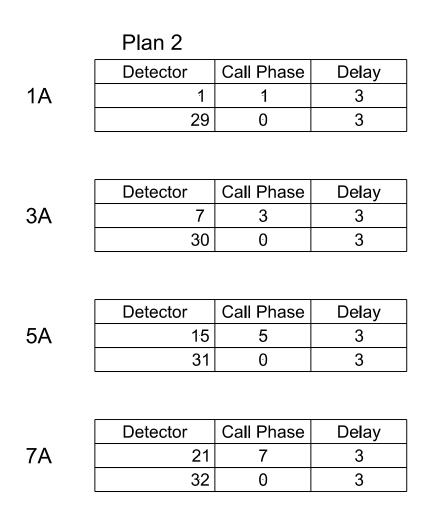
## MAXTIME DETECTOR PROGRAMMING DETAIL FOR ALTERNATE PHASING LOOPS 1A, 3A, 5A & 7A

Front Panel Main Menu >Controller >Detector >Veh Det Plans

Web Interface

Home >Controller >Detector Configuration >Vehicle Detectors

In the table view of web interface right click on "Detector" in the top left corner of the table. Copy the entire contents of Detector Plan 1. Paste Detector Plan 1 into Detector Plan 2. Modify Detector Plan 2 as shown below and save changes.



## MAXTIME OVERLAP PROGRAMMING DETAIL FOR DEFAULT PHASING

Front Panel

Main Menu >Controller >Overlap >Overlap Parameters/Overlap Timings

#### Web Interface

Home >Controller >Overlap Configuration >Overlaps

#### Overlap Plan 1

Overlap	1	2	3	4
Туре	FYA 4 - Section			
Included Phases	2	4	6	8
Modifier Phases	1	3	5	7
Modifier Overlaps	<u>-</u>	÷	-	-
Trail Green	0	0	0	0
Trail Yellow	0.0	0.0	0.0	0:0
Trail Red	0.0	0.0	0.0	0:0

MAXT	IME ALTERNATE	E PHASING	G ACTIVA	TION DETA			L.	PROJECT REFERENCE NO. SHEET NO. R-2577A Sig. 31.2
To run alterna A Pattern can	te phasing, select a Pattern that be selected through the schedul	is programmed to r er or manually by c	un Overlap Plan 2 hanging the Opera	and Detector Plan 2. tional Mode.				
PHASING			OVERLAP P	LAN VEH I	DET PLAN			
ACTIVE PLAN REQ	UIRED TO RUN DEFAULT PHA	ASING	1		1			
ACTIVE PLAN REQ	UIRED TO <u>RUN ALTERNATE F</u>	PHASING	2		2			
	ALTERNAT	E PHASING CHANG	E SUMMARY					
	THE FOLLOWING IS A SUMN OVERLAP PLAN 2 AND VEHI TO CALL THE "ALTERNATE I	CLE DETECTOR PLA			<u>FLA</u>	SHER CIRCUIT	MODIFICATIO	N DETAIL
		overlap included phas 11, 31, 51, and 71 to ted turns only.				R TO INSURE THAT SIGN PROACH, MAKE THE FOI		
	and reduc	bhase 6 call on loop 1 es delay time for phas p 1A to 3 seconds.				PDA - REMOVE WIRE FF		
	and reduc	bhase 8 call on loop 3 es delay time for phas p 3A to 3 seconds.			3. REMOVE FLA		CONTERNIE TZ-SAND	ERMINATE ON TZ-3.
	and reduc	bhase 2 call on loop 5 es delay time for phas p 5A to 3 seconds.			THE CHANGES I	LISTED ABOVE TIES ALL	PHASES AND OVERLA	APS TO FLASHER UNIT 1.
	and reduc	bhase 4 call on loop 7 es delay time for phas p 7A to 3 seconds.			MAXTIM	I <u>E ALTERNATE F</u> PROGRAMMIN		<u>ERN</u>
					Front Pan Main Men	el u >Controller >Coordina	tion >Patterns	
					Web Inter			
	<u>P PROGRAMMING [</u> RNATE PHASING	DETAIL			Pattern P	ontroller >Coordination > arameters		
					Pattern	Veh Det PlanOverlap*22	Plan	
Front Panel Main Menu >Controller >	Overlap >Overlap Parameter	s/Overlap Timing	IS		<b>*</b> The Pat	tern number(s) are to be det	ermined by	
Web Interface Home >Controller >Over	lap Configuration >Overlaps				the Divis	sion and/or City Traffic Engir	neer.	
"Overlap" in the top left or entire contents of Overla	veb interface, right click on corner of the table. Copy the up Plan 1. Paste Overlap Plan dify Overlap Plan 2 as shown s.	1			THE SIGNAL DESIGNED:	RICAL DETAIL IS FOR DESIGN: 09-0264T2 February 2024 ebruary 12, 2024		
Overlap Plan 2	- <u>_</u>				Signal Upgrade - Te	mporary Design 2 ctrical Detail - Sheet 2 of		DOCUMENT NOT CONSIDERED
Overlap 1	2 3	4			(TMP Phase II) - Ele Electrical and Programmer Details	ning		FINAL UNLESS ALL SIGNATURES COMPLETED SEAL
TypeFYA 4 - SectionIncluded Phases-Modifier Phases1	n FYA 4 - Section FYA 4 - Section FYA		CE INCLUDED PHASE		Prepared for the Offices of:		teidsville Rd.) at d Hollow Rd.)	NORTH CAROL
Modifier Overlaps-Trail Green0Trail Yellow0.0	Image: Constraint of the second sec	- 0 0:0	<b>RKKK</b> P: (919) 878-9560 8601 Six Forks Road Suite 7	700   Raleigh, North Carolina 27615-2965	Transien noito	· ·	yth County Walker REVIEWED BY: DT Sears	town Docusigned by:
Trail Red 0.0	0.0 0.0	0:0	NC License No. F-0112	lanagers   Planners   Scientists	750 N. Greenfield Pkwy, Garner, NC 2	REVISIONS	INIT. DA	NTE Porter Jours DOGETISESADE0498 2/12/2024 DATE SIG. INVENTORY NO. 09-0264T2

ſ		ALTERNATE PHASING CHANGE SUMMARY	
	OVERLAP PLAN 2	IS A SUMMARY OF WHAT TAKES PLACE WHEN AND VEHICLE DETECTOR PLAN 2 ACTIVATE FERNATE PHASING":	
	OVERLAP PLAN 2:	Modifies overlap included phases for heads 11, 31, 51, and 71 to run protected turns only.	
	VEH DET PLAN 2:	Disables phase 6 call on loop 1A and reduces delay time for phase 1 call on loop 1A to 3 seconds.	
		Disables phase 8 call on loop 3A and reduces delay time for phase 3 call on loop 3A to 3 seconds.	
		Disables phase 2 call on loop 5A and reduces delay time for phase 5 call on loop 5A to 3 seconds.	
		Disables phase 4 call on loop 7A and reduces delay time for phase 7 call on loop 7A to 3 seconds.	

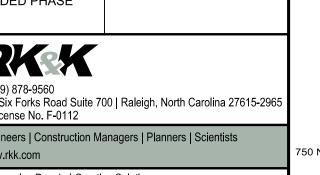
# MAX

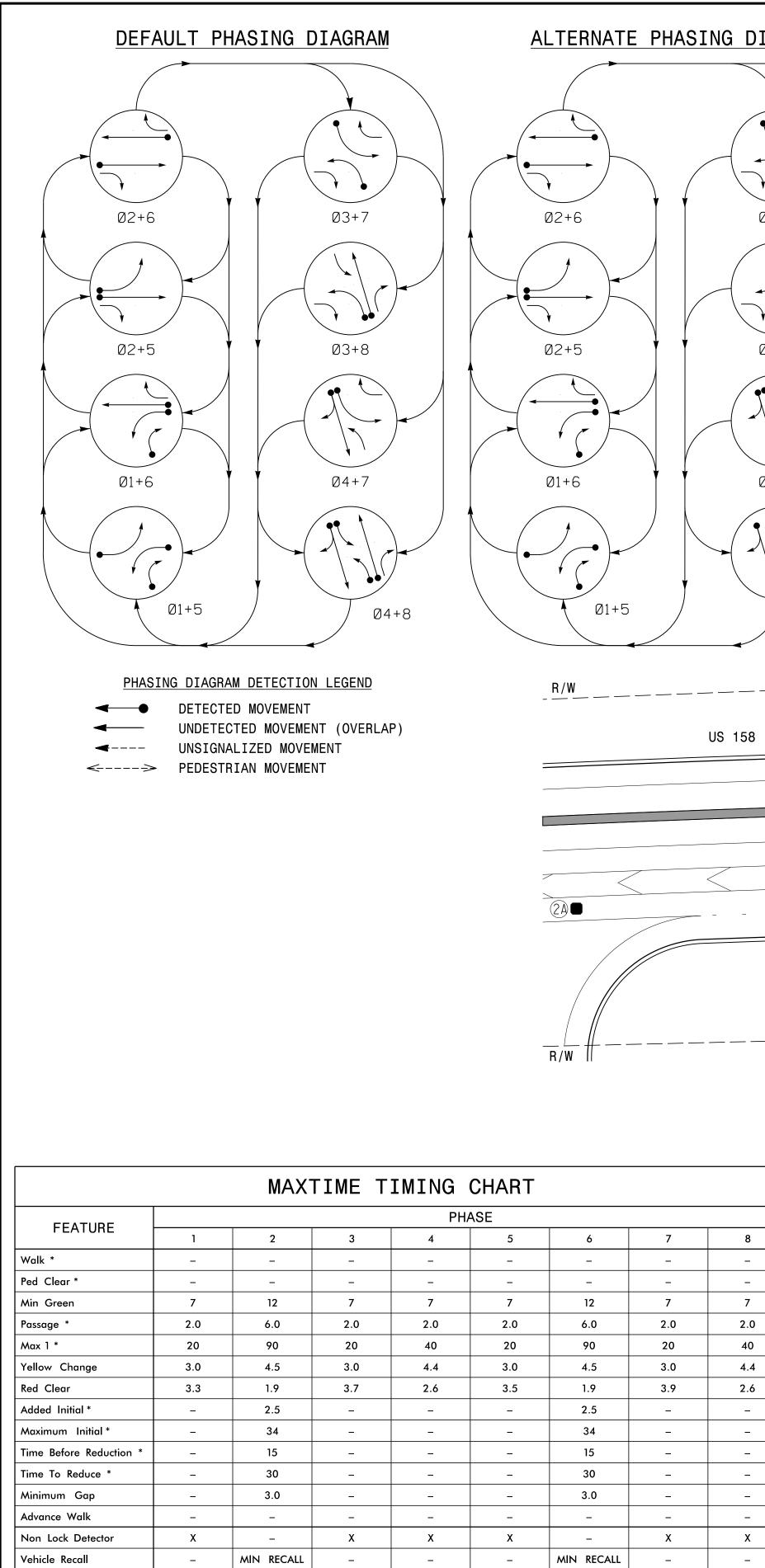
### Fro

#### We

#### Ov

Overlap	1	2	3	4	
Туре	FYA 4 - Section				
Included Phases	÷	÷	÷	-	
Modifier Phases	1	3	5	7	
Modifier Overlaps	÷	-	÷	<u>-</u>	
Trail Green	0	0	0	0	
Trail Yellow	0.0	0.0	0.0	0:0	
Trail Red	0.0	0.0	0.0	0.0	]





 $\sim$  +

N N

Dual Entry

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\* These values may be field adjusted. Do not adjust Min Green and Extension times for phases 2 and 6 lower than what is shown. Min Green fo other phases should not be lower than 4 seconds.

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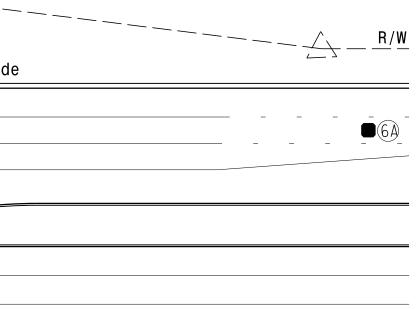
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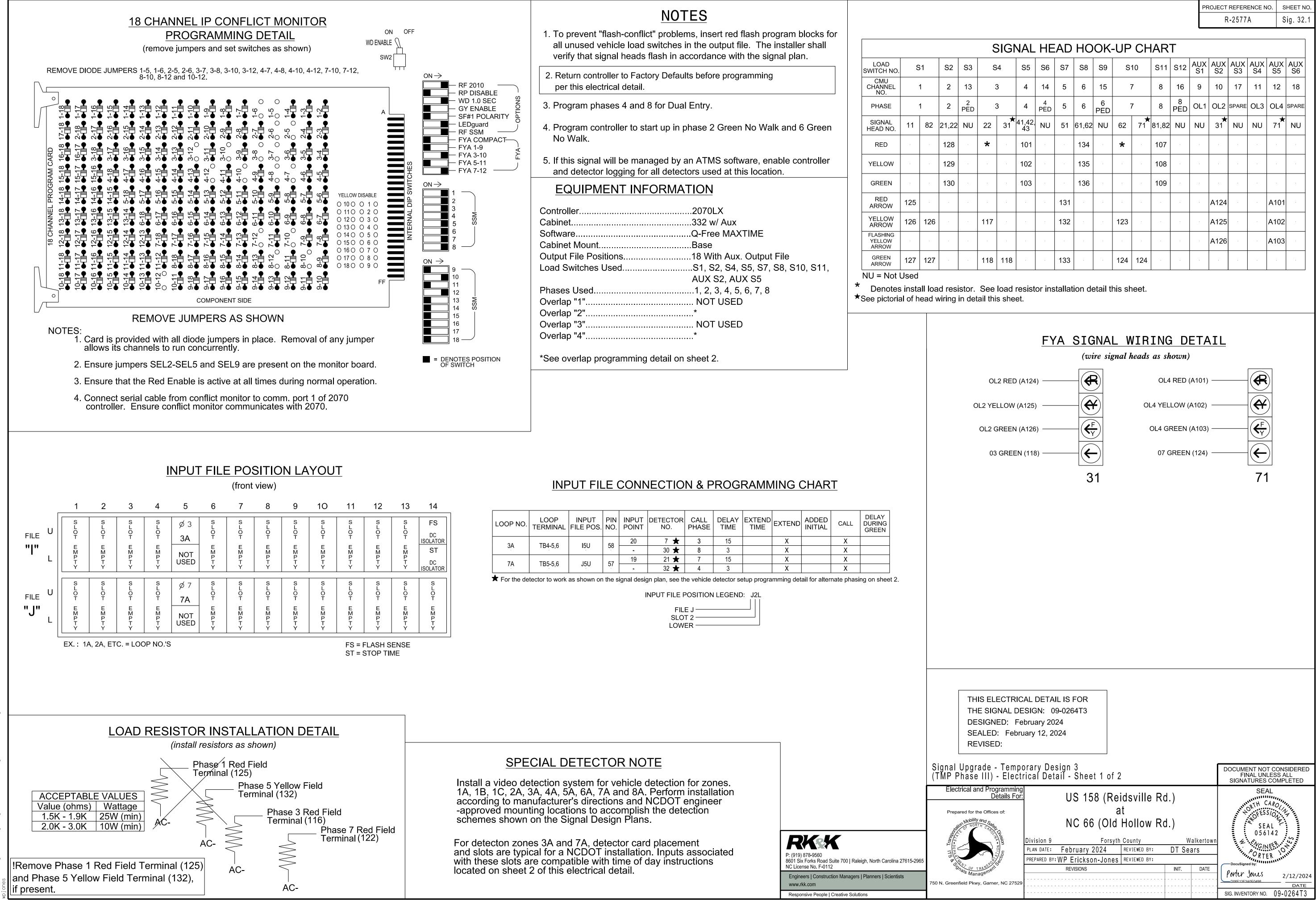
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				PROJECT REFERENCE NO. SHEET NO.
NG DIAGRAM	SIGNAL FACE I.D.	DEFAULT PHASING TABLE OF OPERATION	ALTERNATE PHASING TABLE OF OPERATION	R-2577A Sig. 32.0
	SIGNAL FACE I.D. All Heads L.E.D. $ \begin{array}{c} \hline \hline$	PHABLE OF OPERATION         SIGNAL       0 <th>PHASE         SIGNAL       <math>\emptyset</math> <math>\emptyset</math></th> <th>night flashing operation unless otherwise noted by the Engineer. Phase 1 and/or phase 5 may be lagged. Phase 3 and/or phase 7 may be lagged.</th>	PHASE         SIGNAL $\emptyset$	night flashing operation unless otherwise noted by the Engineer. Phase 1 and/or phase 5 may be lagged. Phase 3 and/or phase 7 may be lagged.
Ø4+8 JS 158 (Reidsville Rd.)	43 (A) (B) (B) (B) (C) (C) (C) (C) (C) (C) (C) (C) (C) (C	US 158 (Re	PROPOS ■ ■ ■ ■ ■ ■ ■ ■ ■ ■ ■ ■ ■	<ul> <li>Traffic Signal Head</li> <li>Modified Signal Head</li> <li>N/A</li> <li>Sign</li> <li>Pedestrian Signal Head</li> <li>With Push Button &amp; Sign</li> <li>Signal Pole with Guy</li> </ul>
ſ		Grade		, Signal Pole with Sidewalk Guy
	ME DETECTOR INSTALLATION CHART	$\setminus$		Junction Box
8 - SIZE	ECTORPROGRAMMINGDISTANCEImage: Standard S		N/A A A B	Right of Way
2.6 1C * 6X40 - 2A * 6X6	0       *       *       1       15       -       X       -       *         300       *       *       2       -       -       X       X       -       *			DOCUMENT NOT CONSIDERED
3A * 6X40	$\begin{array}{c ccccccccccccccccccccccccccccccccccc$		L Upgrade - Temporary Design 3 (T	MP Phase III) FINAL UNLESS ALL SIGNATURES COMPLETED SEAL
$ \begin{array}{c ccccccccccccccccccccccccccccccccccc$	0   *  *  8   -   -  X - X - *	<b>RKKK</b> 2: (919) 878-9560		<pre>1le Rd.) low Rd.) Walkertown D BY: WP Erickson-Jones</pre>
Green for all # Reduce Delay to 3	B Seconds During Alternate Phasing Operation.	101 Six Forks Road Suite 700   Raleigh, North Carolina 27615-2965       750 N. Greent         102 License No. F-0112       Image: Construction Managers   Planners   Scientists         103 minute result       Image: Construction Managers   Planners   Scientists         104 minute result       Image: Construction Managers   Planners   Scientists         105 minute result       Image: Construction Managers   Planners   Scientists         106 minute result       Image: Construction Managers   Planners   Scientists         107 minute result       Image: Construction Managers   Planners   Scientists         108 minute result       Image: Construction Managers   Planners   Scientists         109 minute result       Image: Construction Managers   Planners   Scientists         100 minute result       Image: Construction Managers   Planners   Scientists         107 minute result       Image: Construction Managers   Planners   Scientists         108 minute result       Image: Construction Managers   Planners   Scientists         109 minute result       Imagers   Planners   Planners   Planners   Plann	field Pkwy.Garner.NC 27529 PREPARED BY: H TOWNSEND REVIEWE SCALE REVISIONS 1" = 40'	INIT.     DATE       INIT.     DATE       DOBE13E3A0E0498     2/12/2024       SIGNATURE     DATE       SIG. INVENTORY NO.     09-0264T3

ALTERNATE PHASING TABLE OF OPERATION										
				Р	HAS	E				
SIGNAL FACE	Ø 1 + 5	Ø 1 + 6	Ø 2+5	Ø2+6	Ø 3 + 7	Ø 3 + 8	Ø 4 + 7	Ø 4 + 8	FLAST	
11	-	-	<del>≺R</del>	<del>≺R</del>	<del>≺R</del>	<del>≺R</del>	<del>≺R</del>	<del>≺R</del>	<del>≺R</del>	
21	R	R	G	G	R	R	R	R	Y	
22	R	R	G	G	R	R	R	R	Y	
31	≺R	<del>≺R</del>	≺R	<del>≺R</del>	-	-	<del>≺R</del>	<del>≺R</del>	<del>≺R</del>	
41,42,43	R	R	R	R	R	R	G	G	R	
51		<del>≺R</del>	-	<del>≺R</del>	<del>≺R</del>	<del>≺R</del>	<del>≺R</del>	<del>≺R</del>	<del>≺R</del>	
61	R	G	R	G	R	R	R	R	Y	
62	R	G	R	G	R	R	R	R	Y	
71	<del>≺R</del>	<del>≺R</del>	<del>≺R</del>	<del>⊀R</del>	-	╉	-	<del>≺R</del>	<del>≺R</del>	
81	R	R	R	R	R	G	R	G	R	
82	R	R	R	R	R	G	R	G	R	

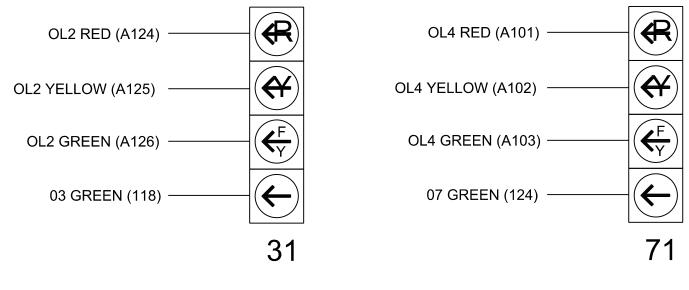


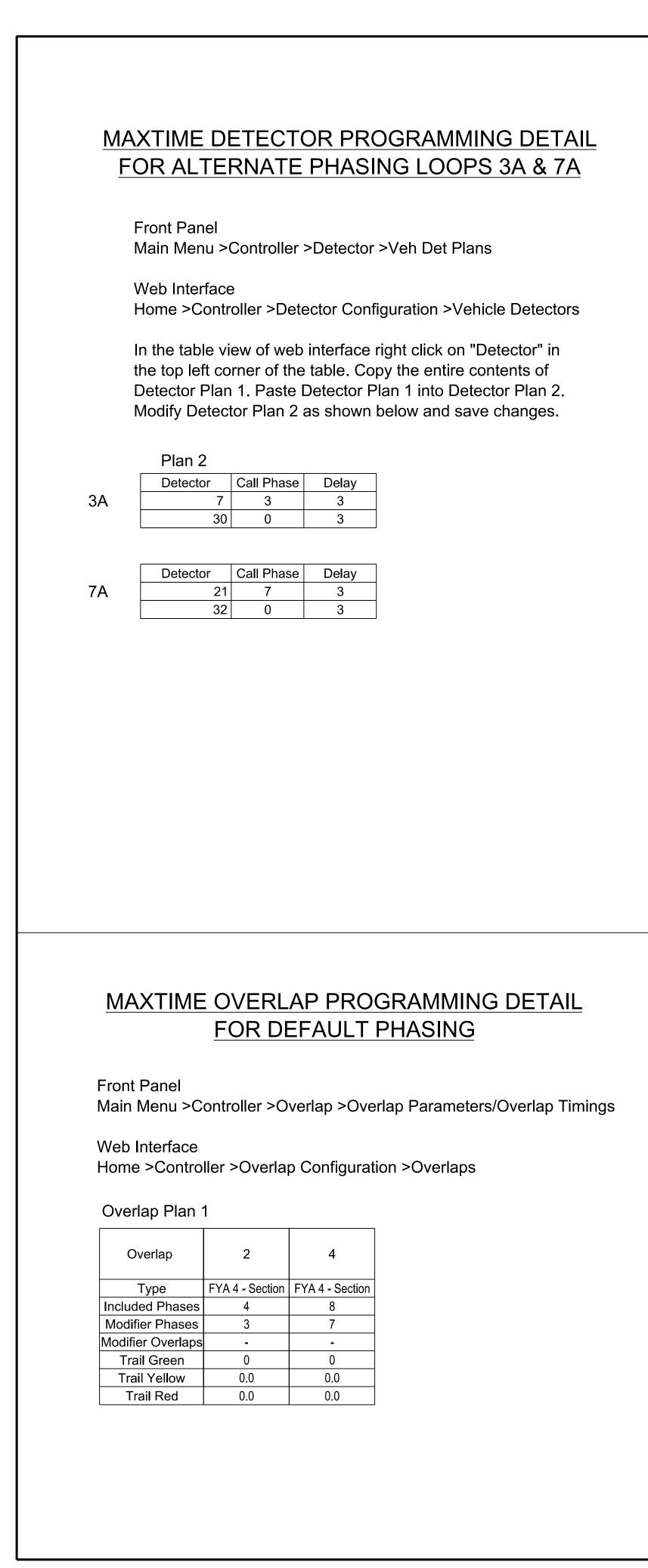


LOOP NO.	LOOP TERMINAL	INPUT FILE POS.	PIN NO.	INPUT POINT	DETECTOR NO.	CALL PHASE	DELAY TIME	EXTEND TIME	EXTEND	ADDED INITIAL	CALL	DELAY DURING GREEN
3A		I5U	58	20	7 ★	3	15		Х		Х	
JA JA	TB4-5,6		00	-	30 ★	8	3		Х		Х	
7A	TB5-5,6	J5U	57	19	21 ★	7	15		Х		Х	
				-	32 ★	4	3		Х		Х	

PROJECT REFERENCE NO.	SHEET
R-2577A	Sig.3

		S	SIGI	NAL	HE	AD	HO	OK	-UP	CH	IAR	Т						
S2	S3	S	64	S5	S6	S7	S8	S9	S	10	S11	S12	AUX S1	AUX S2	AUX S3	AUX S4	AUX S5	AUX S6
2	13		3	4	14	5	6	15	7		8	16	9	10	1.7	11	12	18
2	2 PED	ć	3	4	4 PED	5	6	6 PED	7		8	8 PÉD	OL1		SPARE	OL3		SPARE
l;22	NU	22	★ 31	41 <u>,</u> 42, 43	NU	51	61,62	NU	62	★ 71	81,82	NU	NU	★ 31	NU	NU	<b>★</b> 71	NU
28		*		101			134		*		107							
29				102			135				108					-		
30				103			136				109							
					·	131								A124			A101	
		117				132			123					A125			A102	
														A126			A103	
		118	118			133			124	124								





# MAXTI

To run alternate A Pattern can be

PHASING	OVERLAP PLAN	VEH DET
ACTIVE PLAN REQUIRED TO RUN DEFAULT PHASING	1	1
ACTIVE PLAN REQUIRED TO RUN ALTERNATE PHASING	2	2

ME ALTERNATE PHASING	ACTIVATION	DETAIL		PROJECT REFERENCE NO. SHEET NO. R-2577A Sig. 32.2
e phasing, select a Pattern that is programmed to run of selected through the scheduler or manually by change				
	OVERLAP PLAN	VEH DET PLAN		
IRED TO RUN DEFAULT PHASING	1			
JIRED TO <u>RUN ALTERNATE PHASING</u>	2	2		
ALTERNATE PHASING CHANGE SU THE FOLLOWING IS A SUMMARY OF WHAT TAKES P OVERLAP PLAN 2 AND VEHICLE DETECTOR PLAN 2	PLACE WHEN			
TO CALL THE "ALTERNATE PHASING": OVERLAP PLAN 2: Modifies overlap included phases for heads 31 and 71 to		IN ORDER	SHER CIRCUIT MODIFICATION TO INSURE THAT SIGNALS FLASH CONCUR ROACH, MAKE THE FOLLOWING FLASHER (	RENTLY ON THE
run protected turns only. VEH DET PLAN 2: Disables phase 8 call on loop 3A and reduces delay time for phase 3 call on loop 3A to 3 seconds. Disables phase 4 call on loop 7A and reduces delay time for phase 7 call on loop 7A to 3 seconds.		2. ON REAR OF F 3. REMOVE FLAS	PDA - REMOVE WIRE FROM TERM. T2-4 AND PDA - REMOVE WIRE FROM TERM. T2-5 AND SHER UNIT 2.	) TERMINATE ON T2-3.
			<u>MAXTIME ALTERNATE PHAS</u> <u>PROGRAMMING DE</u>	
LAP PROGRAMMING DETAIL TERNATE PHASING	ings		Front Panel Main Menu >Controller >Coordination >P Web Interface Home >Controller >Coordination >Pattern Pattern Parameters Pattern Veh Det Plan Overlap Plan * 2 2	
Overlap Configuration >Overlaps			* The Pattern number(s) are to be determined the Division and/or City Traffic Engineer.	by
he web interface, right click on left corner of the table. Copy the /erlap Plan 1. Paste Overlap Plan 1 Modify Overlap Plan 2 as shown nges.			THE SIGNAL DESIGNED:	RICAL DETAIL IS FOR DESIGN: 09-0264T3 February 2024 ebruary 12, 2024
2 4 Section FYA 4 - Section - NOTICE INCLUDED PHASE 7	RKK	Signal Upgrade - Ter (TMP Phase III) - Ele Electrical and Programm Details Prepared for the Offices of:	at NC 66 (Old Hollow Rd.)	DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED SEAL OFFESSION SEAL 056142
O         O:0         P: (91         8601 st         NC Lic           O         O:0         Engi         Engi         Engi         Engi         Engi	19) 878-9560 Six Forks Road Suite 700   Raleigh, North Caroli License No. F-0112 gineers   Construction Managers   Planners   Scie w.rkk.com	Onals An another	PLAN DATE:       February 2024       REVIEWED BY:       DT Sea         PREPARED BY:       WP Erickson-Jones       REVIEWED BY:         REVISIONS       INIT.	

MAXTIME OVERLAP PROGRAMMING [	DETAIL
FOR ALTERNATE PHASING	

Front Panel Main Menu >Controll

Web Interface Home >Controller >C

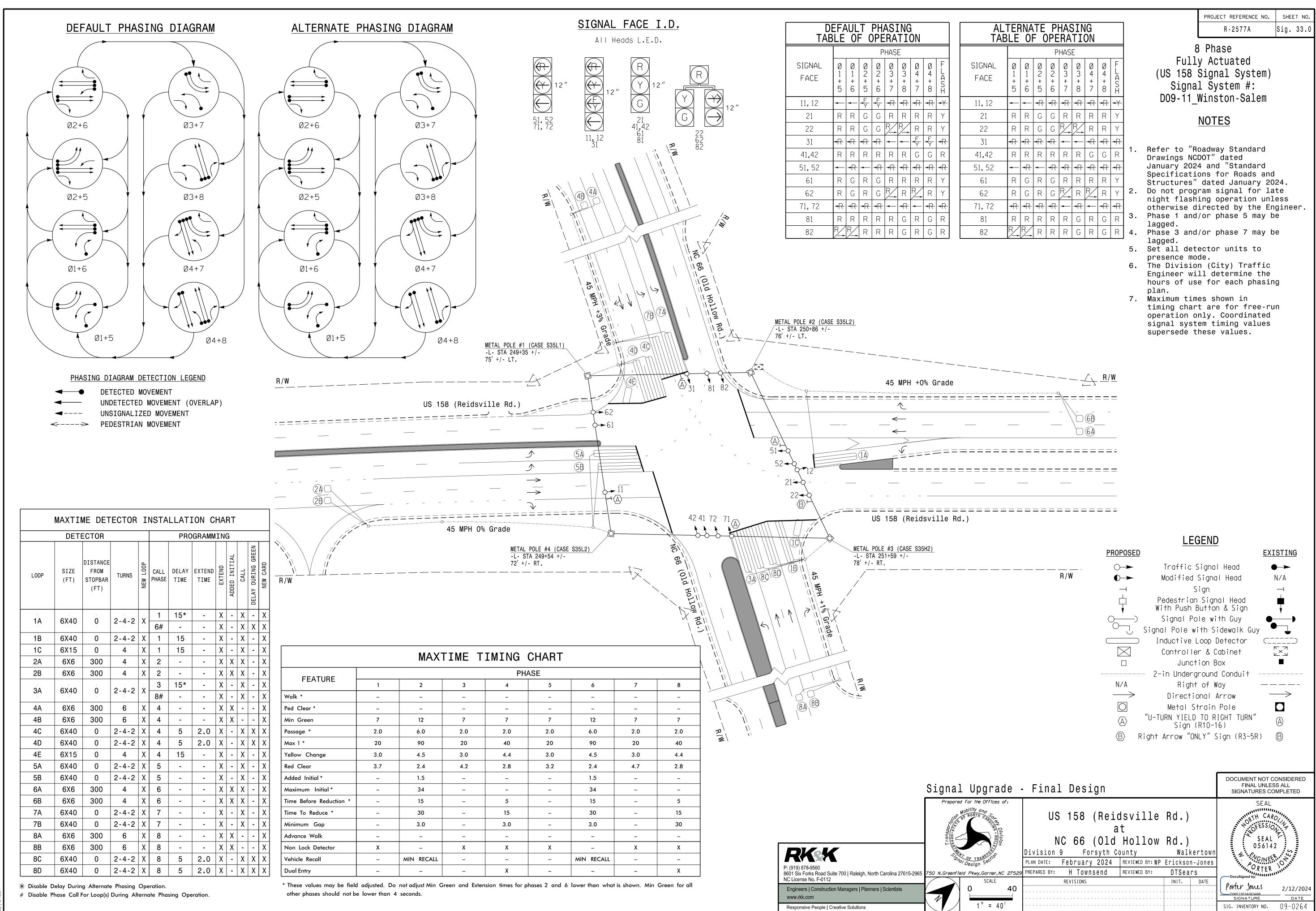
In the table view of th "Overlap" in the top entire contents of Ov into Overlap Plan 2. below and save char

#### Overlap Plan 2

Overlap	2	4	
Туре	FYA 4 - Section	FYA 4 - Section	
Included Phases	÷	÷	
Modifier Phases	3	7	
Modifier Overlaps	÷	÷	
Trail Green	0	0	
Trail Yellow	0.0	0.0	
Trail Red	0:0	0.0	

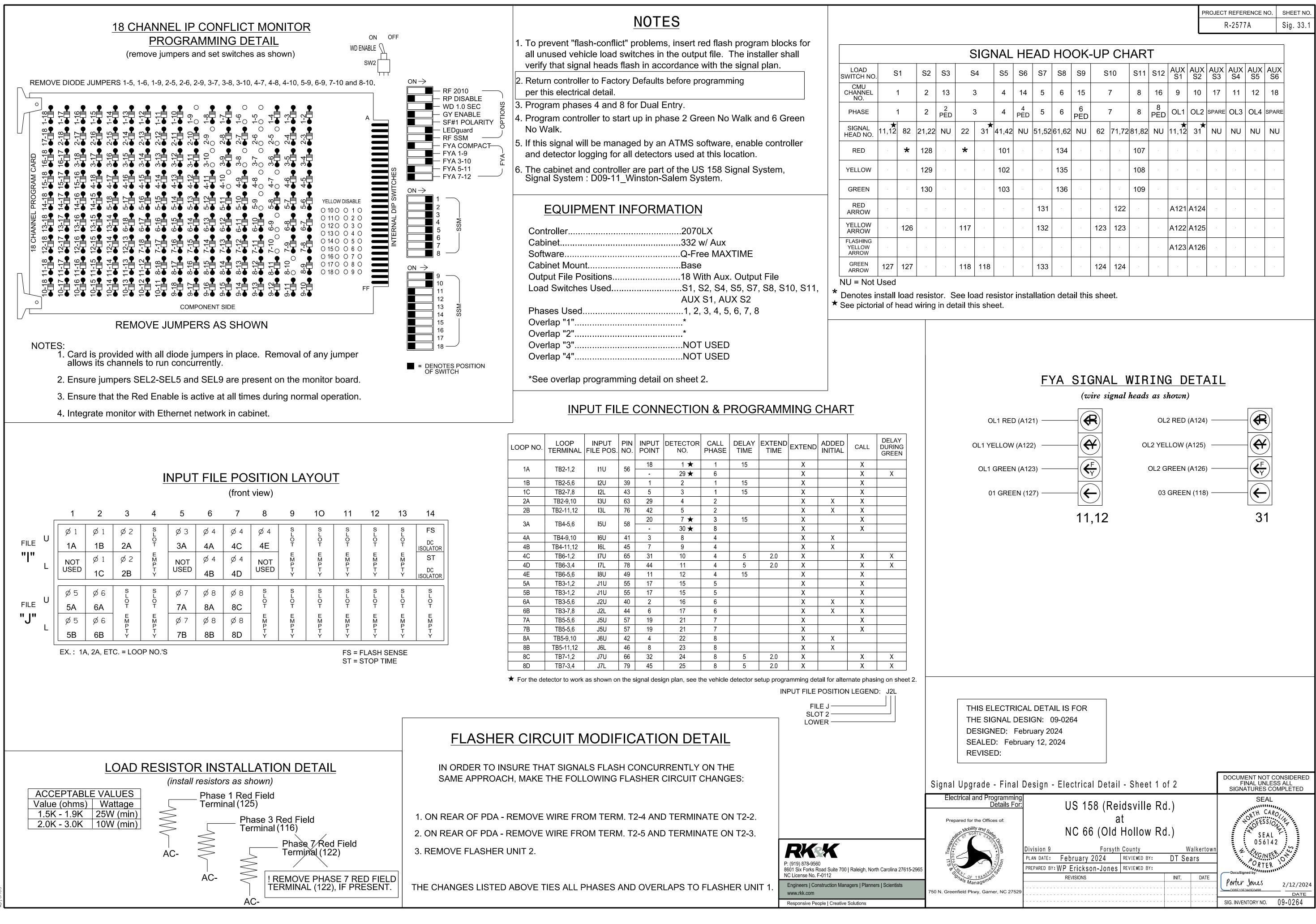
Responsive People | Creative Solutions

			PROJECT REFERENCE NO.	SHEET NO.
T	AIL		R-2577A	Sig. 32.2
า 2				
1 2				
		]		
E⊦	I DET PLAN			
	1			
	2			
	<u>FLASH</u>	ER CIRCUIT MODIFICATION	N DETAIL	
		INSURE THAT SIGNALS FLASH CONCURR ACH, MAKE THE FOLLOWING FLASHER CIF		
	1. ON REAR OF PDA	- REMOVE WIRE FROM TERM. T2-4 AND T	ERMINATE ON T2	-2.
		- REMOVE WIRE FROM TERM. T2-5 AND T	ERMINATE ON T2	-3.
	3. REMOVE FLASHE	R UNIT 2.		
	THE CHANGES LISTE	ED ABOVE TIES ALL PHASES AND OVERLA	PS TO FLASHER U	JNIT 1.
	M	AXTIME ALTERNATE PHASIN		N
		PROGRAMMING DET	AIL	
		ront Panel lain Menu >Controller >Coordination >Pat	torns	
		/eb Interface ome >Controller >Coordination >Patterns	,	
	F	Pattern Parameters Pattern Veh Det Plan Overlap Plan		
		* 2 2		
		* The Pattern number(s) are to be determined by	У	
		the Division and/or City Traffic Engineer.		
		THIS ELECTRI	CAL DETAIL IS FOR	
		THE SIGNAL D DESIGNED: F	ESIGN: 09-0264T3 ebruary 2024	
		SEALED: Feb REVISED:	ruary 12, 2024	
	Signal Upgrade - Tempo	rary Design 3	DOCUMENT NOT (	
٢	Electrical and Programming	rary Design 3 ical Detail - Sheet 2 of 2	FINAL UNLE SIGNATURES CO SEAL	SS ALL OMPLETED
ŀ	Details For: Prepared for the Offices of:	US 158 (Reidsville Rd.) at	PRTH CA/	ROLINA NATION
	Nobility and Sach	NC 66 (Old Hollow Rd.)	SEAL 05614	2
	Superior of the second	Division 9     Forsyth County     Walk       PLAN DATE:     February 2024     REVIEWED BY:     DT Sears       PREPARED BY:     WP Erickson-Jones     REVIEWED BY:	ertown ORTER	P Konst O
35	Contraction of the second seco		DATE DocuSigned by: Porter Jones	2/12/2024
	750 N. Greenfield Pkwy, Garner, NC 27529			<u>DATE</u> 09-0264T3

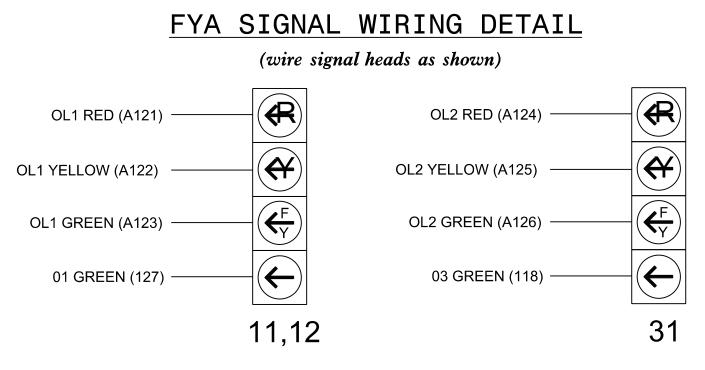


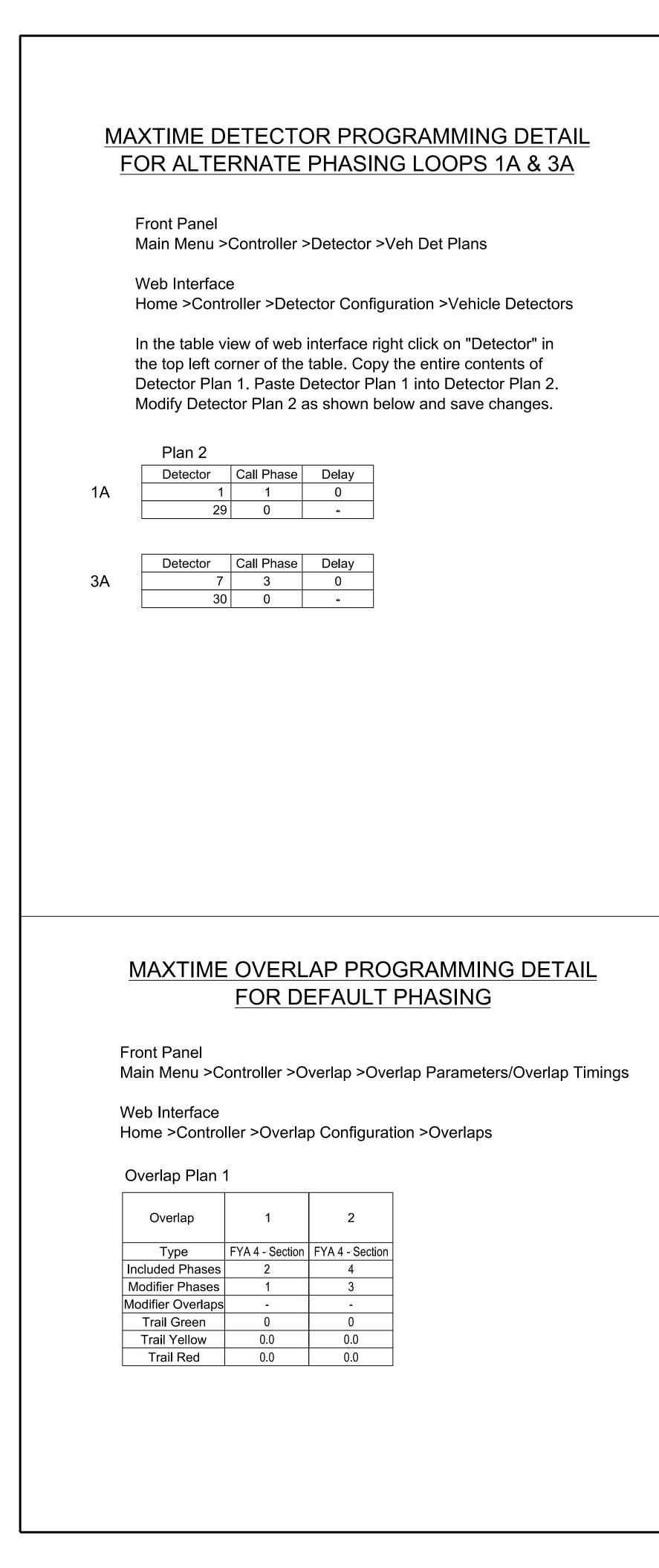
AL <sup>-</sup> TAB					HAS RA		_			
				P	HAS	E .	_			
SIGNAL FACE	Ø 1 + 5	Ø 1 + 6	Ø 2+ 5	Ø 2+ 6	Ø 3 + 7	Ø 3 + 8	Ø 4 + 7	Ø 4 + 8	FLANT	
11, 12	<b>-</b>	◄	<del>≺R</del>	<del>≺R</del>	−R	<del>≺R</del>	<del>≺R</del>	<del>≺R</del>	╶┽	
21	R	R	G	G	R	R	R	R	Y	
2'2	R	R	G	G	R	R	R	R	Y	
31	≺R	≺R	≺R	≺R	-	-	<del>≺R</del>	<del>≺R</del>	<del>≺R</del>	
41,42	R	R	R	R	R	R	G	G	R	
51,52	-	≺R	-	≺R	−R	<del>≺R</del>	<del>≺R</del>	<del>≺R</del>	<del>≺R</del>	
61	R	G	R	G	R	R	R	R	Y	
62	R	G	R	G	R	R	R	R	Y	:
71, 72	≺R	<del>≺R</del>	<del>≺R</del>	<del>≺R</del>	-	╉	-	<del>≺R</del>	╉	
81	R	R	R	R	R	G	R	G	R	;
82	R	R	R	R	R	G	R	G	R	,

	PROJEC	T REFE	RENC	E NO.		SHEE	T NO.
		R-257	7 A		Si	g.	33.0
Full US 158 Signa D09-11_	lŠys	tuate al Sy stem con-S	yst #:				
fer to "Re awings NC puary 2024 ecification ructures" not progra pht flash herwise di ase 1 and gged. ase 3 and gged. t all deter esence mode pineer will urs of use an. kimum time ning char- eration of gnal syste bersede the	DOT" of 4 and ons fo dated ram si ing op irecte /or ph ector de for ph ector de for e for es sho t are nly. ( em tir	dated "Sta or Ro d Jan ignal perat ed by nase nase unit termi each for Coord ming	nda ads ion 5 m 7 m s t raf ne fre ina val	rd y 2 r un e E ay o fic asi e-r	024 ate ngi be be ng un	S	er.
LEG	<u>END</u>						
т сс. с	•			<u>EX</u>	<u>(IST</u>	TNG	Ż



	S	SIGN	NAL	HE	AD	HO	OK-	-UP	CH	AR	Т						
3	S	4	S5	S6	S7	S8	S9	S	S10		S12	AUX S1	AŲX S2	AUX S3	AUX S4	AUX S5	AŲX S6
3	3	3	4	14	5	6	15		7	8	16	9	10	17	11	12	18
2 D		3	4	4 PÉD	5	6	6 PED		7	8	8 PÉD	OL1	OL2	SPARE	OL3	OL4	SPARE
U	22	★ 31	41,42	NU	51,52	61;62	NU	62	71;72	81,82	NU	<b>★</b> 11;12	★ 31	NU	NU	NU	NU
	*		101			134				107			·				
	-		102			135				108							
			103			136				109							
					131				122			A121	A124				
	117				132			123	123			A122	A125				
												A123	A126				
	118	118			133			124	124				·				





# MAXTIME ALTERNATE PHASING ACTIVATION DETAIL

To run alternate phasing, select a Pattern that is programmed to run Overlap Plan 2 and Detector Plan 2. A Pattern can be selected through the scheduler or manually by changing the Operational Mode.

PHASING	OVERLAP PLAN	VEH DET PLAN
ACTIVE PLAN REQUIRED TO RUN DEFAULT PHASING	1	1
ACTIVE PLAN REQUIRED TO RUN ALTERNATE PHASING	2	2

	ALTERNATE PHASING CHANGE SUMMARY
OVERLAP PLAN 2	IS A SUMMARY OF WHAT TAKES PLACE WHEN AND VEHICLE DETECTOR PLAN 2 ACTIVATE FERNATE PHASING":
OVERLAP PLAN 2:	Modifies overlap included phases for heads 11, 12, and 31 to run protected turns only.
VEH DET PLAN 2:	Disables phase 6 call on loop 1A and reduces delay time for phase 1 call on loop 1A to 0 seconds.
	Disables phase 8 call on loop 3A and reduces delay time for phase 3 call on loop 3A to 0 seconds.

# MAXTIME OVERLAP PROGRAMMING DETAIL FOR ALTERNATE PHASING

Front Panel Main Menu >Controller >Overlap >Overlap Parameters/Overlap Timings

Web Interface Home >Controller >Overlap Configuration >Overlaps

In the table view of the web interface, right click on "Overlap" in the top left corner of the table. Copy the entire contents of Overlap Plan 1. Paste Overlap Plan 1 into Overlap Plan 2. Modify Overlap Plan 2 as shown below and save changes.

#### Overlap Plan 2

I I					
Overlap	1	2			Signal
Туре	FYA 4 - Section	FYA 4 - Section			
Included Phases	÷	<u>-</u>	NOTICE INCLUDED PHASE		Pre
Modifier Phases	1	3			110
Modifier Overlaps	<u> </u>	<u>-</u>	Г		Š
Trail Green	0	0		RKK	Trans,
Trail Yellow	0.0	0.0		P: (919) 878-9560	DEPA
Trail Red	0.0	0.0		8601 Six Forks Road Suite 700   Raleigh, North Carolina 27615-2965 NC License No. F-0112	ACOSUEL ITS &
				Engineers   Construction Managers   Planners   Scientists www.rkk.com	750 N. Gree

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PROJECT REFERENCE NO.	SHEET NO.
R-2577A	Sig. 33.2

# MAXTIME ALTERNATE PHASING PATTERN PROGRAMMING DETAIL

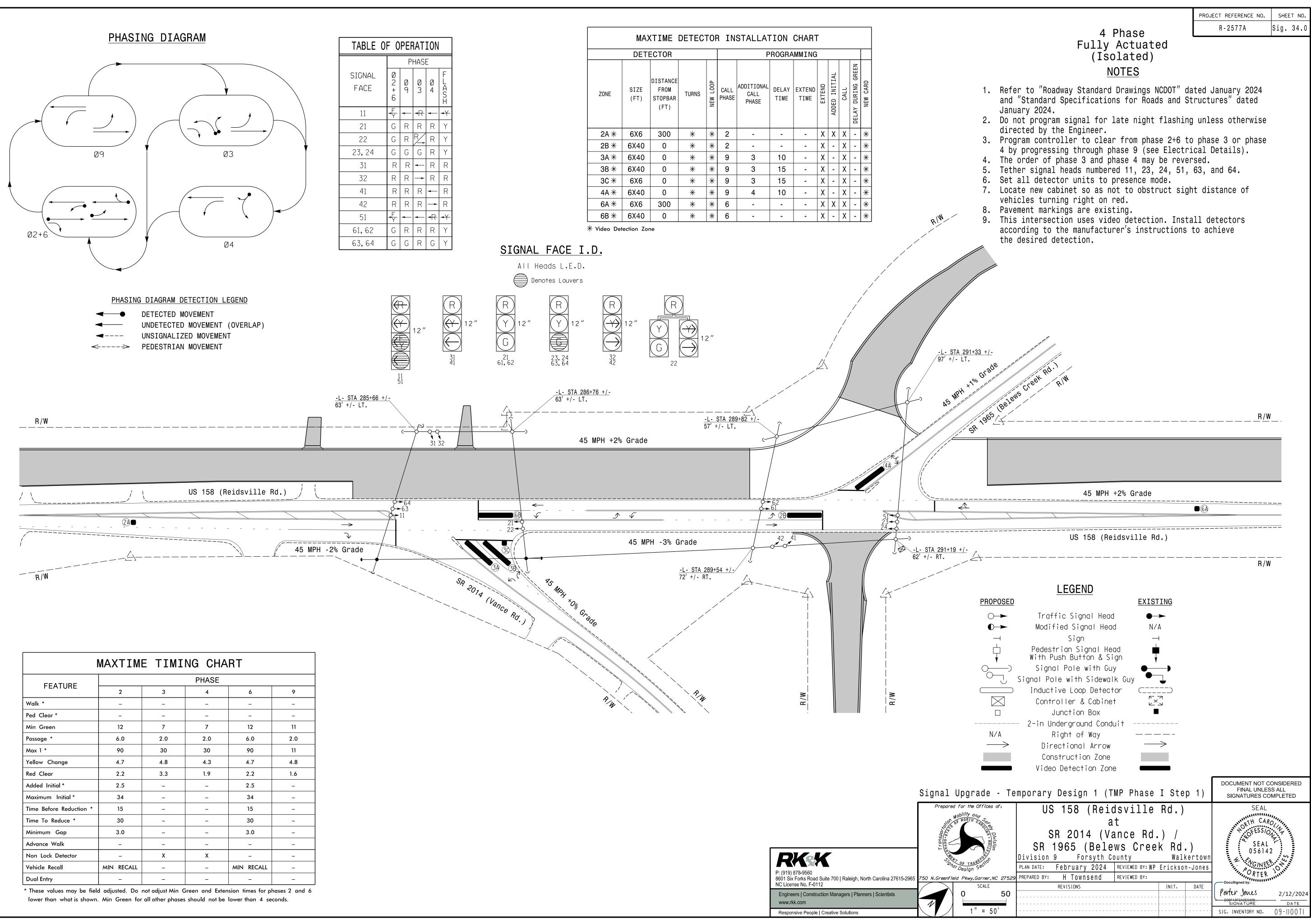
#### Front Panel Main Menu >Controller >Coordination >Patterns

Web Interface Home >Controller >Coordination >Patterns

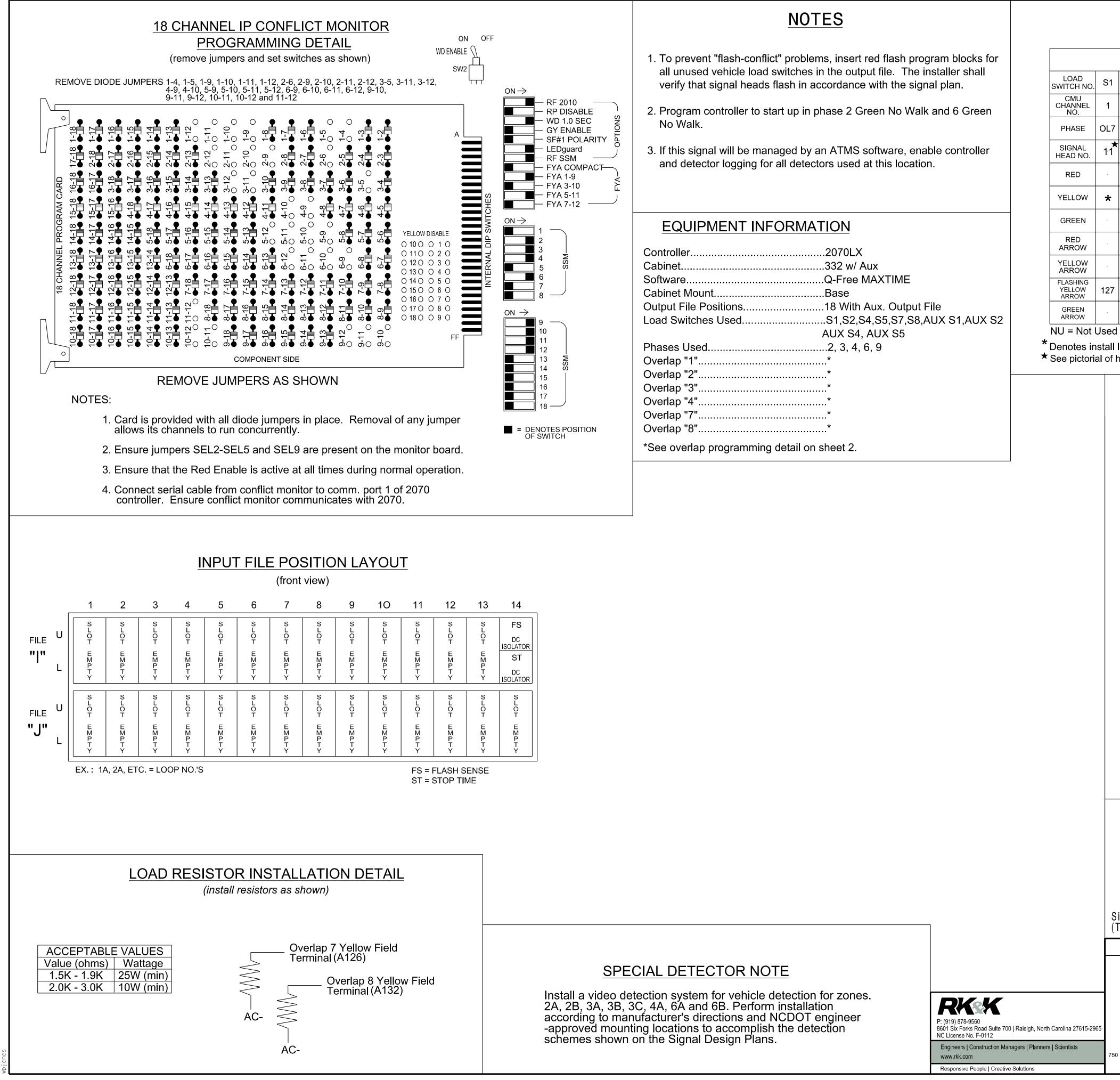
Pattern Para	meters	
Pattern	Veh Det Plan	Overlap Plan
*	2	2

\* The Pattern number(s) are to be determined by the Division and/or City Traffic Engineer.

THIS ELECTRIC THE SIGNAL DE DESIGNED: Fe SEALED: Febru REVISED:	SIGN: 09 bruary 202	9-0264 24					
l Upgrade - Final	Design -	Electrical Detai	I - Sheet 2	of 2		DOCUMENT NOT ( FINAL UNLE SIGNATURES C	SS ALL
ctrical and Programming Details For:		US 158 (Re	eidsville F at	ld.)		SEAL	
in Nobility and Sale		NC 66 (Old		ld.)		SEAL	
in Alexandree	Division 9 Plan Date:	Forsyt February 2024	h County REVIEWED BY:	W DT Sea	alkertown Ars	Z	R. C. L.
PART OF TRANSPORTS	PREPARED BY:	WP Erickson-Jones REVISIONS	REVIEWED BY:	INIT.	DATE	Porter Jones	
enfield Pkwy, Garner, NC 27529						SIG. INVENTORY NO.	2/12/2024 



	MAXTIM	E TIMI	NG CHA	RT	
			PHASE		
FEATURE	2	3	4	6	9
Walk *	-	-	-	-	-
Ped Clear *	-	_	-	-	-
Min Green	12	7	7	12	11
Passage *	6.0	2.0	2.0	6.0	2.0
Max 1 *	90	30	30	90	11
Yellow Change	4.7	4.8	4.3	4.7	4.8
Red Clear	2.2	3.3	1.9	2.2	1.6
Added Initial *	2.5	-	-	2.5	-
Maximum Initial *	34	-	-	34	-
Time Before Reduction *	15	-	-	15	-
Time To Reduce *	30	-	-	30	-
Minimum Gap	3.0	_	_	3.0	-
Advance Walk	_	_	_	_	_
Non Lock Detector	_	Х	Х	_	_
Vehicle Recall	MIN RECALL	_	_	MIN RECALL	_
Dual Entry	-	-	_	-	_



# 8601 Six Forks Road Suite 700 | Raleigh, North Carolina 27615-296 Engineers | Construction Managers | Planners | Scientists

PHASE

RED

YELLOW

GREEN

RED

ARROW

YELLOW ARROW

FLASHING

YELLOW

ARROW

GREEN

ARROW

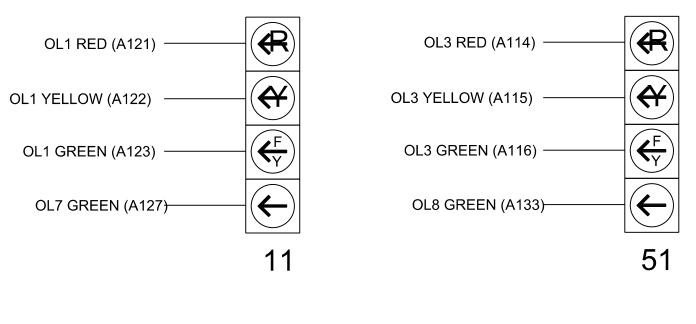
											PF	roject F	REFER	NO.		неет N g. 34.	
	S	SIG	VAL	HE	AD	НО	OK	-UP	C⊦	IAR <sup>.</sup>	T			 			
00		0.5					0.40		<b>.</b>	AUX	AUX				ГXI		

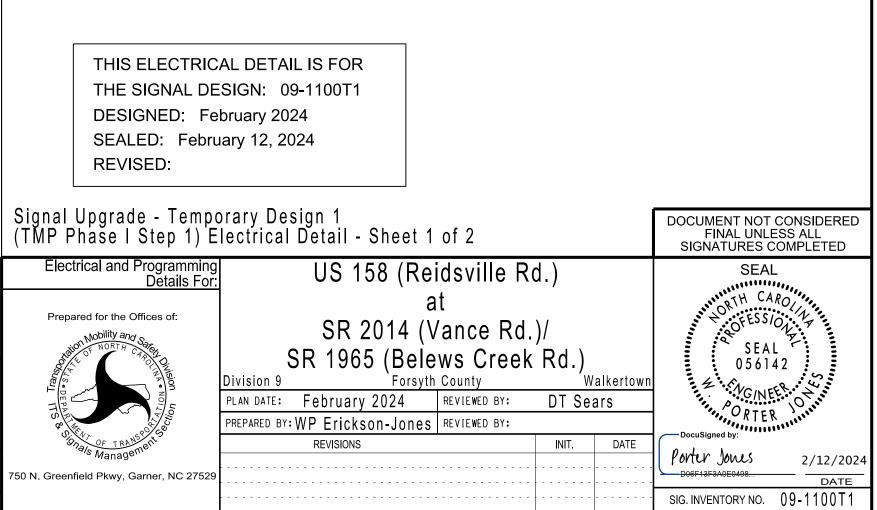
. S1	S2	S3	S	<b>4</b>	S5	S6	S7	S8	S9	S10	S11	S12	AUX S1	AUX S2	AUX S3	AUX S4	AUX S5	AUX S6
1	2	13		3	4	14	5	6	15	7	8	16	9	10	1.7	11	12	18
OL7	2	2 PED		3	4	4 PED	OL8	6	6 PED	7	8	8 PÉD	OL1		SPARE	OL3	OL4	SPARE
11	21,22	NU	22	31,32	41,42	NU	<b>★</b> 51	61,62	NU	NU	NU	NU	★ 11	63,64	NU	★ 51	23,24	NU
	128							134						A124			A101	
*	129						*	135						A125			A102	
	130				-			136						A126	-		A103	
				116	101								A121			A114		
		-	117	117	102		-					-	A122			A115		
127							133						A123			A116		
			118	118	103													

\* Denotes install load resistor. See load resistor installation detail this sheet.  $\star$  See pictorial of head wiring in detail this sheet.

# FYA SIGNAL WIRING DETAIL

(wire signal heads as shown)





## SEQUENCE DETAIL

Front Panel Main Menu >Controller >Sequence & Phs Config>Sequences

Web Interface Home >Controller >Sequence

Sequence 1

Ring	Sequence Data
1	2,a,9,b,3,4,c
2	6,a,b,c

# FLASHER CIRCUIT MODIFICATION DETAIL

IN ORDER TO INSURE THAT SIGNALS FLASH CONCURRENTLY ON THE SAME APPROACH, MAKE THE FOLLOWING FLASHER CIRCUIT CHANGES:

- 1. ON REAR OF PDA REMOVE WIRE FROM TERM. T2-4 AND TERMINATE ON T2-2.
- 2. ON REAR OF PDA REMOVE WIRE FROM TERM. T2-5 AND TERMINATE ON T2-3.
- 3. REMOVE FLASHER UNIT 2.

THE CHANGES LISTED ABOVE TIES ALL PHASES AND OVERLAPS TO FLASHER UNIT 1.

# OVERLAP PROGRAMMING

Front Panel

Web Interface Home >Controller >Overlap Configuration >Overlaps

Overlap Plan 1

Overlap	1	2	3	4	7	8
Туре	FYA 4 - Section	Normal	FYA 4 - Section	Normal	Normal	Normal
Included Phases	2	4,6,9	6	2,3,9	4,9	3,9
Modifier Phases	-	-	<b>-</b>	-	-	-
Modifier Overlaps	7	-	8	<u>-</u>	-	÷
Trail Green	0	0	0	0	0	0
Trail Yellow	0.0	0.0	0.0	0:0	0.0	0.0
Trail Red	0.0	0.0	0.0	0:0	0.0	0.0

# **OUTPUT CHANNEL CONFIGURATION**

Front Panel Main Menu >Controller >More>Channels>Channels Config

Web Interface

#### Channel Configuration

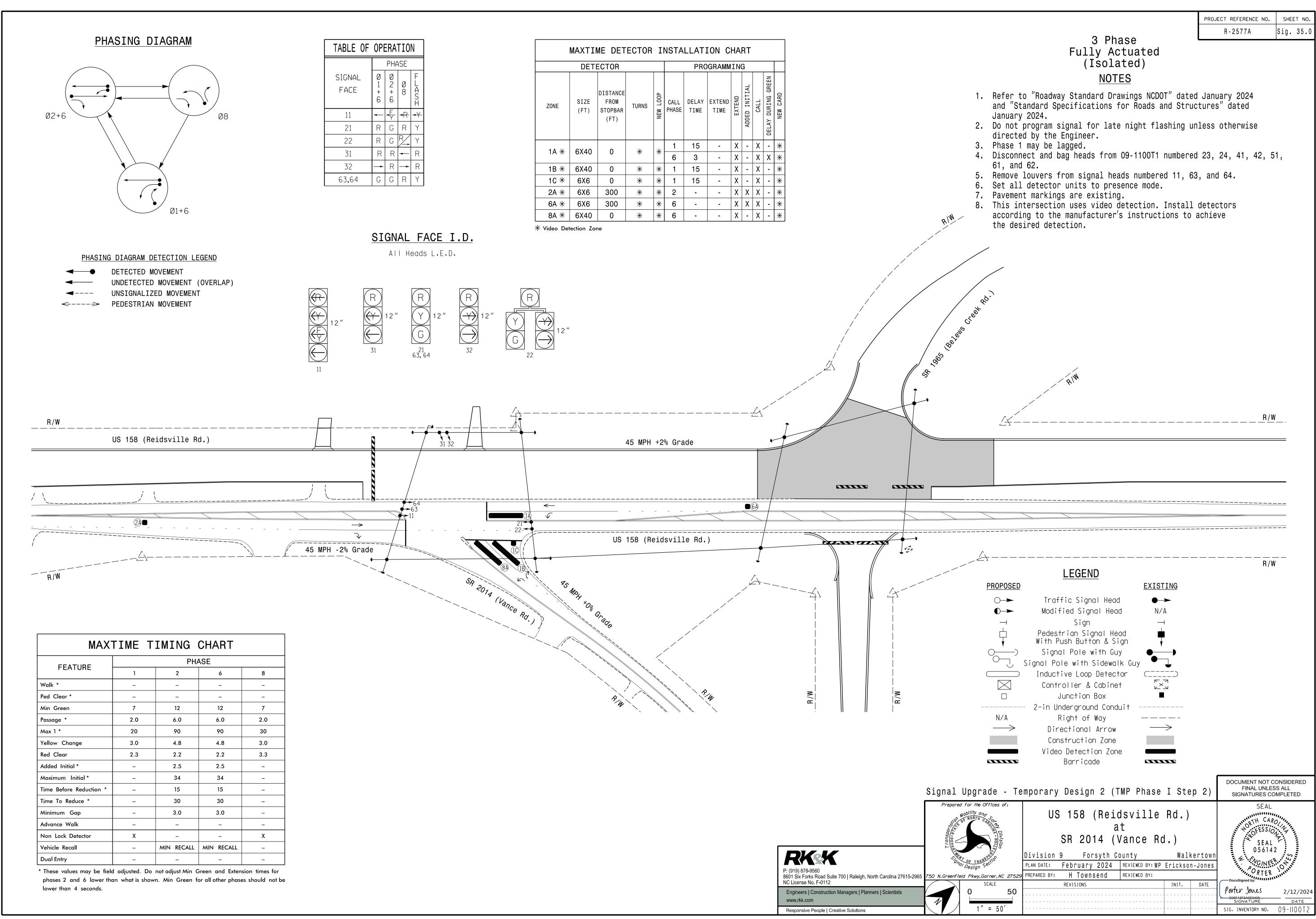
NOTICE OVERLAP 7	Channel	Control Type	Control Source	Flash Yellow	Flash Red	Flash Alt	MMU Channel	
ASSIGNED TO CHANNEL 1.	1	Overlap	7	Х		X	1	NOTICE CHANNEL 1
	2	Phase Vehicle	2	Х			2	YELLOW FLASH
	3	Phase Vehicle	3		Х	Х	3	
	4	Phase Vehicle	4		Х		4	
	5	Phase Vehicle	5		Х		5	
NOTICE OVERLAP 8	6	Phase Vehicle	6	Х		Х	6	
ASSIGNED TO CHANNEL 7.	7	Overlap	8	Х			7	NOTICE CHANNEL 7
	8	Phase Vehicle	8		Х	Х	8	YELLOW FLASH
	9	Overlap	1	Х		Х	9	
	10	Overlap	2		Х	Х	10	
	11	Overlap	3	Х			11	
	12	Overlap	4		Х		12	
	13	Phase Ped	2				13	
	14	Phase Ped	4				14	
	15	Phase Ped	6				15	
	16	Phase Ped	8				16	
	17	Overlap	5		Х	Х	17	
	18	Overlap	6		Х	-	18	

THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 09-1100T1	Signal Upgrade - Tempo (TMP Phase I Step 1) E		2 of 2			DOCUMENT NOT CONSIDEREI FINAL UNLESS ALL SIGNATURES COMPLETED			
DESIGNED: February 2024 SEALED: February 12, 2024 REVISED:	Electrical and Programming Details For: Prepared for the Offices of:		at	,		SEAL NORTH CAR			
<b>P:</b> (919) 878-9560	TEPPE	SR 1965 (Bele	County REVIEWED BY:	Rd.)	/alkertown ars	SEAL 05614 Connet ORTER	2 R		
8601 Síx Forks Road Suite 700   Raleigh, North Carolina 27615-2965 NC License No. F-0112 Engineers   Construction Managers   Planners   Scientists www.rkk.com	750 N. Greenfield Pkwy, Garner, NC 27529	REVISIONS		INIT.	DATE	DocuSigned by: Porter Jones DOGEF13F3A0E0408	2/12/2024 DATE		
Responsive People   Creative Solutions						SIG. INVENTORY NO.	09-1100T1		

PROJECT REFERENCE NO.	SHEET NO.
R-2577A	Sig. 34.2

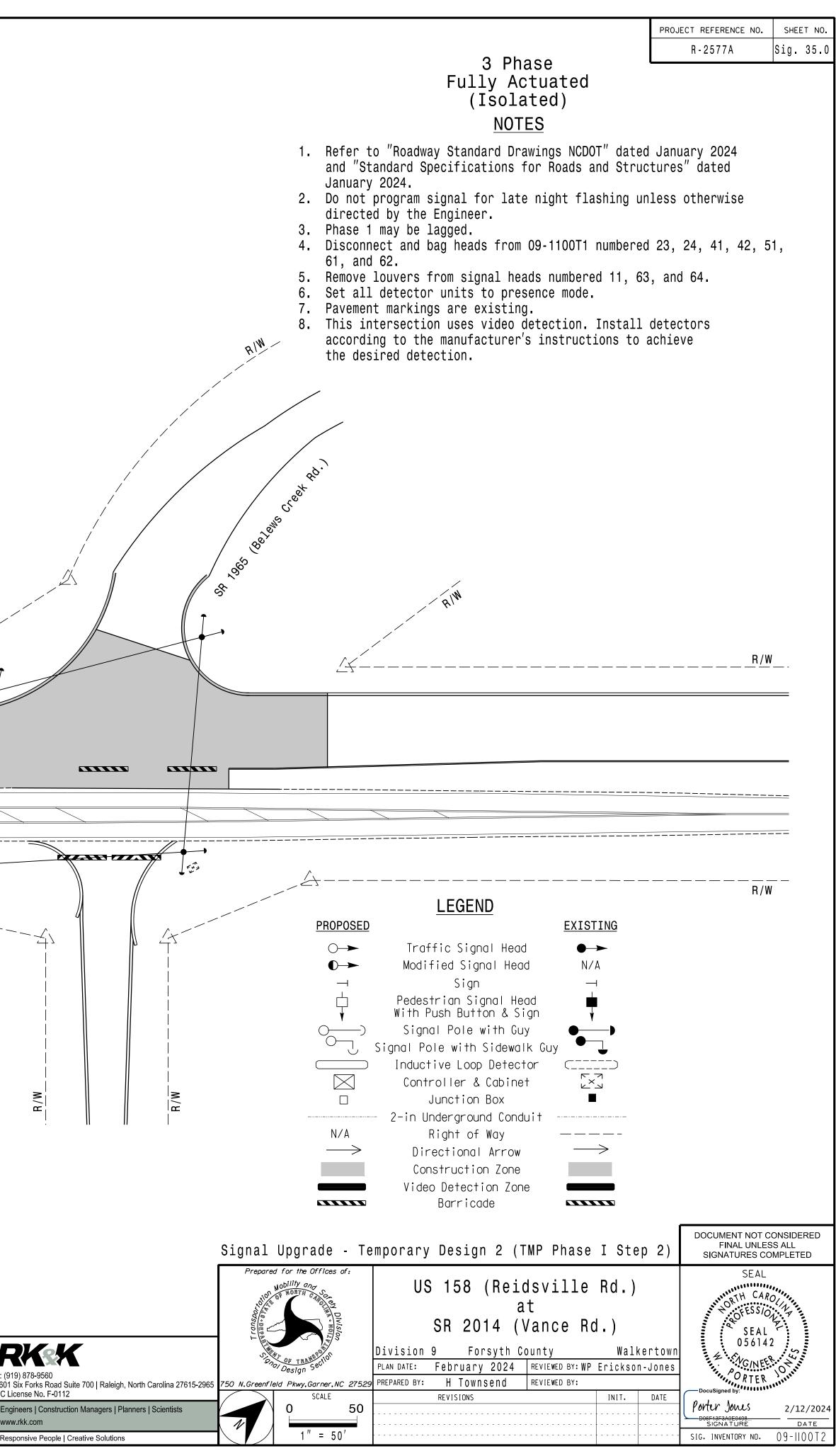
## Main Menu >Controller >Overlap >Overlap Parameters/Overlap Timings

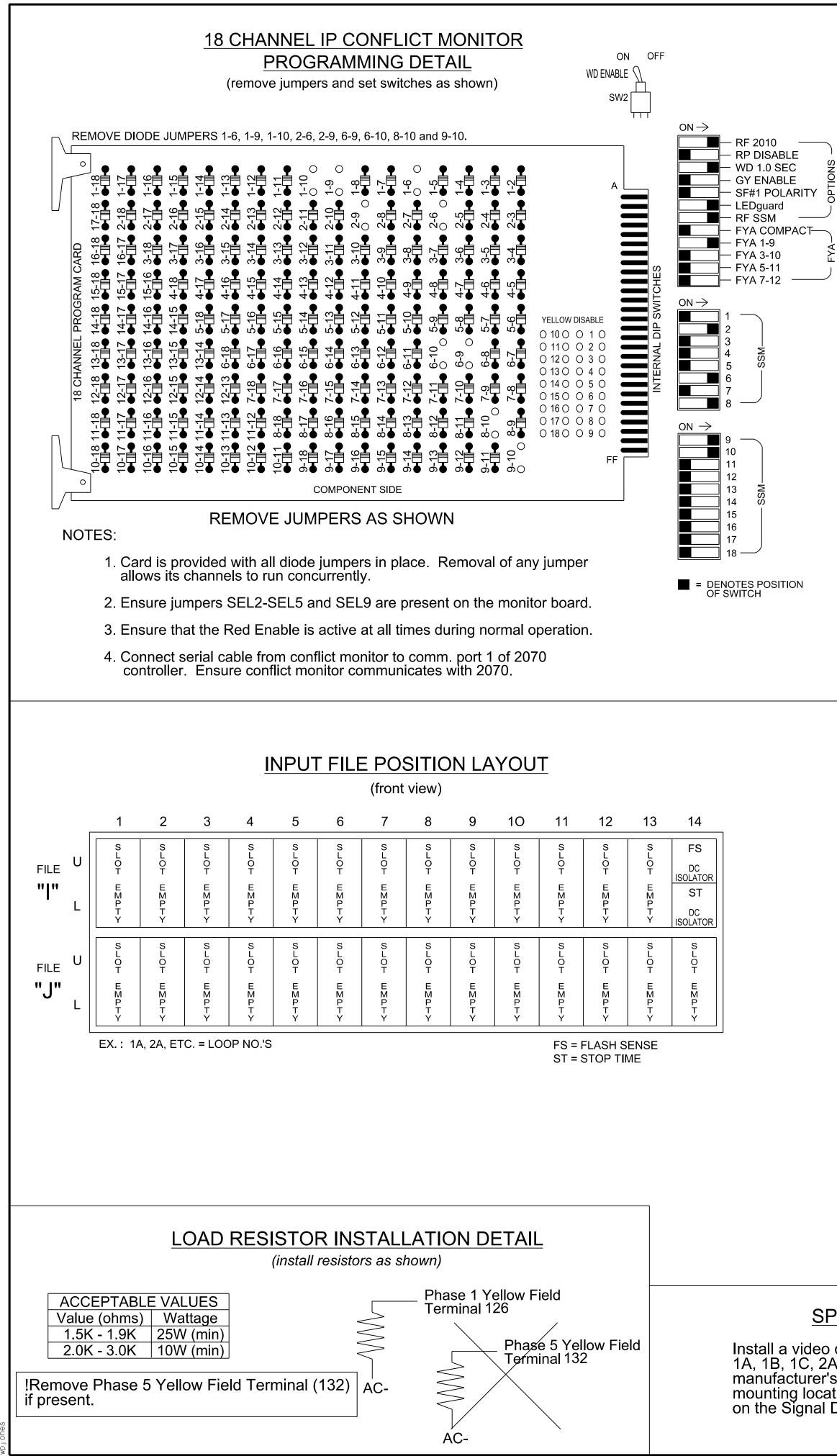
## Home >Controller >Advanced IO>Channels>Channels Configuration



ual Entry	-	-	-	-	
These values may be field hases 2 and 6 lower the ower than 4 seconds.	•	•			

DETECTOR PROGRAMMING											Γ	
ZONE	SIZE (FT)	DISTANCE FROM STOPBAR (FT)	TURNS	NEW LOOP	CALL PHASE	DELAY TIME	EXTEND TIME	EXTEND	ADDED INITIAL	CALL	DELAY DURING GREEN	,
1A *	6X40	0	*	*	1	15	-	Х	-	Х	-	
		Ū			6	3	<u> </u>	Х	-	Х	Х	
1B *	6X40	0	*	*	1	15	÷	Х	-	X	-	
1C *	6X6	0	*	*	1	15	÷	Х	÷	Х	÷	
2A *	6X6	300	*	*	2	÷	÷	Х	Х	Х	<u> -</u>	Γ
6A *	6X6	300	*	*	6	<u>+</u>	÷	Х	Х	Х	<u> </u>	I
8A *	6X40	0	*	*	6	-	<u>+</u>	Х	-	Х	-	Ī





## NOTES 1. To prevent "flash-conflict" problems, insert red flash program blocks for all unused vehicle load switches in the output file. The installer shall verify that signal heads flash in accordance with the signal plan. 2. Return controller to Factory Defaults before programming per this electrical detail.

- 3. Program controller to start up in phase 2 Green No Walk and 6 Green No Walk.
- 4. If this signal will be managed by an ATMS software, enable controller and detector logging for all detectors used at this location.

## EQUIPMENT INFORMATION

Controller	2070LX
Cabinet	332 w/ Aux
Software	Q-Free MAXTIME
Cabinet Mount	Base
Output File Positions	18 With Aux. Output File
Load Switches Used	S1, S2, S8, S11, AUX S1, AUX S2
Phases Used	1, 2, 6, 8
Overlap "1"	*
Overlap "2"	*
Overlap "3"	NOT USED
Overlap "4"	NOT USED

\*See overlap programming detail on this sheet.

# **OVERLAP PROGRAMMING**

Front Panel

Main Menu >Controller >Overlap >Overlap Parameters/Overlap Timings

Web Interface

Home >Controller >Overlap Configuration >Overlaps

Overlap Plan 1

Overlap	1	2
Туре	FYA 4 - Section	Normal
Included Phases	2	1;8
Modifier Phases	1	-
Modifier Overlaps	-	4
Trail Green	0	0
Trail Yellow	0:0	0:0
Trail Red	0:0	0:0

# FLASHER CIRCUIT MODIFICATION DETAIL

IN ORDER TO INSURE THAT SIGNALS FLASH CONCURRENTLY ON THE SAME APPROACH, MAKE THE FOLLOWING FLASHER CIRCUIT CHANGES:

1. ON REAR OF PDA - REMOVE WIRE FROM TERM. T2-4 AND TERMINATE ON T2-2.

2. ON REAR OF PDA - REMOVE WIRE FROM TERM. T2-5 AND TERMINATE ON T2-3.

3. REMOVE FLASHER UNIT 2.

THE CHANGES LISTED ABOVE TIES ALL PHASES AND OVERLAPS TO FLASHER UNIT 1.

## SPECIAL DETECTOR NOTE

Install a video detection system for vehicle detection for zones. 1A, 1B, 1C, 2A, 6A and 8A. Perform installation according to manufacturer's directions and NCDOT engineer-approved mounting locations to accomplish the detection schemes shown on the Signal Design Plans.

																PROJE	ECT REI	FERENC	CE NO.	SHEET NO.
																	R-2	577A		Sig. 35.1
				S	3IGI	NAL	HE	EAD	HO	OK	-UP	C⊢	IAR	Т						
LOAD SWITCH NO.	S1	S2	S3	S4	S5	S6	S7	S8	S9	S10	S	11	S12	AUX S1	AUX S2	AUX S3	AUX S4	AUX S5	AUX S6	
CMU CHANNEL NO.	1	2	13	3	4	14	5	6	15	7		8	16	9	10	17	11	12	18	
PHASE	1	2	2 PED	3	4	4 PED	5	6	6 PED	7		8	8 PED	OL1		SPARE	OL3	OL4	SPARE	
SIGNAL HEAD NO	<b>★</b> 11	21,22	NU	NU	NU	NU	NU	63,64	NU	NU	22	31	NU	<b>★</b> 11	32	NU	NU	NU	NU	
RED		128						134												
YELLOW	*	129						135												
GREEN		130						136												
RED ARROW				-								107		A121	A124				-	
YELLOW ARROW											108	108		A122	A125					
FLASHING YELLOW ARROW														A123						
GREEN ARROW	127										109	109			A126				-	
NII = Not I	العصط	1			<u>.</u>	<u> </u>		<u> </u>	1	1	1	1	1	1	1	1	1	1	<u> </u>	

NU = Not Used  $\star$ See pictorial of head wiring in detail this sheet.

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P: (919) 878-9560 8601 Six Forks Road Suite 700   Raleigh, North Carolina 27615 NC License No. F-0112

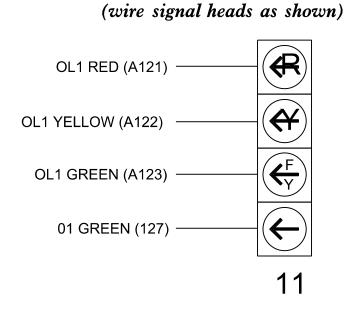
Engineers | Construction Managers | Planners | Scientists

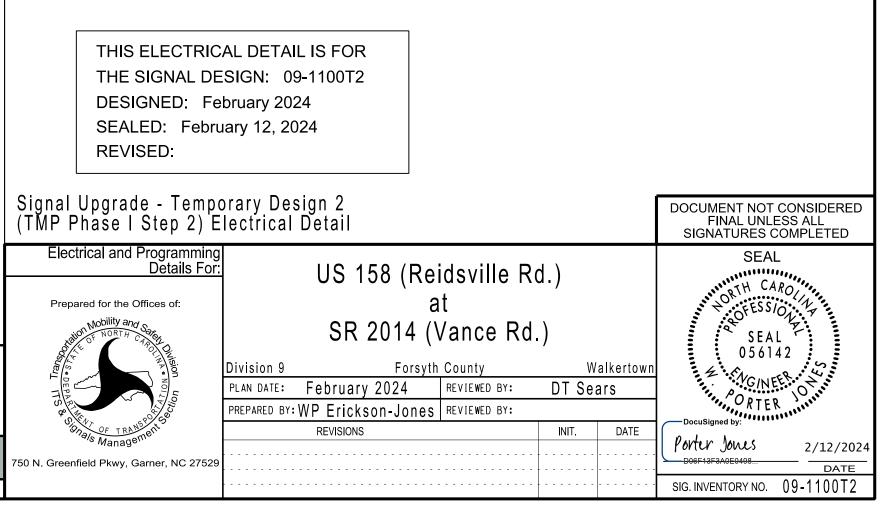
Responsive People | Creative Solutions

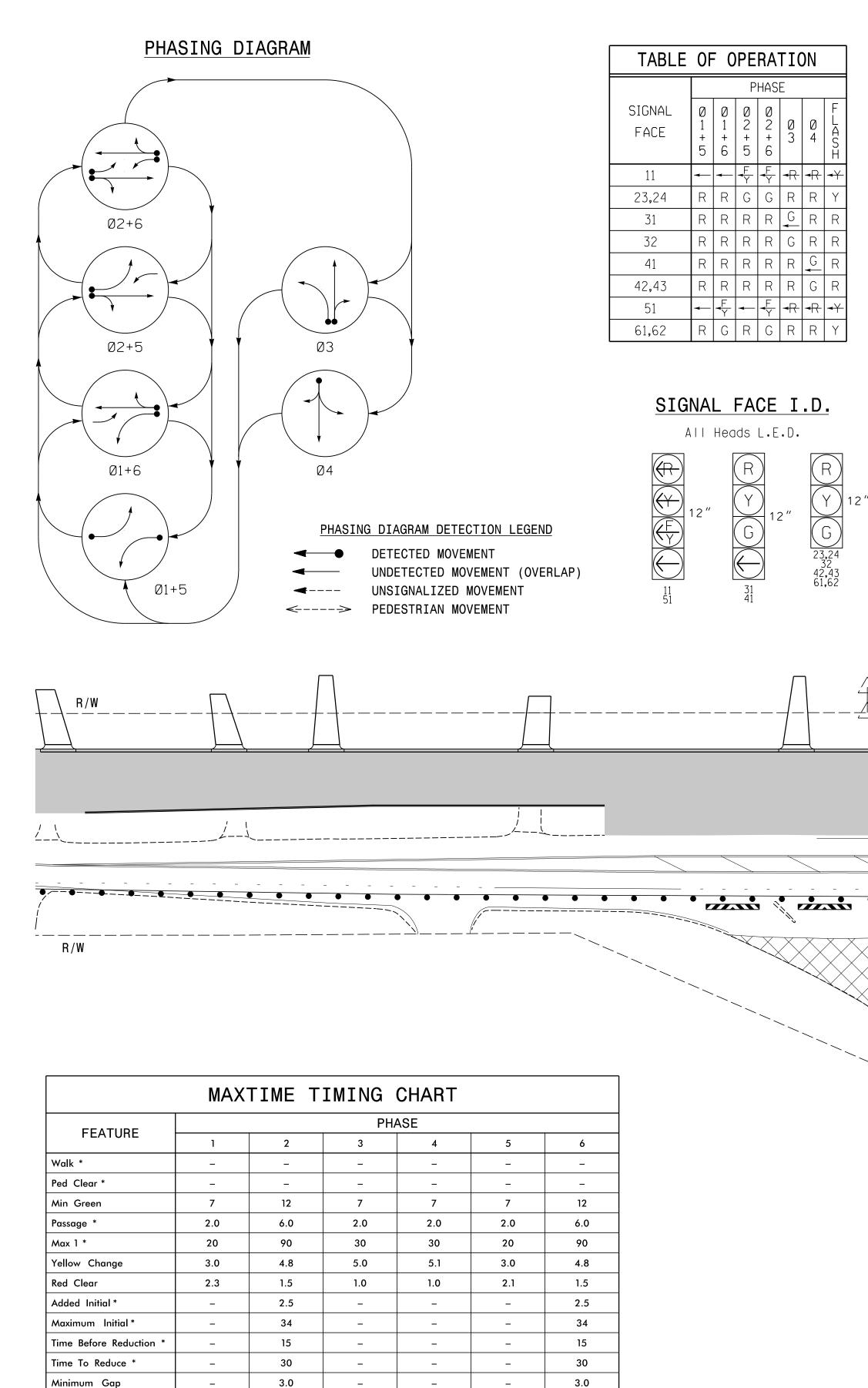
www.rkk.com

\*Denotes install load resistor. See load resistor installation detail this sheet.









Advance Walk

Vehicle Recall

Dual Entry

Non Lock Detector

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MIN RECALL

\* These values may be field adjusted. Do not adjust Min Green and Extension times for phases 2 and 6 lower than what is shown. Min Green for all other phases should not be lower than 4 seconds.

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MIN RECALL

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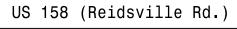
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	DETI	ECTOR			PRC	GRAMM	IN	G				
ZONE	SIZE (FT)	DISTANCE FROM STOPBAR (FT)	TURNS	NEW LOOP	CALL PHASE	DELAY TIME	EXTEND TIME	EXTEND	ADDED INITIAL	CALL	DELAY DURING GREEN	NEW CARD
10 1/	6Y40	0	V	~	1	15	<u> </u>	Х	-	Х	-	*
1A <del>*</del>	6X40		*	*	6	3	-	Х	-	Х	Х	*
2A *	6X6	300	*	*	2	÷	<u>+</u>	Х	Х	Х	-	*
3A *	6X40	0	*	*	3	3	<u> </u>	Х	<u> -</u>	Х	<u>-</u>	*
3B *	6X40	0	*	*	3	10	<u> </u>	Х	-	Х	<u>-</u>	*
3C *	6X6	0	*	*	3	10	-	Х	-	Х	-	*
4A *	6X40	0	*	*	4	10	÷	Х	-	Х	<u> </u>	*
5 A ¥	6740	0	¥	V	5	15	÷	Х	-	Х	<u> </u>	*
5A *	6X40		* *	*	2	3	<u> </u>	Х	-	Х	Х	*
6A *	6X6	300	*	*	6	<u>+</u>	÷	Х	X	Х	<u> </u>	*

\* Video Detection Zone

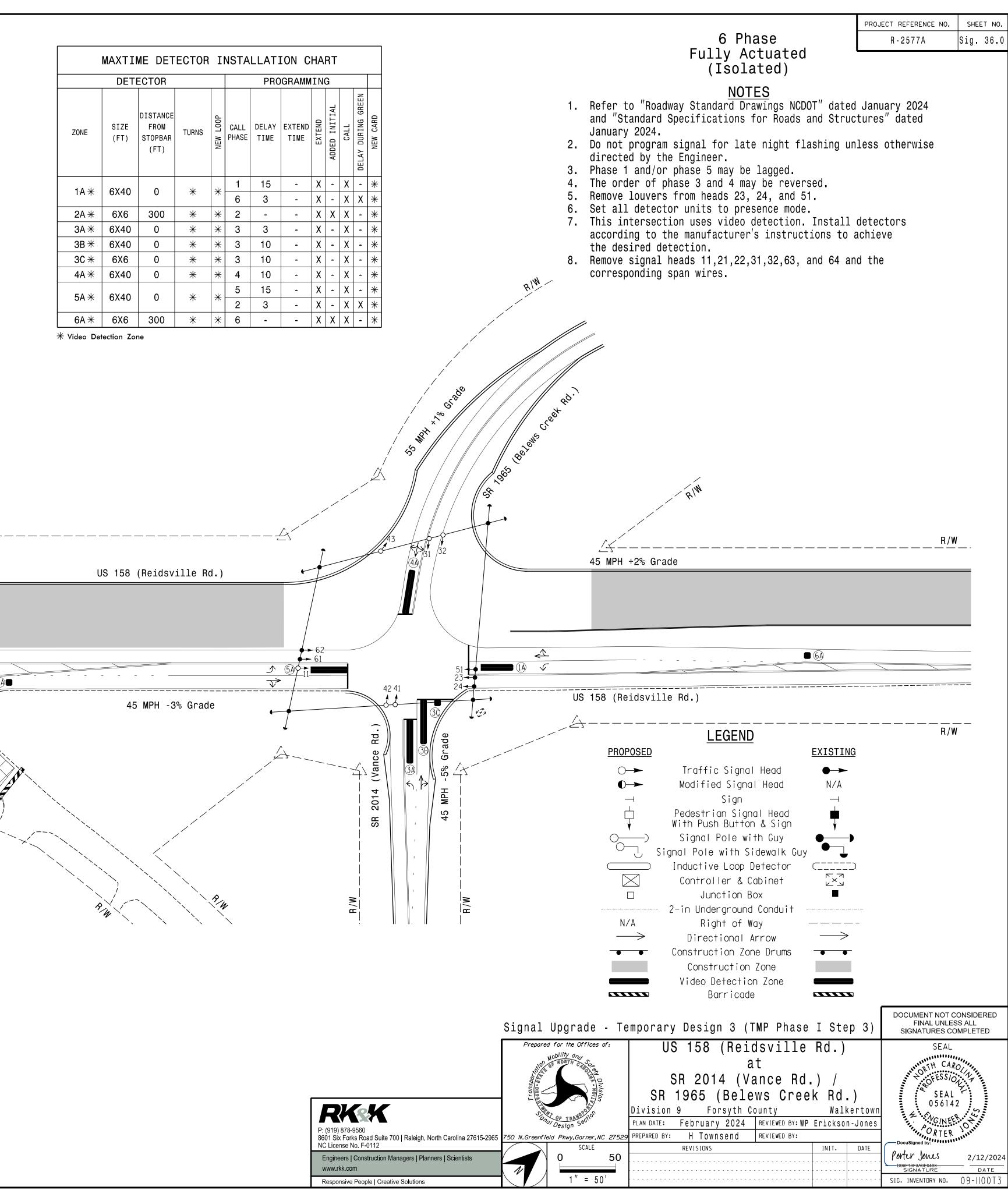
2A



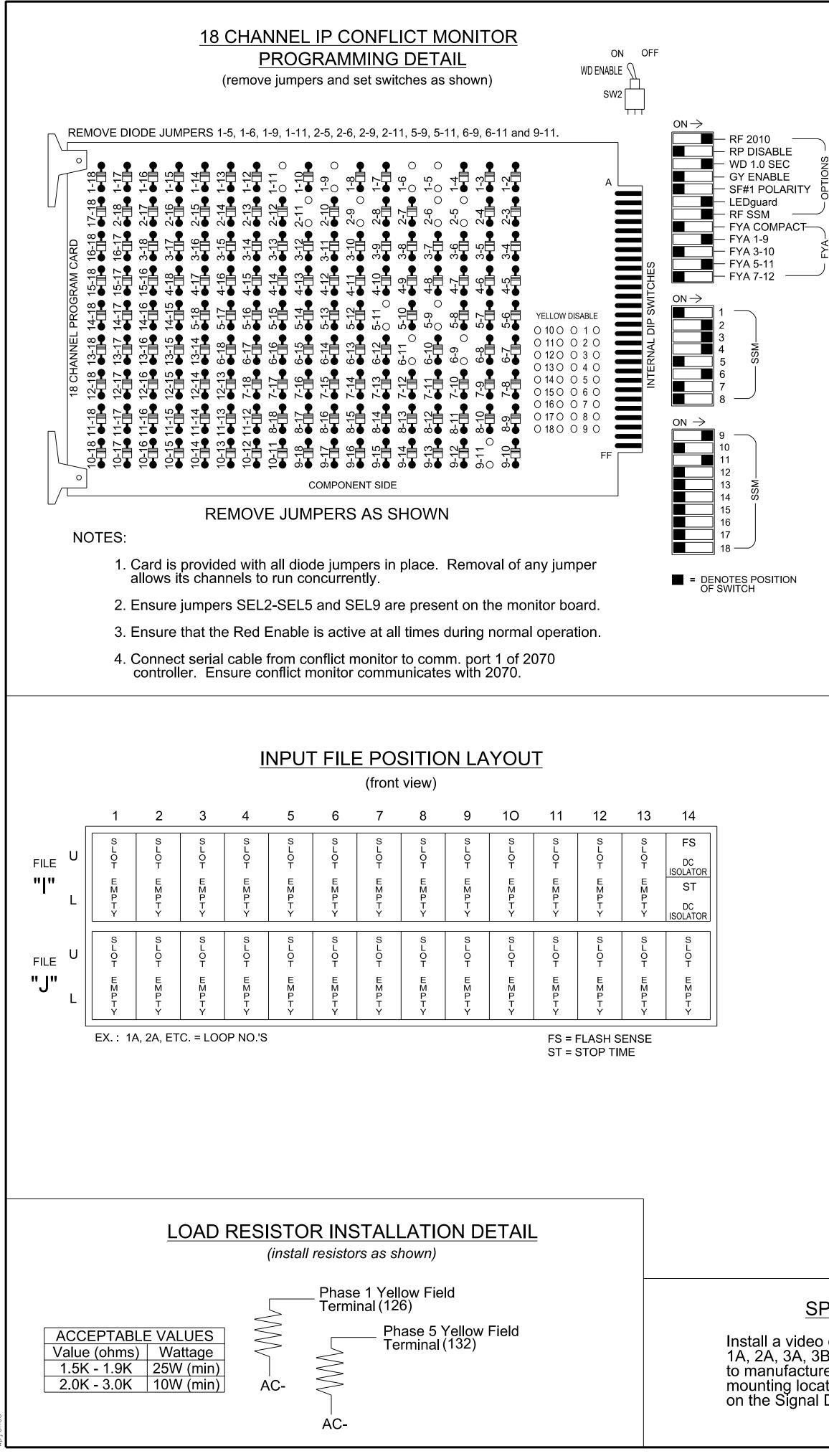
45 MPH -3% Grade

∳→ 61

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# NOTES

- 1. To prevent "flash-conflict" problems, insert red flash program blocks for all unused vehicle load switches in the output file. The installer shall verify that signal heads flash in accordance with the signal plan.
- 2. Return controller to Factory Defaults before programming per this electrical detail.
- 3. Program controller to start up in phase 2 Green No Walk and 6 Green No Walk.
- 4. If this signal will be managed by an ATMS software, enable controller and detector logging for all detectors used at this location.

## EQUIPMENT INFORMATION

Controller	2070LX
Cabinet	332 w/ Aux
Software	Q-Free MAXTIME
Cabinet Mount	Base
Output File Positions	18 With Aux. Output File
Load Switches Used	S1, S2, S4, S5, S7, S8,
	AUX S1, AUX S4
Phases Used	1, 2, 3, 4, 5, 6
Overlap "1"	*
Overlap "2"	NOT USED
Overlap "3"	**
Overlap "4"	NOT USED
*See overlap programming detail on	this sheet.

					SIC	GN/	۹L F	IEA	DH	00	K-U	PC	HA	RT						
LOAD SWITCH NO.	S1	S2	S3	S	4	S	65	S6	S7	S8	S9	S10	S11	S12	AUX S1	AUX S2	AUX S3	AUX S4	AUX S5	AUX S6
CMU CHANNEL NO.	1	2	13		3		4		5	6	15	7	8	16	9	10	17	11	12	18
PHASE	1	2	2 PED		3		4		5	6	6 PED	7	8	8 PED	OL1	OL2	SPARE	OL3	OL4	SPARE
SIGNAL HEAD NO	★ 11	23,24	NU	31	32	41	42,43	NU	★ 51	61,62	NU	NU	NU	NU	★ 11	NU	NU	51 <sup>★</sup>	NU	NU
RED		128		116	116	101	101			134						·				
YELLOW	*	129	-	117	117	102	102		*	135			-	-						
GREEN		130		118	118	103	103			136										
RED ARROW															A121			A114		·
YELLOW ARROW															A122			A115		
FLASHING YELLOW ARROW															A123			A116		
GREEN ARROW	127			118		103			133					-						

NU = Not Used

★See pictorial of head wiring in detail this sheet.

## **OVERLAP PROGRAMMING**

Front Panel

Main Menu >Controller >Overlap >Overlap Parameters/Overlap Timings

Web Interface Home >Controller >Overlap Configuration >Overlaps

Overlap Plan 1

Overlap	1	3
Туре	FYA 4 - Section	FYA 4 - Section
Included Phases	2	6
Modifier Phases	1	5
Modifier Overlaps	<u>-</u>	<u>-</u>
Trail Green	0	0
Trail Yellow	0.0	0.0
Trail Red	0.0	0:0

## SPECIAL DETECTOR NOTE

Install a video detection system for vehicle detection for zones. 1A, 2A, 3A, 3B, 3C, 4A, 5A and 6A. Perform installation according to manufacturer's directions and NCDOT engineer-approved mounting locations to accomplish the detection schemes shown on the Signal Design Plans.

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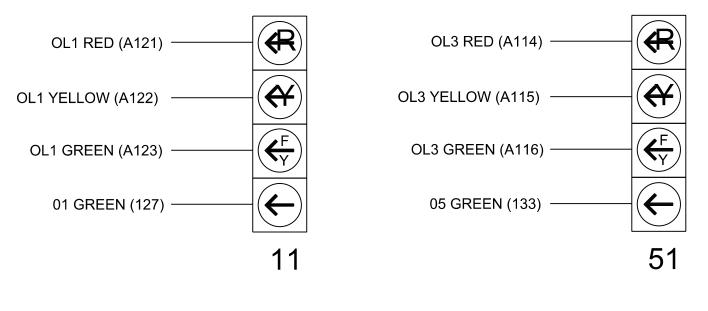
PROJECT REFERENCE NO.	SHEET
R-2577A	Sig. 3

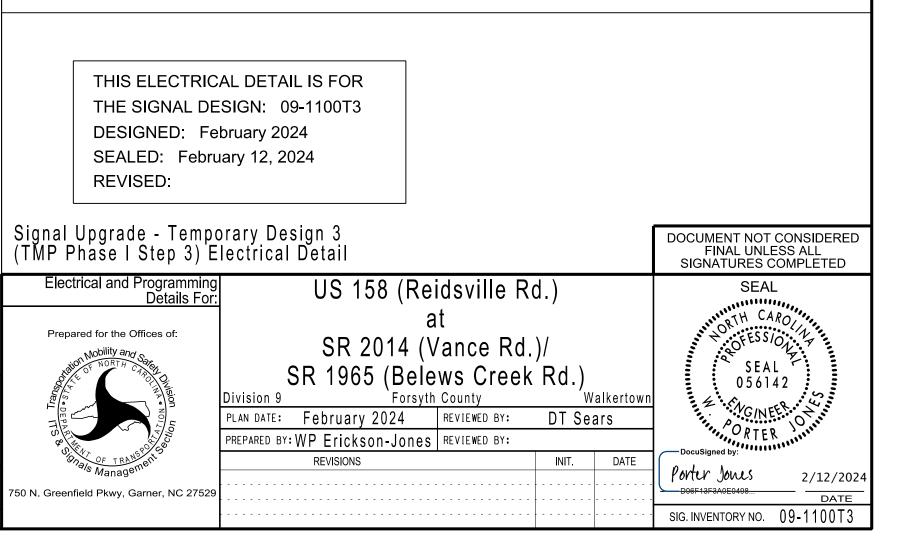
SHEE			J.
Sig.	3(	ô.	1

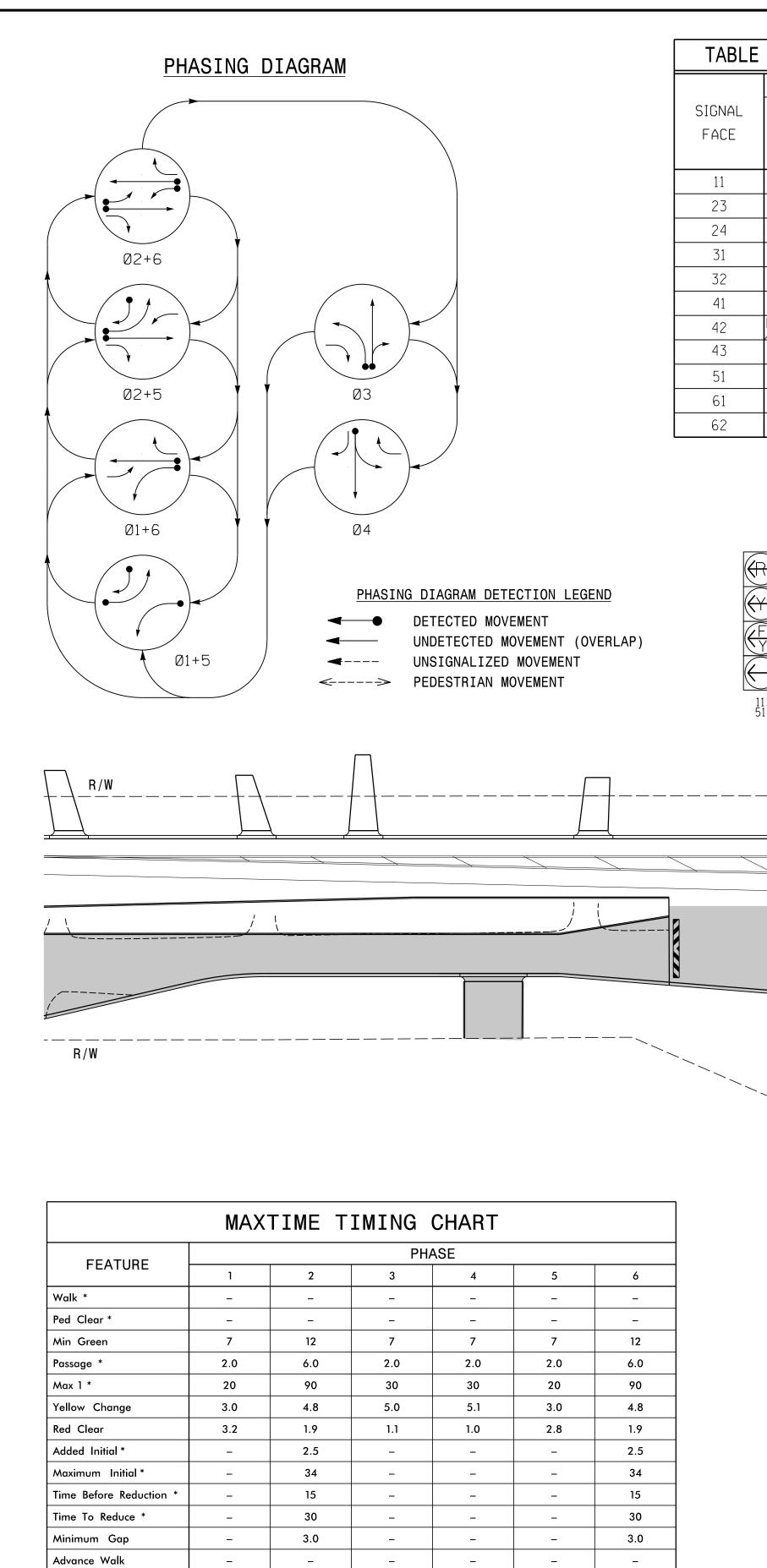
\*Denotes install load resistor. See load resistor installation detail this sheet.

## FYA SIGNAL WIRING DETAIL

(wire signal heads as shown)







\* These values may be field adjusted. Do not adjust Min Green and Extension times for phases 2 and 6 lower than what is shown. Min Green for all other phases should not be lower than 4 seconds.

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MIN RECALL

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MIN RECALL

Non Lock Detector

Vehicle Recall

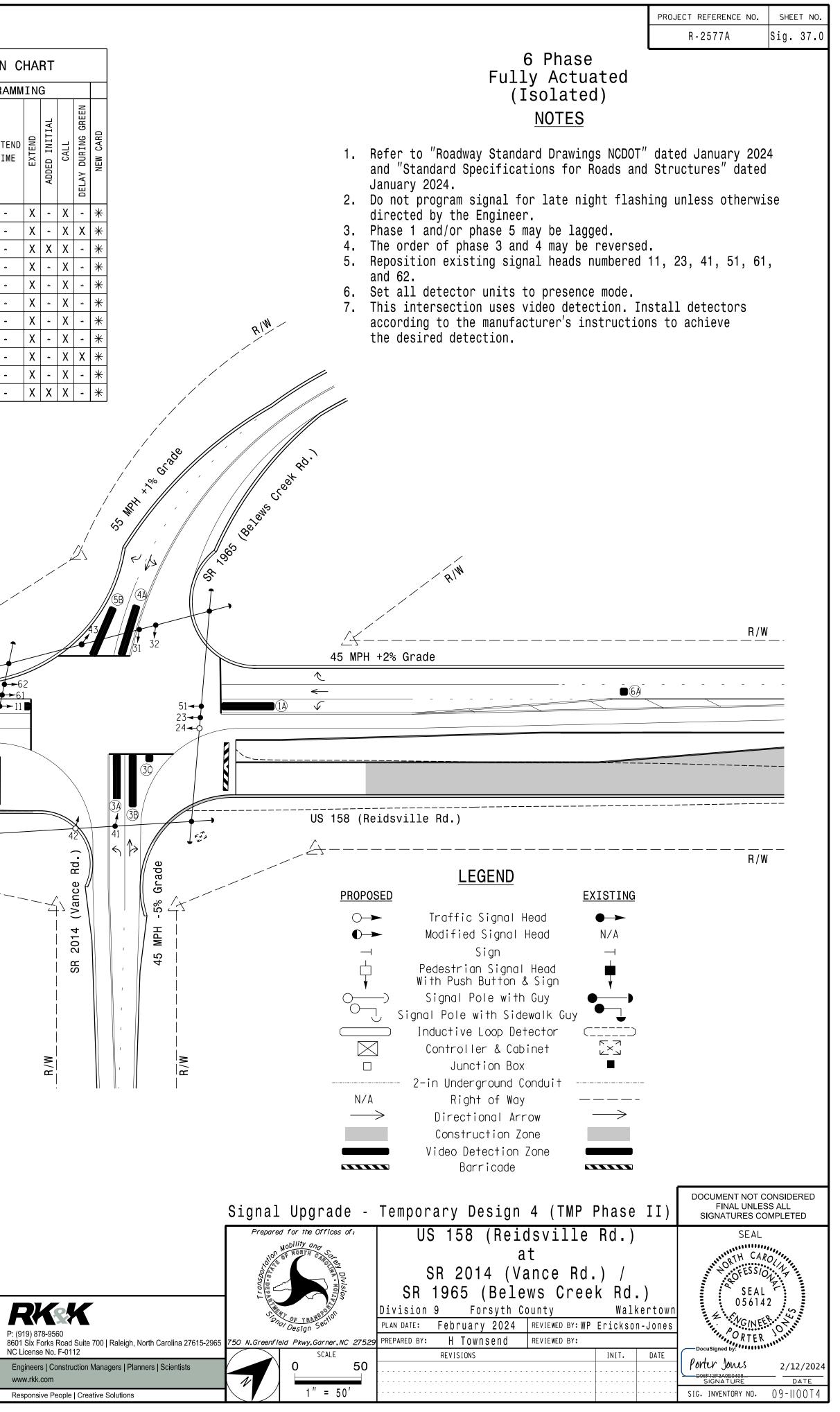
Dual Entry

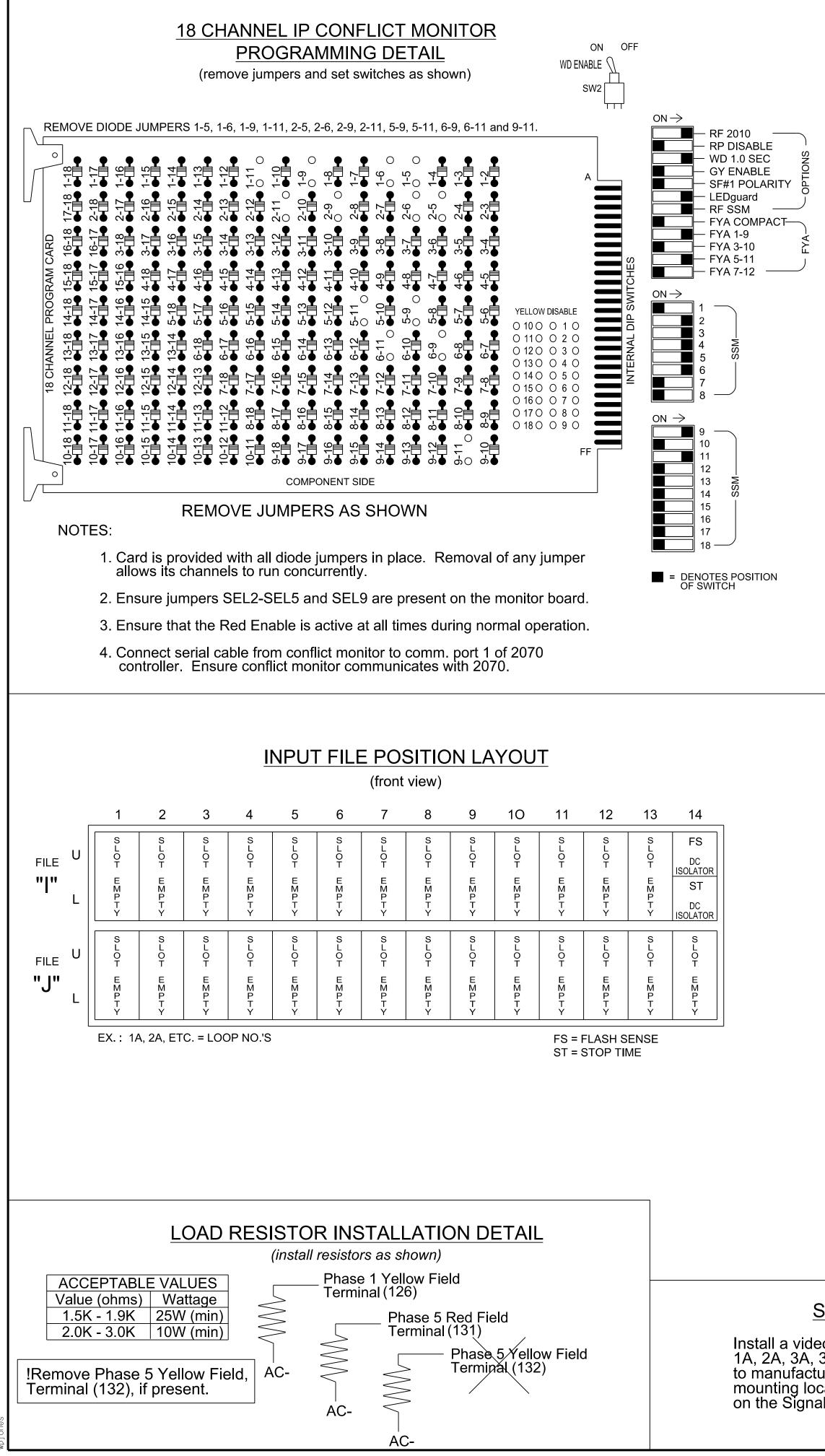
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OF OPERATION														
PHASE		MAXTI	ME DET	ECTOR	I	NSTA	LLAT	ION C	HA	RT				
		DETECTOR PROGRAMMING										T		
$ \begin{array}{cccccccccccccccccccccccccccccccccccc$	ZONE	SIZE (FT)	DISTANCE FROM STOPBAR (FT)	TURNS	NEW LOOP	CALL PHASE		EXTEND TIME	EXTEND	ADDED INITIAL		JEW CARD		
R     R     G $\overrightarrow{R}$ R     Y       R     R     R $\overrightarrow{G}$ R     R						1	15	<u> </u>	X	- X	- DELAY			
R R R G R R	1A *	6X40	0	*	*	6	3	-	X	- X	-	+		
R R R R <u>G</u> R	2A *	6X6	300	*	*	2	<u> </u>	-	X	X X	<u> </u>	*		
R R R G R	3A *	6X40	0	*	*	3	3	<u>.</u>	X	- X		*		
R R R R G R	3B *	6X40	0	*	*	3	10	<u>-</u>	X	- X		*		
← ← ← ← ← ← ← ← ← ← ← ← ← ← ← ← ← ← ←	3C *	6X6	0	*	*		10	<u>-</u>	X	- X		*		
R G R G R R Y	4A *	6X40	0	*	*	4 5	3 15	-	X X	- X - X	-	*		
R G R G R Y	5A *	6X40	0	*	*	2	3		X	- X	-	+ - 1		
	5B *	6X40	0	*	*		15	<u> </u>	X	- X	-	*		
	6A *	6X6	300	*	*	6	<u>-</u>	<u>-</u>	Х	XX	<u> </u>	*		
SIGNAL FACE I.D.	米 Video De	tection Zo	ne											
$\begin{array}{c ccccccccccccccccccccccccccccccccccc$	12"										_/	5B (4)		55 56 56 56
							- 4						2	
	US 15	58 (Rei	ldsville	e Rd.)										
									1					
				<u>∽</u>			5 <b>A</b>	■					51 <b></b> 23 <b></b> 24 <b></b> ♀	
				$\overline{\mathbf{v}}$									24-0	,
												30 3A	7	
		45 MPI	H -3% Gi	rade							Rd . ) 25			<b>•</b>
							A-				ce B		Grade	//





## 1. To prevent "flash-conflict" problems, insert red flash program blocks for all unused vehicle load switches in the output file. The installer shall verify that signal heads flash in accordance with the signal plan.

NOTES

- 2. Program controller to start up in phase 2 Green No Walk and 6 Green No Walk.
- 3. If this signal will be managed by an ATMS software, enable controller and detector logging for all detectors used at this location.

## EQUIPMENT INFORMATION

Controller Cabinet	
Software	Q-Free MAXTIME
Cabinet Mount Output File Positions	18 With Aux. Output File
Load Switches Used	S1, S2, S4, S5, S7, S8, AUX S1, AUX S4
Phases Used Overlap "1"	
Overlap "2" Overlap "3"	NOT USED
Overlap "4"	
*See overlap programming detail on	this sheet.

																							0
	SIGNAL HEAD HOOK-UP CHART																						
LOAD SWITCH NO.	S1	S2	S3		S4			S5			S	7	S8	S9	S10	S11	S12	AUX S1	AUX S2	AUX S3	AUX S4	AUX S5	AUX S6
CMU CHANNEL NO.	1	2	13		3		4		14 5		5	6	15	7	8	16	9	10	1.7	11	12	18	
PHASE	1	2	2 PED		3			4 F		4 PED		5	6	6 PED	7	8	8 PED	OL1	OL2	SPARE	OL3	OL4	SPARE
SIGNAL HEAD NO	★ 11	23;24	NU	31	32	24	41	42,43	62	NU	★ 51	42	61,62	NU	NU	NU	NU	<b>★</b> 11	NU	NU	<b>*</b> 51	NU	NU
RED		128 <sup>.</sup>		116	116		101	101				*	134			·							
YELLOW	*	129		117	117	-	102	102					135						-	-			
GREEN		130		118	118		103	103					136			·							
RED ARROW																		A121			A114		
YELLOW ARROW						117			102			132						A122			A115		
FLASHING YELLOW ARROW																		A123			A116		
GREEN ARROW	127			118		118	103		103		133	133								-			
NU = Not l	Jsed	LI							1	ı													

NU = NOT USED

\*Denotes install load resistor. See load resistor installation detail this sheet. **\***See pictorial of head wiring in detail this sheet.

## **OVERLAP PROGRAMMING**

#### Front Panel

Main Menu >Controller >Overlap >Overlap Parameters/Overlap Timings

Web Interface Home >Controller >Overlap Configuration >Overlaps

Overlap Plan 1

Overlap	1	3
Туре	FYA 4 - Section	FYA 4 - Section
Included Phases	2	6
Modifier Phases	1	5
Modifier Overlaps	-	-
Trail Green	0	0
Trail Yellow	0:0	0.0
Trail Red	0.0	0:0

# SPECIAL DETECTOR NOTE

Install a video detection system for vehicle detection for zones. 1A, 2A, 3A, 3B, 3C, 4A, 5A, 5B and 6A. Perform installation according to manufacturer's directions and NCDOT engineer-approved mounting locations to accomplish the detection schemes shown on the Signal Design Plans.

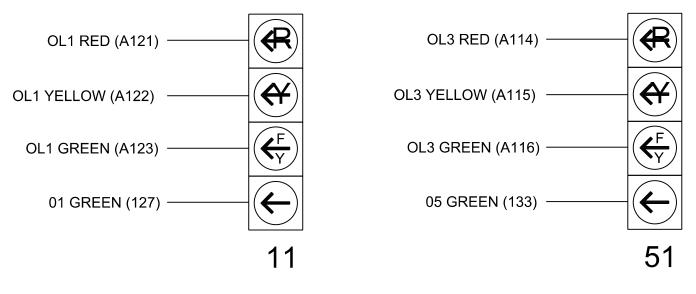
#### RKK P: (919) 878-9560 8601 Six Forks Road Suite 700 | Raleigh, North Carolina 27615-296 NC License No. F-0112 Engineers | Construction Managers | Planners | Scientists www.rkk.com

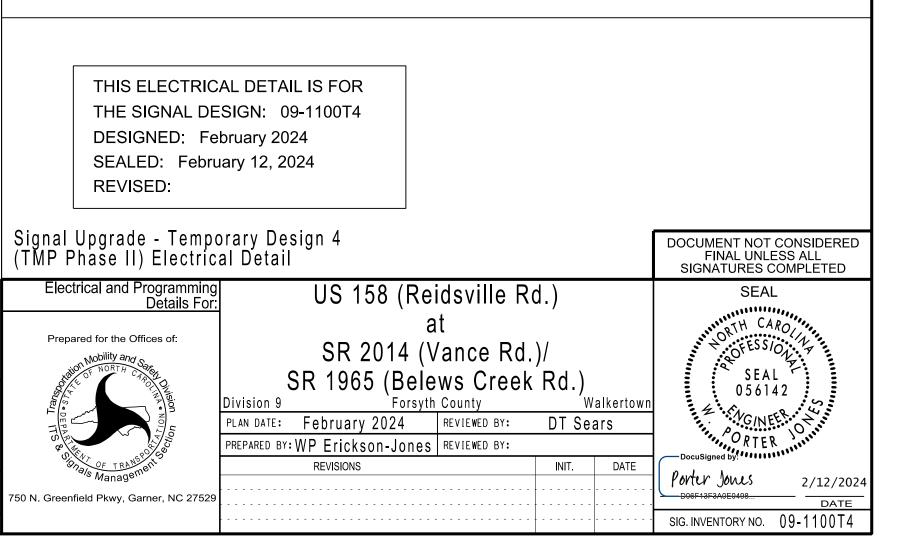
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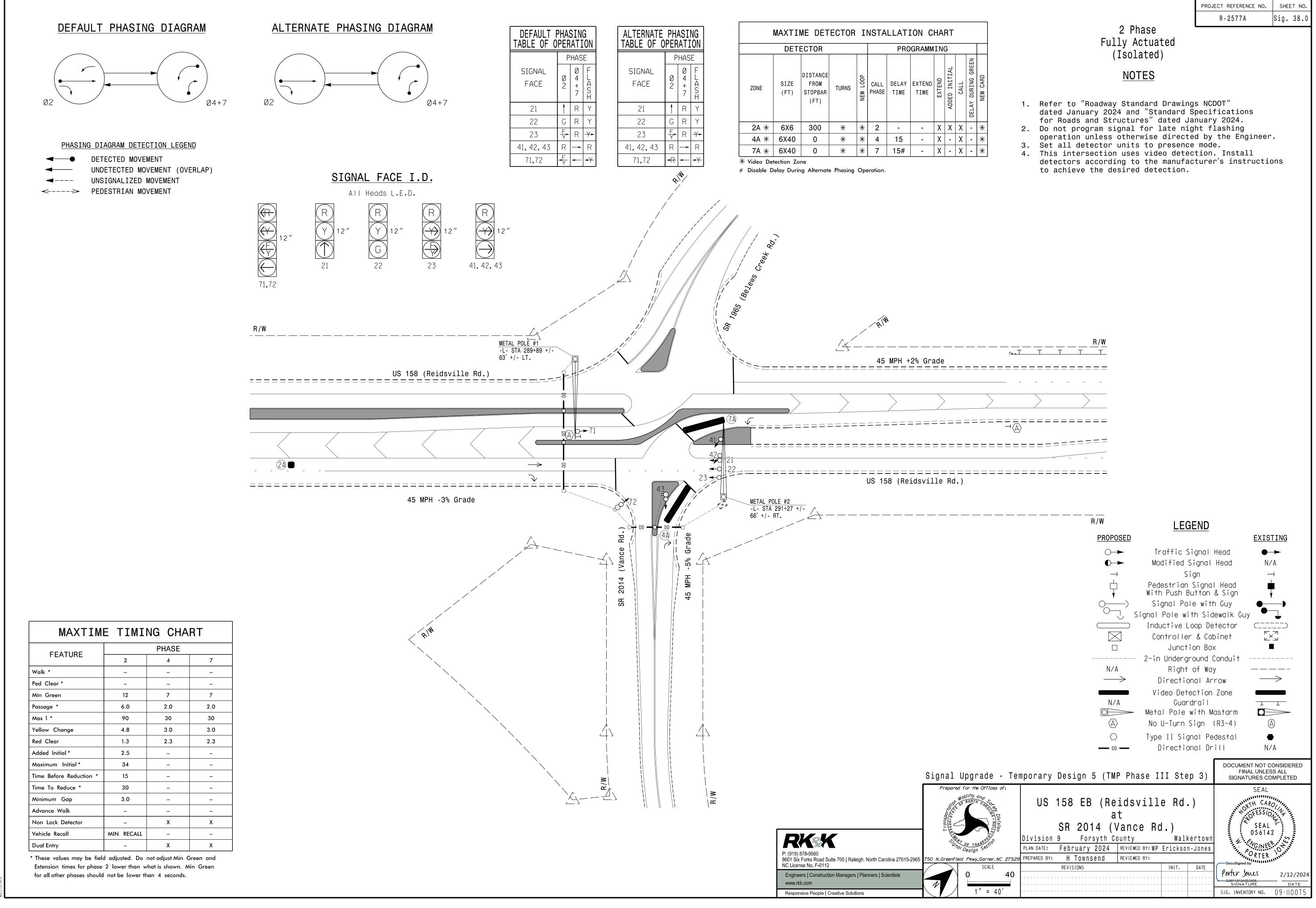
ROJECT REFERENCE NO. SHEET NO. R-2577A Sig. 37.1



(wire signal heads as shown)

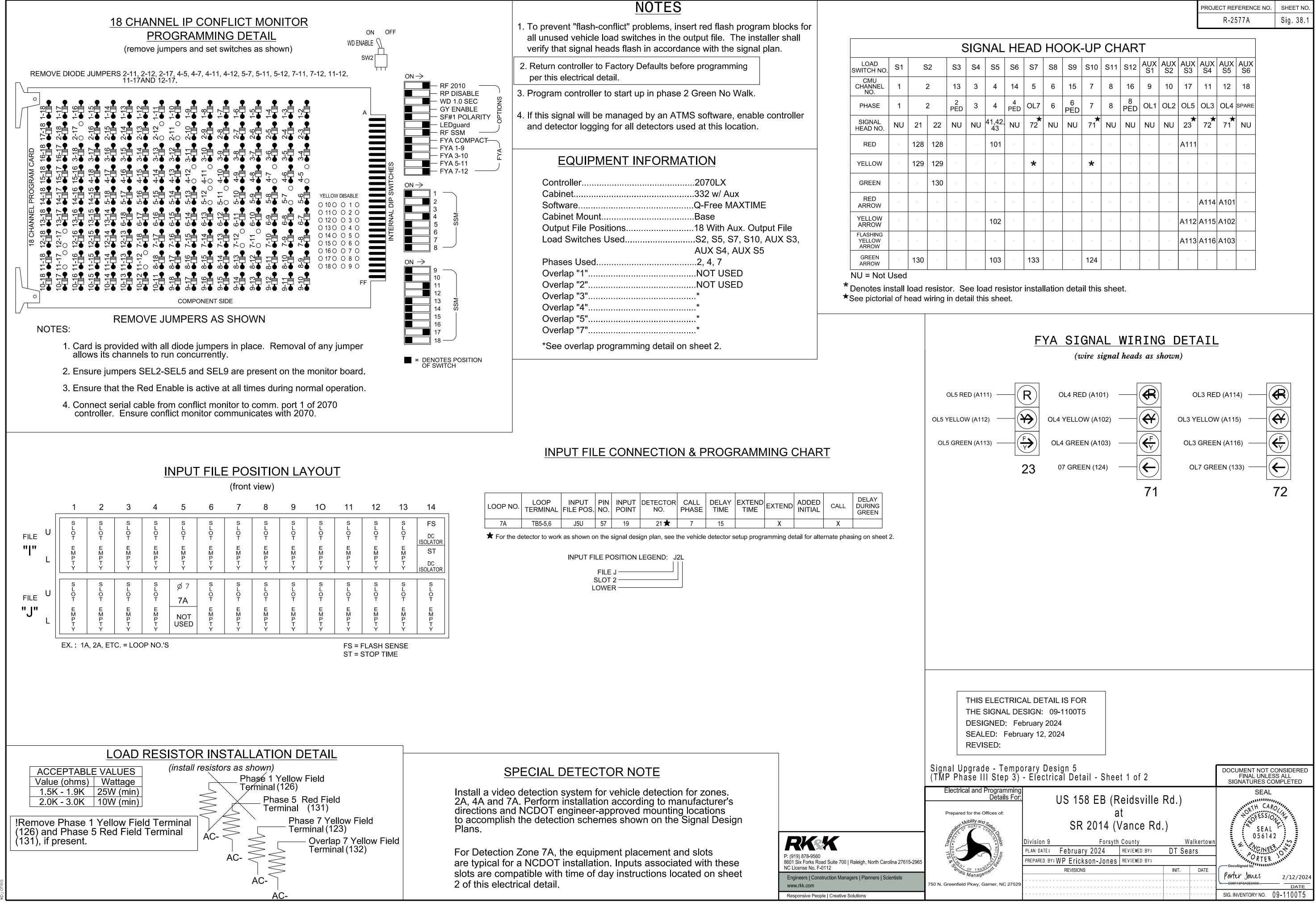






PROJECT REFERENCE NO.		ET NO.
R-2577A	Sia	38.0

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ADDED INITIAL	CALL	DELAY DURING GREEN	NEW CARD	
Х	Х	4	*	
÷	X X X	-	* * *	
÷	Х	÷	*	



LOOP NO.	LOOP TERMINAL	INPUT FILE POS.	PIN NO.	INPUT POINT	DETECTOR NO.	CALL PHASE	DELAY TIME	EXTEND TIME	EXTEND	ADDED INITIAL	CALL	DEL DUR GRE
7:A	TB5-5,6	J5U	57	19	21 ★	7	15		Х		Х	

														PROJE	ECT REF	E NO.	SHEE	T NO.	
															R-25	577A		Sig.	38.1
	S	SIGI	NAL	HE	AD	HO	OK	-UP	CH	IAR	Т								
	S3	S4	S5	S6	S7	S8	S9	S10	S11	S12	AUX S1	AUX S2	AŲX S3	AUX S4	AUX S5	AUX S6			
	13	3	4	14	5	6	15	7	8	16	9	10	1.7	11	12	18			
	2 PED	3	4	4 PÉD	OL7	6	6 PED	7	8	8 PED	OL1	OL2	OL5	OL3		SPARE			
	NU	NU	41 <u>,</u> 42, 43	NU	72	NU	NU	<b>★</b> 71	NU	NU	NU	NU	23 <sup>★</sup>	<b>7</b> 2 ★	★ 71	NU			
		·	101										A111						
	·	·			*			*											
		·												A114	A101				
		·	102										A112	A115	A102				
													A113	A116	A103				
			103		133			124											
_																			

# MAXTIME OVERLAP PROGRAMMING DETAIL FOR DEFAULT PHASING

#### Front Panel

Main Menu >Controller >Overlap >Overlap Parameters/Overlap Timings

Web Interface

Home >Controller >Overlap Configuration >Overlaps

**Overlap Plan 1** 

Overlap	3	4	5	7
Туре	FYA 4 - Section	FYA 4 - Section	FYA 4 - Section	Normal
Included Phases	2	2	2	7
Modifier Phases	<u>-</u>	7	-	-
Modifier Overlaps	7	<u>-</u>	÷	-
Trail Green	0	0	0	0
Trail Yellow	0.0	0.0	0.0	0:0
Trail Red	0.0	0.0	0.0	0.0

NOTICE OVERLAP 7 ASSIGNED TO CHANNEL 5

# MAXTIME OVERLAP PROGRAMMING DETAIL FOR ALTERNATE PHASING

Front Panel

Main Menu >Controller >Overlap >Overlap Parameters/Overlap Timings

Web Interface Home >Controller >Overlap Configuration >Overlaps

In the table view of the web interface, right click on "Overlap" in the top left corner of the table. Copy the entire contents of Overlap Plan 1. Paste Overlap Plan 1 into Overlap Plan 2. Modify Overlap Plan 2 as shown below and save changes.

Overlap Plan 2

Overlap	3	4	5	7	
Туре	FYA 4 - Section	FYA 4 - Section	FYA 4 - Section	Normal	
Included Phases	÷	÷	2	7	
Modifier Phases	÷	7	<u>-</u>	÷	INCLUDED PHASE
Modifier Overlaps	7	÷	<u>-</u>	÷	
Trail Green	0	0	0	0	
Trail Yellow	0.0	0.0	0.0	0.0	
Trail Red	0.0	0.0	0.0	0.0	

# MAXTIME ALTERNATE PHASING PATTERN **PROGRAMMING DETAIL**

Front Panel Main Menu >Controller >Coordination >Patterns

Web Interface Home >Controller >Coordination >Patterns

Pattern Parameters

Veh Det Plan Overlap Plan Pattern \* 2 2

\* The Pattern number(s) are to be determined by the Division and/or City Traffic Engineer.

# OUTPUT CHANNEL CONFIGURATION

Front Panel

Main Menu >Controller >More>Channels>Channels Config

#### Web Interface

Home >Controller >Advanced IO>Channels>Channels Configuration

#### Channel Configuration

Channel	Control Type	Control Source	Flash Yellow	Flash Red	Flash Alt	MMU Channel		ACTIVE PL
1	Phase Vehicle	1		Х	Х	1		
2	Phase Vehicle	2	Х			2		
3	Phase Vehicle	3		Х	Х	3		
4	Phase Vehicle	4		Х	Х	4		
• 5	Overlap	7	Х			5	NOTICE CHANNEL 5	
6	Phase Vehicle	6	Х		Х	6	YELLOW FLASH	]
7	Phase Vehicle	7	Х			7	NOTICE CHANNEL 7	
8	Phase Vehicle	8		Х	Х	8	YELLOW FLASH	
9	Overlap	1	Х		Х	9		
10	Overlap	2		Х	Х	10		
11	Overlap	3	Х		Х	11		
12	Overlap	4	Х		Х	12	NOTICE CHANNEL 12	
13	Phase Ped	2				13	YELLOW FLASH	
14	Phase Ped	4				14		
15	Phase Ped	6				15		
16	Phase Ped	8				16		
17	Overlap	5	Х			17	NOTICE CHANNEL 17	
18	Overlap	6		Х		18	YELLOW FLASH	

## MAXTIME DETECTOR PROGRAMMING DETAIL FOR ALTERNATE PHASING LOOP 7A

#### Front Panel

Main Menu >Controller >Detector >Veh Det Plans

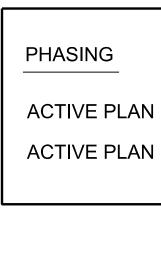
Web Interface Home >Controller >Detector Configuration >Vehicle Detectors

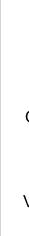
In the table view of web interface right click on "Detector" in the top left corner of the table. Copy the entire contents of Detector Plan 1. Paste Detector Plan 1 into Detector Plan 2. Modify Detector Plan 2 as shown below and save changes.

Plan 2		
Detector	Call Phase	Delay
21	7	0

# MAXTIME ALTERNATE PHASING ACTIVATION DETAIL

To run alternate phasing, select a Pattern that is programmed to run Overlap Plan 2 and Detector Plan 2. A Pattern can be selected through the scheduler or manually by changing the Operational Mode.





IN ORDER TO INSURE THAT SIGNALS FLASH CONCURRENTLY ON THE SAME APPROACH, MAKE THE FOLLOWING FLASHER CIRCUIT CHANGES:

1. ON REAR OF PDA - REMOVE WIRE FROM TERM. T2-4 AND TERMINATE ON T2-2. 2. ON REAR OF PDA - REMOVE WIRE FROM TERM. T2-5 AND TERMINATE ON T2-3. 3. REMOVE FLASHER UNIT 2.

THE CHANGES LISTED ABOVE TIES ALL PHASES AND OVERLAPS TO FLASHER UNIT 1.

		(IMP P
		Elec
		Prepa
P: (919) 878-9560 8601 Six Forks Road Suite 700   Raleigh, NC License No. F-0112	North Carolina 27615-2965	COSUEL ITS &
Engineers   Construction Managers   Pla www.rkk.com	nners   Scientists	750 N. Green
Responsive People   Creative Solutions		

7A

PROJECT REFERENCE NO.	SHEET NO.
R-2577A	Sig. 38.2

	OVERLAP PLAN	VEH DET PLAN
REQUIRED TO RUN DEFAULT PHASING	1	1
REQUIRED TO RUN ALTERNATE PHASING	2	2

ALTERNATE	PHASING	CHANGE	SUMMARY	

THE FOLLOWING IS A SUMMARY OF WHAT TAKES PLACE WHEN **OVERLAP PLAN 2 AND VEHICLE DETECTOR PLAN 2 ACTIVATE** TO CALL THE "ALTERNATE PHASING":

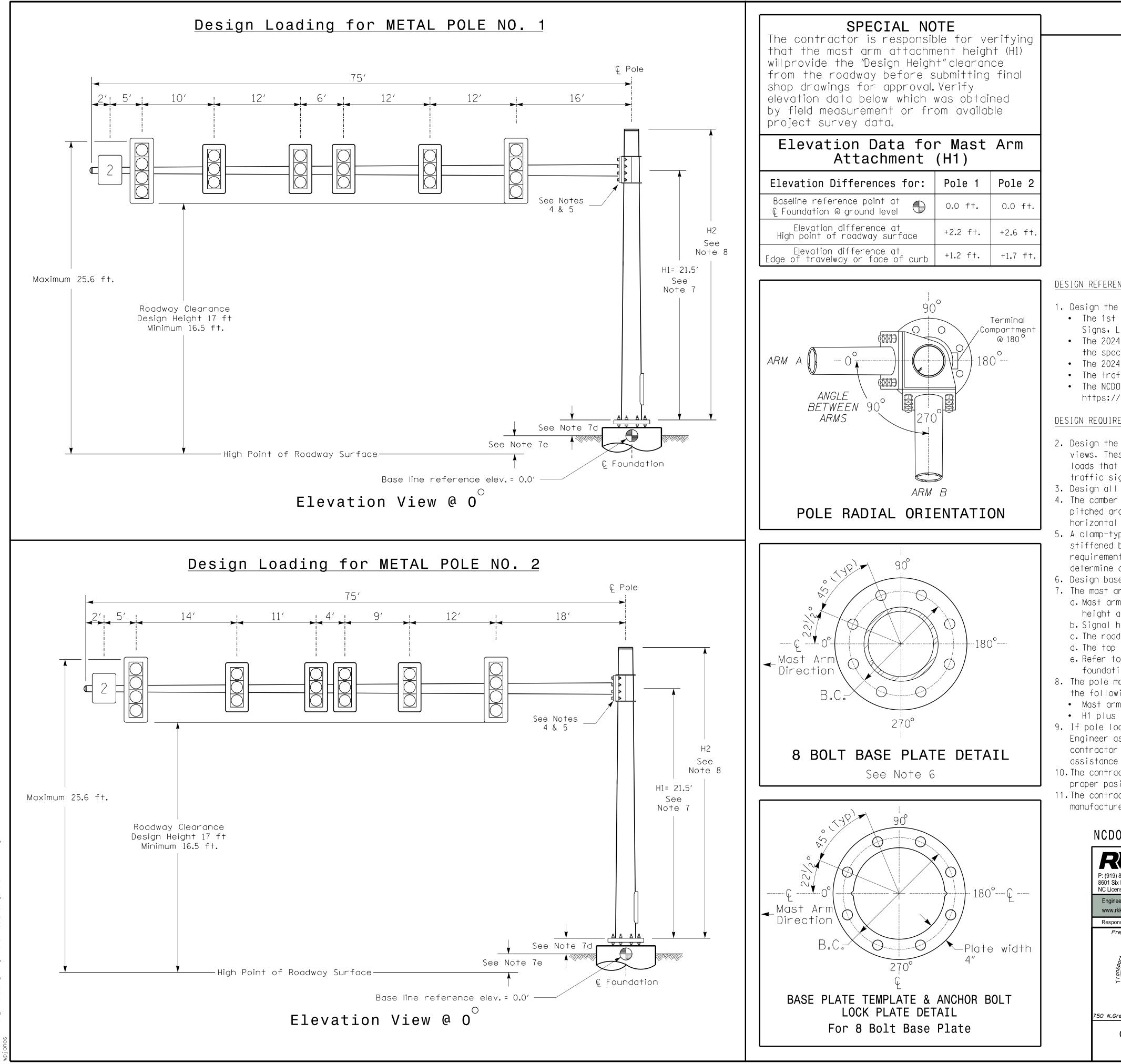
OVERLAP PLAN 2:	Modifies overlap included phases
	for heads 71 and 72 to run protected turns only.

VEH DET PLAN 2: Reduces delay time for phase 7 call on loop 7A to 0 seconds.

# **FLASHER CIRCUIT MODIFICATION DETAIL**

THE SIGNAL DESIGN: 09-1100T5 DESIGNED: February 2024 SEALED: February 12, 2024 **REVISED**: Signal Upgrade - Temporary Design 5 (TMP Phase III Step 3) - Electrical Detail - Sheet 2 of 2 DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED ctrical and Programming Details For: SEAL US 158 EB (Reidsville Rd.) TH CARO at ared for the Offices of: OFESSION SR 2014 (Vance Rd.) SEAL 056142 Forsyth County Walkertown Division 9 Z . MyGINEER. PLAN DATE: February 2024 REVIEWED BY: DT Sears PREPARED BY: WP Erickson-Jones REVIEWED BY: REVISIONS INIT. DATE Porter Jones 2/12/202 nfield Pkwy, Garner, NC 27529 DATE SIG. INVENTORY NO. 09-1100T5

THIS ELECTRICAL DETAIL IS FOR



/2024 raffic#Sianals#Desian#Sianals#091100mp1\_sia\_dsn\_XXXX

## METAL POLE No. 1 AND No. 2

PROJECT REFERENCE NO.

N	IETAL	POLE No. 1	AND No.	2		R - 257	7 A	Sig.38.3
[		MAST ARM I		SCHEDU	F			
	loading symbol	DESCRI		AREA	SIZE	WEIGHT		
		RIGID MOUNTED 12"-3 SECTION-W		9.3 S.F.	25.5″W X 52.5″L	60 LBS		
		RIGID MOUNTER 12"-4 SECTION-W		11.5 S.F.	25.5″W X 66.0″L	74 LBS		
	2	SI RIGID M		7.5 S.F.	30.0″W X 36.0″L	14 LBS		
e Hu 24 i Elu 24 i Control 24 i Control 26 i	dition 2018 minaires, o NCDOT "Star fications o NCDOT Roady ic signal p "Metal Po onnect.ncdo <u>MENTS</u> raffic sig e are antic vill be app hal plans f signal supp design for h where the vhen fully e bolted ma bx connecti s. This req plate with n attachment slope and o they are o ads are ric ads are ric ads are ric ads are ric ads are ric ads are ric attachment /2 of the stion adjus this may a hay contact attachment of is resp for is resp	nal structure and f 5 AASHTO LRFD "Stand and Traffic Signals and ard Specifications can be found in the way Standard Drawing project plans and sp le Standards" locate of.gov/resources/sat lied at the time of or the actual loads orts using force ra the mast arm deflec tip or the free en loaded. st arm-to-pole conn on shown as long as uires staggering th arm connection poin 8 anchor bolt hole t height (H1) shown deflection are not co asumed to offset eo gidly mounted and ve ce height for design base plate is 0.75 ion Data Chart for the evel and the high po will determine the height (H1) plus 2 total height of the tments are required ffect the mast arm the Signal Design	dard Specificati , including all s for Roads and traffic signal gs. Decial provision ed at the follow fety/Pages/ITS-D , the loading cor design loads" ar the installation that will be ap tion should prov- ad of the mast ar nection may be us the connections. ats. es. Provide 2 indo- to a based on the considered in de ach other. ertically center h is based on the considered in de ach other. ertically center h is as shown in feet above the the elevation di oint of the road total height (H2 feet, or mast arm attach , the contractor lengths and arm Section Senior S ng that the mast er the roadway. ng soil penetra	ons for Str of the late Structures, project spe as, ding NCDOT we esign-Resound and may not me on. The composited at the texceed 0.9 vide an apper of does not sed instead meets all of Use elevat ch x 60 incles to the elevat of the elevat of the elevat of the elevat stormining the of each me termining the of each me tarm length tion testing	uctural st inte "The l cial pr ebsite: cebsite:	erim revi atest ad covisions the elevent of the according of the according of a low t below welded r design a for eaccording welded r design a for eaccording the prop ing the g s 1 foot. al from t s. The r for will all	sions. denda to denda to v tual refer to nstallo v ting ch arm t ons: nt osed greater	o the tion. of
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th Carolina 27615-2965						
rs   Scientists					FINAL UNLES	SS ALL
					SIGNATURES CC	MPLETED
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REV	ISTONS		INIT.	DATE	Porter Jours DOGET13F3ADE0408 SIGNATURE SIG. INVENTORY NO.	2/12/2024 
	rs   Scientists US 15 SF Division 9 PLAN DATE: Fet PREPARED BY:WP E	th Carolina 27615-2965 rs   Scientists US 158 EB (Re a SR 2014 (\ Division 9 Forsyth Co PLAN DATE: February 2024	th Carolina 27615-2965 rs   Scientists US 158 EB (Reidsville at SR 2014 (Vance Rd Division 9 Forsyth County PLAN DATE: February 2024 REVIEWED BY: PREPARED BY:WP Erickson-Jones REVIEWED BY:	th Carolina 27615-2965 rs   Scientists US 158 EB (Reidsville Rd. at SR 2014 (Vance Rd.) Division 9 Forsyth County Walk PLAN DATE: February 2024 Reviewed By: DT Sears PREPARED BY:WP Erickson-Jones Reviewed By:	th Carolina 27615-2965 rs   Scientists US 158 EB (Reidsville Rd.) at SR 2014 (Vance Rd.) Division 9 Forsyth County Walkertown PLAN DATE: February 2024 REVIEWED BY: DT Sears PREPARED BY:WP Erickson-Jones REVIEWED BY:	th Carolina 27615-2965 rs   Scientists  DOCUMENT NOT C FINAL UNLESS SIGNATURES CO SEAL SEAL SEAL SEAL SEAL SEAL SEAL SEAL

DEFAULT PHASING DIAGRAM ALTERNATE PHASING DIAGRAM PHASING DIAGRAM DETECTION LEGEND DETECTED MOVEMENT **---**UNDETECTED MOVEMENT (OVERLAP) SIGNAL FACE I.D. UNSIGNALIZED MOVEMENT **----**PEDESTRIAN MOVEMENT <----> All Heads L.E.D. R (Y G  $\overleftarrow{}$ 23 21 22 R/W -----MAXTIME TIMING CHART PHASE FEATURE 7 2 4 Walk \* \_ \_ \_ Ped Clear \* \_ \_ \_ Min Green 12 7 7 6.0 2.0 2.0 Passage \* Max 1 \* 90 30 30 3.0 4.8 3.0 Yellow Change Red Clear 1.3 2.3 2.3 Added Initial \* 1.5 \_ \_ Maximum Initial \* 34 \_ \_ Time Before Reduction 15 \_ \_ Time To Reduce \* 30 — \_ 3.0 Minimum Gap \_ \_ Advance Walk \_ \_ \_ Х Non Lock Detector Х \_ Vehicle Recall MIN RECALL \_ -

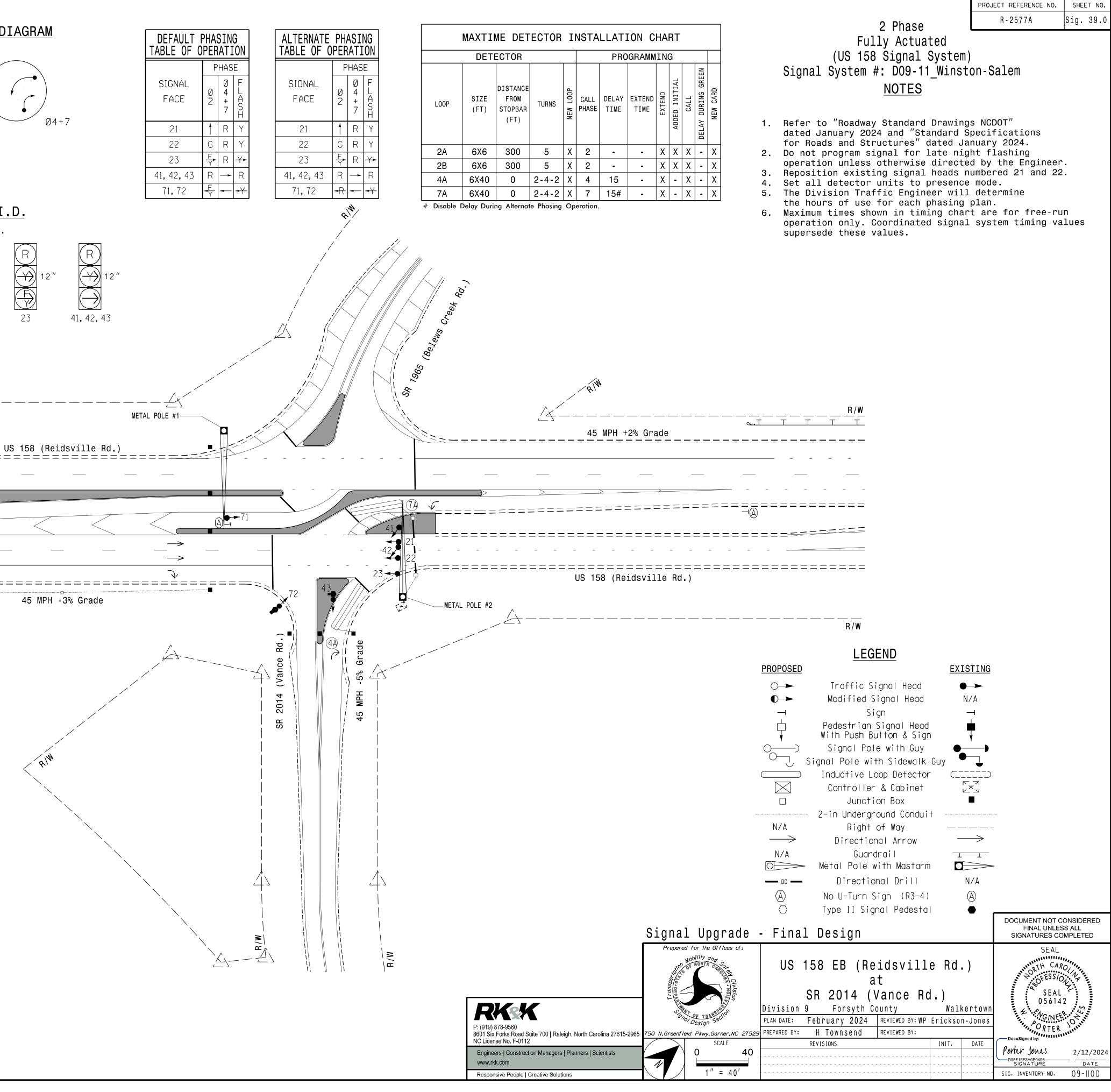
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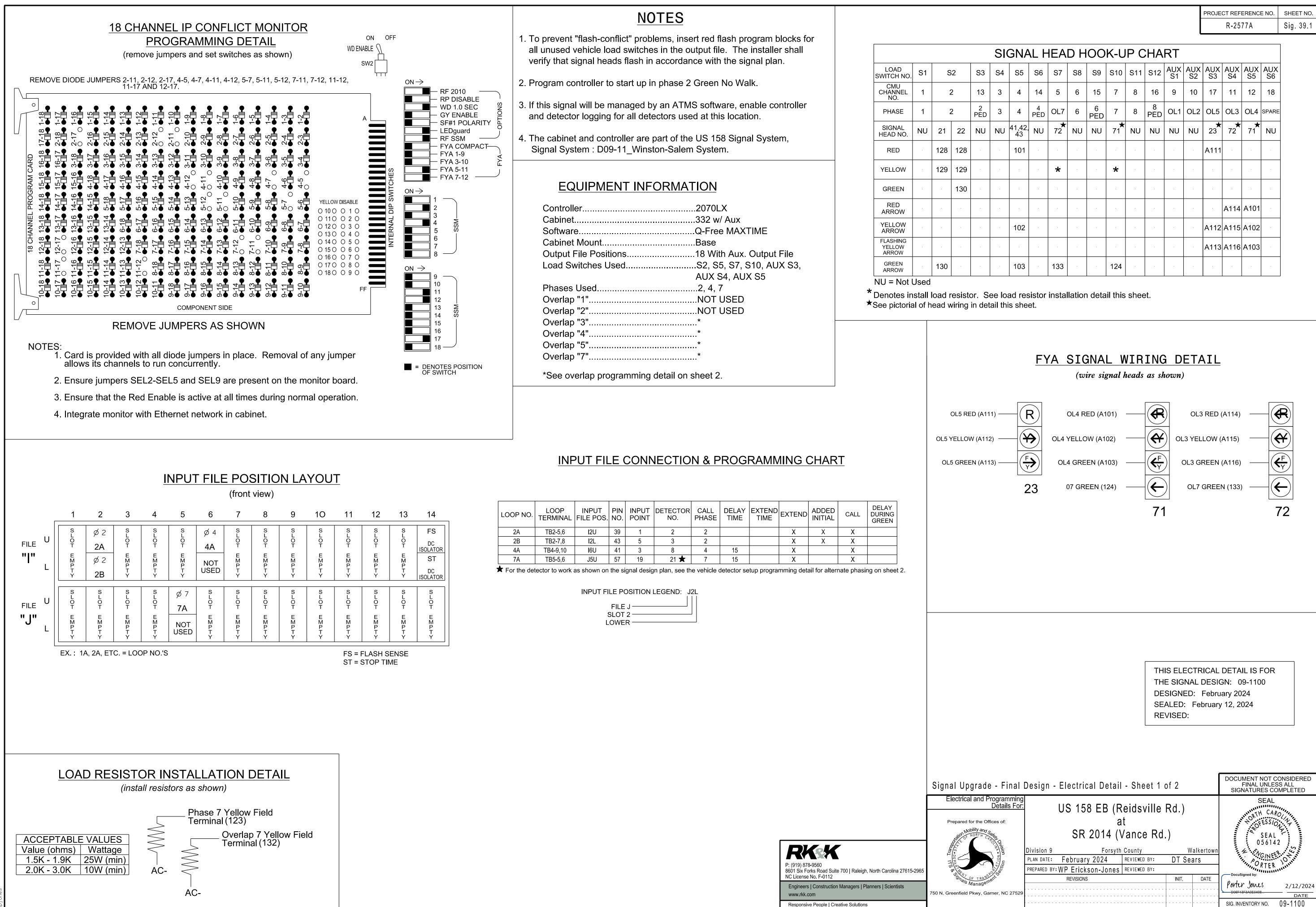
\* These values may be field adjusted. Do not adjust Min Green and Extension times for phase 2 lower than what is shown. Min Green for all other phases should not be lower than 4 seconds.

—

Dual Entry



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à				
ADDED INITIAL	CALL	DELAY DURING GREEN	NEW CARD	
Х	Х	<b></b>	Х	
X X	X X X	÷	X X X X	
<u>.</u>	Х	÷	Х	
<u>.</u>	Х	÷	Х	



LOOP NO.	LOOP TERMINAL	INPUT FILE POS.	PIN NO.	INPUT POINT	DETECTOR NO.	CALL PHASE	DELAY TIME	EXTEND TIME	EXTEND	ADDED INITIAL	CALL	DELAY DURING GREEN
2A	TB2-5,6	I2U	39	1	2	2			Х	Х	Х	
2B	TB2-7,8	I2L	43	5	3	2			Х	Х	Х	
4A	TB4-9,10	I6U	41	3	8	4	15		Х		Х	
7A	TB5-5,6	J5U	57	19	21 ★	7	15		Х		Х	

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															R-25	77A		Sig. 3	39.1
													•				ł		
			SI	GNA	∖L F	IEA	DΗ	00	K-U	P C	ΉА	RT							
S	2	S3	S4	S5	S6	S7	S8	S9	S10	S11	S12	AUX S1	AUX S2	AUX S3	AUX S4	AUX S5	AŲX S6		
2	2	13	3	4	14	5	6	15	7	8	16	9	10	17	11	12	18		
2	2	2 PED	3	4	4 PÉD	OL7	6	6 PED	7	8	8 PÉD	OL1	OL2	OL5	OL3	OL4	SPARE		
	22	NU	NU	41 <u>,</u> 42, 43	NU	★ 72	NU	NU	<b>★</b> 71	NU	NU	NU	NU	23 <sup>★</sup>	★ 72	<b>7</b> 1★	NU		
3	128			101										A111					
Э	129		·			*			*										
	130						-												
															A114	A101			
				102										A112	A115	A102			
								·		÷	·		·	A113	A116	A103			
)				103		133			124										
_																		1	



# MAXTIME OVERLAP PROGRAMMING DETAIL FOR DEFAULT PHASING

#### Front Panel

Main Menu >Controller >Overlap >Overlap Parameters/Overlap Timings

Web Interface

Home >Controller >Overlap Configuration >Overlaps

**Overlap Plan 1** 

Overlap	3	4	5	7
Туре	FYA 4 - Section	FYA 4 - Section	FYA 4 - Section	Normal
Included Phases	2	2	2	7
Modifier Phases	÷	7	-	-
Modifier Overlaps	7	<u>-</u>	÷	<u>-</u>
Trail Green	0	0	0	0
Trail Yellow	0.0	0:0	0.0	0:0
Trail Red	0.0	0.0	0.0	0.0

NOTICE OVERLAP 7 ASSIGNED TO CHANNEL 5

# MAXTIME OVERLAP PROGRAMMING DETAIL FOR ALTERNATE PHASING

Front Panel

Main Menu >Controller >Overlap >Overlap Parameters/Overlap Timings

Web Interface

Home >Controller >Overlap Configuration >Overlaps

In the table view of the web interface, right click on "Overlap" in the top left corner of the table. Copy the entire contents of Overlap Plan 1. Paste Overlap Plan 1 into Overlap Plan 2. Modify Overlap Plan 2 as shown below and save changes.

Overlap Plan 2

Overlap	3	4	5	7	
Туре	FYA 4 - Section	FYA 4 - Section	FYA 4 - Section	Normal	
Included Phases	÷	÷	2	7	
Modifier Phases	÷	7	÷	<u>-</u>	INCLUDED PHASE
Modifier Overlaps	7	÷	÷	÷	
Trail Green	0	0	0	0	
Trail Yellow	0.0	0.0	0.0	0.0	
Trail Red	0.0	0.0	0.0	0.0	

# MAXTIME ALTERNATE PHASING PATTERN **PROGRAMMING DETAIL**

2

Front Panel Main Menu >Controller >Coordination >Patterns

Web Interface

Home >Controller >Coordination >Patterns

\* 2

Pattern Parameters Pattern Veh Det Plan Overlap Plan

**\*** The Pattern number(s) are to be determined by the Division and/or City Traffic Engineer.

# OUTPUT CHANNEL CONFIGURATION

#### Front Panel

Main Menu >Controller >More>Channels>Channels Config

#### Web Interface

Home >Controller >Advanced IO>Channels>Channels Configuration

#### Channel Configuration

[					1		1	ACTIVE PLAN R
Channel	Control Type	Control Source	Flash Yellow	Flash Red	Flash Alt	MMU Channel		ACTIVE PLAN R
1	Phase Vehicle	1		Х	Х	1		
2	Phase Vehicle	2	Х			2		
3	Phase Vehicle	3		Х	Х	3		
4	Phase Vehicle	4		Х	Х	4		
5	Overlap	7	Х			5	NOTICE CHANNEL 5 YELLOW FLASH	
6	Phase Vehicle	6	Х		Х	6		
7	Phase Vehicle	7	Х			7		
8	Phase Vehicle	8		Х	Х	8	YELLOW FLASH	
9	Overlap	1	Х		Х	9		-
10	Overlap	2		Х	Х	10		
11	Overlap	3	Х		Х	11	]	
12	Overlap	4	Х		Х	12		
13	Phase Ped	2				13	YELLOW FLASH	C
14	Phase Ped	4				14		
1.5	Phase Ped	6				15		
16	Phase Ped	8				16		
1.7	Overlap	5	Х			17	NOTICE CHANNEL 17	
18	Overlap	6		Х		18	YELLOW FLASH	
18	Overlap	6		X		18		

## MAXTIME DETECTOR PROGRAMMING DETAIL FOR ALTERNATE PHASING LOOP 7A

#### Front Panel

Main Menu >Controller >Detector >Veh Det Plans

Web Interface

Home >Controller >Detector Configuration >Vehicle Detectors

In the table view of web interface right click on "Detector" in the top left corner of the table. Copy the entire contents of Detector Plan 1. Paste Detector Plan 1 into Detector Plan 2. Modify Detector Plan 2 as shown below and save changes.

Plan 2

Detector	Call Phase	Delay
21	7	0



			THIS ELECTRICA THE SIGNAL DES DESIGNED: Febr SEALED: Februa REVISED:	IGN: 09-1100 ruary 2024
	Signal Upgrade - Final D	esign - Electrical Detail - Shee	et 2 of 2	DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED
	Electrical and Programming Details For: Prepared for the Offices of:		ville Rd.)	SEAL NORTH CARO
RKK	Nobility and Sacar Division	Division 9 Forsyth County PLAN DATE: February 2024 REVIEWED	Walkertowr	SEAL 056142
P: (919) 878-9560 8601 Six Forks Road Suite 700   Raleigh, North Carolina 27615-2965 NC License No. F-0112	TS & GALLER TEAMSON	PREPARED BY: WP Erickson-Jones REVIEWED REVISIONS		DocuSigned by:
Engineers   Construction Managers   Planners   Scientists www.rkk.com	750 N. Greenfield Pkwy, Garner, NC 27529			Porter Jones 2/12/2024 
Responsive People   Creative Solutions				- SIG. INVENTORY NO. 09-1100

# MAXTIME ALTERNATE PHASING ACTIVATION DETAIL

To run alternate phasing, select a Pattern that is programmed to run Overlap Plan 2 and Detector Plan 2. A Pattern can be selected through the scheduler or manually by changing the Operational Mode.

PHASING

ACTIVE PLAN REC

RE

OVE

IN ORDER TO INSURE THAT SIGNALS FLASH CONCURRENTLY ON THE SAME APPROACH, MAKE THE FOLLOWING FLASHER CIRCUIT CHANGES:

1. ON REAR OF PDA - REMOVE WIRE FROM TERM. T2-4 AND TERMINATE ON T2-2. 2. ON REAR OF PDA - REMOVE WIRE FROM TERM. T2-5 AND TERMINATE ON T2-3. 3. REMOVE FLASHER UNIT 2.

PROJECT REFERENCE NO.	SHEET NO.
R-2577A	Sig. 39.2

	OVERLAP PLAN	VEH DET PLAN
EQUIRED TO <u>RUN DEFAULT PHASING</u>	1	1
EQUIRED TO RUN ALTERNATE PHASING	2	2

ALTERNATE	PHASING	CHANGE	SUMMARY	
		OHANOL		

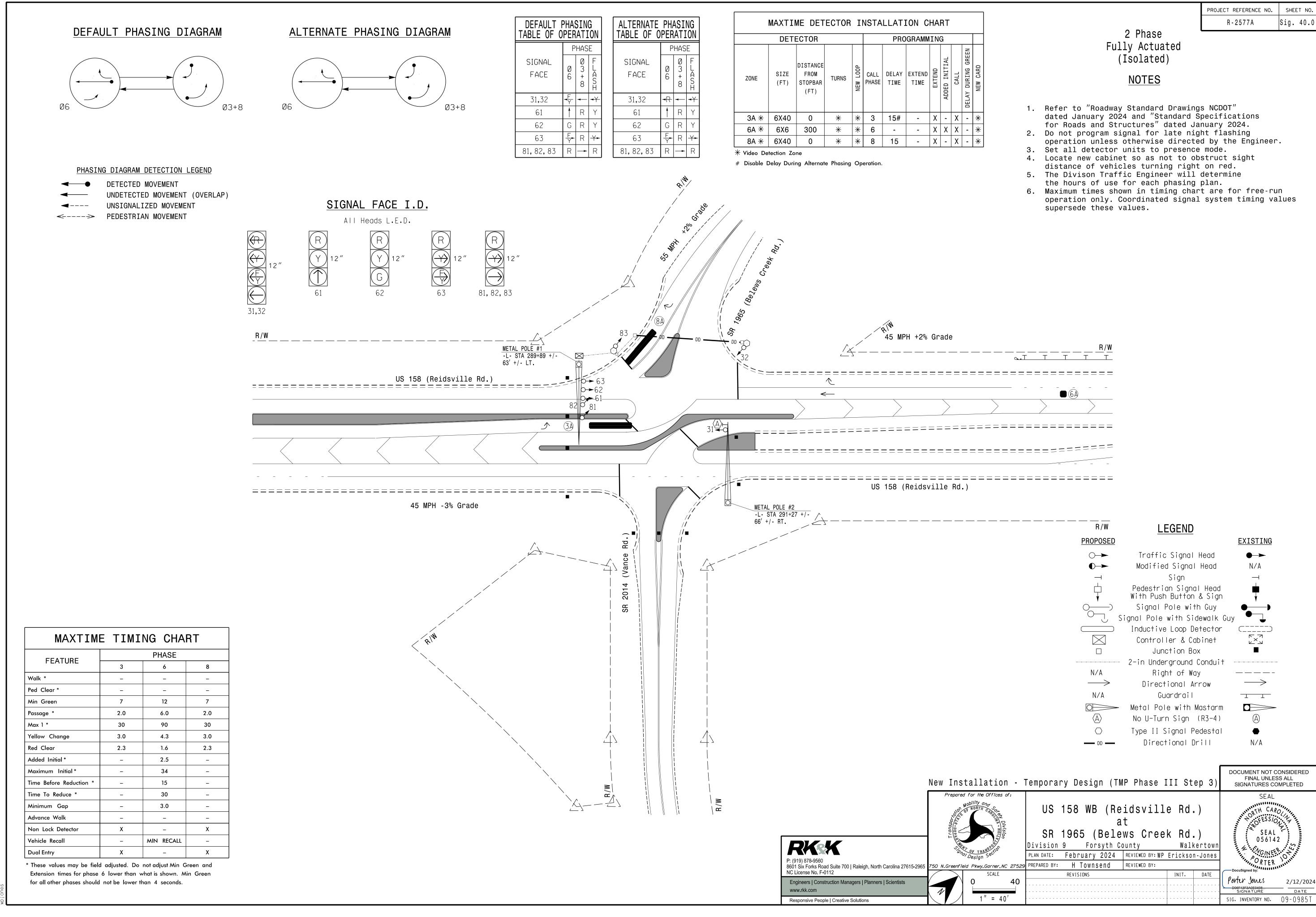
THE FOLLOWING IS A SUMMARY OF WHAT TAKES PLACE WHEN **OVERLAP PLAN 2 AND VEHICLE DETECTOR PLAN 2 ACTIVATE** TO CALL THE "ALTERNATE PHASING":

/ERLAP PLAN 2:	Modifies overlap included phases
	for heads 71 and 72 to run protected turns only.

VEH DET PLAN 2: Reduces delay time for phase 7 call on loop 7A to 0 seconds.

# **FLASHER CIRCUIT MODIFICATION DETAIL**

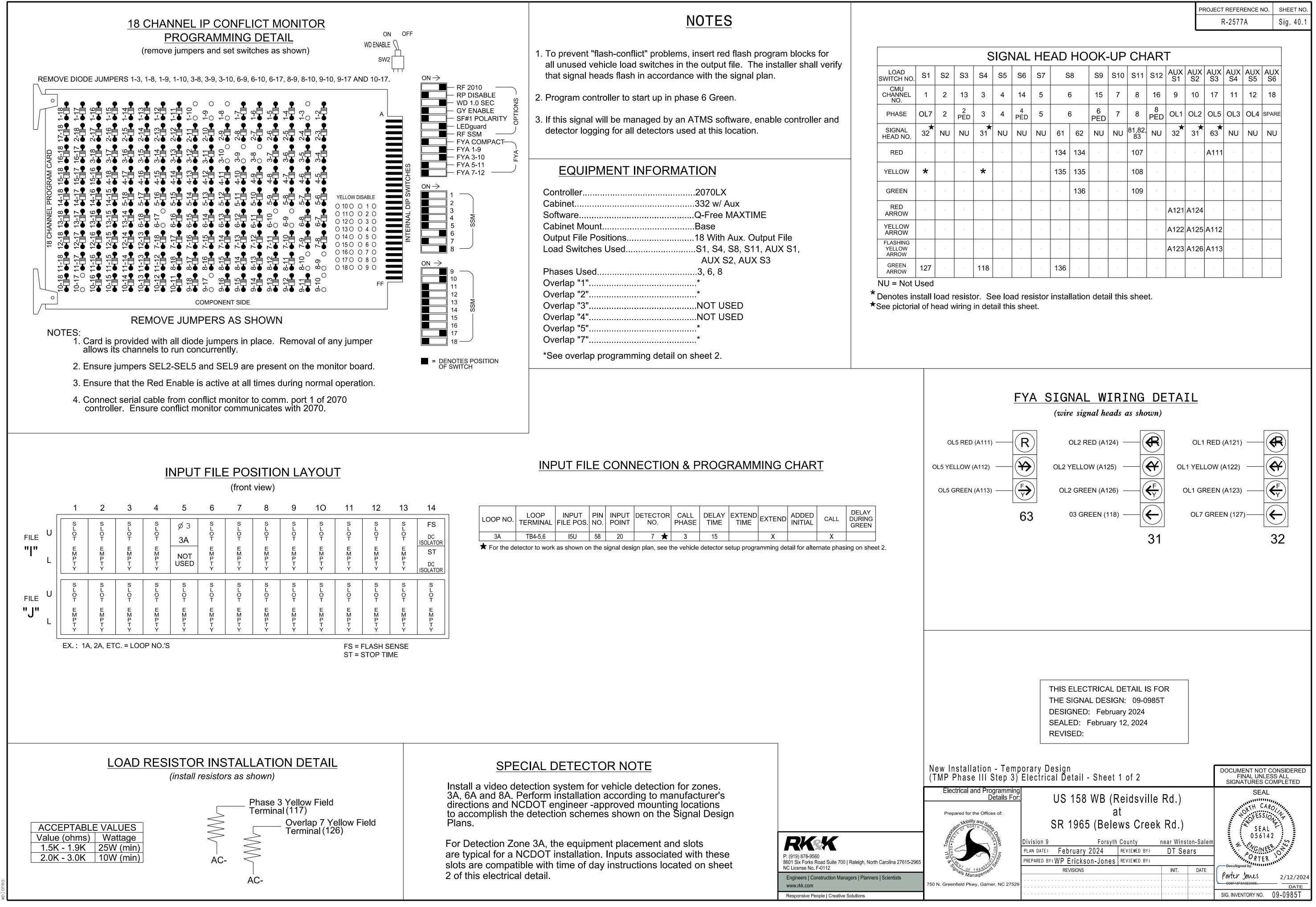
### THE CHANGES LISTED ABOVE TIES ALL PHASES AND OVERLAPS TO FLASHER UNIT 1.



PROJECT REFERENCE NO.	SHEE	ET NO.
R - 2577A	Sig.	40.0

- operation only. Coordinated signal system timing values

Installation -	Temporary Design (TM	MP Phase III Step 3)	DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED
Prepared for the Offices of:	US 158 WB (Re	eidsville Rd.) It	SEAL OFESSION
Colision Colision	Division 9 Forsyth C		SEAL 056142
Design Sect	PLAN DATE: February 2024	REVIEWED BY: WP Erickson-Jones	ORTER STATES
Greenfield Pkwy,Garner,NC 27529	PREPARED BY: H TOWNSEND	REVIEWED BY:	DocuSigned by:
0 SCALE 40	REVISIONS	INIT. DATE	Porter Jones 2/12/2024
			SIGNATURE DATE
/ 1″ = 40′			SIG. INVENTORY NO. 09-0985T



LOOP NO.	LOOP TERMINAL	INPUT FILE POS.	PIN NO.	INPUT POINT	DETECTOR NO.	CALL PHASE	DELAY TIME	EXTEND TIME		ADDED INITIAL	CALL	DELAY DURING GREEN
3A	TB4-5,6	I5U	58	20	7 ★	3	15		Х		Х	

PROJECT REFERENCE NO.	SHEET NO
R-2577A	Sig. 40.

		S	SIGN	JAL	ΗE	AD	HO	OK-	-UP	CH	AR <sup>-</sup>	Т					
2	S3	S4	S5	S6	S7	S	8	S9	S10	S11	S12	AUX S1	AUX S2	AUX S3	AUX S4	AUX S5	AUX S6
2	13	3	4	14	5	6	6	15	7	8	16	9	10	17	11	12	18
2	2 PED	3	4	4 PED	5	6	6	6 PED	7	8	8 PÉD	OL1	OL2	OL5	OL3	OL4	SPARE
U	NU	★ 31	NU	NU	NU	61	62	NU	NU	81 <u>,</u> 82, 83	NU	32 <sup>★</sup>	<b>★</b> 31	<b>★</b> 63	NU	NU	NU
						134	134			107				A111			
		*				135	135			108				-	-		
						-	136			109				-			
												A121	A124				
						-						A122	A125	A112			
												A123	A126	A113			
		118				136											

# MAXTIME OVERLAP PROGRAMMING DETAIL FOR DEFAULT PHASING

Front Panel

Main Menu >Controller >Overlap >Overlap Parameters/Overlap Timings

Web Interface

Home >Controller >Overlap Configuration >Overlaps

Overlap Plan 1

Overlap	1	2	5	7
Туре	FYA 4 - Section	FYA 4 - Section	FYA 4 - Section	Normal
Included Phases	6	6	6	3
Modifier Phases	-	3	-	÷
Modifier Overlaps	7	÷	<u>-</u>	-i
Trail Green	0	0	0	0
Trail Yellow	0.0	0:0	0:0	0.0
Trail Red	0.0	0:0	0:0	0.0

NOTICE OVERLAP 7 ASSIGNED TO CHANNEL 1.

# MAXTIME OVERLAP PROGRAMMING DETAIL FOR ALTERNATE PHASING

Front Panel

Main Menu >Controller >Overlap >Overlap Parameters/Overlap Timings

Web Interface Home >Controller >Overlap Configuration >Overlaps

In the table view of the web interface, right click on "Overlap" in the top left corner of the table. Copy the entire contents of Overlap Plan 1. Paste Overlap Plan 1 into Overlap Plan 2. Modify Overlap Plan 2 as shown below and save changes.

Overlap Plan 2

Overlap	1	2	5	7	
Туре	FYA 4 - Section	FYA 4 - Section	FYA 4 - Section	Normal	
Included Phases	÷	÷	6	3	
Modifier Phases	<u>-</u>	3	<u>-</u>	<u>-</u>	INCLUDED PHASE
Modifier Overlaps	7	÷	-	÷	
Trail Green	0	0	0	0	
Trail Yellow	0.0	0.0	0.0	0.0	
Trail Red	0.0	0.0	0.0	0.0	

# MAXTIME ALTERNATE PHASING PATTERN **PROGRAMMING DETAIL**

Front Panel Main Menu >Controller >Coordination >Patterns Web Interface

Home >Controller >Coordination >Patterns

Pattern Parameters Veh Det Plan Overlap Plan Pattern 2 \* 2

\* The Pattern number(s) are to be determined by the Division and/or City Traffic Engineer.

## OUTPUT CHANNEL CONFIGURATION

Front Panel

Main Menu >Controller >More>Channels>Channels Config

Web Interface

Home >Controller >Advanced IO>Channels>Channels Configuration

#### **Channel Configuration**

	MMU Channel	Flash Alt	Flash Red	Flash Yellow	Control Source	Control Type	Channel
YELLOW FLA	1 1	Х		Х	7	Overlap	1
NOTICE CHANNE	2			Х	2	Phase Vehicle	2
YELLOW FLA	3	Х		Х	3	Phase Vehicle	3
	4		Х		4	Phase Vehicle	4
	5		Х		5	Phase Vehicle	5
	6	Х		X	6	Phase Vehicle	6
	7		Х		7	Phase Vehicle	7
	8		Х		8	Phase Vehicle	8
	9			Х	1	Overlap	9
YELLOW FLA	10			Х	2	Overlap	10
	11			Х	3	Overlap	11
	12		Х		4	Overlap	12
	13				2	Phase Ped	13
	14				4	Phase Ped	14
	15				6	Phase Ped	15
	16				8	Phase Ped	16
YELLOW FLA	17	Х		Х	5	Overlap	17
	18		Х		6	Overlap	18

MAXTIME DETECTOR PROGRAMMING DETAIL FOR ALTERNATE PHASING LOOP 3A

Front Panel Main Menu >Controller >Detector >Veh Det Plans

Web Interface Home >Controller >Detector Configuration >Vehicle Detectors

In the table view of web interface right click on "Detector" in the top left corner of the table. Copy the entire contents of Detector Plan 1. Paste Detector Plan 1 into Detector Plan 2. Modify Detector Plan 2 as shown below and save changes.

Plan 2

3A

Detector	Call Phase	Delay
7	3	0

## FLASHER

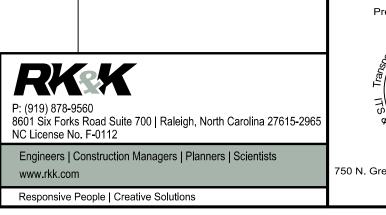
IN ORDER TO INSUF SAME APPROACH,

1. ON REAR OF PDA - REM

2. ON REAR OF PDA - REM

3. REMOVE FLASHER UNI

THE CHANGES LISTED ABO



# MAXTI

To run alternate A Pattern can be

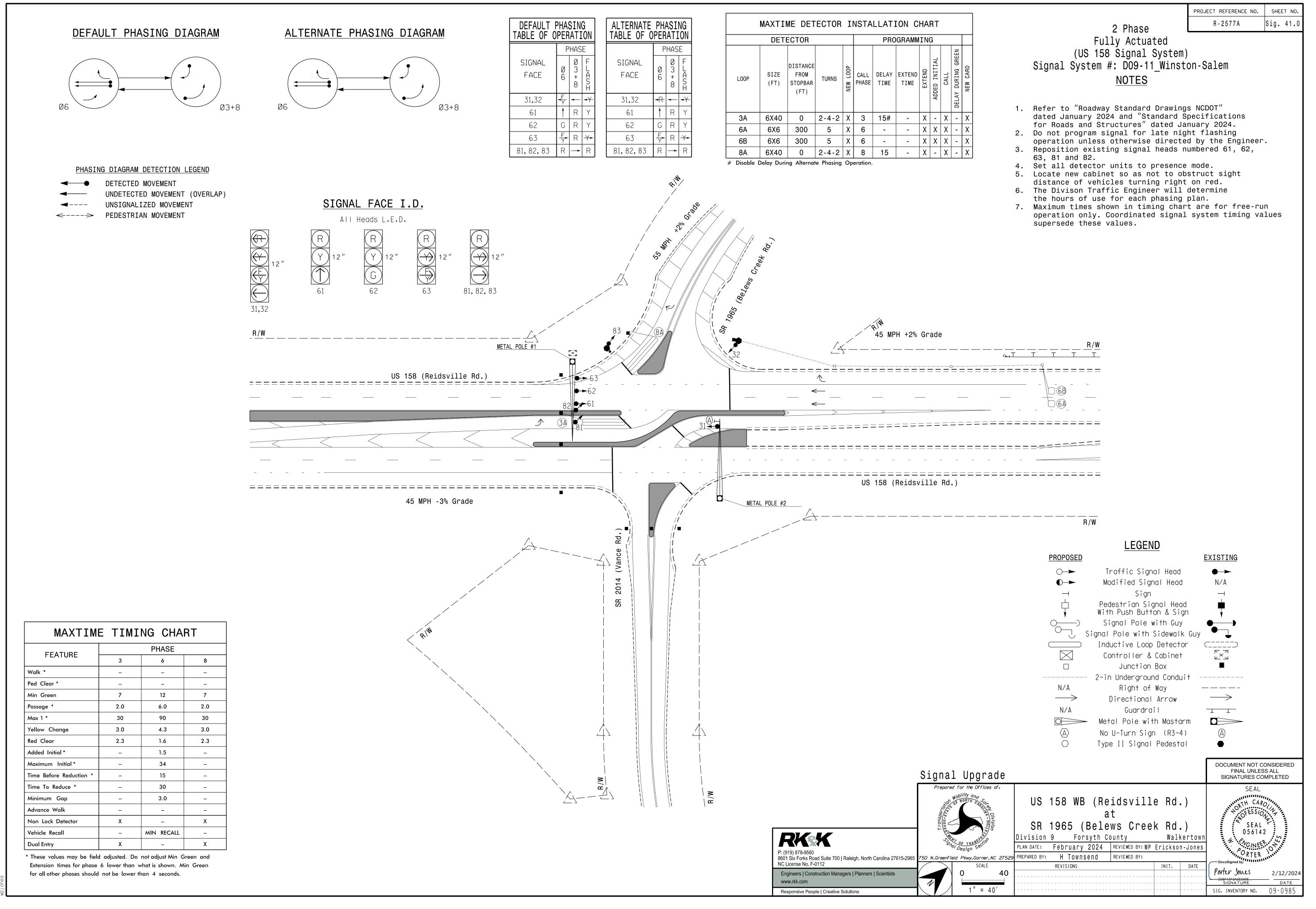
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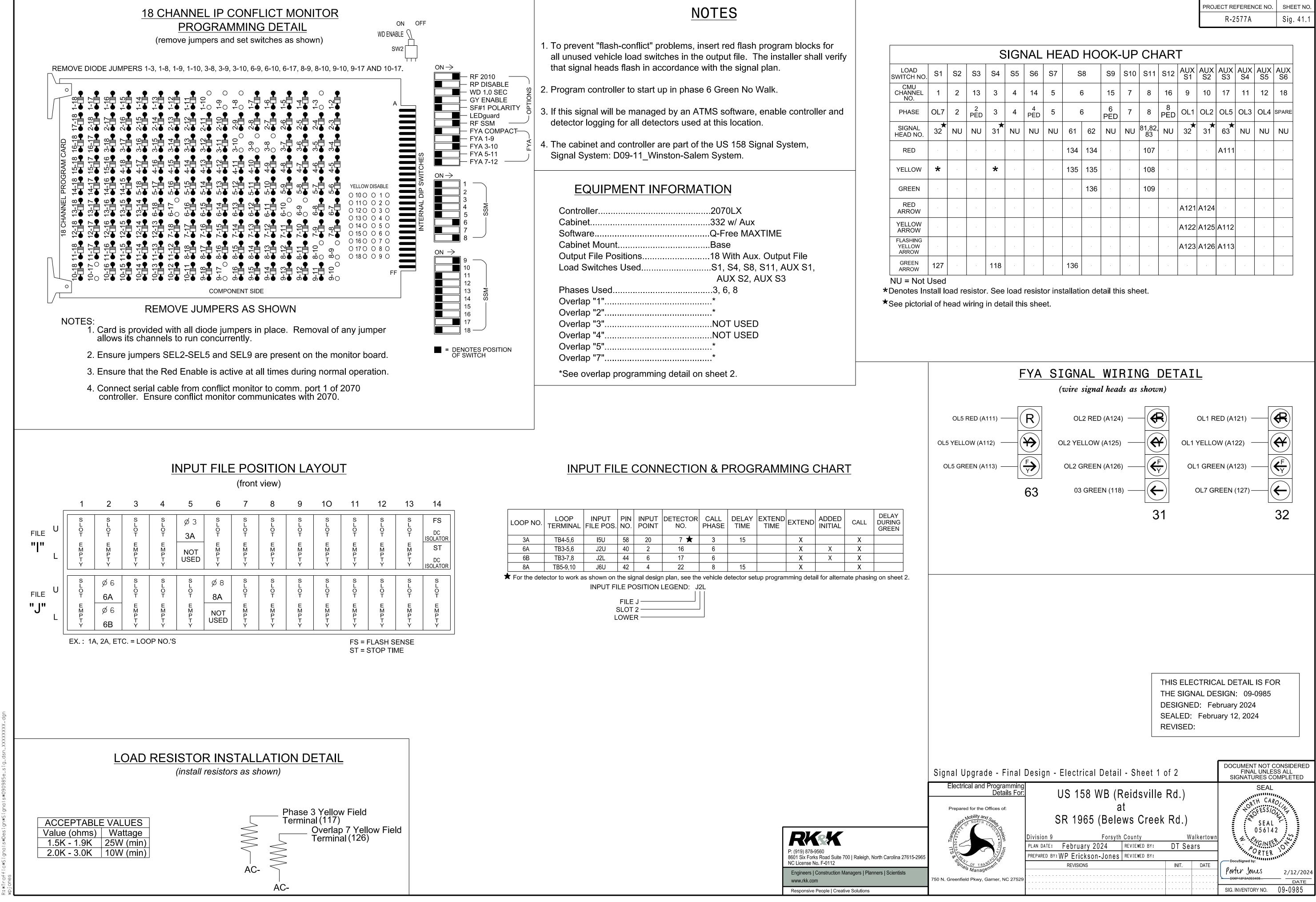
CTIVE PLA

CTIVE PLA

		PROJECT REFERE		et no. 40.2
	I	11-2011.	ישיט. א וישיש.	4∪.∠
TIME ALTERNATE PHASING	G ACTIV	ATION	DETA	<u>IL</u>
rnate phasing, select a Pattern that is programmed to run an be selected through the scheduler or manually by cha	n Overlap Plan anging the Ope	2 and Detector rational Mode	or Plan 2. e.	
IG	OVERLAP PL	AN VEH	DET PLAN	
E PLAN REQUIRED TO <u>RUN DEFAULT PHASING</u> E PLAN REQUIRED TO <u>RUN ALTERNATE PHASING</u>	1 2		1 2	
ALTERNATE PHASING CHANGE SUM	MMARY			
THE FOLLOWING IS A SUMMARY OF WHAT TAKES PL OVERLAP PLAN 2 AND VEHICLE DETECTOR PLAN 2 A TO CALL THE "ALTERNATE PHASING":	_ACE WHEN			
OVERLAP PLAN 2: Modifies overlap included phases for heads 31 and 32 to run protected	turns only.			
VEH DET PLAN 2: Reduces delay time for phase 3 call on loop 3A to 0 seconds.				
ER CIRCUIT MODIFICATION DETA	AIL			
NSURE THAT SIGNALS FLASH CONCURRENTLY ON CH, MAKE THE FOLLOWING FLASHER CIRCUIT CHA				
- REMOVE WIRE FROM TERM. T2-4 AND TERMINATE				
- REMOVE WIRE FROM TERM. T2-5 AND TERMINATE R UNIT 2.	= UN 12-3.			
D ABOVE TIES ALL PHASES AND OVERLAPS TO FLA	ASHER UNIT 1			
	THIS ELEC	TRICAL DETAI	L IS FOR	
	DESIGNED	L DESIGN: 09 : February 202 February 12, 20	24	
New Installation Tomporary Decian				
New Installation - Temporary Design (TMP Phase III Step 3) Electrical Detail - Sheet 2 of 2 Electrical and Programming	2	FIN	NT NOT CONSIDI AL UNLESS ALL TURES COMPLET	
Details For: US 158 WB (Reids Prepared for the Offices of: at	ville Rd.)		SEAL	
	Creek Rd.)	artown	SEAL	
SR 1965 (Belews C Division 9 Forsyth County PLAN DATE: February 2024 REVIEWED	D BY: DT Sears		056142	
PREPARED BY: WP Erickson-Jones REVIEWED REVISIONS		DATE Porter S		2/2024
750 N. Greenfield Pkwy, Garner, NC 27529			E0498	

 	 Porter Jones	2/12/202
 		DATE
 	 SIG. INVENTORY NO.	09-0985T





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	LOOP NO.	LOOP TERMINAL	INPUT FILE POS.	PIN NO.	INPUT POINT	DETECTOR NO.	CALL PHASE	DELAY TIME	EXTEND TIME	EXTEND	ADDED INITIAL	CALL	DELAY DURING GREEN
	3A	TB4-5,6	I5U	58	20	7 ★	3	15		Х		Х	
	6A	TB3-5,6	J2U	40	2	16	6			Х	Х	Х	
	6B	TB3-7,8	J2L	44	6	17	6			Х	Х	Х	
[	8A	TB5 <del>-</del> 9,10	J6U	42	4	22	8	15		Х		Х	

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	SIGNAL HEAD HOOK-UP CHART																	
S2	S2 S3 S4 S5 S6 S7 S8 S9 S10 S11 S12 AUX S1 S1								AŲX S2	AUX S3	AUX S4	AUX S5	AŲX S6					
2	13	3	4	14	5	6	3	15	7	8	16	9	10	1.7	11	12	18	
2	2 PED	3	4	4 PED	5	6	3	6 PED	7	8	8 PÉD	OL1	OL2		OL3	OL4	SPARE	
NU	NU	<b>31</b> ★	NU	NU	NU	61	62	NU	NU	81 <u>,</u> 82, 83	NU	32	31 <sup>★</sup>	63 <sup>★</sup>	NU	NU	NU	
						134	134			107				A111				
		*				135	135			108								
							136			109								
												A121	A124					
												A122	A125	A112				
												A123	A126	A113				
		118				136												

	PREPARED BY: WP Erickson-Jones R	EVIEWED BY:			DocuSigned by:		
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enfield Pkwy, Garner, NC 27529					D06F13F3A0E0408		
					SIG. INVENTORY NO.	09-0	

# MAXTIME OVERLAP PROGRAMMING DETAIL FOR DEFAULT PHASING

Front Panel

Main Menu >Controller >Overlap >Overlap Parameters/Overlap Timings

Web Interface

Home >Controller >Overlap Configuration >Overlaps

Overlap Plan 1

Overlap	1	2	5	7
Туре	FYA 4 - Section	FYA 4 - Section	FYA 4 - Section	Normal
Included Phases	6	6	6	3
Modifier Phases	-	3	-	-
Modifier Overlaps	7	<u>-</u>	<u>-</u>	<u> -</u>
Trail Green	0	0	0	0
Trail Yellow	0.0	0.0	0.0	0.0
Trail Red	0.0	0.0	0.0	0:0

NOTICE OVERLAP 7 ASSIGNED TO CHANNEL 1

## MAXTIME OVERLAP PROGRAMMING DETAIL FOR ALTERNATE PHASING

Front Panel

Main Menu >Controller >Overlap >Overlap Parameters/Overlap Timings

Web Interface

Home >Controller >Overlap Configuration >Overlaps

In the table view of the web interface, right click on "Overlap" in the top left corner of the table. Copy the entire contents of Overlap Plan 1. Paste Overlap Plan 1 into Overlap Plan 2. Modify Overlap Plan 2 as shown below and save changes.

Overlap Plan 2

Overlap	1	2	5	7	
Туре	FYA 4 - Section	FYA 4 - Section	FYA 4 - Section	Normal	
Included Phases	<u>-</u>	-	6	3	
Modifier Phases	<u>-</u>	3	<u>-</u>	÷	INCLUDED PHASE
Modifier Overlaps	7	÷	<u>-</u>	÷	
Trail Green	0	0	0	0	
Trail Yellow	0.0	0.0	0:0	0.0	
Trail Red	0.0	0.0	0.0	0.0	

# MAXTIME ALTERNATE PHASING PATTERN **PROGRAMMING DETAIL**

Front Panel Main Menu >Controller >Coordination >Patterns

Web Interface Home >Controller >Coordination >Patterns

Pattern Parameters Veh Det Plan Overlap Plan Pattern 2 \* 2

\* The Pattern number(s) are to be determined by the Division and/or City Traffic Engineer.

# OUTPUT CHANNEL CONFIGURATION

#### Front Panel

Main Menu >Controller >More>Channels>Channels Config

#### Web Interface

Home >Controller >Advanced IO>Channels>Channels Configuration

#### Channel Configuration

Channel	Control Type	Control Source	Flash Yellow	Flash Red	Flash Alt	MMU Channel	
1	Overlap	7	Х		Х	1	YELLOW FLAS
2	Phase Vehicle	2	Х			2	
3	Phase Vehicle	3	Х		Х	3	
4	Phase Vehicle	4		Х		4	YELLOW FLAS
5	Phase Vehicle	5		Х		5	
6	Phase Vehicle	6	Х		Х	6	
7	Phase Vehicle	7		Х		7	
8	Phase Vehicle	8		Х		8	
9	Overlap	1	Х			9	NOTICE CHANNEL
10	Overlap	2	Х			10	YELLOW FLAS
11	Overlap	3	Х			11	TELEOWTEA
12	Overlap	4		Х		12	
13	Phase Ped	2				13	
14	Phase Ped	4				14	
15	Phase Ped	6				15	
16	Phase Ped	8				16	
17	Overlap	5	Х		Х	17	YELLOW FLAS
18	Overlap	6		Х		18	fellow FLA

## MAXTIME DETECTOR PROGRAMMING DETAIL FOR ALTERNATE PHASING LOOP 3A

#### Front Panel Main Menu >Controller >Detector >Veh Det Plans

Web Interface Home >Controller >Detector Configuration >Vehicle Detectors

In the table view of web interface right click on "Detector" in the top left corner of the table. Copy the entire contents of Detector Plan 1. Paste Detector Plan 1 into Detector Plan 2. Modify Detector Plan 2 as shown below and save changes.

Plan 2

3A

Detector	Call Phase	Delay
7	3	0

# MAXTI

# To run alternate A Pattern can b

PHASING

ACTIVE PLA ACTIVE PLA

# FLASHER

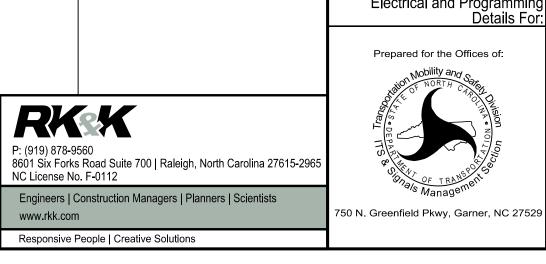
IN ORDER TO INSUF SAME APPROACH,

1. ON REAR OF PDA - REM

2. ON REAR OF PDA - REM

3. REMOVE FLASHER UNI

#### THE CHANGES LISTED ABO



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ernate phasing, select a Pa can be selected through th	ttern that is programmed to ru e scheduler or manually by ch	n Overlap Plan anging the Ope	2 and Detect erational Mode	or Plan e.	2.	
1G		OVERLAP PL/	AN VEH	DET PI	LAN	
 E PLAN REQUIRED TO R	UN DEFAULT PHASING	1		1		
	UN ALTERNATE PHASING	2		2		
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	O VEHICLE DETECTOR PLAN 2 A					
	NATE PHASING .					
OVERLAP PLAN 2. M	odifies overlap included phases					
	heads 31 and 32 to run protected	turns only.				
	duces delay time for phase 3					
	I on loop 3A to 0 seconds.					
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	LASH CONCURRENTLY ON					
CH, MAKE THE FOLLOW	ING FLASHER CIRCUIT CHA	NGES:				
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R UNIT 2.						
D ABOVE TIES ALL PHAS	SES AND OVERLAPS TO FLA	ASHER UNIT 1.				
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Electrical and Programming	esign - Electrical Detail - She			URES CON		)
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	vision 9 Forsyth County		ertown 52	056142	FS	
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97.9/s Management	REVISIONS	INIT. I	DATE Porter Je	-	2/12/2	2024

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SIG. INVENTORY NO.

DATE

09-0985