

MAXTIME OVERLAP PROGRAMMING DETAIL FOR DEFAULT PHASING

Front Panel
Main Menu >Controller >Overlap >Overlap Parameters/Overlap Timings

Web Interface
Home >Controller >Overlap Configuration >Overlaps

Overlap Plan 1

Overlap	4
Type	FYA 4 - Section
Included Phases	2
Modifier Phases	7
Modifier Overlaps	-
Trail Green	0
Trail Yellow	0.0
Trail Red	0.0

MAXTIME ALTERNATE PHASING ACTIVATION DETAIL

To run alternate phasing, select a Pattern that is programmed to run Overlap Plan 2 and Detector Plan 2. A Pattern can be selected through the scheduler or manually by changing the Operational Mode.

PHASING	OVERLAP PLAN	VEH DET PLAN
ACTIVE PLAN REQUIRED TO RUN DEFAULT PHASING	1	1
ACTIVE PLAN REQUIRED TO RUN ALTERNATE PHASING	2	2

MAXTIME OVERLAP PROGRAMMING DETAIL FOR ALTERNATE PHASING

Front Panel
Main Menu >Controller >Overlap >Overlap Parameters/Overlap Timings

Web Interface
Home >Controller >Overlap Configuration >Overlaps

In the table view of the web interface, right click on "Overlap" in the top left corner of the table. Copy the entire contents of Overlap Plan 1. Paste Overlap Plan 1 into Overlap Plan 2. Modify Overlap Plan 2 as shown below and save changes.

Overlap Plan 2

Overlap	4
Type	FYA 4 - Section
Included Phases	-
Modifier Phases	7
Modifier Overlaps	-
Trail Green	0
Trail Yellow	0.0
Trail Red	0.0

← NOTICE INCLUDED PHASE

ALTERNATE PHASING CHANGE SUMMARY

THE FOLLOWING IS A SUMMARY OF WHAT TAKES PLACE WHEN OVERLAP PLAN 2 AND VEHICLE DETECTOR PLAN 2 ACTIVATE TO CALL THE "ALTERNATE PHASING":

OVERLAP PLAN 2: Modifies overlap included phases for head 71 to run protected turns only.

VEH DET PLAN 2: Reduces delay time for phase 7 call on loop 7A to 0 seconds.

MAXTIME DETECTOR PROGRAMMING DETAIL FOR ALTERNATE PHASING LOOPS 7A

Front Panel
Main Menu >Controller >Detector >Veh Det Plans

Web Interface
Home >Controller >Detector Configuration >Vehicle Detectors

In the table view of web interface right click on "Detector" in the top left corner of the table. Copy the entire contents of Detector Plan 1. Paste Detector Plan 1 into Detector Plan 2. Modify Detector Plan 2 as shown below and save changes.

Plan 2

Detector	Call Phase	Delay
21	7	0

7A

MAXTIME ALTERNATE PHASING PATTERN PROGRAMMING DETAIL

Front Panel
Main Menu >Controller >Coordination >Patterns

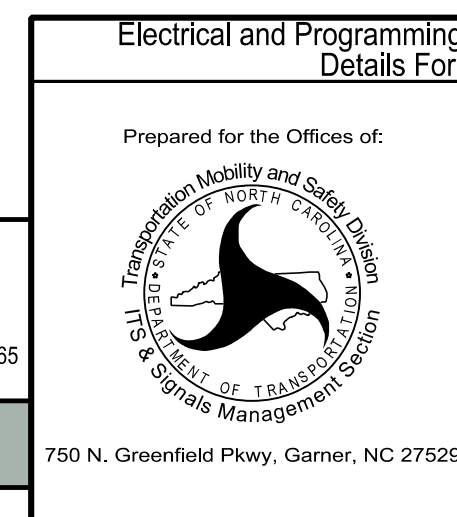
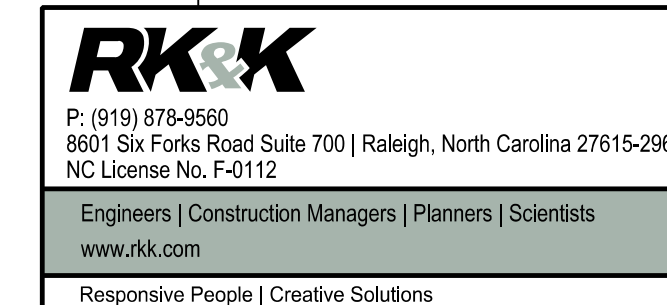
Web Interface
Home >Controller >Coordination >Patterns

Pattern	Veh Det Plan	Overlap Plan
*	2	2

* The Pattern number(s) are to be determined by the Division and/or City Traffic Engineer.

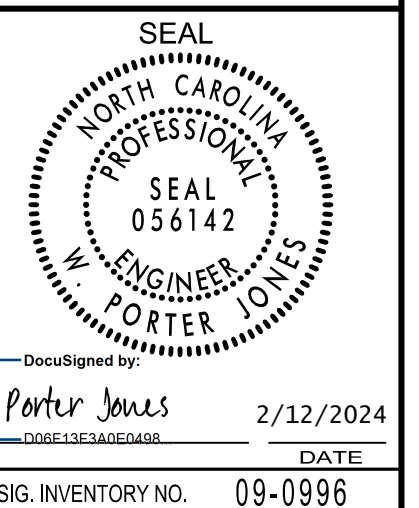
THIS ELECTRICAL DETAIL IS FOR
THE SIGNAL DESIGN: 09-0966
DESIGNED: February 2024
SEALED: February 12, 2024
REVISED:

Signal Upgrade - Final Design - Electrical Detail - Sheet 2 of 2

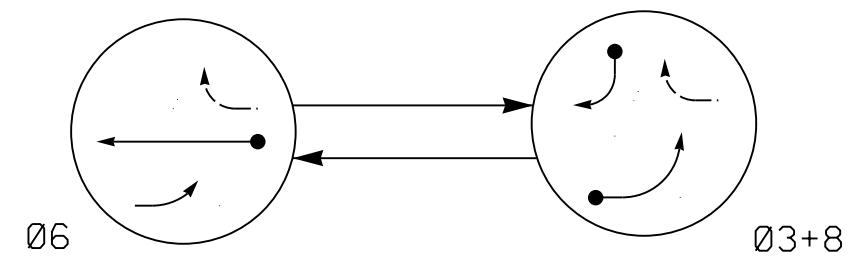


Prepared for the Offices of:		US 158 EB (Reidsville Rd.) at SR 2385 (Darrow Road)	
Division 9	Forsyth County	Walkertown	
PLAN DATE: February 2024	REVIEWED BY: DT Sears		
PREPARED BY: WP Erickson-Jones	REVIEWED BY:		
REVISIONS	INIT.	DATE	

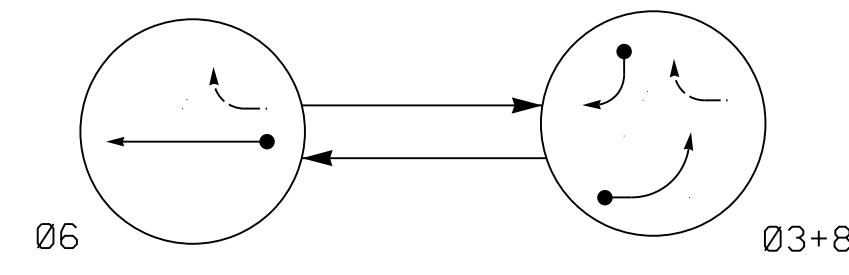
DOCUMENT NOT CONSIDERED
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SIGNATURES COMPLETED



DEFAULT PHASING DIAGRAM



ALTERNATE PHASING DIAGRAM



PHASING DIAGRAM DETECTION LEGEND

- DETECTED MOVEMENT
- UNDETECTED MOVEMENT (OVERLAP)
- UNSIGNALIZED MOVEMENT
- PEDESTRIAN MOVEMENT

DEFAULT PHASING TABLE OF OPERATION

SIGNAL FACE	PHASE		
	06	03+8	FLASH
31	←	←	←
61, 62	↑	↑	↑
81, 82	→	→	→

DEFAULT PHASING TABLE OF OPERATION

SIGNAL FACE	PHASE		
	06	03+8	FLASH
31	←	←	←
61, 62	↑	↑	↑
81, 82	→	→	→

MAXTIME DETECTOR INSTALLATION CHART

ZONE	SIZE (FT)	DISTANCE FROM STOPBAR (FT)	TURNS	NEW LOOP	PROGRAMMING							
					CALL PHASE	DELAY TIME	EXTEND TIME	EXTEND INITIAL	CALL	DELAY DURING GREEN		
3A *	6X40	0	*	*	3	15 #	-	X	-	X	-	*
6A *	6X6	300	*	*	6	-	-	X	X	X	-	*
8A *	6X40	0	*	*	8	15	-	X	-	X	-	*

* Video Detection Zone
Disable Delay During Alternate Phasing Operation.

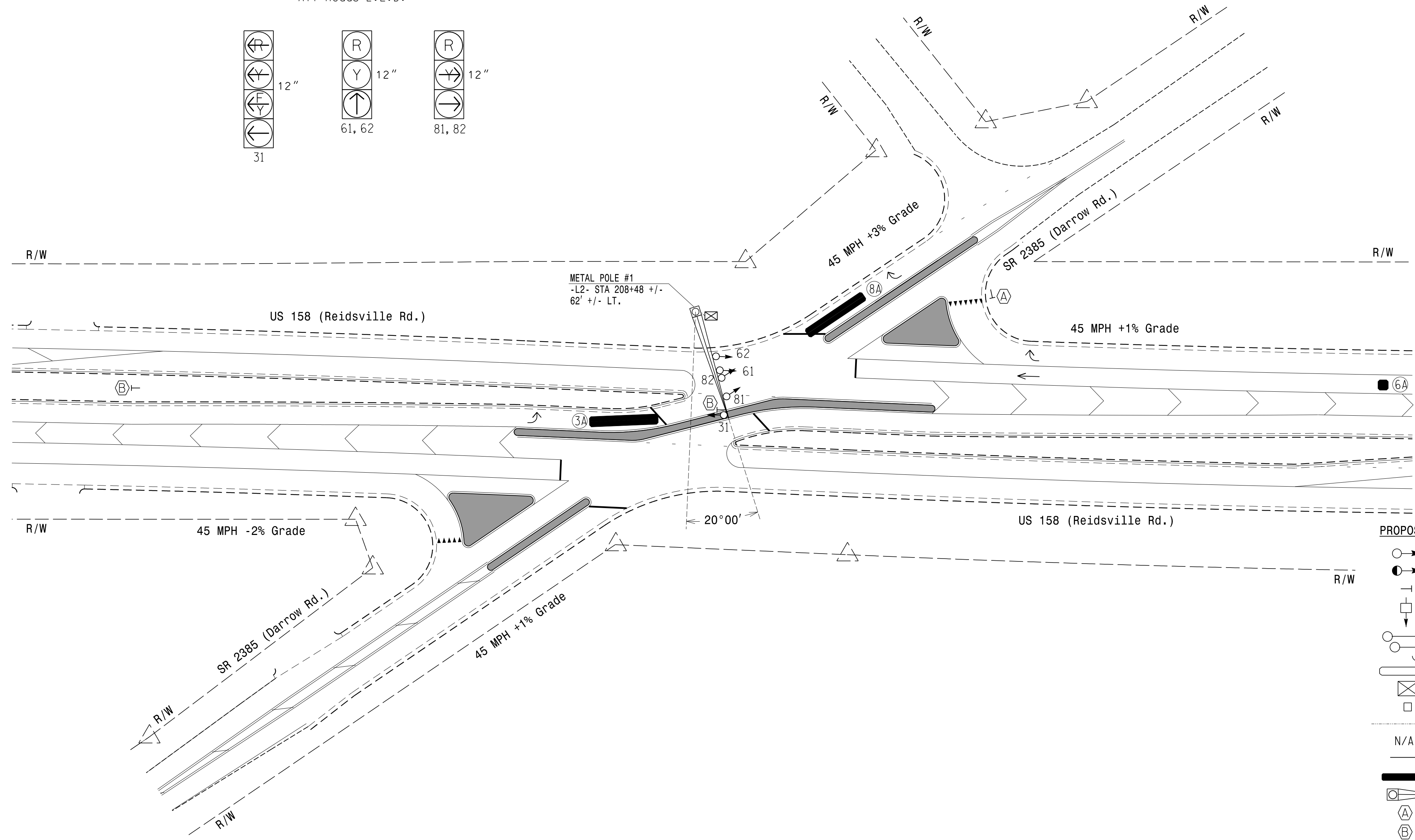
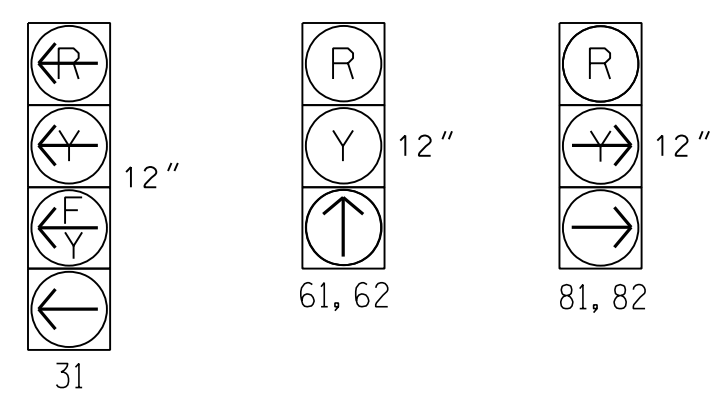
2 Phase Fully Actuated (Isolated)

NOTES

- Refer to "Roadway Standard Drawings NCDOT" dated January 2024 and "Standard Specifications for Roads and Structures" dated January 2024.
- Do not program signal for late night flashing operation unless otherwise directed by the Engineer.
- Set all detector units to presence mode.
- This intersection uses video detection. Install detectors according to the manufacturer's instructions to achieve the desired detection.

SIGNAL FACE I.D.

All Heads L.E.D.

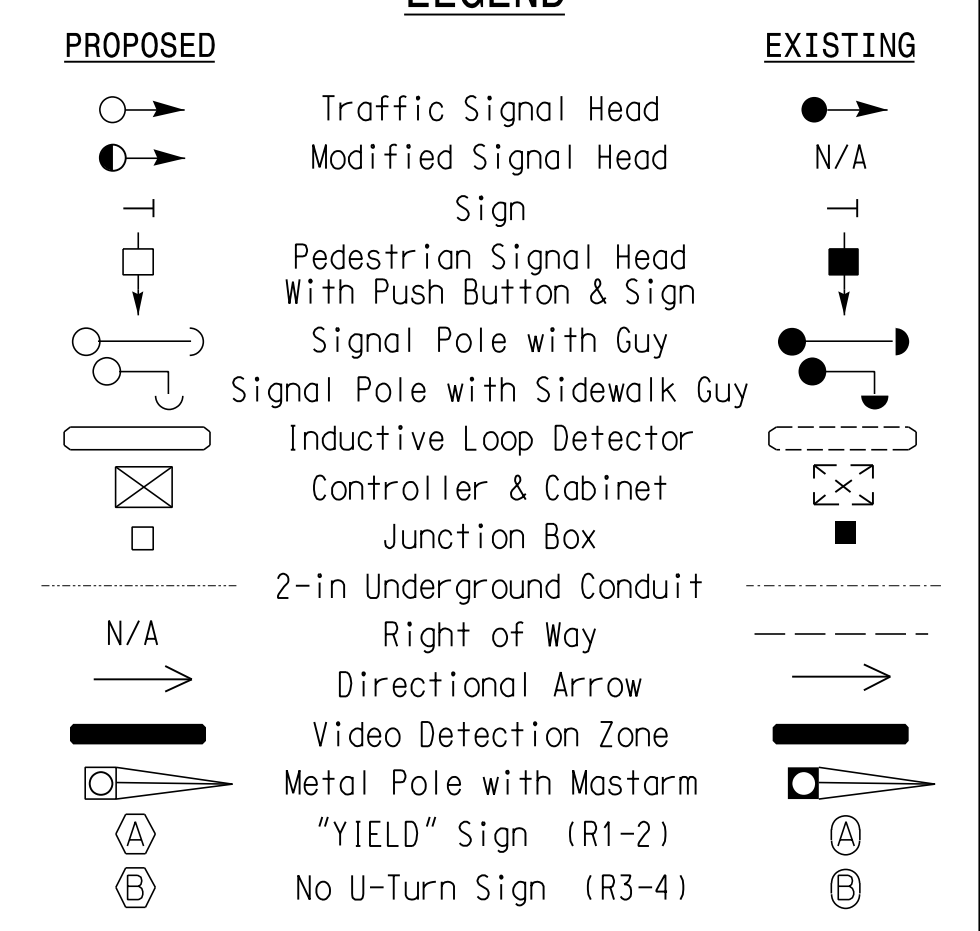


MAXTIME TIMING CHART

FEATURE	PHASE		
	3	6	8
Walk *	-	-	-
Ped Clear *	-	-	-
Min Green	7	12	7
Passage *	2.0	6.0	2.0
Max I *	30	90	30
Yellow Change	3.0	4.4	3.0
Red Clear	3.2	1.2	3.2
Added Initial *	-	2.5	-
Maximum Initial *	-	34	-
Time Before Reduction *	-	15	-
Time To Reduce *	-	30	-
Minimum Gap	-	3.0	-
Advance Walk	-	-	-
Non Lock Detector	X	-	X
Vehicle Recall	-	MIN RECALL	-
Dual Entry	X	-	X

* These values may be field adjusted. Do not adjust Min Green and Extension times for phase 6 lower than what is shown. Min Green for all other phases should not be lower than 4 seconds.

LEGEND



New Installation - Temporary Design (TMP Phase III Step 5)

DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED

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Prepared for the Offices of:
 Transportation Mobility and Safety Division
 DEPARTMENT OF TRANSPORTATION
 SIGNAL DESIGN SECTION
 750 N. Greenfield Pkwy, Garner, NC 27529

US 158 WB (Reidsville Rd.)
 at
 SR 2385 (Darrow Rd.)
 Division 9 Forsyth County Walkertown
 PLAN DATE: February 2024 REVIEWED BY: WP Erickson-Jones
 PREPARED BY: H Townsend REVIEWED BY:

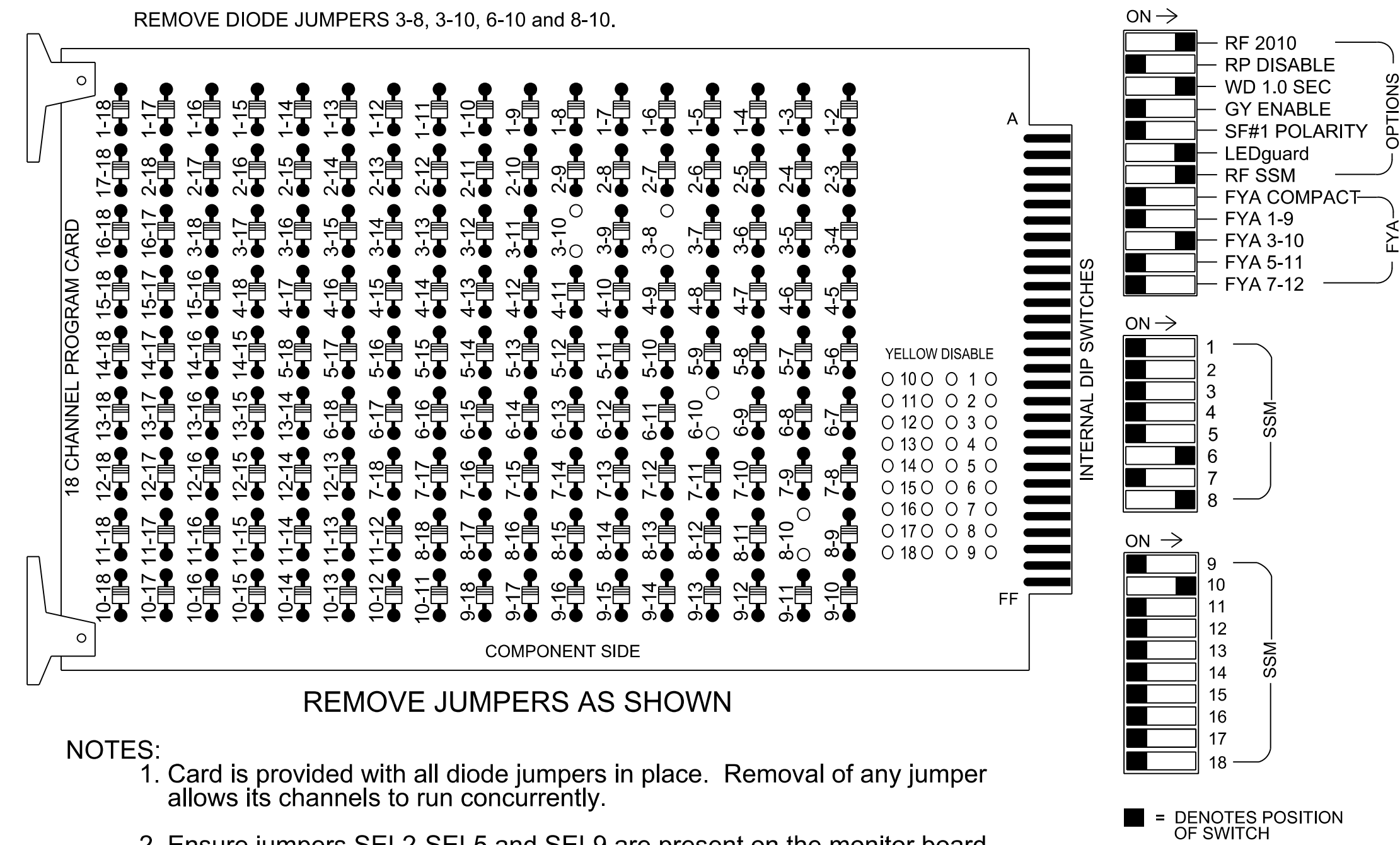
REVISIONS	INIT.	DATE

SEAL
 NORTH CAROLINA
 PROFESSIONAL ENGINEER
 SEAL 056142
 W. PORTER JONES
 PORTER JONES
 2/12/2024
 SIGNATURE DATE
 SIG. INVENTORY NO. 09-0984T

2/12/2024
 R:\Traffic\c4s1\gnal\04090984T1_s1.dgn...XXXXXX.dgn
 wpJones

18 CHANNEL IP CONFLICT MONITOR PROGRAMMING DETAIL

(remove jumpers and set switches as shown)



- NOTES:**
- Card is provided with all diode jumpers in place. Removal of any jumper allows its channels to run concurrently.
 - Ensure jumpers SEL2-SEL5 and SEL9 are present on the monitor board.
 - Ensure that the Red Enable is active at all times during normal operation.
 - Connect serial cable from conflict monitor to comm. port 1 of 2070 controller. Ensure conflict monitor communicates with 2070.

NOTES

- To prevent "flash-conflict" problems, insert red flash program blocks for all unused vehicle load switches in the output file. The installer shall verify that signal heads flash in accordance with the signal plan.
- Program controller to start up in phase 6 Green No Walk.
- If this signal will be managed by an ATMS software, enable controller and detector logging for all detectors used at this location.

EQUIPMENT INFORMATION

Controller.....2070LX
 Cabinet.....332 w/ Aux
 Software.....Q-Free MAXTIME
 Cabinet Mount.....Base
 Output File Positions.....18 With Aux. Output File
 Load Switches Used.....S4, S8, S11, AUX S2
 Phases Used.....3, 6, 8
 Overlap "1".....NOT USED
 Overlap "2".....*
 Overlap "3".....NOT USED
 Overlap "4".....NOT USED

*See overlap programming detail on sheet 2.

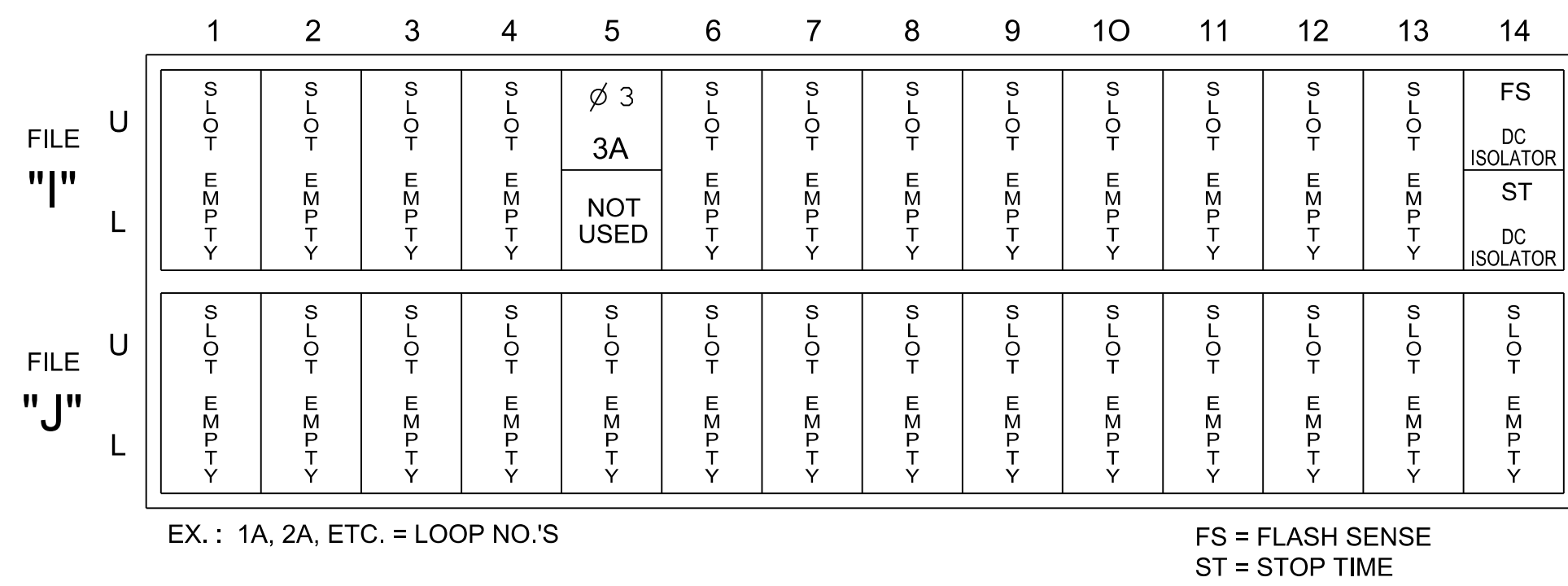
SIGNAL HEAD HOOK-UP CHART

LOAD SWITCH NO.	S1	S2	S3	S4	S5	S6	S7	S8	S9	S10	S11	S12	AUX S1	AUX S2	AUX S3	AUX S4	AUX S5	AUX S6
CMU CHANNEL NO.	1	2	13	3	4	14	5	6	15	7	8	16	9	10	17	11	12	18
PHASE	1	2	2 PED	3	4	4 PED	5	6	6 PED	7	8	8 PED	OL1	OL2	SPARE	OL3	OL4	SPARE
SIGNAL HEAD NO.	NU	NU	NU	31*	NU	NU	NU	61,62	NU	NU	81,82	NU	NU	31*	NU	NU	NU	NU
RED								134			107							
YELLOW				*				135										
GREEN																		
RED ARROW																A124		
YELLOW ARROW											108					A125		
FLASHING YELLOW ARROW																A126		
GREEN ARROW						118		136		109								

NU = Not Used
 * Denotes install load resistor. See load resistor installation detail this sheet.
 * See pictorial of head wiring in detail this sheet.

INPUT FILE POSITION LAYOUT

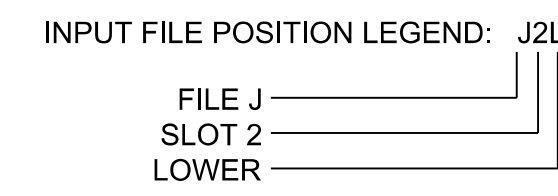
(front view)



INPUT FILE CONNECTION & PROGRAMMING CHART

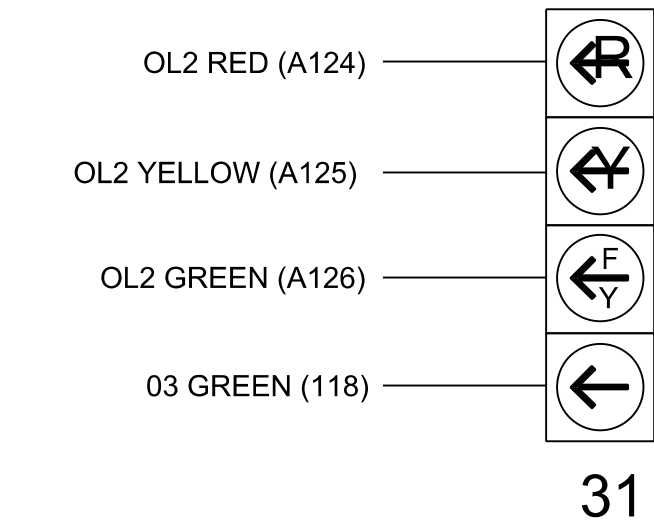
LOOP NO.	LOOP TERMINAL	INPUT FILE POS.	PIN NO.	INPUT POINT	DETECTOR NO.	CALL PHASE	DELAY TIME	EXTEND TIME	EXTEND	ADDED INITIAL	CALL	DELAY DURING GREEN
3A	TB4-5,6	I5U	58	20	7★	3	15		X		X	

★ For the detector to work as shown on the signal design plan, see the vehicle detector setup programming detail for alternate phasing on sheet 2.



FYA SIGNAL WIRING DETAIL

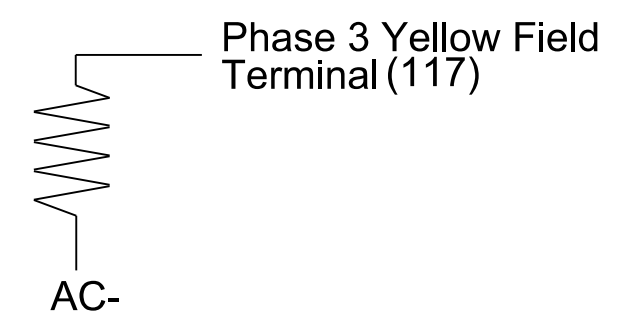
(wire signal heads as shown)



LOAD RESISTOR INSTALLATION DETAIL

(install resistors as shown)

ACCEPTABLE VALUES	
Value (ohms)	Wattage
1.5K - 1.9K	25W (min)
2.0K - 3.0K	10W (min)



SPECIAL DETECTOR NOTE

Install a video detection system for vehicle detection for zones 3A, 6A and 8A. Perform installation according to manufacturer's directions and NCDOT engineer -approved mounting locations to accomplish the detection schemes shown on the Signal Design Plans.

For Detection Zone 3A, the equipment placement and slots are typical for a NCDOT installation. Inputs associated with these slots are compatible with time of day instructions located on sheet 2 of this electrical detail.

New Installation - Temporary Design
 (TMP Phase III Step 5) Electrical Detail - Sheet 1 of 2

THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 09-0984T
 DESIGNED: February 2024
 SEALED: February 12, 2024
 REVISED:

Electrical and Programming Details For:

Prepared for the Offices of:

750 N. Greenfield Pkwy, Garner, NC 27529

US 158 WB (Reidsville Rd.)
 at
SR 2385 (Darrow Road)

Division 9 Forsyth County Walkertown

PLAN DATE: February 2024 REVIEWED BY: DT Sears

PREPARED BY: WP Erickson-Jones REVIEWED BY:

REVISIONS	INIT.	DATE

DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED

Porter Jones
 2/12/2024

MAXTIME OVERLAP PROGRAMMING DETAIL FOR DEFAULT PHASING

Front Panel
Main Menu >Controller >Overlap >Overlap Parameters/Overlap Timings

Web Interface
Home >Controller >Overlap Configuration >Overlaps

Overlap Plan 1

Overlap	2
Type	FYA 4 - Section
Included Phases	6
Modifier Phases	3
Modifier Overlaps	-
Trail Green	0
Trail Yellow	0.0
Trail Red	0.0

MAXTIME ALTERNATE PHASING ACTIVATION DETAIL

To run alternate phasing, select a Pattern that is programmed to run Overlap Plan 2 and Detector Plan 2. A Pattern can be selected through the scheduler or manually by changing the Operational Mode.

PHASING	OVERLAP PLAN	VEH DET PLAN
ACTIVE PLAN REQUIRED TO RUN DEFAULT PHASING	1	1
ACTIVE PLAN REQUIRED TO RUN ALTERNATE PHASING	2	2

MAXTIME OVERLAP PROGRAMMING DETAIL FOR ALTERNATE PHASING

Front Panel
Main Menu >Controller >Overlap >Overlap Parameters/Overlap Timings

Web Interface
Home >Controller >Overlap Configuration >Overlaps

In the table view of the web interface, right click on "Overlap" in the top left corner of the table. Copy the entire contents of Overlap Plan 1. Paste Overlap Plan 1 into Overlap Plan 2. Modify Overlap Plan 2 as shown below and save changes.

Overlap Plan 2

Overlap	2
Type	FYA 4 - Section
Included Phases	-
Modifier Phases	3
Modifier Overlaps	-
Trail Green	0
Trail Yellow	0.0
Trail Red	0.0

← NOTICE INCLUDED PHASE

ALTERNATE PHASING CHANGE SUMMARY

THE FOLLOWING IS A SUMMARY OF WHAT TAKES PLACE WHEN OVERLAP PLAN 2 AND VEHICLE DETECTOR PLAN 2 ACTIVATE TO CALL THE "ALTERNATE PHASING":

OVERLAP PLAN 2: Modifies overlap included phases for head 31 to run protected turns only.

VEH DET PLAN 2: Reduces delay time for phase 3 call on loop 3A to 0 seconds.

MAXTIME DETECTOR PROGRAMMING DETAIL FOR ALTERNATE PHASING LOOPS 3A

Front Panel
Main Menu >Controller >Detector >Veh Det Plans

Web Interface
Home >Controller >Detector Configuration >Vehicle Detectors

In the table view of web interface right click on "Detector" in the top left corner of the table. Copy the entire contents of Detector Plan 1. Paste Detector Plan 1 into Detector Plan 2. Modify Detector Plan 2 as shown below and save changes.

3A

Plan 2

Detector	Call Phase	Delay
7	3	0

MAXTIME ALTERNATE PHASING PATTERN PROGRAMMING DETAIL

Front Panel
Main Menu >Controller >Coordination >Patterns

Web Interface
Home >Controller >Coordination >Patterns

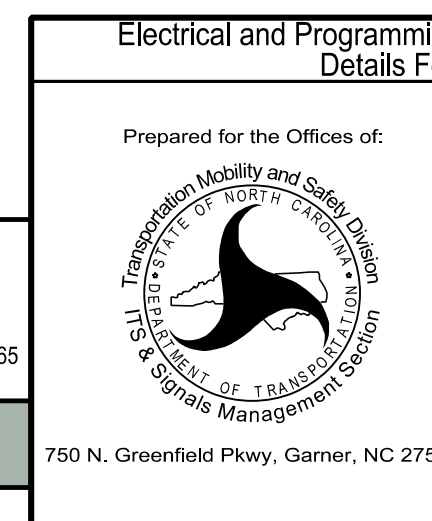
Pattern	Veh Det Plan	Overlap Plan
*	2	2

* The Pattern number(s) are to be determined by the Division and/or City Traffic Engineer.

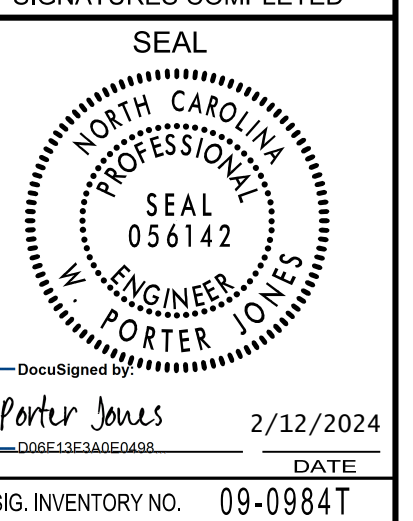
THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 09-0984T
DESIGNED: February 2024
SEALED: February 12, 2024
REVISED:

New Installation - Temporary Design
(TMP Phase III Step 5) Electrical Detail - Sheet 2 of 2

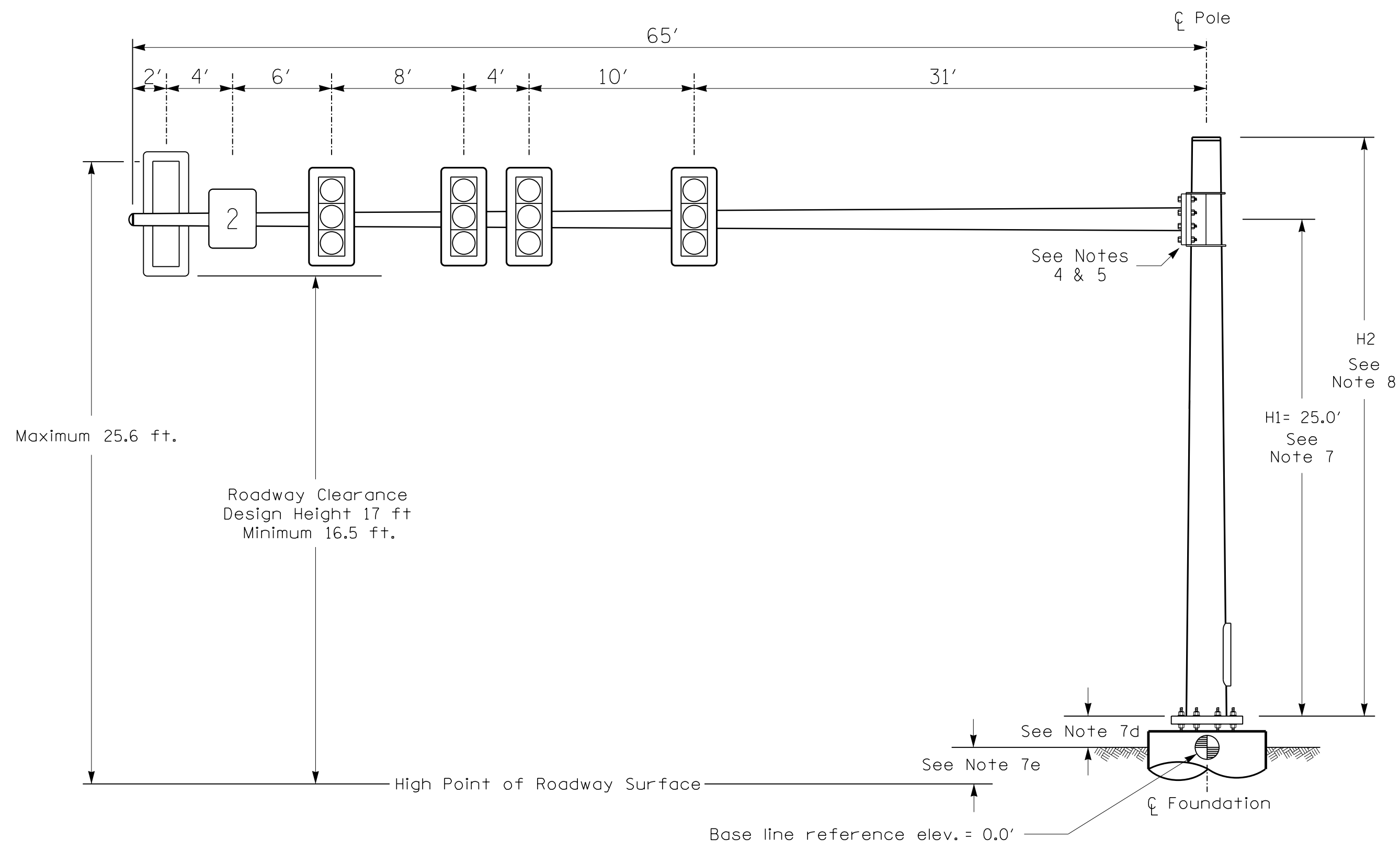
DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED



US 158 WB (Reidsville Rd.) at SR 2385 (Darrow Road)	
Division 9	Forsyth County Walkertown
PLAN DATE: February 2024	REVIEWED BY: DT Sears
PREPARED BY: WP Erickson-Jones	REVIEWED BY:
REVISIONS	INIT. DATE



Design Loading for METAL POLE NO. 1



Elevation View

SPECIAL NOTE
The contractor is responsible for verifying that the mast arm attachment height (H1) will provide the "Design Height" clearance from the roadway before submitting final shop drawings for approval. Verify elevation data below which was obtained by field measurement or from available project survey data.

Elevation Data for Mast Arm Attachment (H1)

Elevation Differences for:	Pole 1
Baseline reference point at ϕ Foundation @ ground level	0.0 ft.
Elevation difference at High point of roadway surface	+5.8 ft.
Elevation difference at Edge of travelway or face of curb	+5.2 ft.

METAL POLE No. 1

PROJECT REFERENCE NO. R-2577A SHEET NO. Sig. 28.3

MAST ARM LOADING SCHEDULE

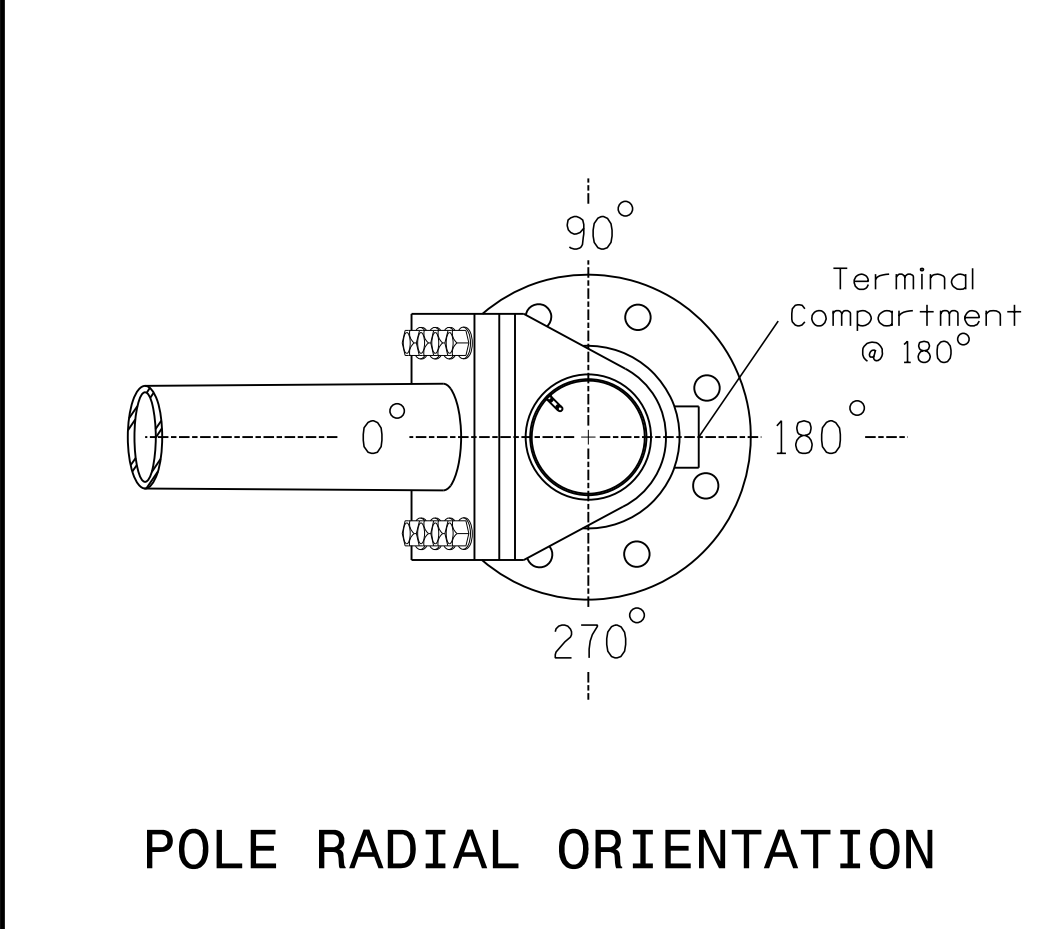
LOADING SYMBOL	DESCRIPTION	AREA	SIZE	WEIGHT
	RIGID MOUNTED SIGNAL HEAD 12"-3 SECTION-WITH BACKPLATE	9.3 S.F.	25.5" W X 52.5" L	60 LBS
	RIGID MOUNTED SIGNAL HEAD 12"-4 SECTION-WITH BACKPLATE	11.5 S.F.	25.5" W X 66.0" L	74 LBS
	SIGN RIGID MOUNTED	7.5 S.F.	30.0" W X 36.0" L	14 LBS

DESIGN REFERENCE MATERIAL

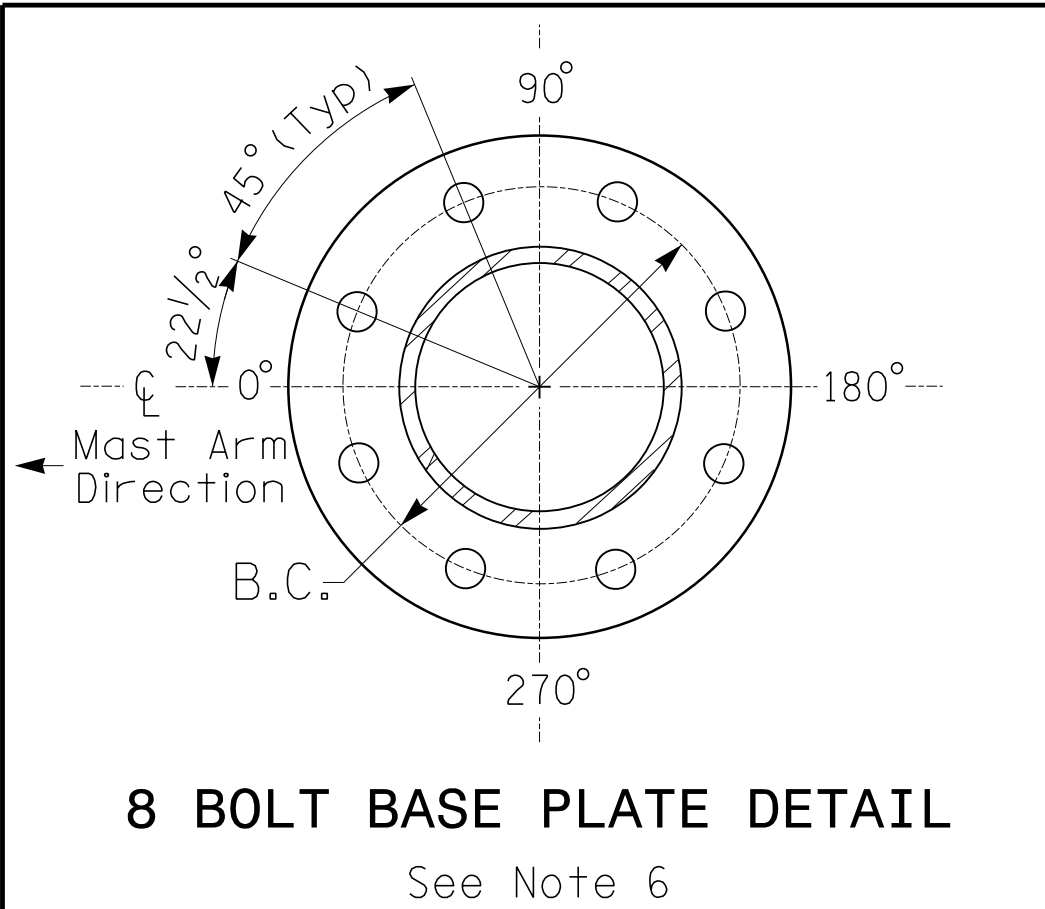
- Design the traffic signal structure and foundation in accordance with:
 - The 1st Edition 2015 AASHTO LRFD "Standard Specifications for Structural Supports for Highway Signs, Luminaires, and Traffic Signals, including all of the latest interim revisions.
 - The 2024 NCDOT "Standard Specifications for Roads and Structures." The latest addenda to the specifications can be found in the traffic signal project special provisions.
 - The 2024 NCDOT Roadway Standard Drawings.
 - The traffic signal project plans and special provisions.
 - The NCDOT "Metal Pole Standards" located at the following NCDOT website: <https://connect.ncdot.gov/resources/safety/Pages/ITS-Design-Resources.aspx>

DESIGN REQUIREMENTS

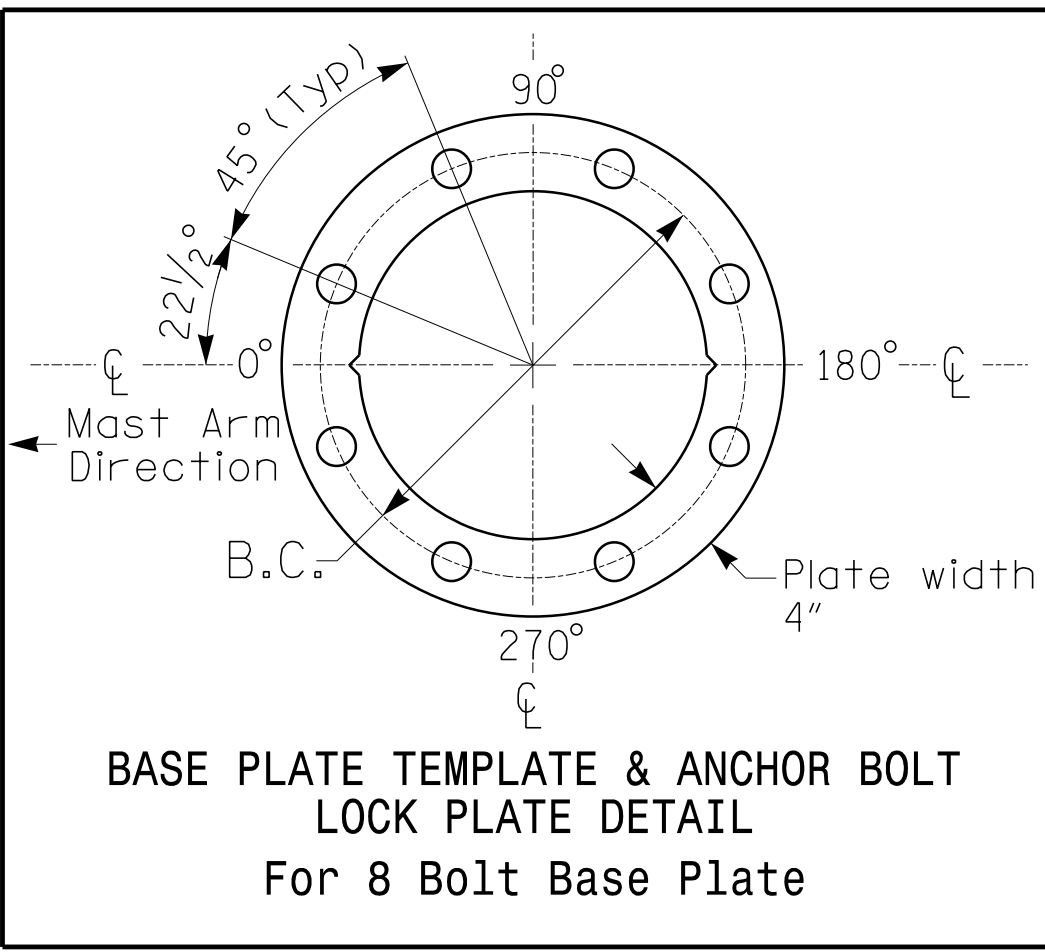
- Design the traffic signal structure using the loading conditions shown in the elevation views. These are anticipated worst case "design loads" and may not represent the actual loads that will be applied at the time of the installation. The contractor should refer to the traffic signal plans for the actual loads that will be applied at the time of the installation.
- Design all signal supports using force ratios that do not exceed 0.9.
- The camber design for the mast arm deflection should provide an appearance of a low pitched arch where the tip or the free end of the mast arm does not deflect below horizontal when fully loaded.
- A clamp-type bolted mast arm-to-pole connection may be used instead of the welded ring stiffened box connection shown as long as the connection meets all of the design requirements.
- Design base plate with 8 anchor bolt holes. Provide 2 inch x 60 inch anchor bolts.
- The mast arm attachment height (H1) shown is based on the following design assumptions:
 - Mast arm slope and deflection are not considered in determining the arm attachment height as they are assumed to offset each other.
 - Signal heads are rigidly mounted and vertically centered on the mast arm.
 - The roadway clearance height for design is as shown in the elevation views.
 - The top of the pole base plate is 0.75 feet above the ground elevation.
 - Refer to the Elevation Data Chart for the elevation differences between the proposed foundation ground level and the high point of the roadway.
- The pole manufacturer will determine the total height (H2) of each pole using the greater of the following:
 - Mast arm attachment height (H1) plus 2 feet, or
 - H1 plus 1/2 of the total height of the mast arm attachment assembly plus 1 foot.
- If pole location adjustments are required, the contractor must gain approval from the Engineer as this may affect the mast arm lengths and arm attachment heights. The contractor may contact the Signal Design Section Senior Structural Engineer for assistance at (919) 814-5000.
- The contractor is responsible for verifying that the mast arm length shown will allow proper positioning of the signal heads over the roadway.
- The contractor is responsible for providing soil penetration testing data (SPT) to the pole manufacturer so site specific foundations can be designed.



POLE RADIAL ORIENTATION



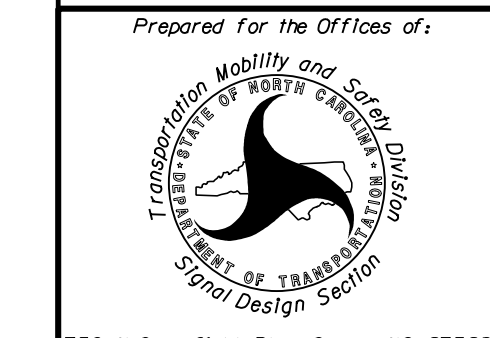
8 BOLT BASE PLATE DETAIL



BASE PLATE TEMPLATE & ANCHOR BOLT LOCK PLATE DETAIL
For 8 Bolt Base Plate

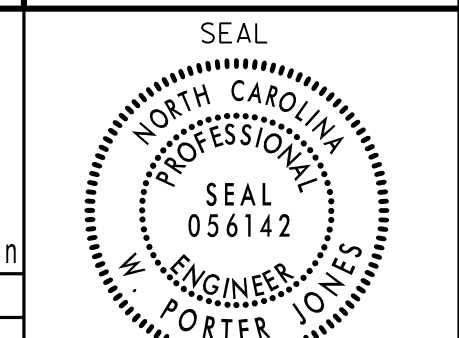
NCDOT Wind Zone 4 (90 mph)

RK&K
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US 158 WB (Reidsville Rd.)
at
SR 2385 (Darrow Road)
Division 9 Forsyth County Walkertown
PLAN DATE: February 2024 REVIEWED BY: DT Sears
PREPARED BY: WP Erickson-Jones REVIEWED BY:

DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED

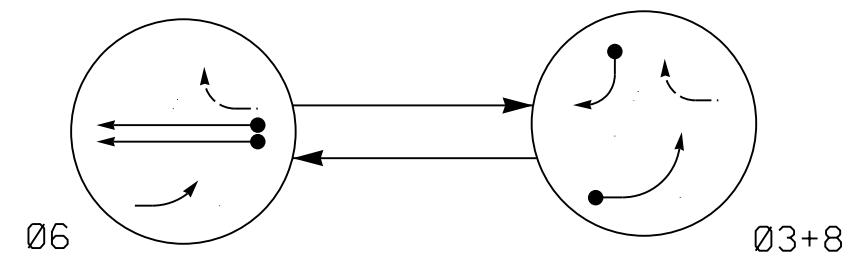


REVISIONS	INIT.	DATE

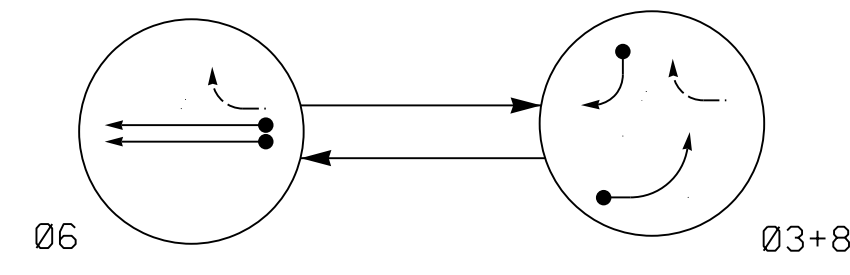
Signature: Porter Jones, Date: 2/12/2024
SIC INVENTORY NO. 09-0984T

2/12/2024 R:\Traffic\c4s\gnal\gnal\090984T\p...s\fig_dscrn_XXXXXXX.dgn wplones

DEFAULT PHASING DIAGRAM



ALTERNATE PHASING DIAGRAM



PHASING DIAGRAM DETECTION LEGEND
 ● DETECTED MOVEMENT
 ○ UNDETECTED MOVEMENT (OVERLAP)
 ○ UNSIGNALIZED MOVEMENT
 ○ PEDESTRIAN MOVEMENT

DEFAULT PHASING TABLE OF OPERATION

SIGNAL FACE	PHASE		
	06	03+8	FLASH
31	↑	←	→
61, 62	↑	R	Y
81, 82	R	→	R

ALTERNATE PHASING TABLE OF OPERATION

SIGNAL FACE	PHASE		
	06	03+8	FLASH
31	←	→	→
61, 62	↑	R	Y
81, 82	R	→	R

MAXTIME DETECTOR INSTALLATION CHART

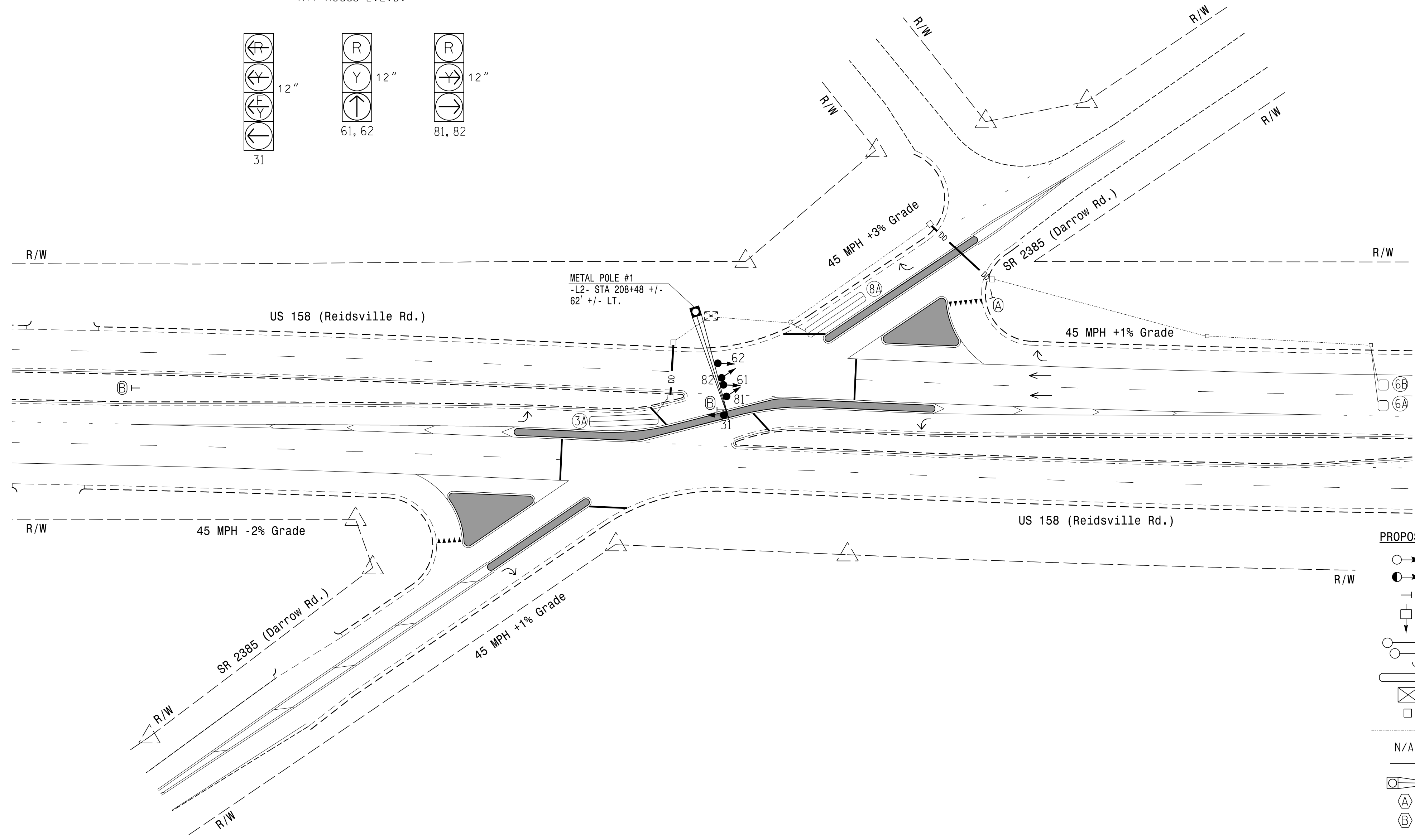
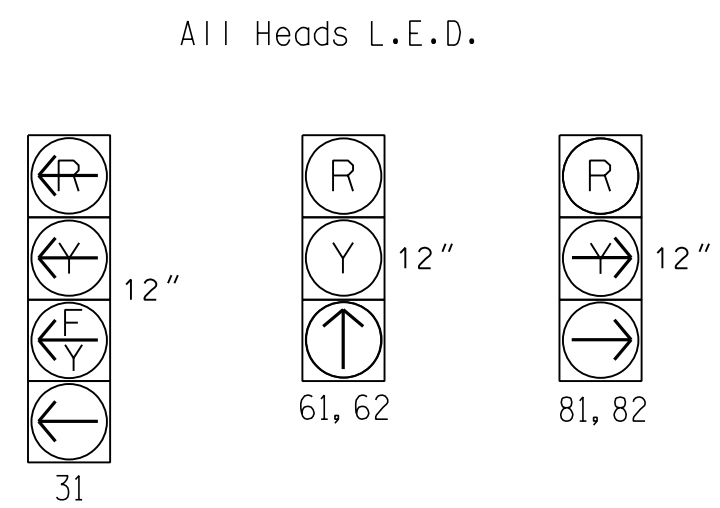
LOOP	SIZE (FT)	DISTANCE FROM STOPBAR (FT)	TURNS	NEW LOOP	PROGRAMMING							
					CALL PHASE	DELAY TIME	EXTEND TIME	EXTEND	ADDED INITIAL CALL	DELAY DURING GREEN	NEW CARD	
3A	6X40	0	2-4-2	X	3	15#	-	X	-	X	-	X
6A	6X6	300	5	X	6	-	-	X	X	X	-	X
6B	6X6	300	5	X	6	-	-	X	X	X	-	X
8A	6X40	0	2-4-2	X	8	15	-	X	-	X	-	X

Disable Delay During Alternate Phasing Operation.

2 Phase Fully Actuated (US 158 Signal System)
 Signal System #: D09-11_Winston-Salem
 NOTES

- Refer to "Roadway Standard Drawings NCDOT" dated January 2024 and "Standard Specifications for Roads and Structures" dated January 2024.
- Do not program signal for late night flashing operation unless otherwise directed by the Engineer.
- Reposition existing signal heads numbered 61 and 62.
- Set all detector units to presence mode.
- The Division Traffic Engineer will determine the hours of use for each phasing plan.
- Maximum times shown in timing chart are for free-run operation only. Coordinated signal system timing values supersede these values.

SIGNAL FACE I.D.



MAXTIME TIMING CHART

FEATURE	PHASE		
	3	6	8
Walk *	-	-	-
Ped Clear *	-	-	-
Min Green	7	12	7
Passage *	2.0	6.0	2.0
Max I *	30	90	30
Yellow Change	3.0	4.4	3.0
Red Clear	3.2	1.2	3.2
Added Initial *	-	1.5	-
Maximum Initial *	-	34	-
Time Before Reduction *	-	15	-
Time To Reduce *	-	30	-
Minimum Gap	-	3.0	-
Advance Walk	-	-	-
Non Lock Detector	X	-	X
Vehicle Recall	-	MIN RECALL	-
Dual Entry	X	-	X

* These values may be field adjusted. Do not adjust Min Green and Extension times for phase 6 lower than what is shown. Min Green for all other phases should not be lower than 4 seconds.

LEGEND

PROPOSED	EXISTING
○ Traffic Signal Head	● N/A
○ Modified Signal Head	○ N/A
○ Sign	○ N/A
○ Pedestrian Signal Head With Push Button & Sign	○ N/A
○ Signal Pole with Guy	○ N/A
○ Signal Pole with Sidewalk Guy	○ N/A
□ Inductive Loop Detector	□ N/A
□ Controller & Cabinet	□ N/A
□ Junction Box	□ N/A
○ 2-in Underground Conduit	○ N/A
○ Right of Way	○ N/A
→ Directional Arrow	→ N/A
○ Metal Pole with Mastarm	○ N/A
(A) "YIELD" Sign (R1-2)	(A) N/A
(B) No U-Turn Sign (R3-4)	(B) N/A

Signal Upgrade - Final Design

RK&K
 P: (919) 878-9550
 8001 Six Forks Road Suite 700 | Raleigh, North Carolina 27615-2965
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Prepared for the Offices of:
 Transportation Mobility and Safety Division
 DEPARTMENT OF TRANSPORTATION
 NORTH CAROLINA
 Signal Design Section
 750 N. Greenfield Pkwy, Garner, NC 27529

US 158 WB (Reidsville Rd.)
 at
 SR 2385 (Darrow Rd.)
 Division 9 Forsyth County Walkertown
 PLAN DATE: February 2024 REVIEWED BY: WP Erickson-Jones
 PREPARED BY: H Townsend REVIEWED BY:

REVISIONS	INIT.	DATE

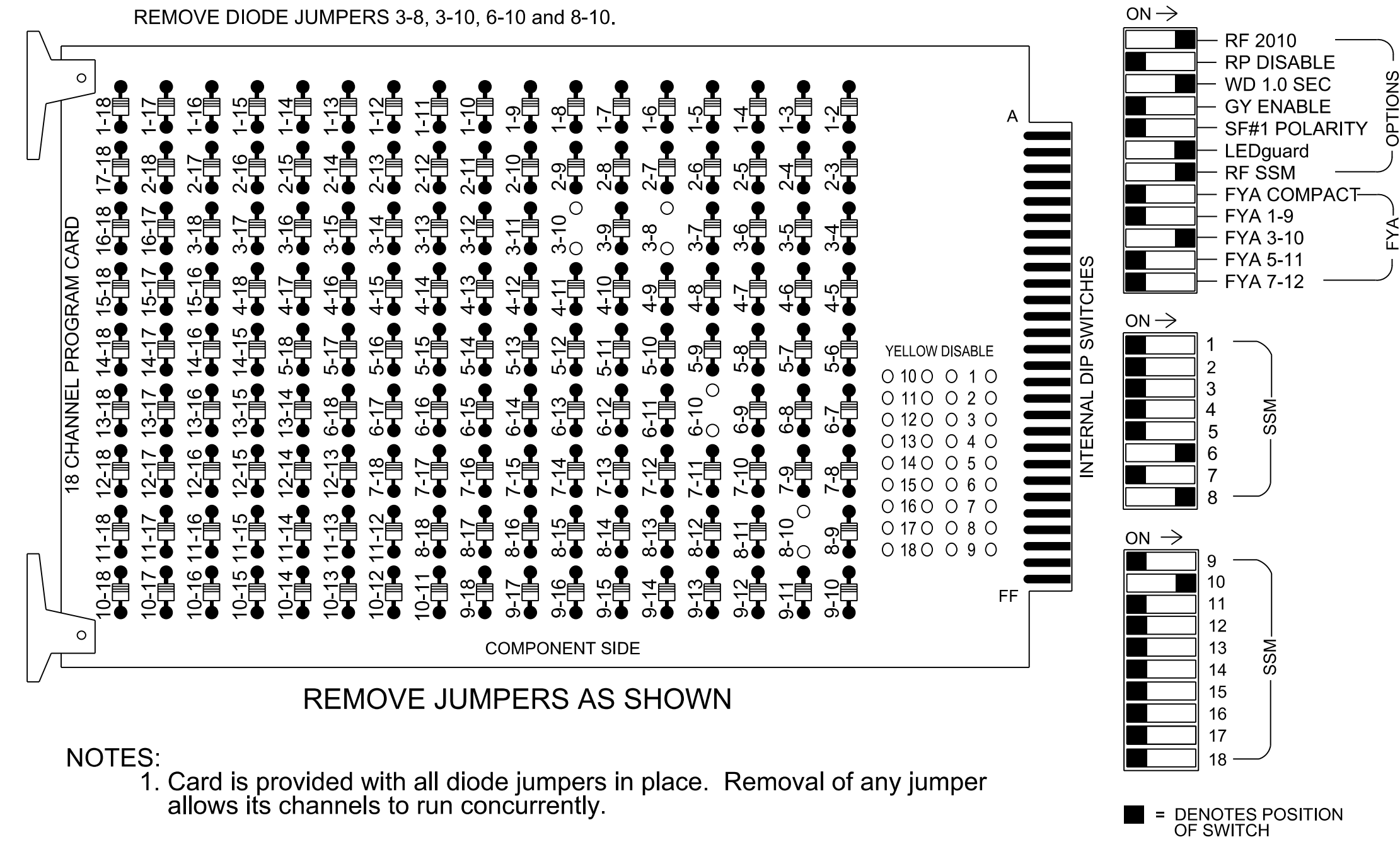
DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED

SEAL
 NORTH CAROLINA PROFESSIONAL ENGINEER
 SEAL 056142
 W. PORTER JONES
 PORTER JONES
 Signature: Porter Jones
 Date: 2/12/2024
 SIG. INVENTORY NO. 09-0984

2/12/2024
 R:\Traffic\c4s1\gnal\090984...s1\gnal...XXXXXX.dgn
 wplones

18 CHANNEL IP CONFLICT MONITOR PROGRAMMING DETAIL

(remove jumpers and set switches as shown)



- NOTES:
- Card is provided with all diode jumpers in place. Removal of any jumper allows its channels to run concurrently.
 - Ensure jumpers SEL2-SEL5 and SEL9 are present on the monitor board.
 - Ensure that the Red Enable is active at all times during normal operation.
 - Integrate monitor with Ethernet network in cabinet.

NOTES

- To prevent "flash-conflict" problems, insert red flash program blocks for all unused vehicle load switches in the output file. The installer shall verify that signal heads flash in accordance with the signal plan.
- Program controller to start up in phase 6 Green No Walk.
- If this signal will be managed by an ATMS software, enable controller and detector logging for all detectors used at this location.
- The cabinet and controller are part of the US 158 Signal System, Signal System : D09-11_Winston-Salem System.

EQUIPMENT INFORMATION

Controller.....2070LX
 Cabinet.....332 w/ Aux
 Software.....Q-Free MAXTIME
 Cabinet Mount.....Base
 Output File Positions.....18 With Aux. Output File
 Load Switches Used.....S4, S8, S11, AUX S2
 Phases Used.....3, 6, 8
 Overlap "1".....NOT USED
 Overlap "2".....*
 Overlap "3".....NOT USED
 Overlap "4".....NOT USED

*See overlap programming detail on sheet 2.

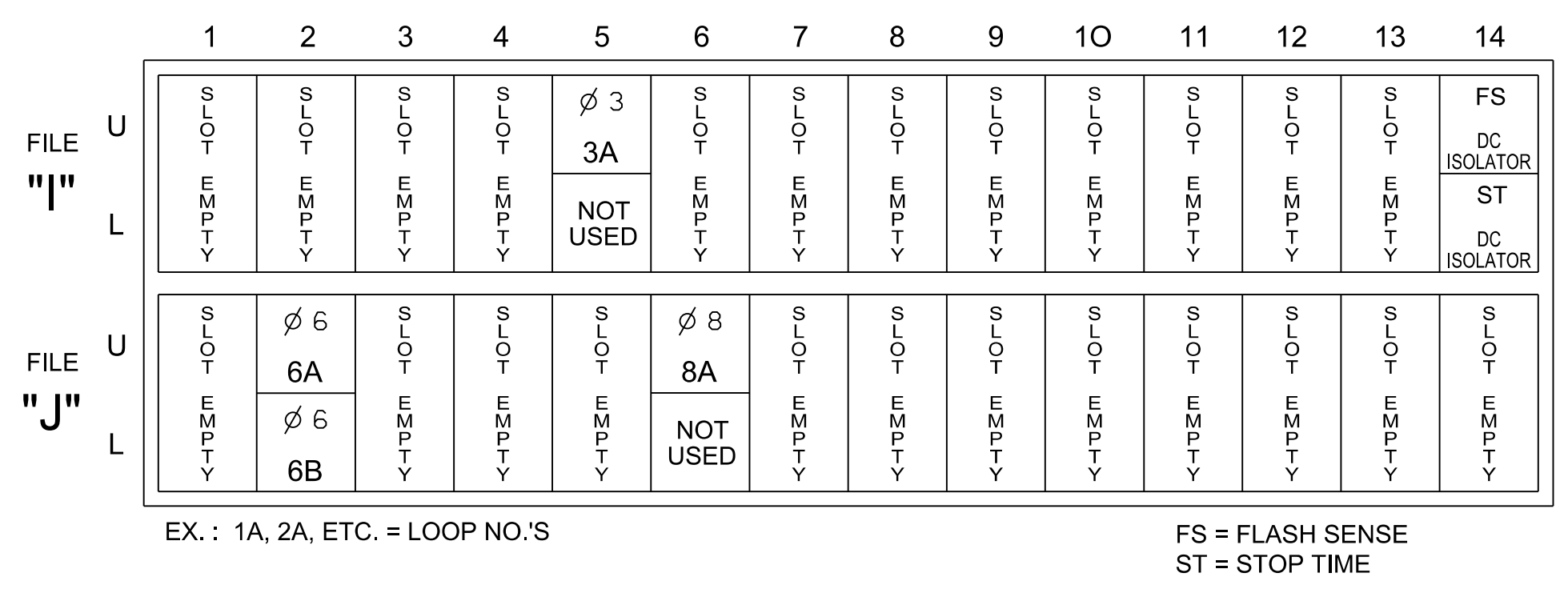
SIGNAL HEAD HOOK-UP CHART

LOAD SWITCH NO.	S1	S2	S3	S4	S5	S6	S7	S8	S9	S10	S11	S12	AUX S1	AUX S2	AUX S3	AUX S4	AUX S5	AUX S6
CMU CHANNEL NO.	1	2	13	3	4	14	5	6	15	7	8	16	9	10	17	11	12	18
PHASE	1	2	2 PED	3	4	4 PED	5	6	6 PED	7	8	8 PED	OL1	OL2	SPARE	OL3	OL4	SPARE
SIGNAL HEAD NO.	NU	NU	NU	31*	NU	NU	NU	61,62	NU	NU	81,82	NU	NU	31*	NU	NU	NU	NU
RED								134			107							
YELLOW				*				135										
GREEN																		
RED ARROW																		A124
YELLOW ARROW											108							A125
FLASHING YELLOW ARROW																		A126
GREEN ARROW					118			136			109							

NU = Not Used
 * Denotes install load resistor. See load resistor installation detail this sheet.
 * See pictorial of head wiring in detail this sheet.

INPUT FILE POSITION LAYOUT

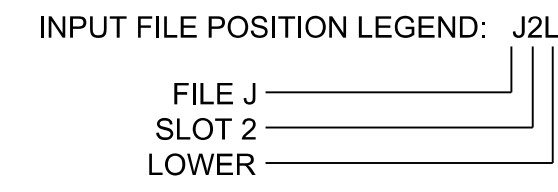
(front view)



INPUT FILE CONNECTION & PROGRAMMING CHART

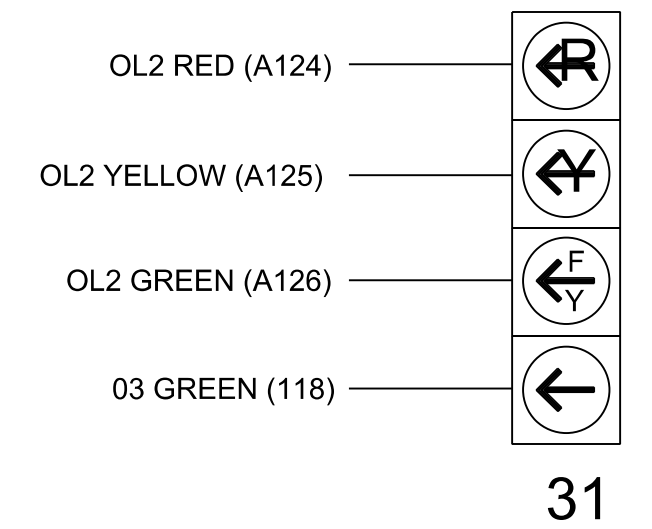
LOOP NO.	LOOP TERMINAL	INPUT FILE POS.	PIN NO.	INPUT POINT	DETECTOR NO.	CALL PHASE	DELAY TIME	EXTEND TIME	EXTEND	ADDED INITIAL	CALL	DELAY DURING GREEN
3A	TB4-5,6	I5U	58	20	7	3	15		X		X	
6A	TB3-5,6	J2U	40	2	16	6			X	X	X	
6B	TB3-7,8	J2L	44	6	17	6			X	X	X	
8A	TB5-9,10	J6U	42	4	22	8	15		X		X	

* For the detector to work as shown on the signal design plan, see the vehicle detector setup programming detail for alternate phasing on sheet 2.



FYA SIGNAL WIRING DETAIL

(wire signal heads as shown)



LOAD RESISTOR INSTALLATION DETAIL

(install resistors as shown)



Signal Upgrade - Final Design - Electrical Detail - Sheet 1 of 2

Electrical and Programming Details For:

Prepared for the Offices of:

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US 158 WB (Reidsville Rd.)
 at
 SR 2385 (Darrow Road)

Division 9 Forsyth County Walkertown

PLAN DATE: February 2024 REVIEWED BY: DT Sears

PREPARED BY: WP Erickson-Jones REVIEWED BY:

REVISIONS

REVISIONS	INIT.	DATE

750 N. Greenfield Pkwy, Garner, NC 27529

DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED

SEAL

NORTH CAROLINA PROFESSIONAL ENGINEER

SEAL 056142

W. PORTER JONES

DocuSigned by: Porter Jones

2/12/2024

SIG. INVENTORY NO. 09-0984

2/12/2024
 R:\Traffic\c4s1\gn01\gn01\is4050984e.s1g_dsn_XXXXXXX.dgn
 wp1.ones

MAXTIME OVERLAP PROGRAMMING DETAIL FOR DEFAULT PHASING

Front Panel
Main Menu >Controller >Overlap >Overlap Parameters/Overlap Timings

Web Interface
Home >Controller >Overlap Configuration >Overlaps

Overlap Plan 1

Overlap	2
Type	FYA 4 - Section
Included Phases	6
Modifier Phases	3
Modifier Overlaps	-
Trail Green	0
Trail Yellow	0.0
Trail Red	0.0

MAXTIME ALTERNATE PHASING ACTIVATION DETAIL

To run alternate phasing, select a Pattern that is programmed to run Overlap Plan 2 and Detector Plan 2.
A Pattern can be selected through the scheduler or manually by changing the Operational Mode.

PHASING	OVERLAP PLAN	VEH DET PLAN
ACTIVE PLAN REQUIRED TO RUN DEFAULT PHASING	1	1
ACTIVE PLAN REQUIRED TO RUN ALTERNATE PHASING	2	2

MAXTIME OVERLAP PROGRAMMING DETAIL FOR ALTERNATE PHASING

Front Panel
Main Menu >Controller >Overlap >Overlap Parameters/Overlap Timings

Web Interface
Home >Controller >Overlap Configuration >Overlaps

In the table view of the web interface, right click on "Overlap" in the top left corner of the table. Copy the entire contents of Overlap Plan 1. Paste Overlap Plan 1 into Overlap Plan 2. Modify Overlap Plan 2 as shown below and save changes.

Overlap Plan 2

Overlap	2
Type	FYA 4 - Section
Included Phases	-
Modifier Phases	3
Modifier Overlaps	-
Trail Green	0
Trail Yellow	0.0
Trail Red	0.0

← NOTICE INCLUDED PHASE

ALTERNATE PHASING CHANGE SUMMARY

THE FOLLOWING IS A SUMMARY OF WHAT TAKES PLACE WHEN OVERLAP PLAN 2 AND VEHICLE DETECTOR PLAN 2 ACTIVATE TO CALL THE "ALTERNATE PHASING":

OVERLAP PLAN 2: Modifies overlap included phases for head 31 to run protected turns only.

VEH DET PLAN 2: Reduces delay time for phase 3 call on loop 3A to 0 seconds.

MAXTIME DETECTOR PROGRAMMING DETAIL FOR ALTERNATE PHASING LOOPS 3A

Front Panel
Main Menu >Controller >Detector >Veh Det Plans

Web Interface
Home >Controller >Detector Configuration >Vehicle Detectors

In the table view of web interface right click on "Detector" in the top left corner of the table. Copy the entire contents of Detector Plan 1. Paste Detector Plan 1 into Detector Plan 2. Modify Detector Plan 2 as shown below and save changes.

Plan 2

Detector	Call Phase	Delay
3A 7	3	0

MAXTIME ALTERNATE PHASING PATTERN PROGRAMMING DETAIL

Front Panel
Main Menu >Controller >Coordination >Patterns

Web Interface
Home >Controller >Coordination >Patterns


Pattern Parameters

Pattern	Veh Det Plan	Overlap Plan
*	2	2

*The Pattern number(s) are to be determined by the Division and/or City Traffic Engineer.

THIS ELECTRICAL DETAIL IS FOR
THE SIGNAL DESIGN: 09-0984
DESIGNED: February 2024
SEALED: February 12, 2024
REVISED:

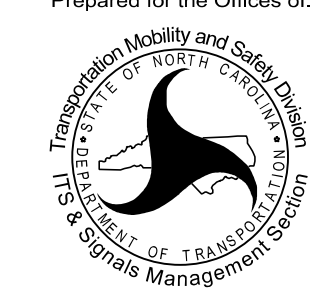
Signal Upgrade - Final Design - Electrical Detail - Sheet 2 of 2



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Electrical and Programming Details For:

Prepared for the Offices of:



750 N. Greenfield Pkwy, Garner, NC 27529

US 158 WB (Reidsville Rd.)
at
SR 2385 (Darrow Road)


Division 9 Forsyth County Walkertown

PLAN DATE: February 2024 REVIEWED BY: DT Sears

PREPARED BY: WP Erickson-Jones REVIEWED BY:

REVISIONS	INIT.	DATE

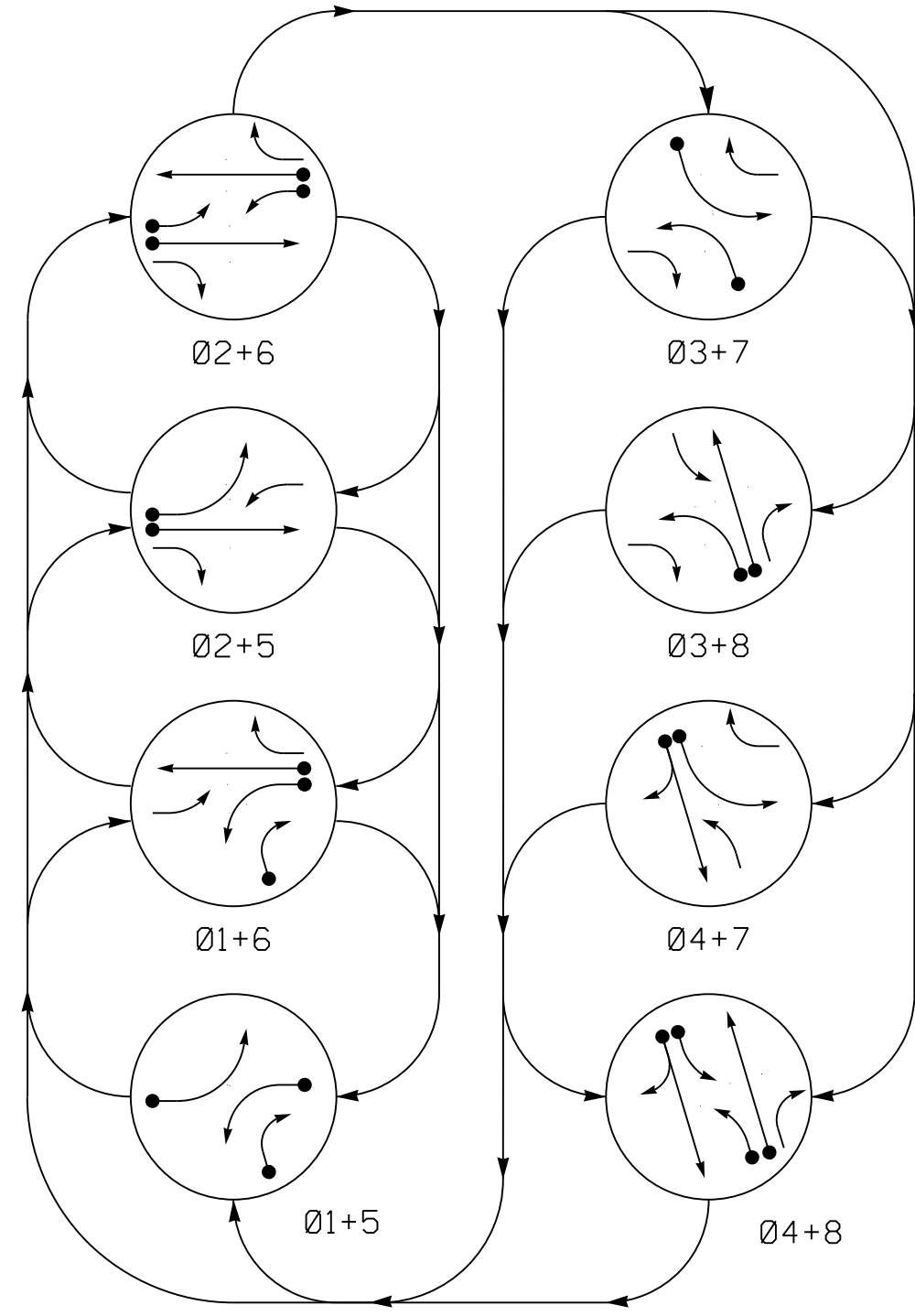
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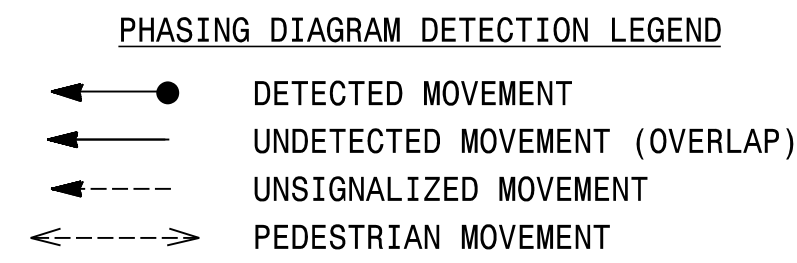
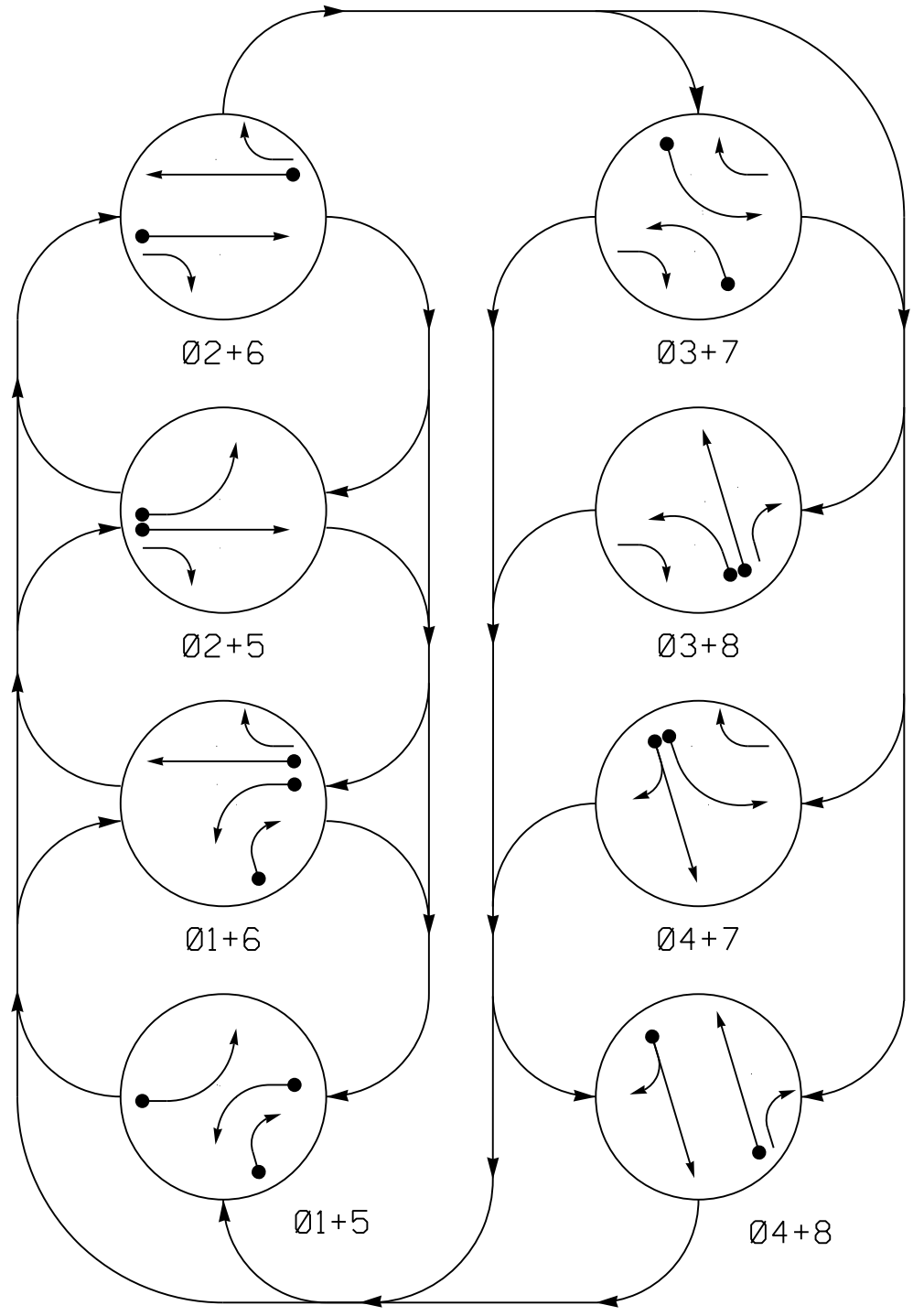
DocuSigned by:
Porter Jones
2/12/2024

SIG. INVENTORY NO. 09-0984

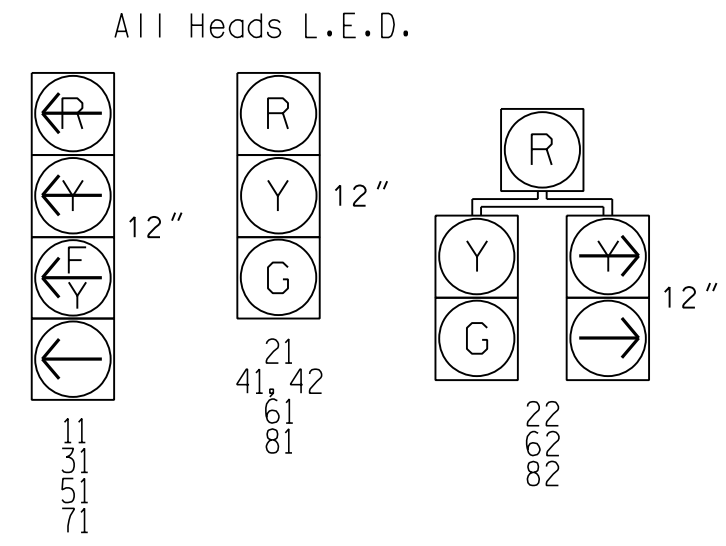
DEFAULT PHASING DIAGRAM



ALTERNATE PHASING DIAGRAM



SIGNAL FACE I.D.



DEFAULT PHASING TABLE OF OPERATION

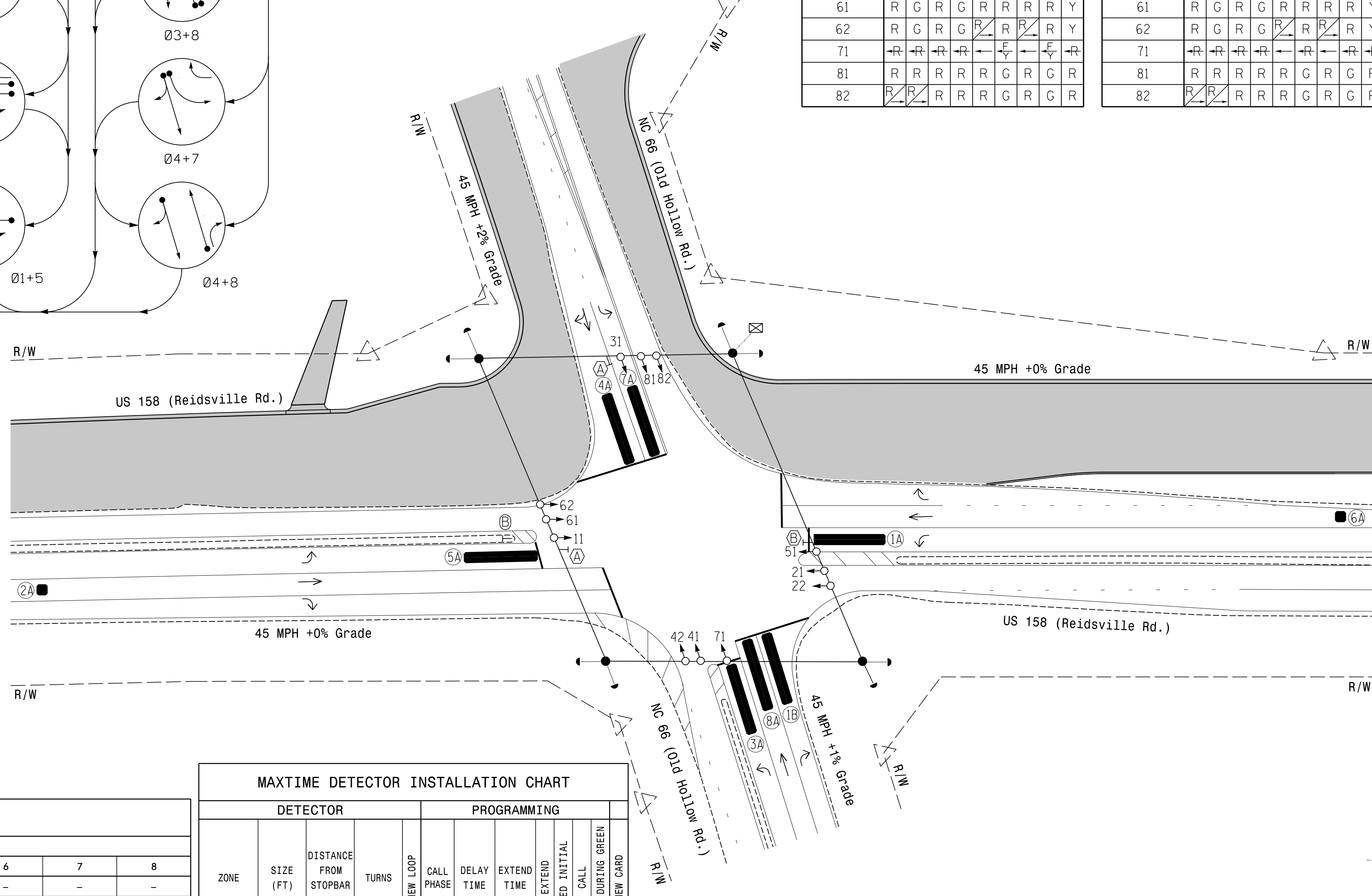
SIGNAL FACE	PHASE							
	Ø 1 + 5	Ø 1 + 6	Ø 2 + 5	Ø 2 + 6	Ø 3 + 7	Ø 3 + 8	Ø 4 + 7	Ø 4 + 8
11	←	←	F	F	R	R	R	Y
21	R	R	G	G	R	R	R	Y
22	R	R	G	G	R	R	R	Y
31	R	R	R	R	←	←	F	F
41,42	R	R	R	R	R	R	G	G
51	←	F	←	F	R	R	R	Y
61	R	G	R	G	R	R	R	Y
62	R	G	R	G	R	R	R	Y
71	R	R	R	R	←	←	F	F
81	R	R	R	R	R	G	G	R
82	R	R	R	R	R	G	G	R

ALTERNATE PHASING TABLE OF OPERATION

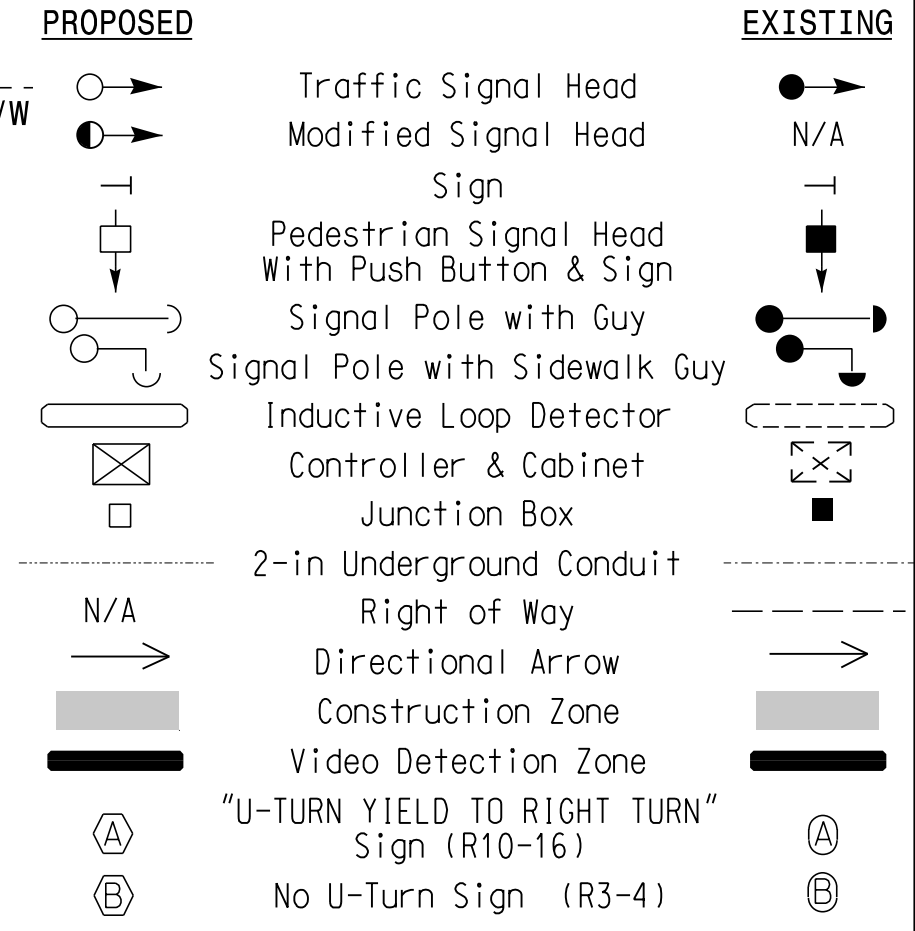
SIGNAL FACE	PHASE							
	Ø 1 + 5	Ø 1 + 6	Ø 2 + 5	Ø 2 + 6	Ø 3 + 7	Ø 3 + 8	Ø 4 + 7	Ø 4 + 8
11	←	←	R	R	R	R	R	Y
21	R	R	G	G	R	R	R	Y
22	R	R	G	G	R	R	R	Y
31	R	R	R	R	←	←	R	R
41,42	R	R	R	R	R	R	G	G
51	←	R	←	R	R	R	R	Y
61	R	G	R	G	R	R	R	Y
62	R	G	R	G	R	R	R	Y
71	R	R	R	R	←	←	R	R
81	R	R	R	R	R	G	G	R
82	R	R	R	R	R	G	G	R

8 Phase Fully Actuated (Isolated)

- NOTES
1. Refer to "Roadway Standard Drawings NCDOT" dated January 2024 and "Standard Specifications for Roads and Structures" dated January 2024.
 2. Do not program signal for late night flashing operation unless otherwise directed by the Engineer.
 3. Phase 1 and/or phase 5 may be lagged.
 4. Phase 3 and/or phase 7 may be lagged.
 5. Set all detector units to presence mode.
 6. Locate new cabinet so as not to obstruct sight distance of vehicles turning right on red.
 7. This intersection uses video detection. Install detectors according to the manufacturer's instructions to achieve the desired detection.
 8. The Division Traffic Engineer will determine the hours of use for each phasing plan.



LEGEND



MAXTIME TIMING CHART

FEATURE	PHASE							
	1	2	3	4	5	6	7	8
Walk *	-	-	-	-	-	-	-	-
Ped Clear *	-	-	-	-	-	-	-	-
Min Green	7	12	7	7	7	12	7	7
Passage *	2.0	6.0	2.0	2.0	2.0	6.0	2.0	2.0
Max 1 *	20	90	20	40	20	90	20	40
Yellow Change	3.0	4.5	3.0	4.4	3.0	4.5	3.0	4.4
Red Clear	2.8	1.7	3.3	1.9	3.1	1.7	2.9	1.9
Added Initial *	-	2.5	-	-	-	2.5	-	-
Maximum Initial *	-	34	-	-	-	34	-	-
Time Before Reduction *	-	15	-	-	-	15	-	-
Time To Reduce *	-	30	-	-	-	30	-	-
Minimum Gap	-	3.0	-	-	-	3.0	-	-
Advance Walk	-	-	-	-	-	-	-	-
Non Lock Detector	X	-	X	X	X	-	X	X
Vehicle Recall	-	MIN RECALL	-	-	-	MIN RECALL	-	-
Dual Entry	-	-	-	X	-	-	-	X

* These values may be field adjusted. Do not adjust Min Green and Extension times for phases 2 and 6 lower than what is shown. Min Green for all other phases should not be lower than 4 seconds.

MAXTIME DETECTOR INSTALLATION CHART

ZONE	SIZE (FT)	DISTANCE FROM STOPBAR (FT)	TURNS	NEW LOOP	PROGRAMMING					NEW CARD		
					CALL PHASE	DELAY TIME	EXTEND TIME	EXTEND INITIAL	CALL			
1A *	6X40	0	*	*	1	15#	-	X	-	X	-	*
1B *	6X40	0	*	*	6@	3	-	X	-	X	X	*
2A *	6X6	300	*	*	2	-	-	X	X	X	-	*
3A *	6X40	0	*	*	3	15#	-	X	-	X	-	*
4A *	6X40	0	*	*	8@	3	-	X	-	X	-	*
5A *	6X40	0	*	*	4	10	-	X	-	X	-	*
6A *	6X6	300	*	*	2@	3	-	X	-	X	X	*
7A *	6X40	0	*	*	6	15#	-	X	-	X	-	*
8A *	6X40	0	*	*	4@	3	-	X	-	X	-	*
					8	-	-	X	-	X	-	*

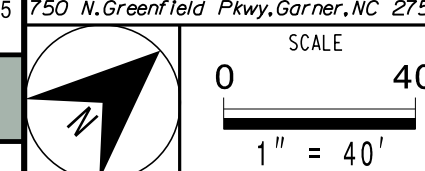
Disable Delay During Alternate Phasing Operation.
Reduce Delay to 3 Seconds During Alternate Phasing Operation.
@ Disable Phase Call For Zone(s) During Alternate Phasing Operation.
* Video Detection Zone

Signal Upgrade - Temporary Design 1 (TMP Phase I)

US 158 (Reidsville Rd.)
at
NC 66 (Old Hollow Rd.)
Division 9 Forsyth County Walkertown
PLAN DATE: February 2024 REVIEWED BY: WP Erickson-Jones
PREPARED BY: H Townsend REVIEWED BY:

DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED

DocuSign
Porter Jones
2/12/2024



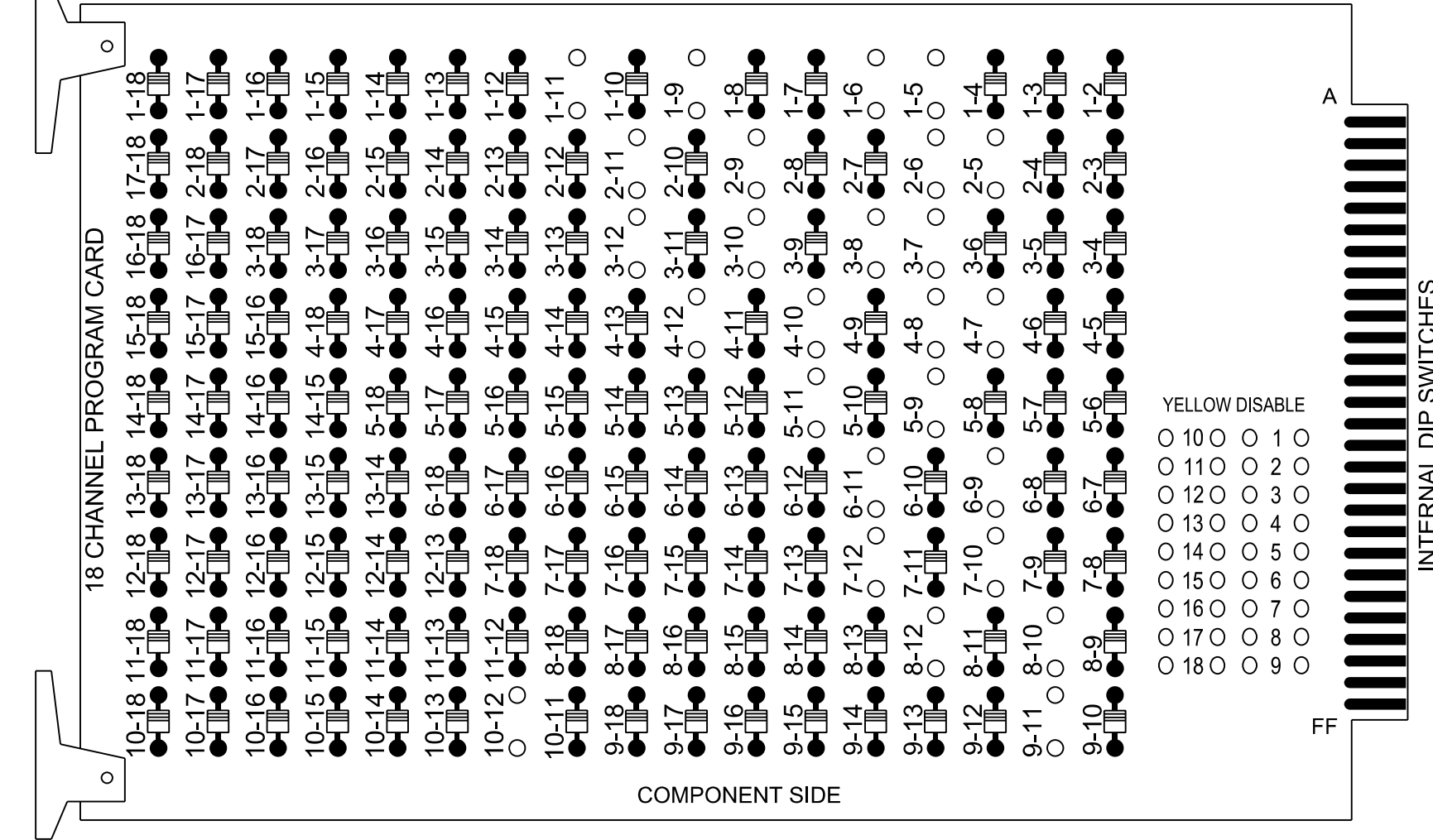
REVISIONS	INIT.	DATE

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18 CHANNEL IP CONFLICT MONITOR PROGRAMMING DETAIL

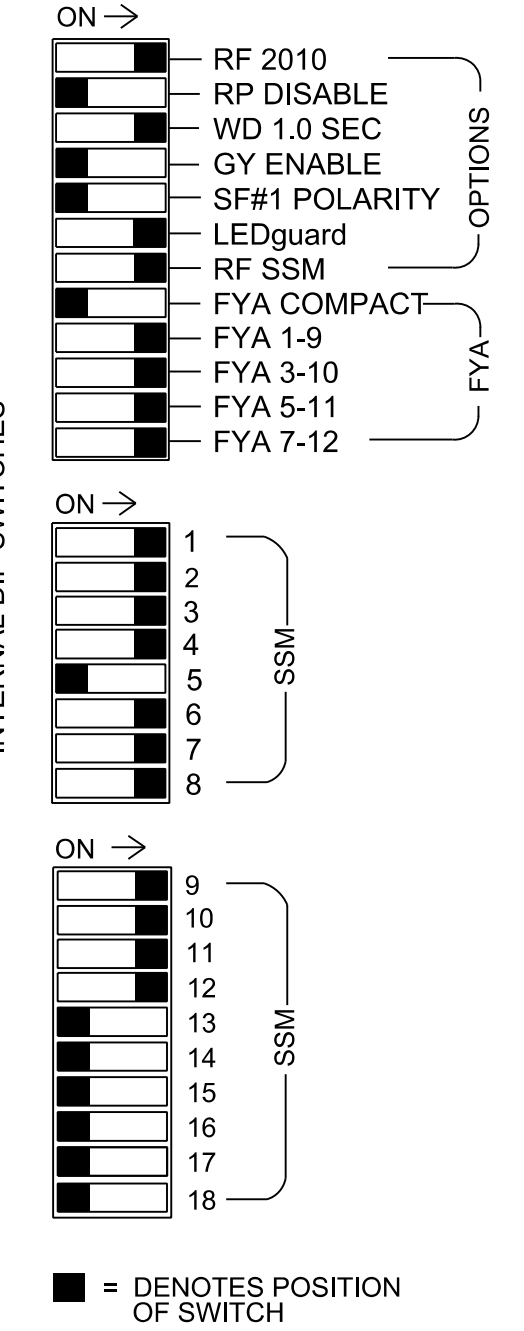
(remove jumpers and set switches as shown)

REMOVE DIODE JUMPERS 1-5, 1-6, 1-9, 1-11, 2-5, 2-6, 2-9, 2-11, 3-7, 3-8, 3-10, 3-12, 4-7, 4-8, 4-10, 4-12, 5-9, 5-11, 6-9, 6-11, 7-10, 7-12, 8-10, 8-12, 9-11 and 10-12.



REMOVE JUMPERS AS SHOWN

- NOTES:
- Card is provided with all diode jumpers in place. Removal of any jumper allows its channels to run concurrently.
 - Ensure jumpers SEL2-SEL5 and SEL9 are present on the monitor board.
 - Ensure that the Red Enable is active at all times during normal operation.
 - Connect serial cable from conflict monitor to comm. port 1 of 2070 controller. Ensure conflict monitor communicates with 2070.



NOTES

- To prevent "flash-conflict" problems, insert red flash program blocks for all unused vehicle load switches in the output file. The installer shall verify that signal heads flash in accordance with the signal plan.
- Program phases 4 and 8 for Dual Entry.
- Program controller to start up in phase 2 Green No Walk and 6 Green No Walk.
- If this signal will be managed by an ATMS software, enable controller and detector logging for all detectors used at this location.

EQUIPMENT INFORMATION

Controller.....2070LX
 Cabinet.....332 w/ Aux
 Software.....Q-Free MAXTIME
 Cabinet Mount.....Base
 Output File Positions.....18 With Aux. Output File
 Load Switches Used.....S1, S2, S4, S5, S7, S8, S10, S11, AUX S1, AUX S2, AUX S4, AUX S5
 Phases Used.....1, 2, 3, 4, 5, 6, 7, 8
 Overlap "1".....
 Overlap "2".....
 Overlap "3".....
 Overlap "4".....

*See overlap programming detail on sheet 2.

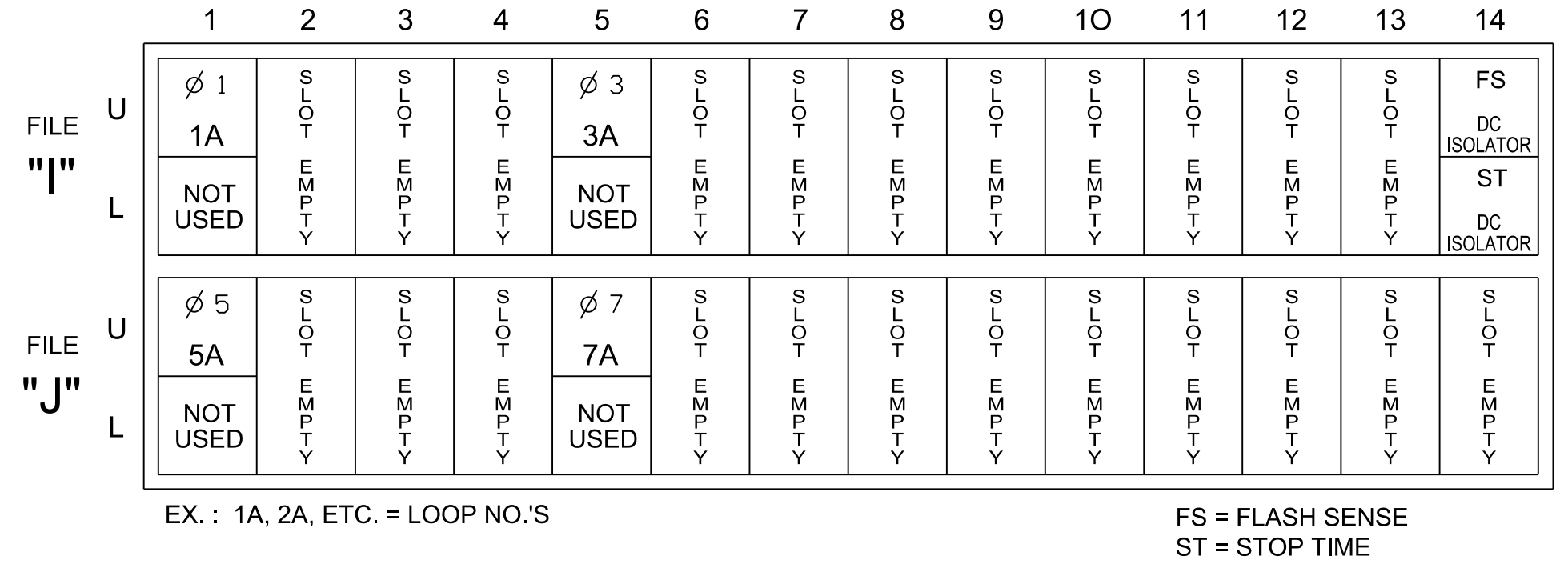
SIGNAL HEAD HOOK-UP CHART

LOAD SWITCH NO.	S1	S2	S3	S4	S5	S6	S7	S8	S9	S10	S11	S12	AUX S1	AUX S2	AUX S3	AUX S4	AUX S5	AUX S6
CMU CHANNEL NO.	1	2	13	3	4	14	5	6	15	7	8	16	9	10	17	11	12	18
PHASE	1	2	2 PED	3	4	4 PED	5	6	6 PED	7	8	8 PED	OL1	OL2	SPARE	OL3	OL4	SPARE
SIGNAL HEAD NO.	11*	82	21,22	22	31*	41,42	51*	61,62	62	71*	81,82	82	11*	31*	51*	71*	71*	NU
RED	*	128		*	101		134		*	107								
YELLOW		129			102		*	135		108								
GREEN		130			103		136			109								
RED ARROW													A121	A124		A114	A101	
YELLOW ARROW	126			117						123			A122	A125		A115	A102	
FLASHING YELLOW ARROW													A123	A126		A116	A103	
GREEN ARROW	127	127		118	118		133		124	124								

NU = Not Used
 * Denotes install load resistor. See load resistor installation detail this sheet.
 *See pictorial of head wiring in detail this sheet.

INPUT FILE POSITION LAYOUT

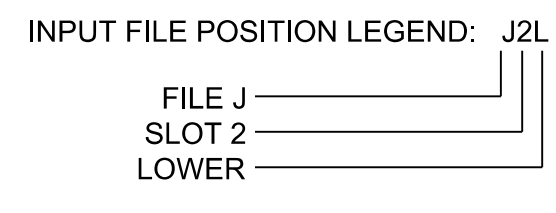
(front view)



INPUT FILE CONNECTION & PROGRAMMING CHART

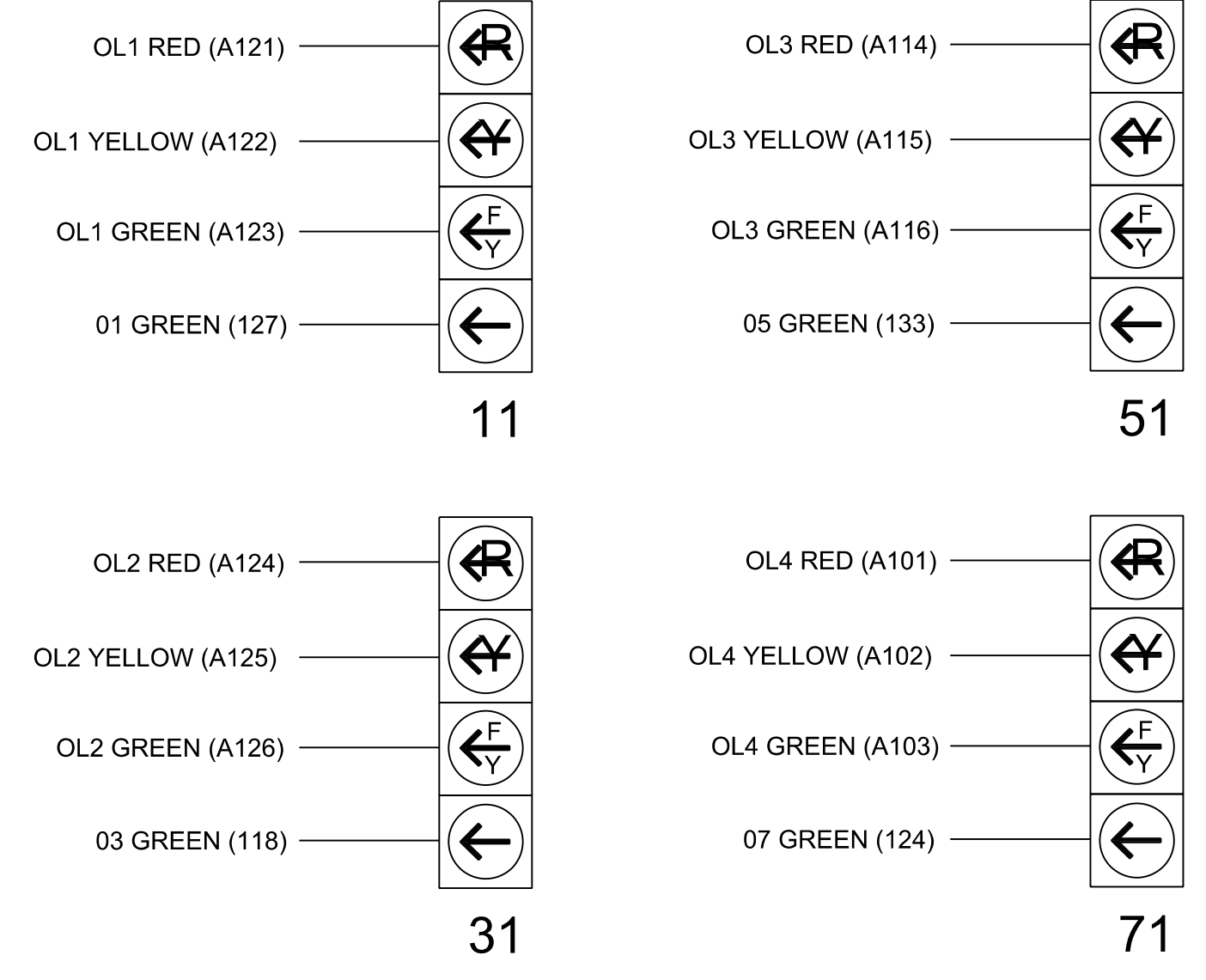
LOOP NO.	LOOP TERMINAL	INPUT FILE POS.	PIN NO.	INPUT POINT	DETECTOR NO.	CALL PHASE	DELAY TIME	EXTEND TIME	EXTEND	ADDED INITIAL	CALL	DELAY DURING GREEN
1A	TB2-1,2	I1U	56	18	1	1	15		X		X	
				-	29	★	6	3		X	X	X
3A	TB4-5,6	I5U	58	20	7	3	15		X		X	
				-	30	★	8	3		X	X	
5A	TB3-1,2	J1U	55	17	15	5	15		X		X	
				-	31	★	2	3		X	X	X
7A	TB5-5,6	J5U	57	19	21	7	15		X		X	
				-	32	★	4	3		X	X	

★ For the detector to work as shown on the signal design plan, see the vehicle detector setup programming detail for alternate phasing on sheet 2.



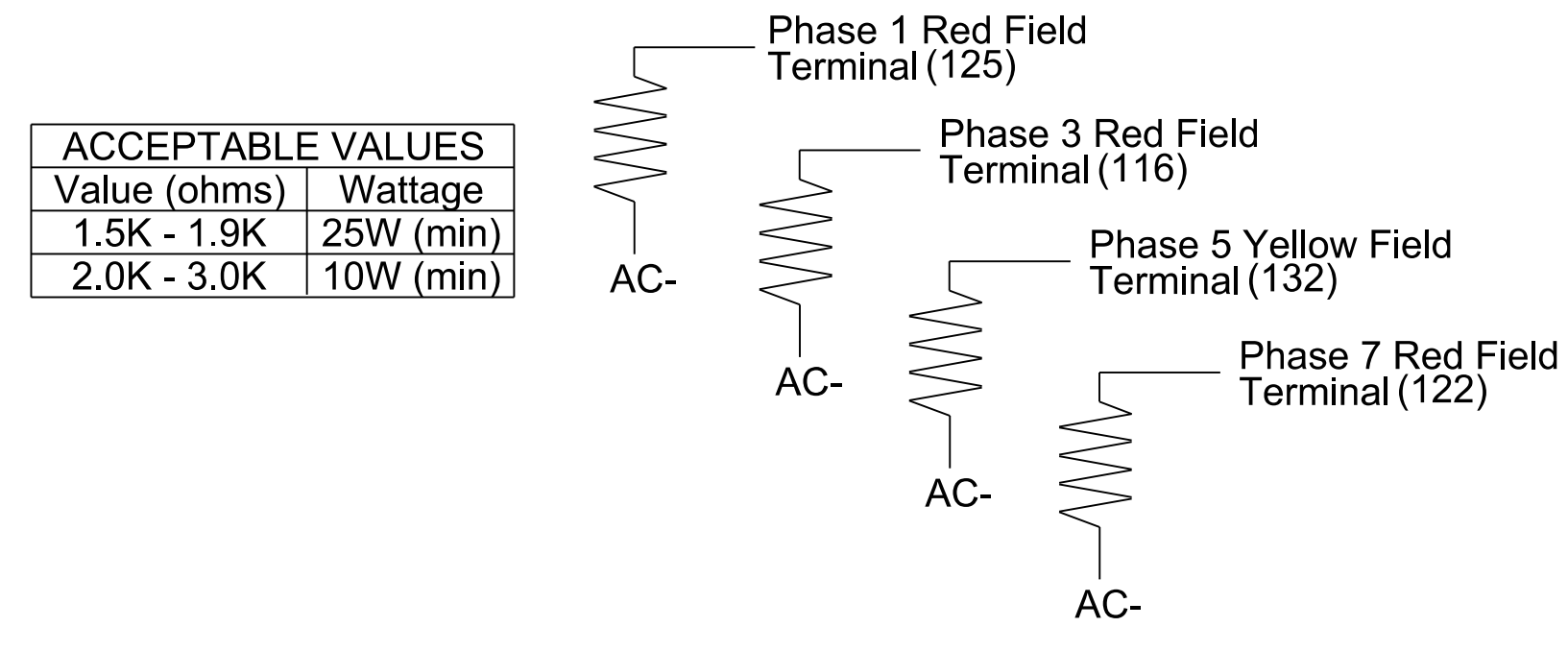
FYA SIGNAL WIRING DETAIL

(wire signal heads as shown)



LOAD RESISTOR INSTALLATION DETAIL

(install resistors as shown)



SPECIAL DETECTOR NOTE

Install a video detection system for vehicle detection for zones. 1A, 1B, 2A, 3A, 4A, 5A, 6A, 7A and 8A. Perform installation according to manufacturer's directions and NCDOT engineer approved mounting locations to accomplish the detection schemes shown on the Signal Design Plans.

For detecton zones 1A, 3A, 5A, and 7A, detector card placement and slots are typical for a NCDOT installation. Inputs associated with these slots are compatible with time of day instructions located on sheet 2 of this electrical detail.

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THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 09-0264T1
 DESIGNED: February 2024
 SEALED: February 12, 2024
 REVISED:

Signal Upgrade - Temporary Design 1 (TMP Phase I) - Electrical Detail - Sheet 1 of 2

Electrical and Programming Details For: US 158 (Reidsville Rd.) at NC 66 (Old Hollow Rd.)

Prepared for the Offices of: [Seal of North Carolina State Engineer Porter Jones]

Division 9 Forsyth County Walkertown
 PLAN DATE: February 2024 REVIEWED BY: DT Sears
 PREPARED BY: WP Erickson-Jones REVIEWED BY:
 REVISIONS INIT. DATE

2/12/2024
 PORTER JONES
 SEAL 056142
 SEAL 056142
 DATE 2/12/2024
 SIG. INVENTORY NO. 09-0264T1

MAXTIME DETECTOR PROGRAMMING DETAIL FOR ALTERNATE PHASING LOOPS 1A, 3A, 5A & 7A

Front Panel
Main Menu >Controller >Detector >Veh Det Plans

Web Interface
Home >Controller >Detector Configuration >Vehicle Detectors

In the table view of web interface right click on "Detector" in the top left corner of the table. Copy the entire contents of Detector Plan 1. Paste Detector Plan 1 into Detector Plan 2. Modify Detector Plan 2 as shown below and save changes.

Plan 2			
	Detector	Call Phase	Delay
1A	1	1	0
	29	0	3
3A	7	3	0
	30	0	3
5A	15	5	0
	31	0	3
7A	21	7	3.0
	32	0	-

MAXTIME ALTERNATE PHASING ACTIVATION DETAIL

To run alternate phasing, select a Pattern that is programmed to run Overlap Plan 2 and Detector Plan 2. A Pattern can be selected through the scheduler or manually by changing the Operational Mode.

PHASING	OVERLAP PLAN	VEH DET PLAN
ACTIVE PLAN REQUIRED TO RUN DEFAULT PHASING	1	1
ACTIVE PLAN REQUIRED TO RUN ALTERNATE PHASING	2	2

ALTERNATE PHASING CHANGE SUMMARY

THE FOLLOWING IS A SUMMARY OF WHAT TAKES PLACE WHEN OVERLAP PLAN 2 AND VEHICLE DETECTOR PLAN 2 ACTIVATE TO CALL THE "ALTERNATE PHASING":

OVERLAP PLAN 2: Modifies overlap included phases for heads 11, 31, 51, and 71 to run protected turns only.

VEH DET PLAN 2: Disables phase 6 call on loop 1A and reduces delay time for phase 1 call on loop 1A to 0 seconds.

Disables phase 8 call on loop 3A and reduces delay time for phase 3 call on loop 3A to 0 seconds.

Disables phase 2 call on loop 5A and reduces delay time for phase 5 call on loop 5A to 0 seconds.

Disables phase 4 call on loop 7A and reduces delay time for phase 7 call on loop 7A to 3 seconds.

FLASHER CIRCUIT MODIFICATION DETAIL

IN ORDER TO INSURE THAT SIGNALS FLASH CONCURRENTLY ON THE SAME APPROACH, MAKE THE FOLLOWING FLASHER CIRCUIT CHANGES:

1. ON REAR OF PDA - REMOVE WIRE FROM TERM. T2-4 AND TERMINATE ON T2-2.
2. ON REAR OF PDA - REMOVE WIRE FROM TERM. T2-5 AND TERMINATE ON T2-3.
3. REMOVE FLASHER UNIT 2.

THE CHANGES LISTED ABOVE TIES ALL PHASES AND OVERLAPS TO FLASHER UNIT 1.

MAXTIME OVERLAP PROGRAMMING DETAIL FOR DEFAULT PHASING

Front Panel
Main Menu >Controller >Overlap >Overlap Parameters/Overlap Timings

Web Interface
Home >Controller >Overlap Configuration >Overlaps

Overlap Plan 1				
Overlap	1	2	3	4
Type	FYA 4 - Section	FYA 4 - Section	FYA 4 - Section	FYA 4 - Section
Included Phases	2	4	6	8
Modifier Phases	1	3	5	7
Modifier Overlaps	-	-	-	-
Trail Green	0	0	0	0
Trail Yellow	0.0	0.0	0.0	0.0
Trail Red	0.0	0.0	0.0	0.0

MAXTIME OVERLAP PROGRAMMING DETAIL FOR ALTERNATE PHASING

Front Panel
Main Menu >Controller >Overlap >Overlap Parameters/Overlap Timings

Web Interface
Home >Controller >Overlap Configuration >Overlaps

In the table view of the web interface, right click on "Overlap" in the top left corner of the table. Copy the entire contents of Overlap Plan 1. Paste Overlap Plan 1 into Overlap Plan 2. Modify Overlap Plan 2 as shown below and save changes.

Overlap Plan 2				
Overlap	1	2	3	4
Type	FYA 4 - Section	FYA 4 - Section	FYA 4 - Section	FYA 4 - Section
Included Phases	-	-	-	-
Modifier Phases	1	3	5	7
Modifier Overlaps	-	-	-	-
Trail Green	0	0	0	0
Trail Yellow	0.0	0.0	0.0	0.0
Trail Red	0.0	0.0	0.0	0.0

← NOTICE INCLUDED PHASE

MAXTIME ALTERNATE PHASING PATTERN PROGRAMMING DETAIL

Front Panel
Main Menu >Controller >Coordination >Patterns

Web Interface
Home >Controller >Coordination >Patterns

Pattern Parameters

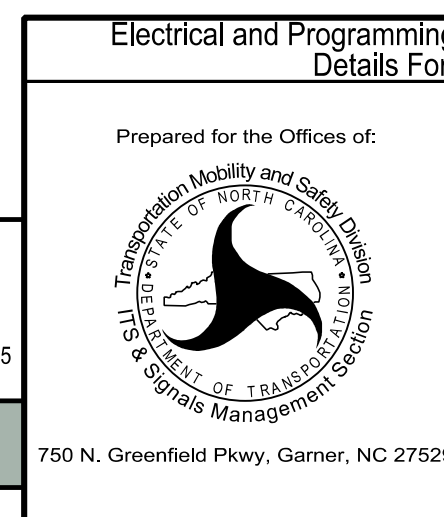
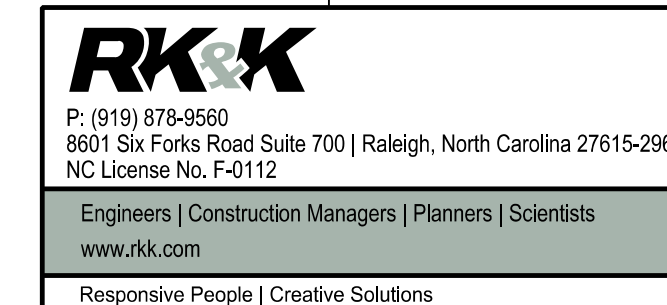
Pattern	Veh Det Plan	Overlap Plan
*	2	2

* The Pattern number(s) are to be determined by the Division and/or City Traffic Engineer.

Signal Upgrade - Temporary Design 1
(TMP Phase I) - Electrical Detail - Sheet 2 of 2

THIS ELECTRICAL DETAIL IS FOR
THE SIGNAL DESIGN: 09-0264T1
DESIGNED: February 2024
SEALED: February 12, 2024
REVISED:

DOCUMENT NOT CONSIDERED
FINAL UNLESS ALL
SIGNATURES COMPLETED



US 158 (Reidsville Rd.)
at
NC 66 (Old Hollow Rd.)

Division 9 Forsyth County Walkertown

PLAN DATE: February 2024 REVIEWED BY: DT Sears

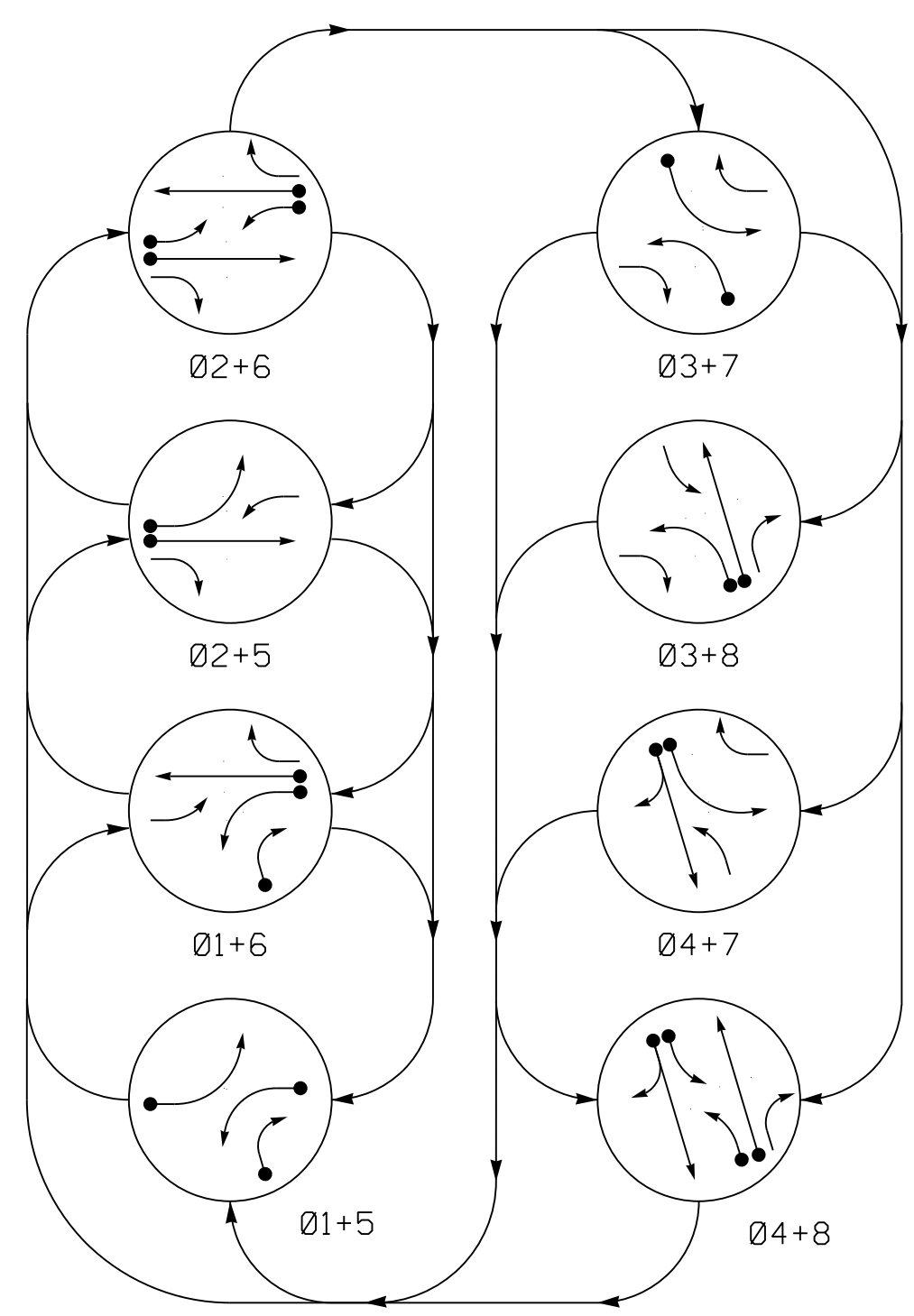
PREPARED BY: WP Erickson-Jones REVIEWED BY:

REVISIONS	INIT.	DATE

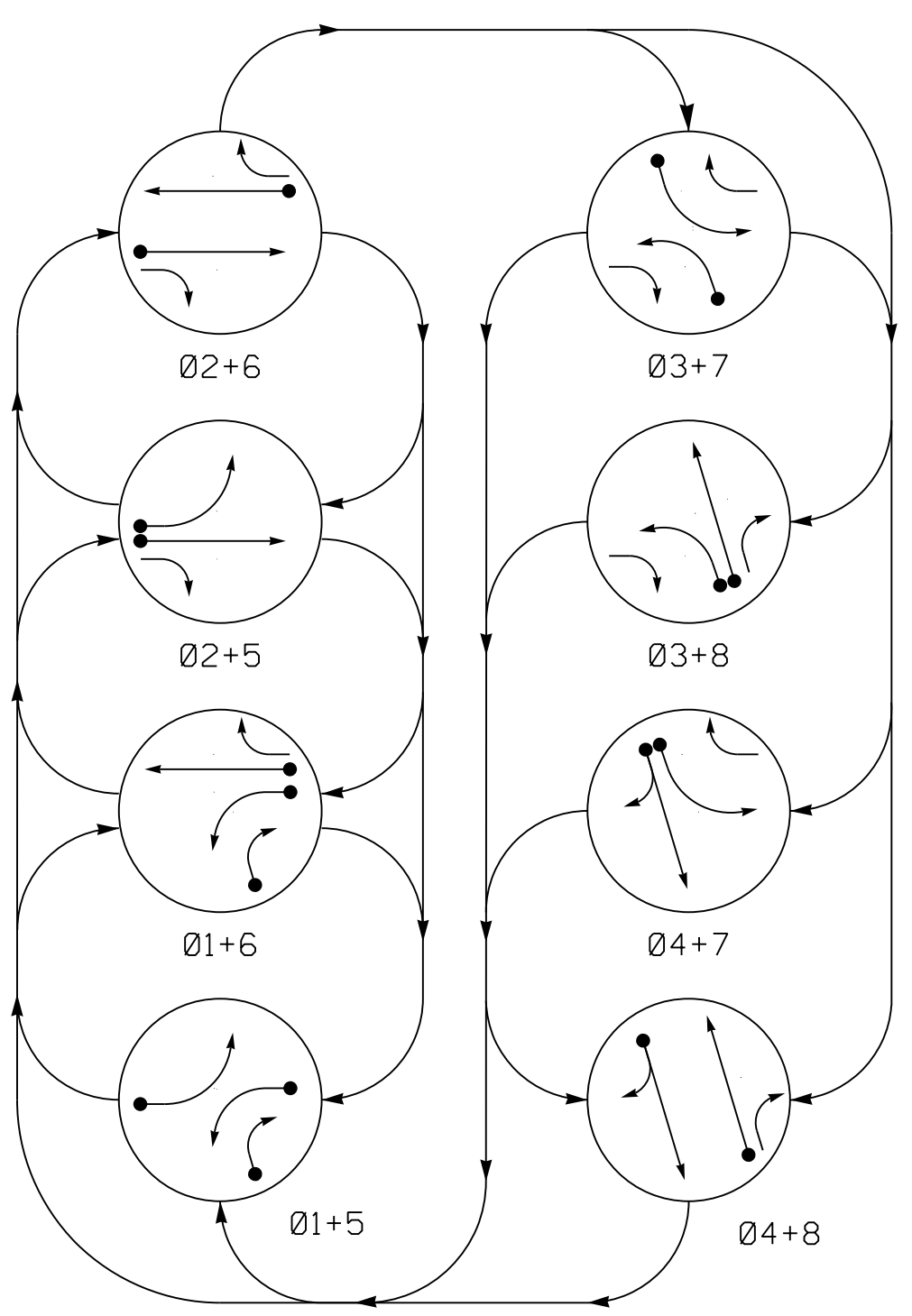
DocuSigned by: Porter Jones 2/12/2024

SIG. INVENTORY NO. 09-0264T1

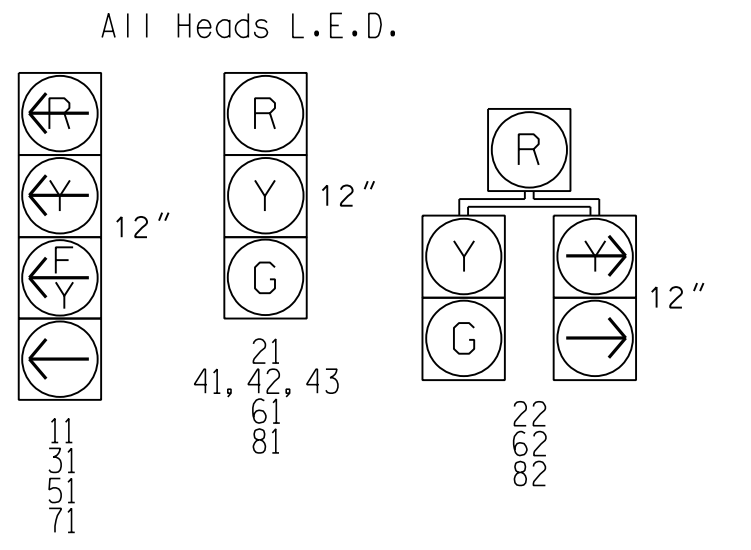
DEFAULT PHASING DIAGRAM



ALTERNATE PHASING DIAGRAM



SIGNAL FACE I.D.



DEFAULT PHASING TABLE OF OPERATION

SIGNAL FACE	PHASE							
	01+5	01+6	02+5	02+6	03+7	03+8	04+7	04+8
11	←	←	←	←	←	←	←	←
21	R	R	G	G	R	R	R	Y
22	R	R	G	G	ℓ	ℓ	R	Y
31	←	←	←	←	←	←	←	←
41,42,43	R	R	R	R	R	R	G	R
51	←	←	←	←	←	←	←	←
61	R	G	R	G	R	R	R	Y
62	R	G	R	G	ℓ	ℓ	R	Y
71	←	←	←	←	←	←	←	←
81	R	R	R	R	R	G	R	G
82	ℓ	ℓ	R	R	R	G	R	G

ALTERNATE PHASING TABLE OF OPERATION

SIGNAL FACE	PHASE							
	01+5	01+6	02+5	02+6	03+7	03+8	04+7	04+8
11	←	←	←	←	←	←	←	←
21	R	R	G	G	R	R	R	Y
22	R	R	G	G	ℓ	ℓ	R	Y
31	←	←	←	←	←	←	←	←
41,42,43	R	R	R	R	R	R	G	R
51	←	←	←	←	←	←	←	←
61	R	G	R	G	R	R	R	Y
62	R	G	R	G	ℓ	ℓ	R	Y
71	←	←	←	←	←	←	←	←
81	R	R	R	R	R	G	R	G
82	ℓ	ℓ	R	R	R	G	R	G

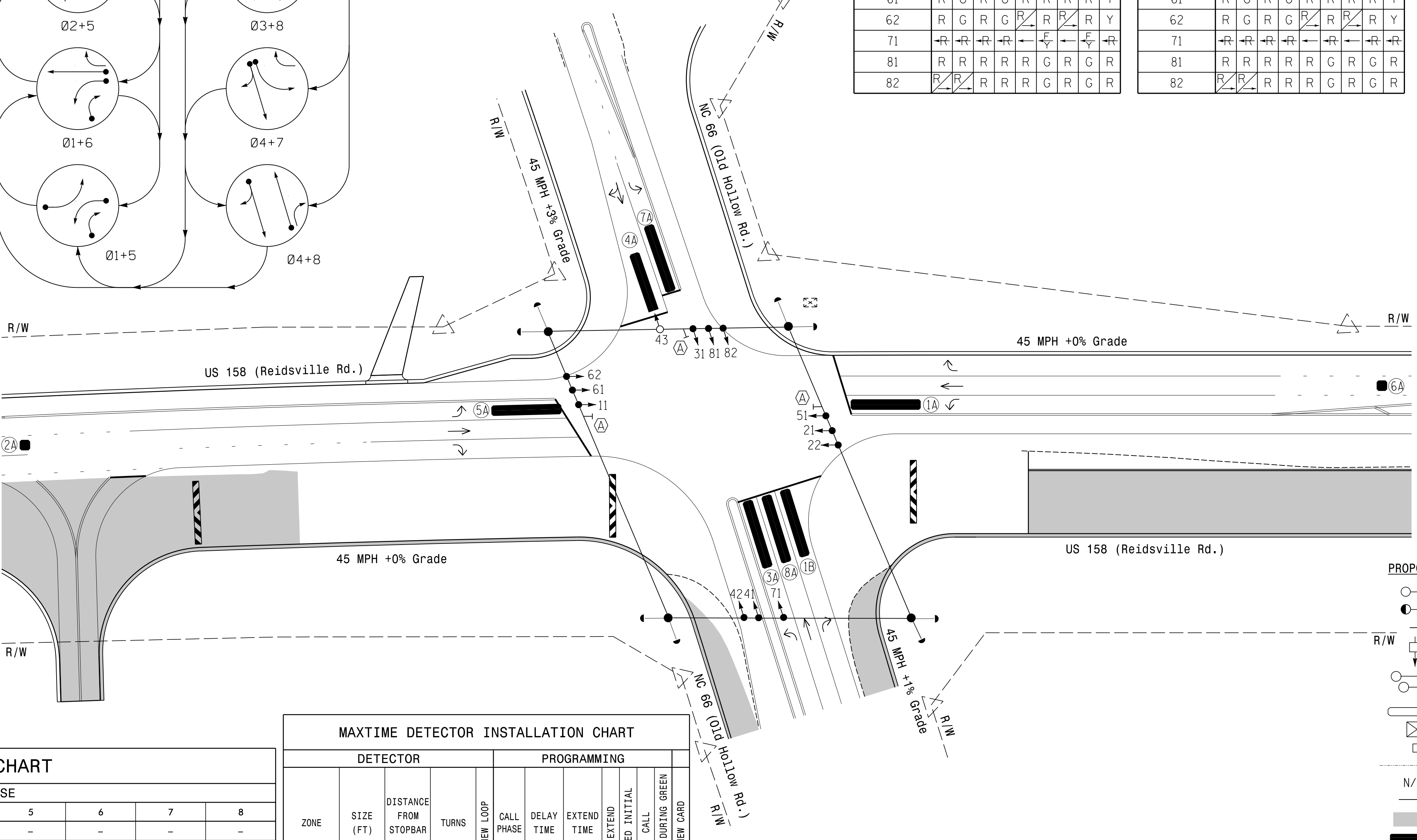
8 Phase Fully Actuated (Isolated)

NOTES

- Refer to "Roadway Standard Drawings NCDOT" dated January 2024 and "Standard Specifications for Roads and Structures" dated January 2024.
- Do not program signal for late night flashing operation unless otherwise noted by the Engineer.
- Phase 1 and/or phase 5 may be lagged.
- Phase 3 and/or phase 7 may be lagged.
- Reposition existing signal heads numbered 11, 21, 22, 31, 51, 61, 62, 81, and 82.
- Set all detector units to presence mode.
- This intersection uses video detection. Install detectors according to the manufacturer's instructions to achieve the desired detection.
- The Division Traffic Engineer will determine the hours of use for each phasing plan.

PHASING DIAGRAM DETECTION LEGEND

- DETECTED MOVEMENT
- UNDETECTED MOVEMENT (OVERLAP)
- UNSIGNALIZED MOVEMENT
- PEDESTRIAN MOVEMENT



MAXTIME TIMING CHART

FEATURE	PHASE							
	1	2	3	4	5	6	7	8
Walk *	-	-	-	-	-	-	-	-
Ped Clear *	-	-	-	-	-	-	-	-
Min Green	7	12	7	7	7	12	7	7
Passage *	2.0	6.0	2.0	2.0	2.0	6.0	2.0	2.0
Max 1 *	20	90	20	40	20	90	20	40
Yellow Change	3.0	4.5	3.0	4.4	3.0	4.5	3.0	4.4
Red Clear	2.9	2.2	2.8	1.9	3.1	2.2	3.2	1.9
Added Initial *	-	2.5	-	-	-	2.5	-	-
Maximum Initial *	-	34	-	-	-	34	-	-
Time Before Reduction *	-	15	-	-	-	15	-	-
Time To Reduce *	-	30	-	-	-	30	-	-
Minimum Gap	-	3.0	-	-	-	3.0	-	-
Advance Walk	-	-	-	-	-	-	-	-
Non Lock Detector	X	-	X	X	X	-	X	X
Vehicle Recall	-	MIN RECALL	-	-	-	MIN RECALL	-	-
Dual Entry	-	-	-	X	-	-	-	X

* These values may be field adjusted. Do not adjust Min Green and Extension times for phases 2 and 6 lower than what is shown. Min Green for all other phases should not be lower than 4 seconds.

MAXTIME DETECTOR INSTALLATION CHART

ZONE	SIZE (FT)	DISTANCE FROM STOPBAR (FT)	TURNS	NEW LOOP	PROGRAMMING							
					CALL PHASE	DELAY TIME	EXTEND TIME	EXTEND	ADDED INITIAL CALL	DELAY DURING GREEN	NEW CARD	
1A *	6X40	0	*	*	1	15#	-	X	X	X	-	*
1B *	6X40	0	*	*	6	3	-	X	-	X	X	*
2A *	6X6	300	*	*	2	-	-	X	X	X	-	*
3A *	6X40	0	*	*	3	15#	-	X	X	-	-	*
4A *	6X40	0	*	*	8	3	-	X	X	-	-	*
5A *	6X40	0	*	*	4	10	-	X	X	-	-	*
6A *	6X6	300	*	*	6	-	-	X	X	X	-	*
7A *	6X40	0	*	*	7	15#	-	X	X	-	-	*
8A *	6X40	0	*	*	4	3	-	X	X	-	-	*
8A *	6X40	0	*	*	8	-	-	X	X	-	-	*

Reduce Delay to 3 Seconds During Alternate Phasing Operation.
@ Disable Phase Call For Zone(s) During Alternate Phasing Operation.
* Video Detection Zone

LEGEND

PROPOSED

- Traffic Signal Head
- Modified Signal Head
- Pedestrian Signal Head With Push Button & Sign
- Signal Pole with Guy
- Signal Pole with Sidewalk Guy
- ☒ Inductive Loop Detector
- ☐ Junction Box
- 2-in Underground Conduit
- N/A Right of Way
- Directional Arrow
- ▬ Construction Zone
- ▬ Video Detection Zone
- ▬ Barricade
- (A) No U-turn Sign (R3-4)

EXISTING

- Traffic Signal Head
- Modified Signal Head
- Pedestrian Signal Head With Push Button & Sign
- Signal Pole with Guy
- Signal Pole with Sidewalk Guy
- ☒ Inductive Loop Detector
- ☐ Junction Box
- 2-in Underground Conduit
- N/A Right of Way
- Directional Arrow
- ▬ Construction Zone
- ▬ Video Detection Zone
- ▬ Barricade
- (A) No U-turn Sign (R3-4)

Signal Upgrade - Temporary Design 2 (TMP Phase II)

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Prepared For the Offices of:

US 158 (Reidsville Rd.)
at
NC 66 (Old Hollow Rd.)

Division 9 Forsyth County Walkertown

PLAN DATE: February 2024 REVIEWED BY: WP Erickson-Jones

PREPARED BY: H Townsend REVIEWED BY:

SCALE: 1" = 40'

DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED

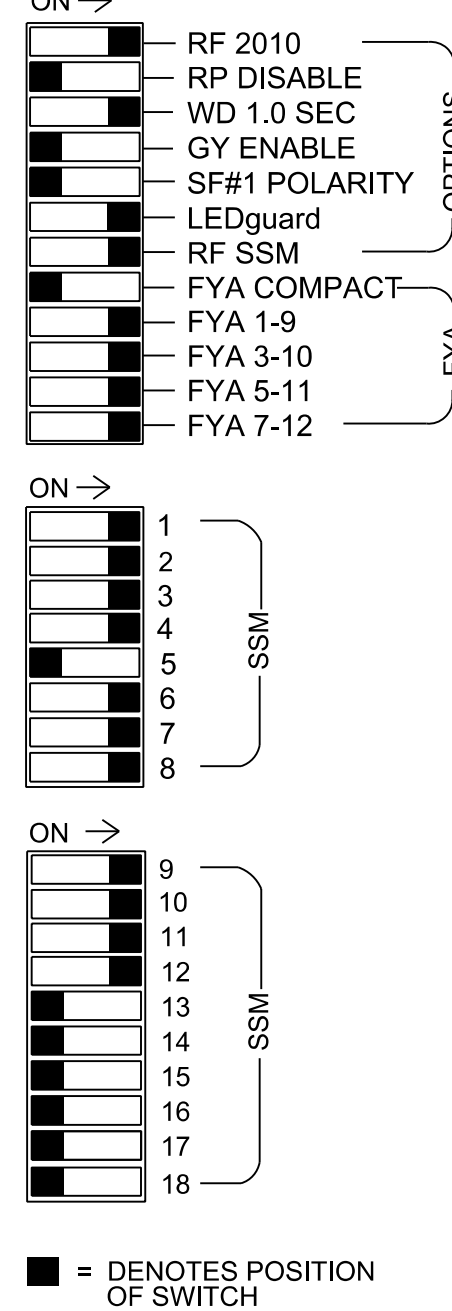
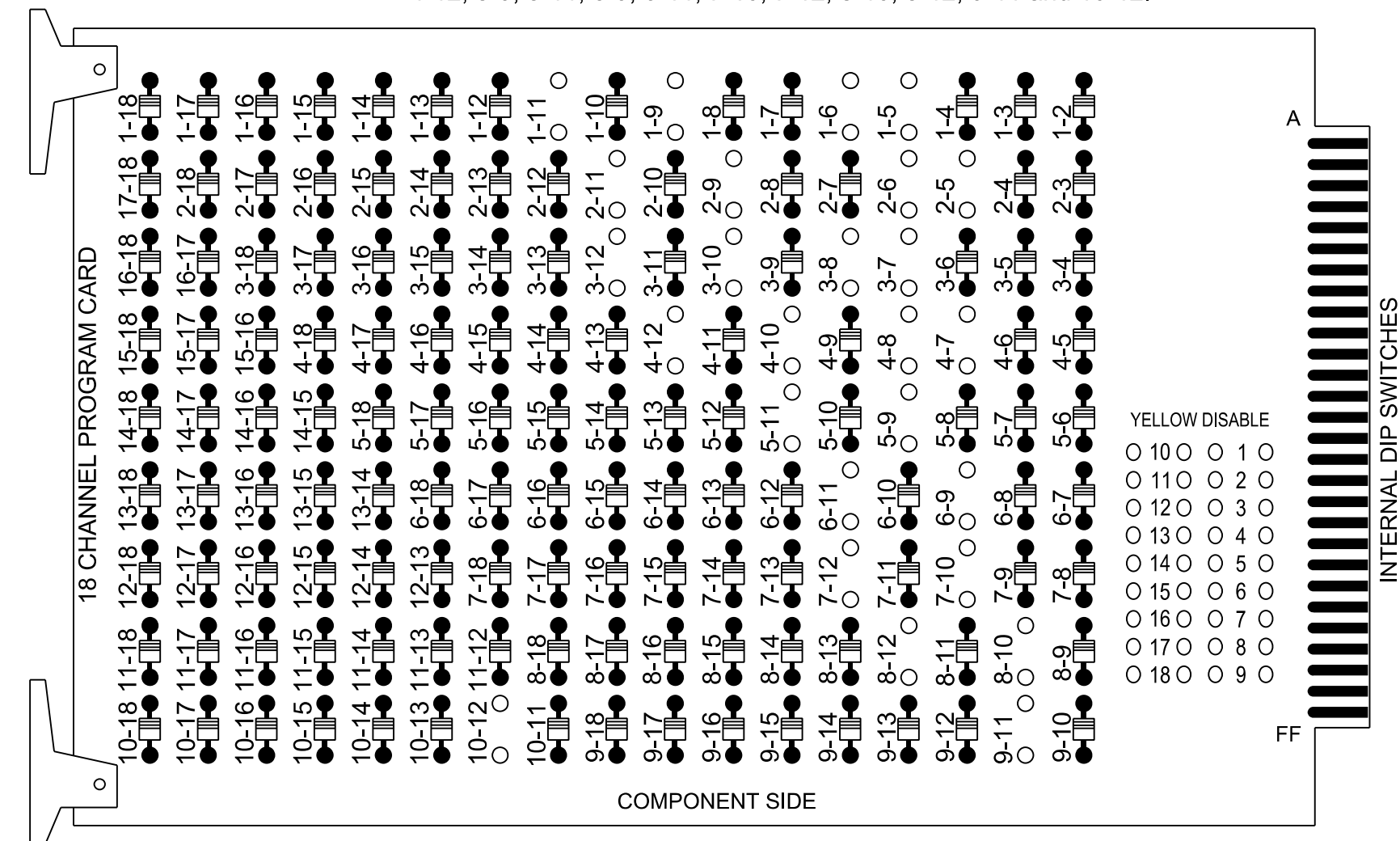
SEAL
NORTH CAROLINA PROFESSIONAL ENGINEER
SEAL 056142
W. PORTER JONES
2/12/2024
DATE
SIG. INVENTORY NO. 09-026412

2/12/2024 6:41:04pm C:\Users\jones1\OneDrive\Documents\2024\2412\026412.dwg:den... XXXXXXXX.dgn wjones

18 CHANNEL IP CONFLICT MONITOR PROGRAMMING DETAIL

(remove jumpers and set switches as shown)

REMOVE DIODE JUMPERS 1-5, 1-6, 1-9, 1-11, 2-5, 2-6, 2-9, 2-11, 3-7, 3-8, 3-10, 3-12, 4-7, 4-8, 4-10, 4-12, 5-9, 5-11, 6-9, 6-11, 7-10, 7-12, 8-10, 8-12, 9-11 and 10-12.



REMOVE JUMPERS AS SHOWN

NOTES:

- Card is provided with all diode jumpers in place. Removal of any jumper allows its channels to run concurrently.
- Ensure jumpers SEL2-SEL5 and SEL9 are present on the monitor board.
- Ensure that the Red Enable is active at all times during normal operation.
- Connect serial cable from conflict monitor to comm. port 1 of 2070 controller. Ensure conflict monitor communicates with 2070.

NOTES

- To prevent "flash-conflict" problems, insert red flash program blocks for all unused vehicle load switches in the output file. The installer shall verify that signal heads flash in accordance with the signal plan.
- Program phases 4 and 8 for Dual Entry.
- Program controller to start up in phase 2 Green No Walk and 6 Green No Walk.
- If this signal will be managed by an ATMS software, enable controller and detector logging for all detectors used at this location.

EQUIPMENT INFORMATION

Controller.....2070LX
 Cabinet.....332 w/ Aux
 Software.....Q-Free MAXTIME
 Cabinet Mount.....Base
 Output File Positions.....18 With Aux. Output File
 Load Switches Used.....S1, S2, S4, S5, S7, S8, S10, S11, AUX S1, AUX S2, AUX S4, AUX S5
 Phases Used.....1, 2, 3, 4, 5, 6, 7, 8
 Overlap "1".....
 Overlap "2".....
 Overlap "3".....
 Overlap "4".....

*See overlap programming detail on sheet 2.

SIGNAL HEAD HOOK-UP CHART

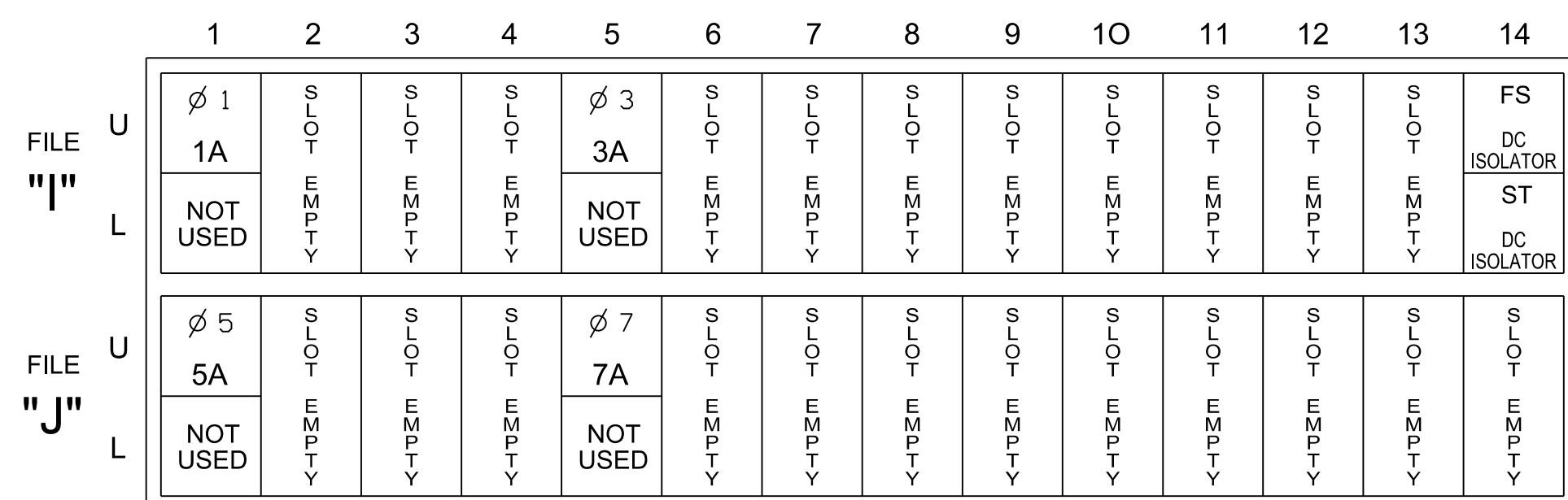
LOAD SWITCH NO.	S1	S2	S3	S4	S5	S6	S7	S8	S9	S10	S11	S12	AUX S1	AUX S2	AUX S3	AUX S4	AUX S5	AUX S6
CMU CHANNEL NO.	1	2	13	3	4	14	5	6	15	7	8	16	9	10	17	11	12	18
PHASE	1	2	2 PED	3	4	4 PED	5	6	6 PED	7	8	8 PED	OL1	OL2	SPARE	OL3	OL4	SPARE
SIGNAL HEAD NO.	11★	82	21,22	22	31★	41,42,43	51★	61,62	62	71★	81,82	82	11★	31★	51★	71★	71★	NU
RED	*	128		*	101		134		*	107								
YELLOW		129			102		*	135		108								
GREEN		130			103		136			109								
RED ARROW													A121	A124		A114	A101	
YELLOW ARROW	126			117						123			A122	A125		A115	A102	
FLASHING YELLOW ARROW													A123	A126		A116	A103	
GREEN ARROW	127	127		118	118		133		124	124								

NU = Not Used

* Denotes install load resistor. See load resistor installation detail this sheet.
 ★See pictorial of head wiring in detail this sheet.

INPUT FILE POSITION LAYOUT

(front view)

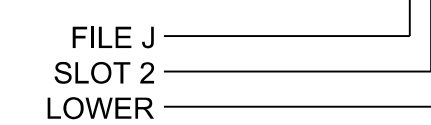


INPUT FILE CONNECTION & PROGRAMMING CHART

LOOP NO.	LOOP TERMINAL	INPUT FILE POS.	PIN NO.	INPUT POINT	DETECTOR NO.	CALL PHASE	DELAY TIME	EXTEND TIME	EXTEND	ADDED INITIAL	CALL	DELAY DURING GREEN
1A	TB2-1,2	I1U	56	18	1★	1	15		X		X	
3A	TB4-5,6	I5U	58	20	7★	3	15		X		X	X
				-	30★	8	3		X		X	
5A	TB3-1,2	J1U	55	17	15★	5	15		X		X	
				-	31★	2	3		X		X	X
7A	TB5-5,6	J5U	57	19	21★	7	15		X		X	
				-	32★	4	3		X		X	

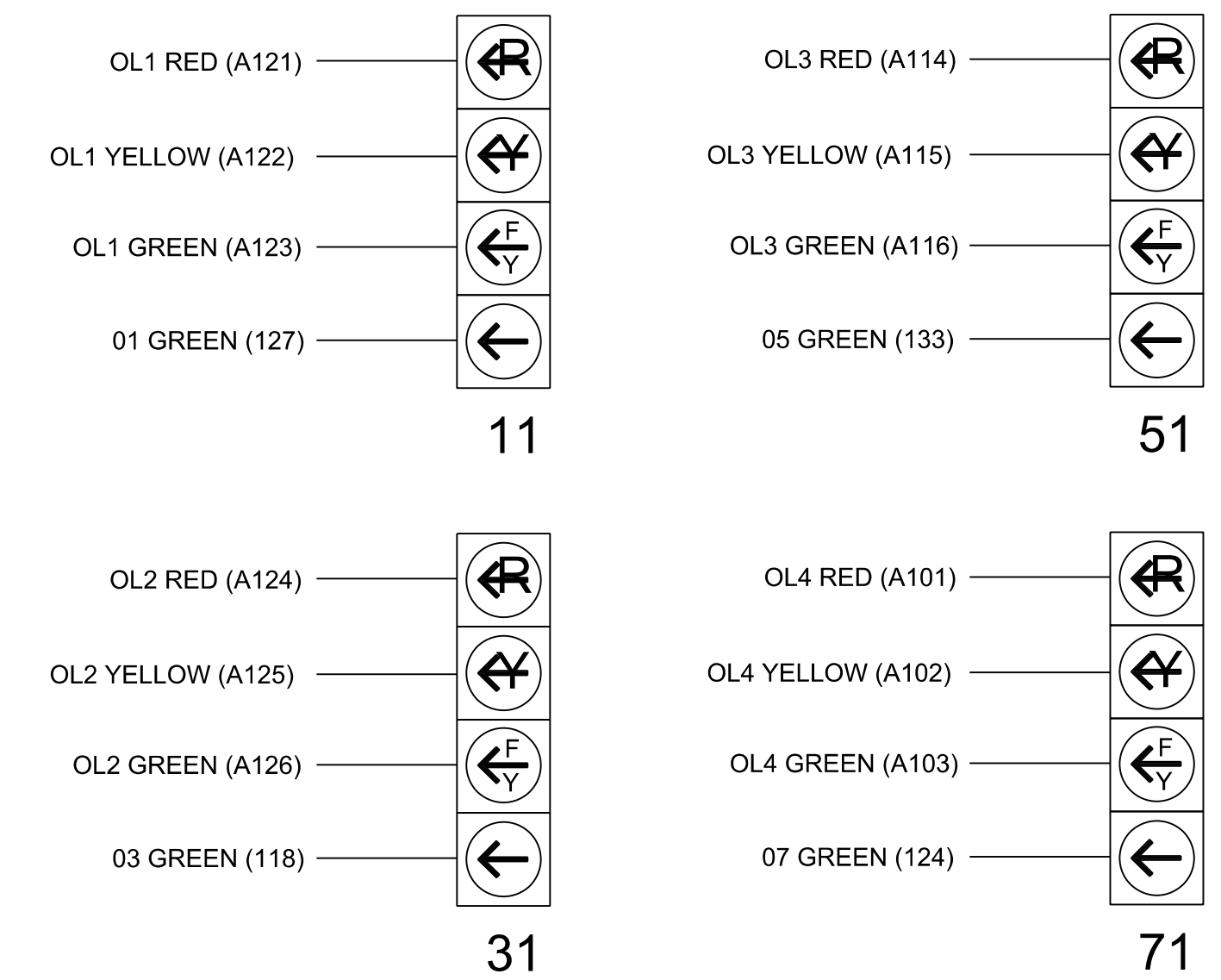
★ For the detector to work as shown on the signal design plan, see the vehicle detector setup programming detail for alternate phasing on sheet 2.

INPUT FILE POSITION LEGEND: J2L



FYA SIGNAL WIRING DETAIL

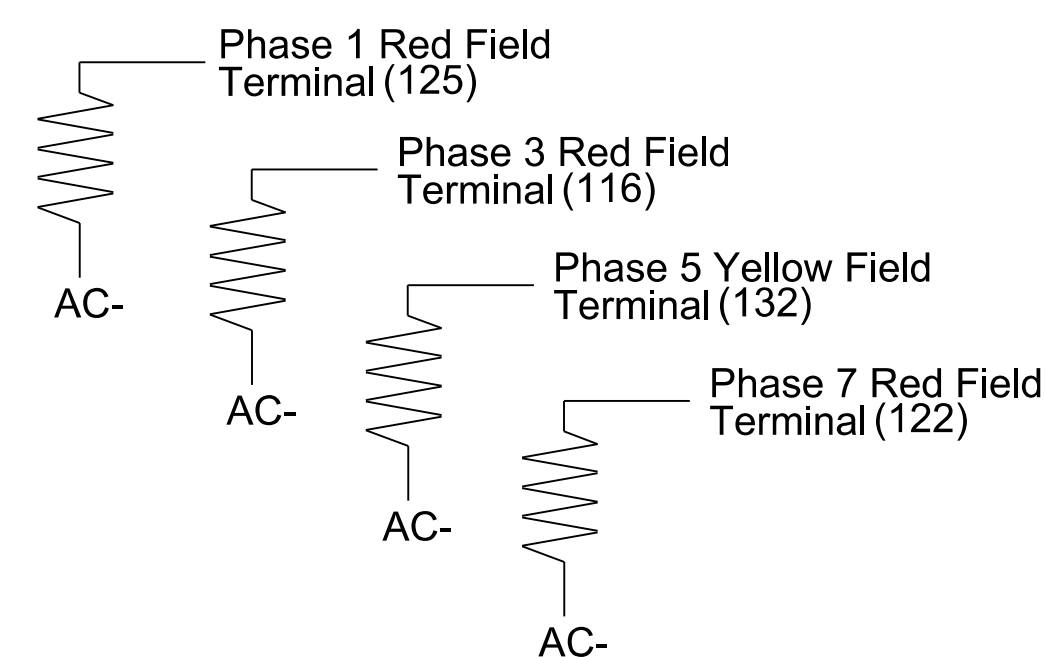
(wire signal heads as shown)



LOAD RESISTOR INSTALLATION DETAIL

(install resistors as shown)

ACCEPTABLE VALUES	
Value (ohms)	Wattage
1.5K - 1.9K	25W (min)
2.0K - 3.0K	10W (min)



SPECIAL DETECTOR NOTE

Install a video detection system for vehicle detection for zones 1A, 1B, 2A, 3A, 4A, 5A, 6A, 7A and 8A. Perform installation according to manufacturer's directions and NCDOT engineer-approved mounting locations to accomplish the detection schemes shown on the Signal Design Plans.

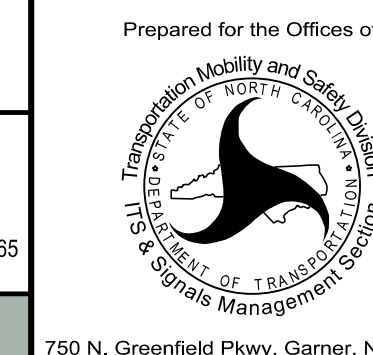
For detecton zones 1A, 3A, 5A, and 7A, detector card placement and slots are typical for a NCDOT installation. Inputs associated with these slots are compatible with time of day instructions located on sheet 2 of this electrical detail.

THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 09-0264T2
 DESIGNED: February 2024
 SEALED: February 12, 2024
 REVISED:

Signal Upgrade - Temporary Design 2 (TMP Phase II) - Electrical Detail - Sheet 1 of 2

Electrical and Programming Details For:

US 158 (Reidsville Rd.) at NC 66 (Old Hollow Rd.)



Division 9 Forsyth County Walkertown

PLAN DATE: February 2024 REVIEWED BY: DT Sears

PREPARED BY: WP Erickson-Jones REVIEWED BY:

REVISIONS INIT. DATE

DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED

Seal and signature of Douglas S. Porter, Professional Engineer, No. 056142.

Porter Jones 2/12/2024

SIG. INVENTORY NO. 09-0264T2



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MAXTIME ALTERNATE PHASING ACTIVATION DETAIL

To run alternate phasing, select a Pattern that is programmed to run Overlap Plan 2 and Detector Plan 2.
A Pattern can be selected through the scheduler or manually by changing the Operational Mode.

PHASING	OVERLAP PLAN	VEH DET PLAN
ACTIVE PLAN REQUIRED TO RUN DEFAULT PHASING	1	1
ACTIVE PLAN REQUIRED TO RUN ALTERNATE PHASING	2	2

MAXTIME DETECTOR PROGRAMMING DETAIL FOR ALTERNATE PHASING LOOPS 1A, 3A, 5A & 7A

Front Panel

Main Menu >Controller >Detector >Veh Det Plans

Web Interface

Home >Controller >Detector Configuration >Vehicle Detectors

In the table view of web interface right click on "Detector" in the top left corner of the table. Copy the entire contents of Detector Plan 1. Paste Detector Plan 1 into Detector Plan 2. Modify Detector Plan 2 as shown below and save changes.

Plan 2			
	Detector	Call Phase	Delay
1A	1	1	3
	29	0	3
3A	7	3	3
	30	0	3
5A	15	5	3
	31	0	3
7A	21	7	3
	32	0	3

ALTERNATE PHASING CHANGE SUMMARY

THE FOLLOWING IS A SUMMARY OF WHAT TAKES PLACE WHEN OVERLAP PLAN 2 AND VEHICLE DETECTOR PLAN 2 ACTIVATE TO CALL THE "ALTERNATE PHASING":

OVERLAP PLAN 2: Modifies overlap included phases for heads 11, 31, 51, and 71 to run protected turns only.

VEH DET PLAN 2: Disables phase 6 call on loop 1A and reduces delay time for phase 1 call on loop 1A to 3 seconds.

Disables phase 8 call on loop 3A and reduces delay time for phase 3 call on loop 3A to 3 seconds.

Disables phase 2 call on loop 5A and reduces delay time for phase 5 call on loop 5A to 3 seconds.

Disables phase 4 call on loop 7A and reduces delay time for phase 7 call on loop 7A to 3 seconds.

FLASHER CIRCUIT MODIFICATION DETAIL

IN ORDER TO INSURE THAT SIGNALS FLASH CONCURRENTLY ON THE SAME APPROACH, MAKE THE FOLLOWING FLASHER CIRCUIT CHANGES:

1. ON REAR OF PDA - REMOVE WIRE FROM TERM. T2-4 AND TERMINATE ON T2-2.
2. ON REAR OF PDA - REMOVE WIRE FROM TERM. T2-5 AND TERMINATE ON T2-3.
3. REMOVE FLASHER UNIT 2.

THE CHANGES LISTED ABOVE TIES ALL PHASES AND OVERLAPS TO FLASHER UNIT 1.

MAXTIME ALTERNATE PHASING PATTERN PROGRAMMING DETAIL

Front Panel

Main Menu >Controller >Coordination >Patterns

Web Interface

Home >Controller >Coordination >Patterns

Pattern Parameters

Pattern	Veh Det Plan	Overlap Plan
*	2	2

* The Pattern number(s) are to be determined by the Division and/or City Traffic Engineer.

THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 09-0264T2
DESIGNED: February 2024
SEALED: February 12, 2024
REVISED:

MAXTIME OVERLAP PROGRAMMING DETAIL FOR DEFAULT PHASING

Front Panel

Main Menu >Controller >Overlap >Overlap Parameters/Overlap Timings

Web Interface

Home >Controller >Overlap Configuration >Overlaps

Overlap Plan 1

Overlap	1	2	3	4
Type	FYA 4 - Section	FYA 4 - Section	FYA 4 - Section	FYA 4 - Section
Included Phases	2	4	6	8
Modifier Phases	1	3	5	7
Modifier Overlaps	-	-	-	-
Trail Green	0	0	0	0
Trail Yellow	0.0	0.0	0.0	0.0
Trail Red	0.0	0.0	0.0	0.0

MAXTIME OVERLAP PROGRAMMING DETAIL FOR ALTERNATE PHASING

Front Panel

Main Menu >Controller >Overlap >Overlap Parameters/Overlap Timings

Web Interface

Home >Controller >Overlap Configuration >Overlaps

In the table view of the web interface, right click on "Overlap" in the top left corner of the table. Copy the entire contents of Overlap Plan 1. Paste Overlap Plan 1 into Overlap Plan 2. Modify Overlap Plan 2 as shown below and save changes.

Overlap Plan 2

Overlap	1	2	3	4
Type	FYA 4 - Section	FYA 4 - Section	FYA 4 - Section	FYA 4 - Section
Included Phases	-	-	-	-
Modifier Phases	1	3	5	7
Modifier Overlaps	-	-	-	-
Trail Green	0	0	0	0
Trail Yellow	0.0	0.0	0.0	0.0
Trail Red	0.0	0.0	0.0	0.0

← NOTICE INCLUDED PHASE

Signal Upgrade - Temporary Design 2
(TMP Phase II) - Electrical Detail - Sheet 2 of 2

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Electrical and Programming Details For:

Prepared for the Offices of:

750 N. Greenfield Pkwy, Garner, NC 27529

US 158 (Reidsville Rd.)
at
NC 66 (Old Hollow Rd.)

Division 9 Forsyth County Walkertown

PLAN DATE: February 2024 REVIEWED BY: DT Sears

PREPARED BY: WP Erickson-Jones REVIEWED BY:

REVISIONS	INIT.	DATE

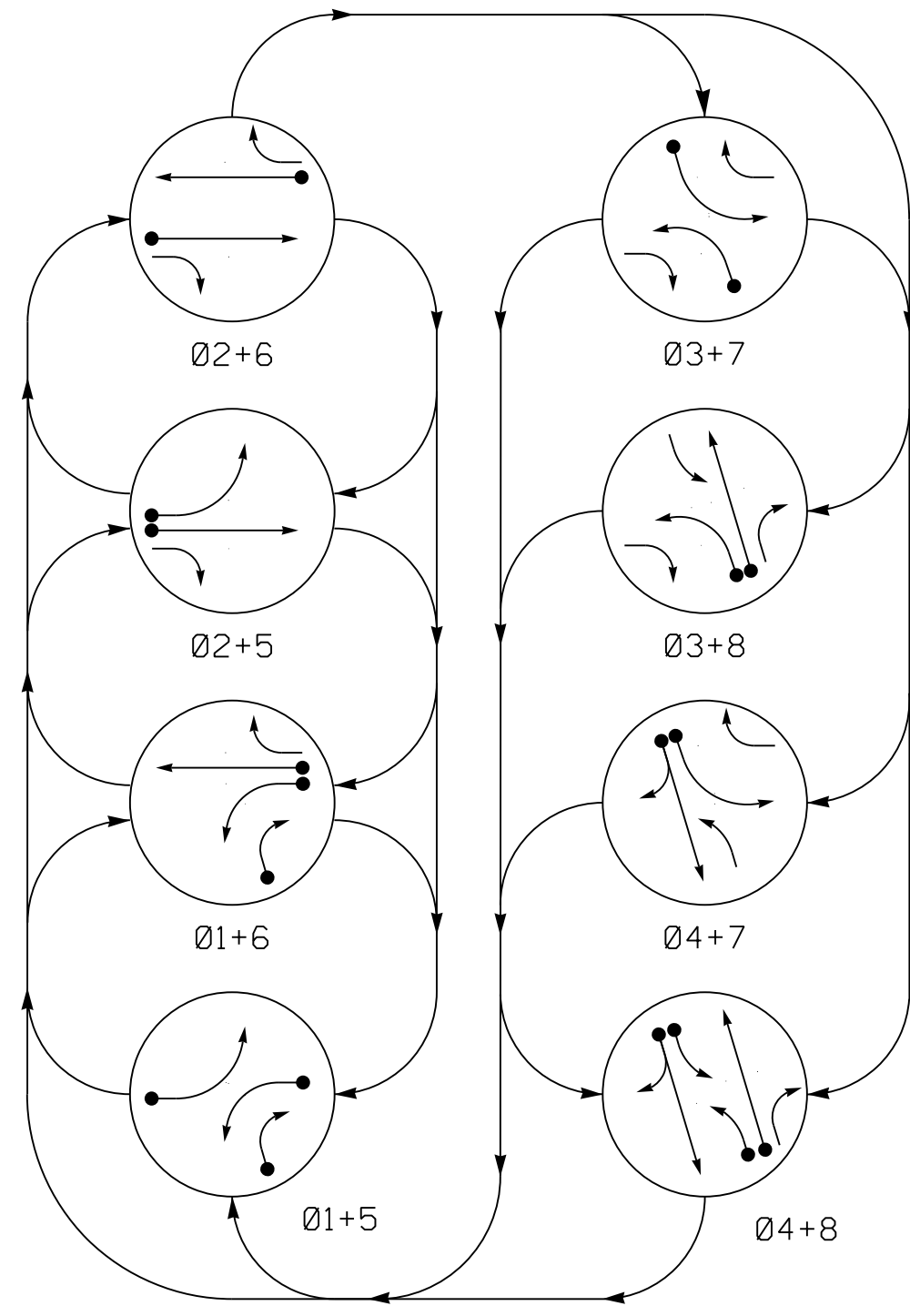
DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED

DocuSigned by:
Porter Jones
056142

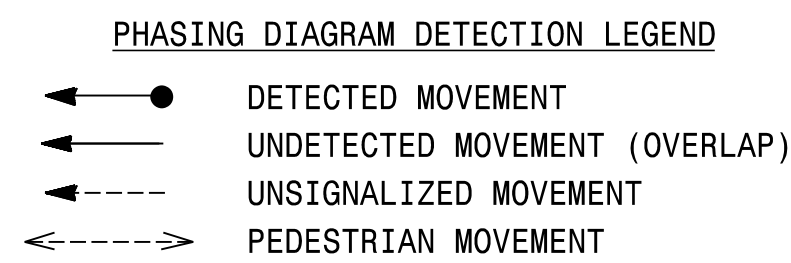
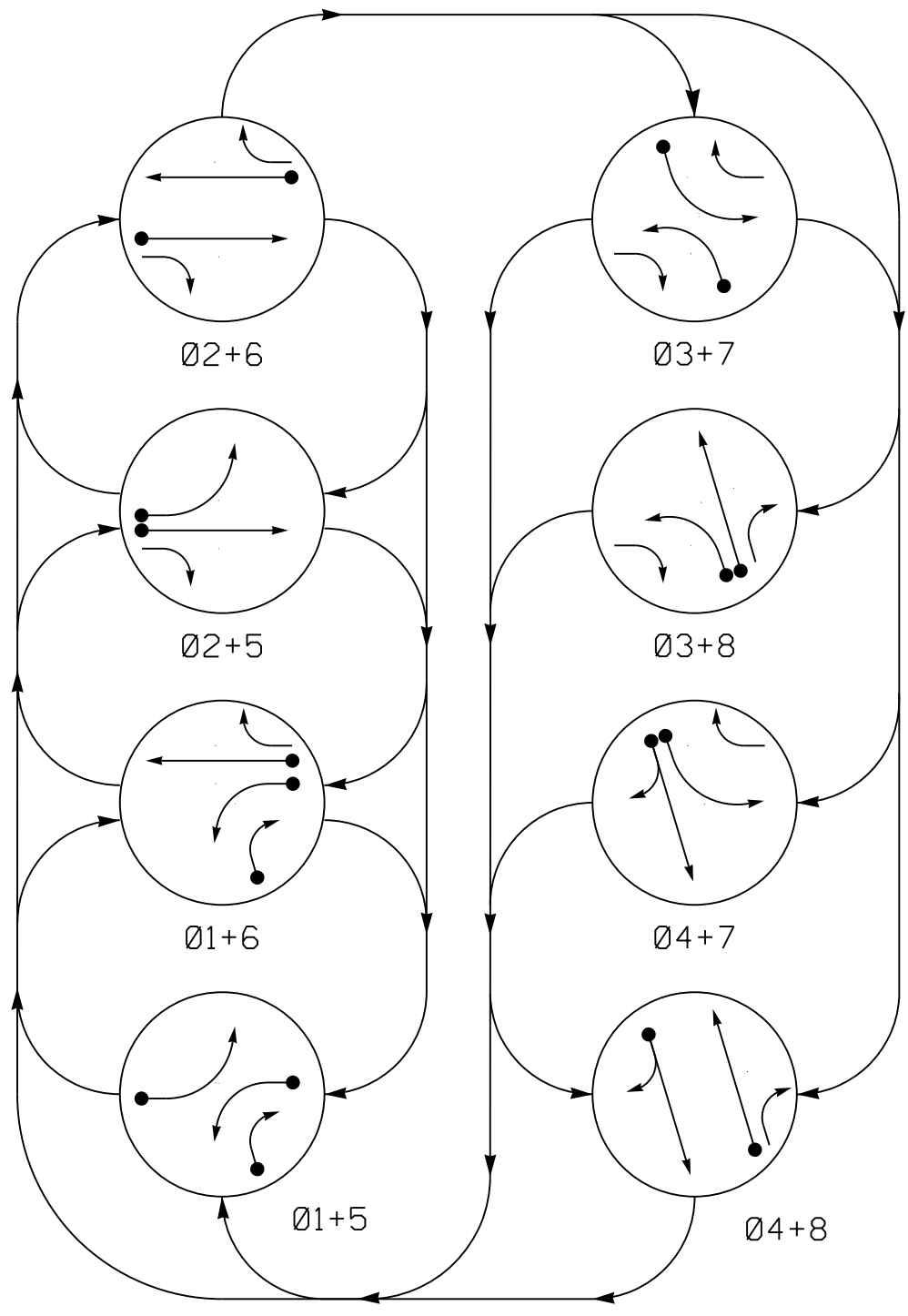
2/12/2024

SIG. INVENTORY NO. 09-0264T2

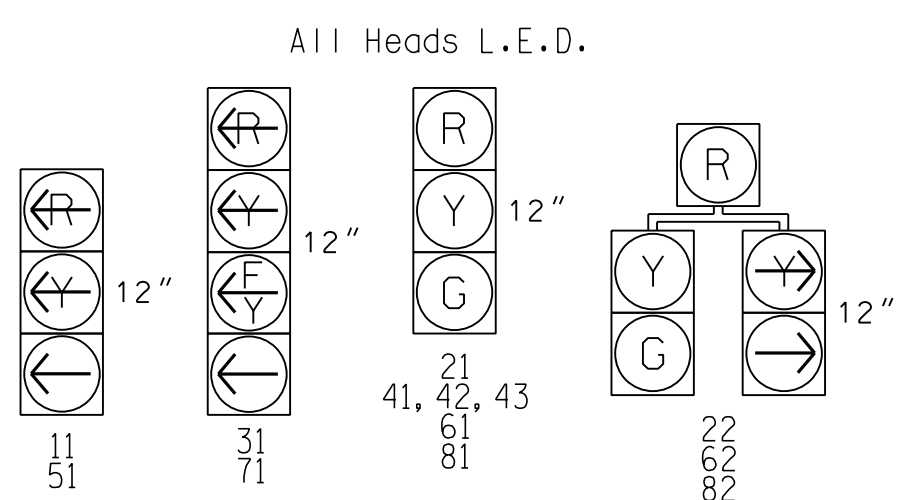
DEFAULT PHASING DIAGRAM



ALTERNATE PHASING DIAGRAM



SIGNAL FACE I.D.



DEFAULT PHASING TABLE OF OPERATION

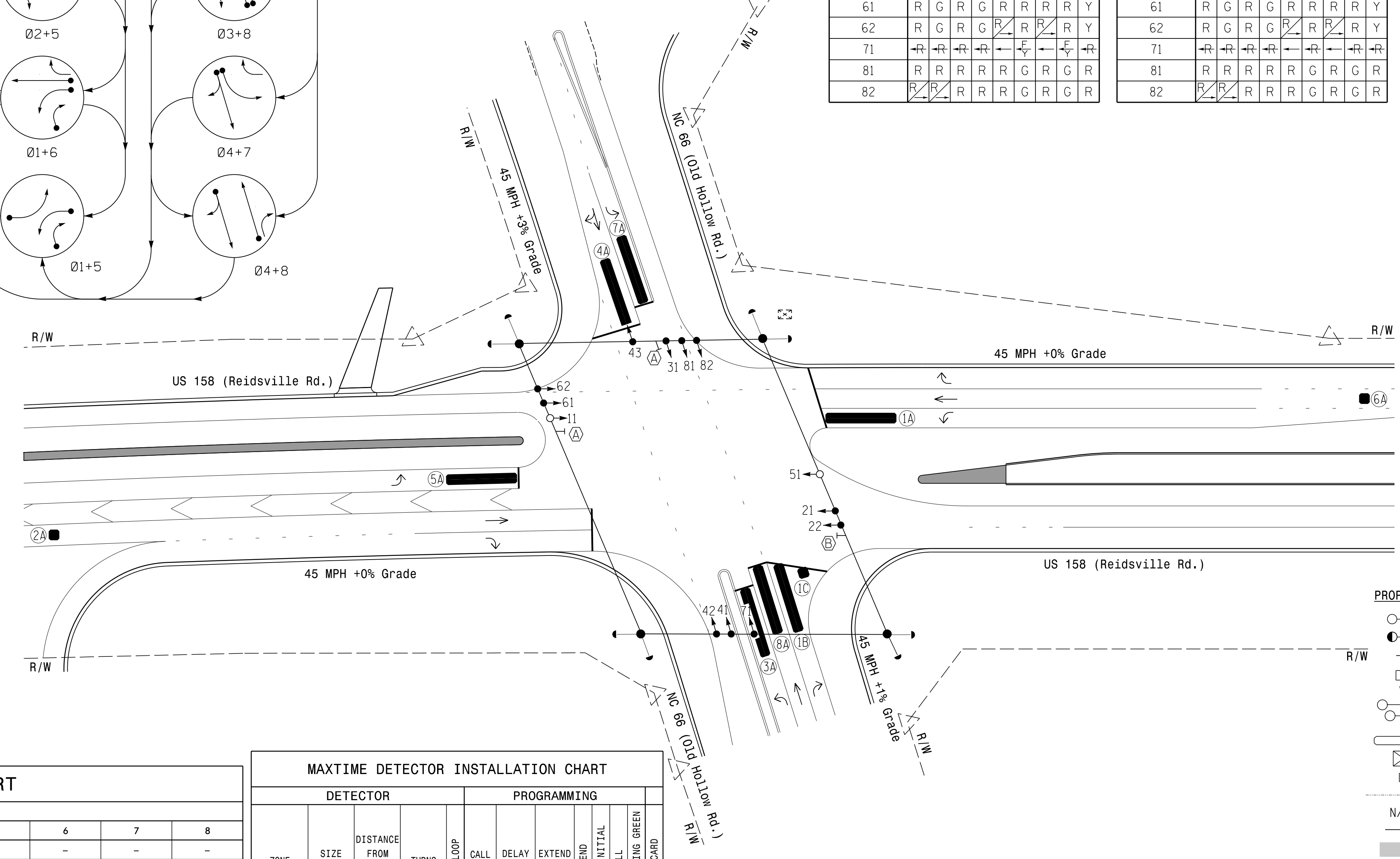
SIGNAL FACE	PHASE							
	01+5	01+6	02+5	02+6	03+7	03+8	04+7	04+8
11	←	←	←	←	←	←	←	←
21	R	R	G	G	R	R	R	Y
22	R	R	G	G	R	R	R	Y
31	←	←	←	←	←	←	←	←
41,42,43	R	R	R	R	R	R	G	G
51	←	←	←	←	←	←	←	←
61	R	G	R	G	R	R	R	Y
62	R	G	R	G	R	R	R	Y
71	←	←	←	←	←	←	←	←
81	R	R	R	R	R	G	G	R
82	R	R	R	R	R	G	G	R

ALTERNATE PHASING TABLE OF OPERATION

SIGNAL FACE	PHASE							
	01+5	01+6	02+5	02+6	03+8	03+7	04+7	04+8
11	←	←	←	←	←	←	←	←
21	R	R	G	G	R	R	R	Y
22	R	R	G	G	R	R	R	Y
31	←	←	←	←	←	←	←	←
41,42,43	R	R	R	R	R	R	G	G
51	←	←	←	←	←	←	←	←
61	R	G	R	G	R	R	R	Y
62	R	G	R	G	R	R	R	Y
71	←	←	←	←	←	←	←	←
81	R	R	R	R	R	G	G	R
82	R	R	R	R	R	G	G	R

8 Phase Fully Actuated (Isolated)

- NOTES
- Refer to "Roadway Standard Drawings NCDOT" dated January 2024 and "Standard Specifications for Roads and Structures" dated January 2024.
 - Do not program signal for late night flashing operation unless otherwise noted by the Engineer.
 - Phase 1 and/or phase 5 may be lagged.
 - Phase 3 and/or phase 7 may be lagged.
 - Reposition existing signal heads numbered 21, 22, and 51.
 - Set all detector units to presence mode.
 - This intersection uses video detection. Install detectors according to the manufacturer's instructions to achieve the desired detection.
 - The Division Traffic Engineer will determine the hours of use for each phasing plan.

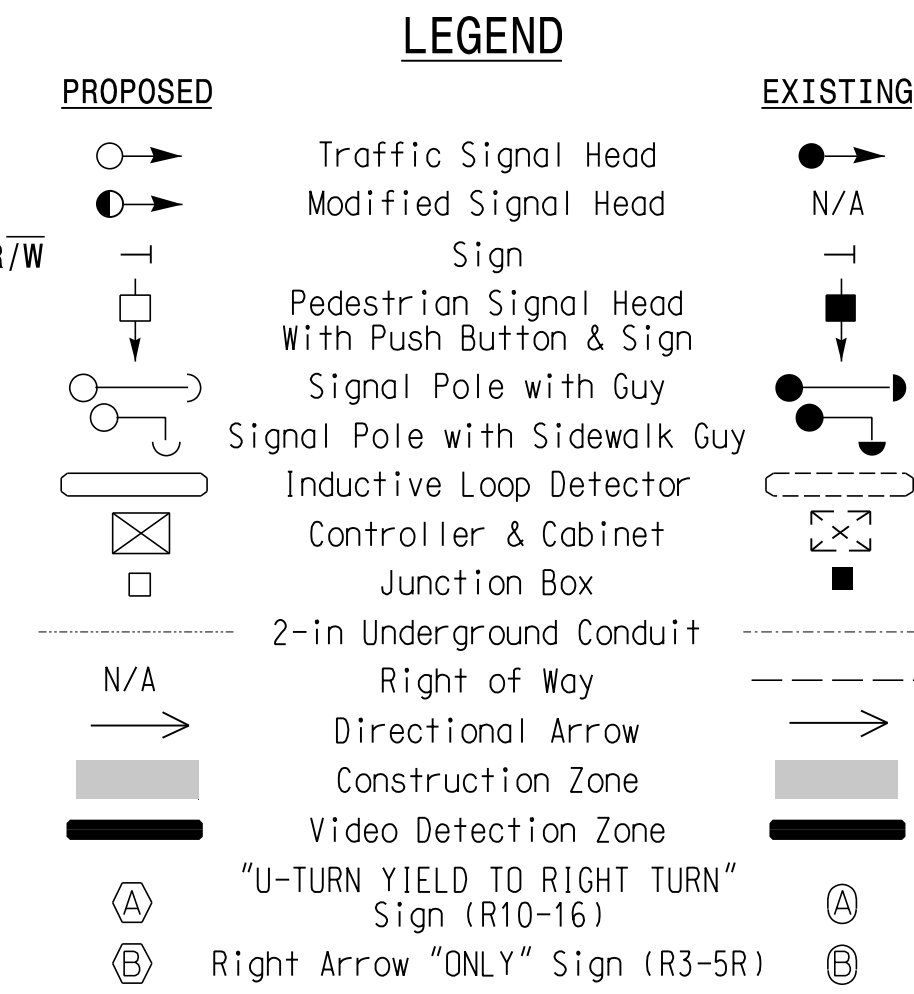


MAXTIME TIMING CHART

FEATURE	PHASE							
	1	2	3	4	5	6	7	8
Walk *	-	-	-	-	-	-	-	-
Ped Clear *	-	-	-	-	-	-	-	-
Min Green	7	12	7	7	7	12	7	7
Passage *	2.0	6.0	2.0	2.0	2.0	6.0	2.0	2.0
Max I *	20	90	20	40	20	90	20	40
Yellow Change	3.0	4.5	3.0	4.4	3.0	4.5	3.0	4.4
Red Clear	3.3	1.9	3.7	2.6	3.5	1.9	3.9	2.6
Added Initial *	-	2.5	-	-	-	2.5	-	-
Maximum Initial *	-	34	-	-	-	34	-	-
Time Before Reduction *	-	15	-	-	-	15	-	-
Time To Reduce *	-	30	-	-	-	30	-	-
Minimum Gap	-	3.0	-	-	-	3.0	-	-
Advance Walk	-	-	-	-	-	-	-	-
Non Lock Detector	X	-	X	X	X	-	X	X
Vehicle Recall	-	MIN RECALL	-	-	-	MIN RECALL	-	-
Dual Entry	-	-	-	X	-	-	-	X

MAXTIME DETECTOR INSTALLATION CHART

ZONE	SIZE (FT)	DISTANCE FROM STOPBAR (FT)	TURNS	NEW LOOP	PROGRAMMING							
					CALL PHASE	DELAY TIME	EXTEND TIME	EXTEND	ADDED INITIAL	CALL	DELAY DURING GREEN	NEW CARD
1A *	6X40	0	*	*	1	-	-	X	-	X	-	*
1B *	6X40	0	*	*	1	15	-	X	-	X	-	*
1C *	6X40	0	*	*	1	15	-	X	-	X	-	*
2A *	6X6	300	*	*	2	-	-	X	X	X	-	*
3A *	6X40	0	*	*	3	15#	-	X	-	X	-	*
8@	3	-	-	-	8	3	-	X	-	X	-	*
4A *	6X40	0	*	*	4	10	-	X	-	X	-	*
5A *	6X40	0	*	*	5	-	-	X	-	X	-	*
6A *	6X6	300	*	*	6	-	-	X	X	X	-	*
7A *	6X40	0	*	*	7	15#	-	X	-	X	-	*
4@	3	-	-	-	4	3	-	X	-	X	-	*
8A *	6X40	0	*	*	8	-	-	X	-	X	-	*



2/12/2024 6:44:11 PM R:\Traffic\c:\ms1\gn\1540815\gn\1540815.dgn

* These values may be field adjusted. Do not adjust Min Green and Extension times for phases 2 and 6 lower than what is shown. Min Green for all other phases should not be lower than 4 seconds.

Reduce Delay to 3 Seconds During Alternate Phasing Operation.
 @ Disable Phase Call For Zone(s) During Alternate Phasing Operation.
 * Video Detection Zone

Signal Upgrade - Temporary Design 3 (TMP Phase III)

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Prepared For the Offices of:

 750 N. Greenfield Pkwy, Garner, NC 27529

US 158 (Reidsville Rd.)
 at
 NC 66 (Old Hollow Rd.)
 Division 9 Forsyth County Walkertown
 PLAN DATE: February 2024 REVIEWED BY: WP Erickson-Jones
 PREPARED BY: H Townsend REVIEWED BY:
 REVISIONS
 INIT. DATE

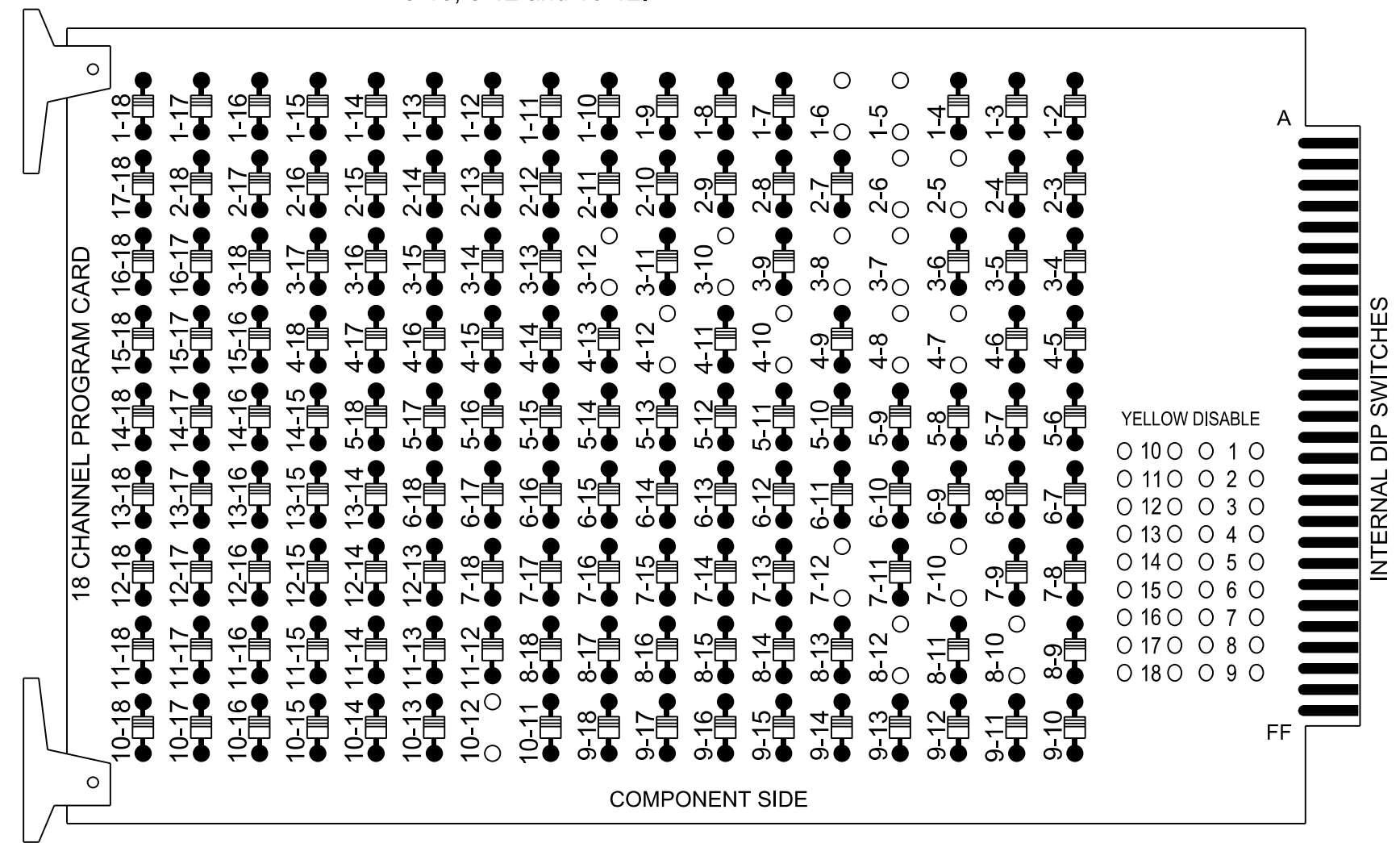
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SEAL
 NORTH CAROLINA PROFESSIONAL ENGINEER
 SEAL 056142
 W. PORTER JONES
 2/12/2024
 SIGNATURE DATE
 SIG. INVENTORY NO. 09-026413

18 CHANNEL IP CONFLICT MONITOR PROGRAMMING DETAIL

(remove jumpers and set switches as shown)

REMOVE DIODE JUMPERS 1-5, 1-6, 2-5, 2-6, 3-7, 3-8, 3-10, 3-12, 4-7, 4-8, 4-10, 4-12, 7-10, 7-12, 8-10, 8-12 and 10-12.



REMOVE JUMPERS AS SHOWN

NOTES:

- Card is provided with all diode jumpers in place. Removal of any jumper allows its channels to run concurrently.
- Ensure jumpers SEL2-SEL5 and SEL9 are present on the monitor board.
- Ensure that the Red Enable is active at all times during normal operation.
- Connect serial cable from conflict monitor to comm. port 1 of 2070 controller. Ensure conflict monitor communicates with 2070.

NOTES

- To prevent "flash-conflict" problems, insert red flash program blocks for all unused vehicle load switches in the output file. The installer shall verify that signal heads flash in accordance with the signal plan.
- Return controller to Factory Defaults before programming per this electrical detail.
- Program phases 4 and 8 for Dual Entry.
- Program controller to start up in phase 2 Green No Walk and 6 Green No Walk.
- If this signal will be managed by an ATMS software, enable controller and detector logging for all detectors used at this location.

EQUIPMENT INFORMATION

Controller.....2070LX
 Cabinet.....332 w/ Aux
 Software.....Q-Free MAXTIME
 Cabinet Mount.....Base
 Output File Positions.....18 With Aux. Output File
 Load Switches Used.....S1, S2, S4, S5, S7, S8, S10, S11, AUX S2, AUX S5
 Phases Used.....1, 2, 3, 4, 5, 6, 7, 8
 Overlap "1".....NOT USED
 Overlap "2".....*
 Overlap "3".....NOT USED
 Overlap "4".....*

*See overlap programming detail on sheet 2.

SIGNAL HEAD HOOK-UP CHART

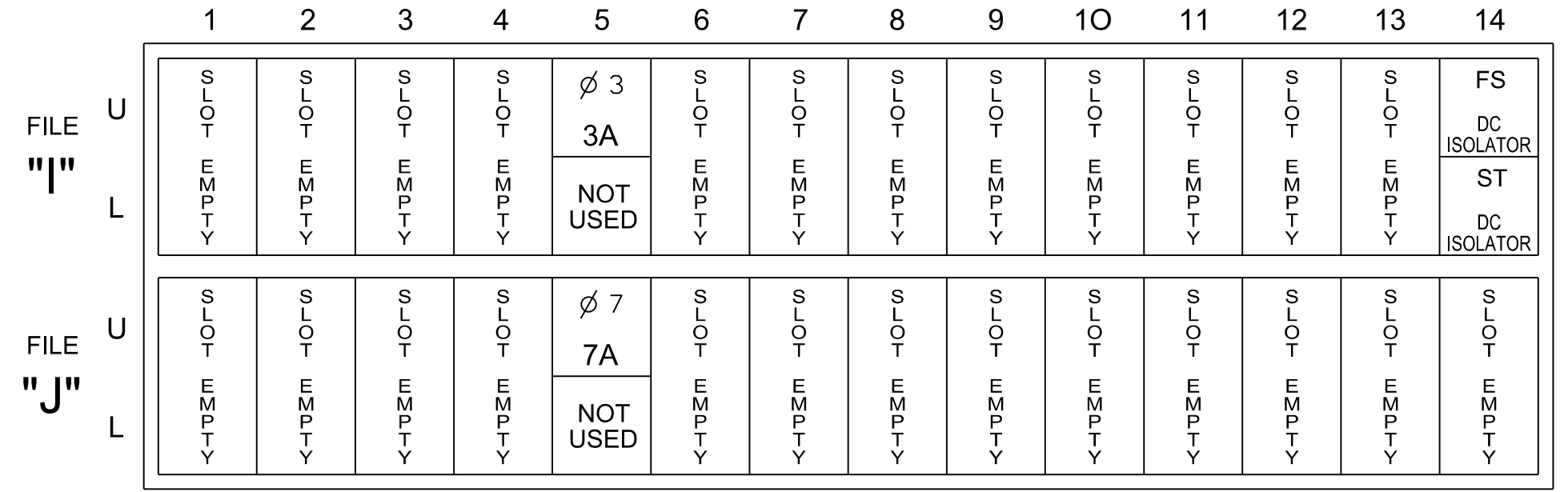
LOAD SWITCH NO.	S1	S2	S3	S4	S5	S6	S7	S8	S9	S10	S11	S12	AUX S1	AUX S2	AUX S3	AUX S4	AUX S5	AUX S6	
CMU CHANNEL NO.	1	2	13	3	4	14	5	6	15	7	8	16	9	10	17	11	12	18	
PHASE	1	2	2 PED	3	4	4 PED	5	6	6 PED	7	8	8 PED	OL1	OL2	SPARE	OL3	OL4	SPARE	
SIGNAL HEAD NO.	11	82	21,22	NU	22	31	41,42,43	NU	51	61,62	NU	62	71	81,82	NU	31	NU	71	NU
RED		128		*	101			134		*	107								
YELLOW		129			102			135			108								
GREEN		130			103			136			109								
RED ARROW	125							131						A124				A101	
YELLOW ARROW	126	126		117				132		123				A125				A102	
FLASHING YELLOW ARROW														A126				A103	
GREEN ARROW	127	127		118	118			133		124	124								

NU = Not Used

* Denotes install load resistor. See load resistor installation detail this sheet.
 *See pictorial of head wiring in detail this sheet.

INPUT FILE POSITION LAYOUT

(front view)



EX.: 1A, 2A, ETC. = LOOP NO.'S

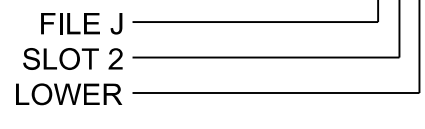
FS = FLASH SENSE
 ST = STOP TIME

INPUT FILE CONNECTION & PROGRAMMING CHART

LOOP NO.	LOOP TERMINAL	INPUT FILE POS.	PIN NO.	INPUT POINT	DETECTOR NO.	CALL PHASE	DELAY TIME	EXTEND TIME	EXTEND	ADDED INITIAL	CALL	DELAY DURING GREEN
3A	TB4-5,6	ISU	58	20	7	3	15		X		X	
				-	30	8	3		X	X		
7A	TB5-5,6	JSU	57	19	21	7	15		X		X	
				-	32	4	3		X	X		

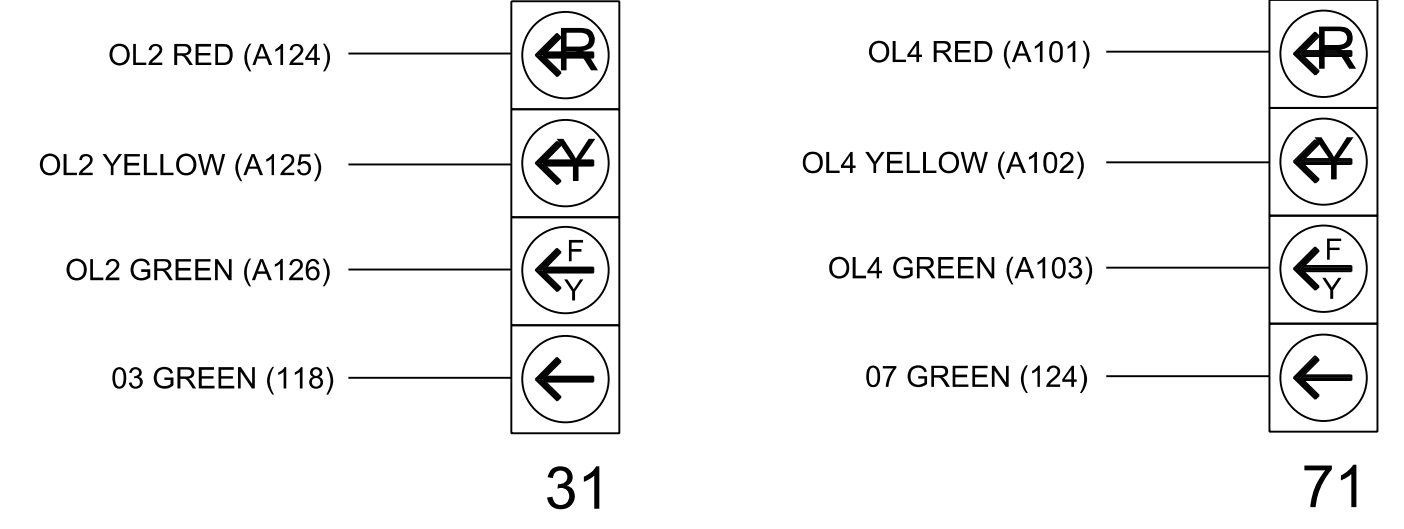
* For the detector to work as shown on the signal design plan, see the vehicle detector setup programming detail for alternate phasing on sheet 2.

INPUT FILE POSITION LEGEND: J2L



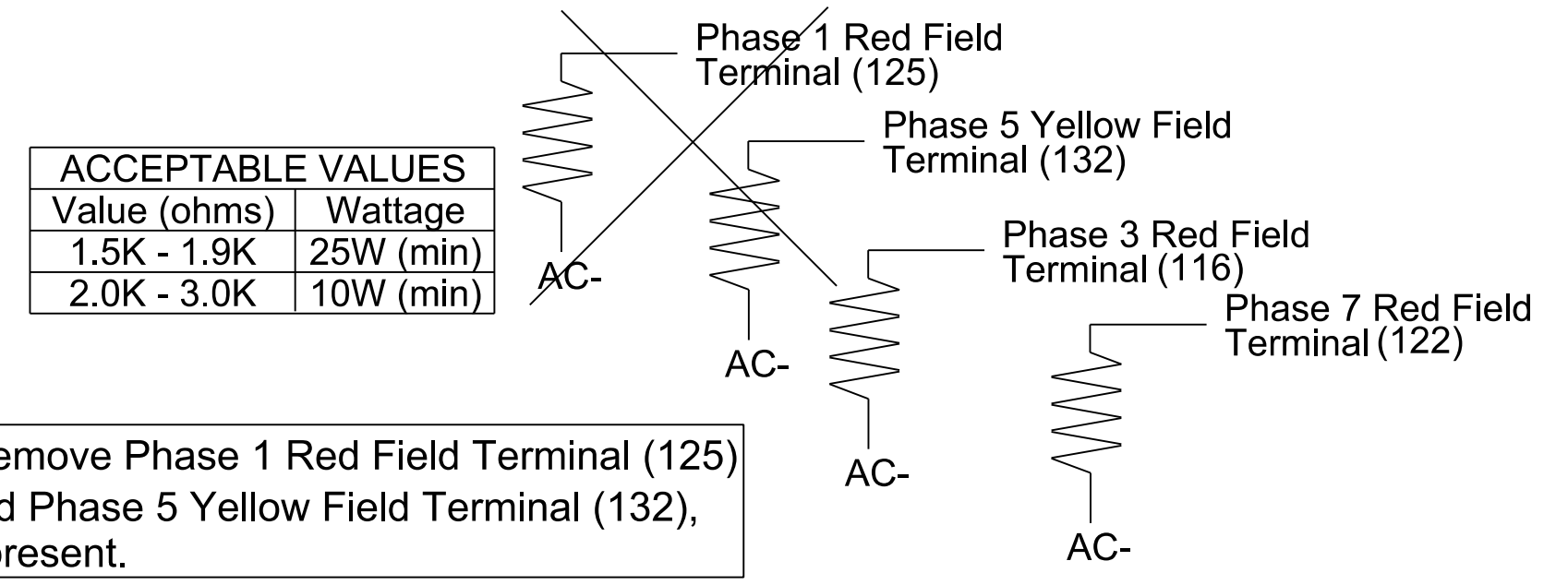
FYA SIGNAL WIRING DETAIL

(wire signal heads as shown)



LOAD RESISTOR INSTALLATION DETAIL

(install resistors as shown)



!Remove Phase 1 Red Field Terminal (125) and Phase 5 Yellow Field Terminal (132), if present.

SPECIAL DETECTOR NOTE

Install a video detection system for vehicle detection for zones. 1A, 1B, 1C, 2A, 3A, 4A, 5A, 6A, 7A and 8A. Perform installation according to manufacturer's directions and NCDOT engineer -approved mounting locations to accomplish the detection schemes shown on the Signal Design Plans.

For detection zones 3A and 7A, detector card placement and slots are typical for a NCDOT installation. Inputs associated with these slots are compatible with time of day instructions located on sheet 2 of this electrical detail.

THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 09-0264T3
 DESIGNED: February 2024
 SEALED: February 12, 2024
 REVISED:

Signal Upgrade - Temporary Design 3 (TMP Phase III) - Electrical Detail - Sheet 1 of 2

Electrical and Programming Details For: US 158 (Reidsville Rd.) at NC 66 (Old Hollow Rd.)

Prepared for the Offices of: [Seal]

Division 9 Forsyth County Walkertown
 PLAN DATE: February 2024 REVIEWED BY: DT Sears
 PREPARED BY: WP Erickson-Jones REVIEWED BY:

REVISIONS: [Table]

DocuSigned by: Porter Jones 2/12/2024

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SEAL NORTH CAROLINA PROFESSIONAL ENGINEER PORTER JONES SEAL 056142

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SIG. INVENTORY NO. 09-0264T3

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MAXTIME ALTERNATE PHASING ACTIVATION DETAIL

To run alternate phasing, select a Pattern that is programmed to run Overlap Plan 2 and Detector Plan 2.
A Pattern can be selected through the scheduler or manually by changing the Operational Mode.

PHASING	OVERLAP PLAN	VEH DET PLAN
ACTIVE PLAN REQUIRED TO RUN DEFAULT PHASING	1	1
ACTIVE PLAN REQUIRED TO RUN ALTERNATE PHASING	2	2

MAXTIME DETECTOR PROGRAMMING DETAIL FOR ALTERNATE PHASING LOOPS 3A & 7A

Front Panel

Main Menu >Controller >Detector >Veh Det Plans

Web Interface

Home >Controller >Detector Configuration >Vehicle Detectors

In the table view of web interface right click on "Detector" in the top left corner of the table. Copy the entire contents of Detector Plan 1. Paste Detector Plan 1 into Detector Plan 2. Modify Detector Plan 2 as shown below and save changes.

3A

Plan 2		
Detector	Call Phase	Delay
7	3	3
30	0	3

7A

Detector	Call Phase	Delay
21	7	3
32	0	3

ALTERNATE PHASING CHANGE SUMMARY

THE FOLLOWING IS A SUMMARY OF WHAT TAKES PLACE WHEN OVERLAP PLAN 2 AND VEHICLE DETECTOR PLAN 2 ACTIVATE TO CALL THE "ALTERNATE PHASING":

OVERLAP PLAN 2: Modifies overlap included phases for heads 31 and 71 to run protected turns only.

VEH DET PLAN 2: Disables phase 8 call on loop 3A and reduces delay time for phase 3 call on loop 3A to 3 seconds.

Disables phase 4 call on loop 7A and reduces delay time for phase 7 call on loop 7A to 3 seconds.

FLASHER CIRCUIT MODIFICATION DETAIL

IN ORDER TO INSURE THAT SIGNALS FLASH CONCURRENTLY ON THE SAME APPROACH, MAKE THE FOLLOWING FLASHER CIRCUIT CHANGES:

1. ON REAR OF PDA - REMOVE WIRE FROM TERM. T2-4 AND TERMINATE ON T2-2.
2. ON REAR OF PDA - REMOVE WIRE FROM TERM. T2-5 AND TERMINATE ON T2-3.
3. REMOVE FLASHER UNIT 2.

THE CHANGES LISTED ABOVE TIES ALL PHASES AND OVERLAPS TO FLASHER UNIT 1.

MAXTIME ALTERNATE PHASING PATTERN PROGRAMMING DETAIL

Front Panel

Main Menu >Controller >Coordination >Patterns

Web Interface

Home >Controller >Coordination >Patterns

Pattern Parameters

Pattern	Veh Det Plan	Overlap Plan
*	2	2

* The Pattern number(s) are to be determined by the Division and/or City Traffic Engineer.

MAXTIME OVERLAP PROGRAMMING DETAIL FOR DEFAULT PHASING

Front Panel

Main Menu >Controller >Overlap >Overlap Parameters/Overlap Timings

Web Interface

Home >Controller >Overlap Configuration >Overlaps

Overlap Plan 1

Overlap	2	4
Type	FYA 4 - Section	FYA 4 - Section
Included Phases	4	8
Modifier Phases	3	7
Modifier Overlaps	-	-
Trail Green	0	0
Trail Yellow	0.0	0.0
Trail Red	0.0	0.0

MAXTIME OVERLAP PROGRAMMING DETAIL FOR ALTERNATE PHASING

Front Panel

Main Menu >Controller >Overlap >Overlap Parameters/Overlap Timings

Web Interface

Home >Controller >Overlap Configuration >Overlaps

In the table view of the web interface, right click on "Overlap" in the top left corner of the table. Copy the entire contents of Overlap Plan 1. Paste Overlap Plan 1 into Overlap Plan 2. Modify Overlap Plan 2 as shown below and save changes.

Overlap Plan 2

Overlap	2	4
Type	FYA 4 - Section	FYA 4 - Section
Included Phases	-	-
Modifier Phases	3	7
Modifier Overlaps	-	-
Trail Green	0	0
Trail Yellow	0.0	0.0
Trail Red	0.0	0.0

← NOTICE INCLUDED PHASE

Signal Upgrade - Temporary Design 3
(TMP Phase III) - Electrical Detail - Sheet 2 of 2

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Electrical and Programming Details For:

Prepared for the Offices of:

750 N. Greenfield Pkwy, Garner, NC 27529

US 158 (Reidsville Rd.)
at
NC 66 (Old Hollow Rd.)

Division 9 Forsyth County Walkertown

PLAN DATE: February 2024 REVIEWED BY: DT Sears

PREPARED BY: WP Erickson-Jones REVIEWED BY:

REVISIONS	INIT.	DATE

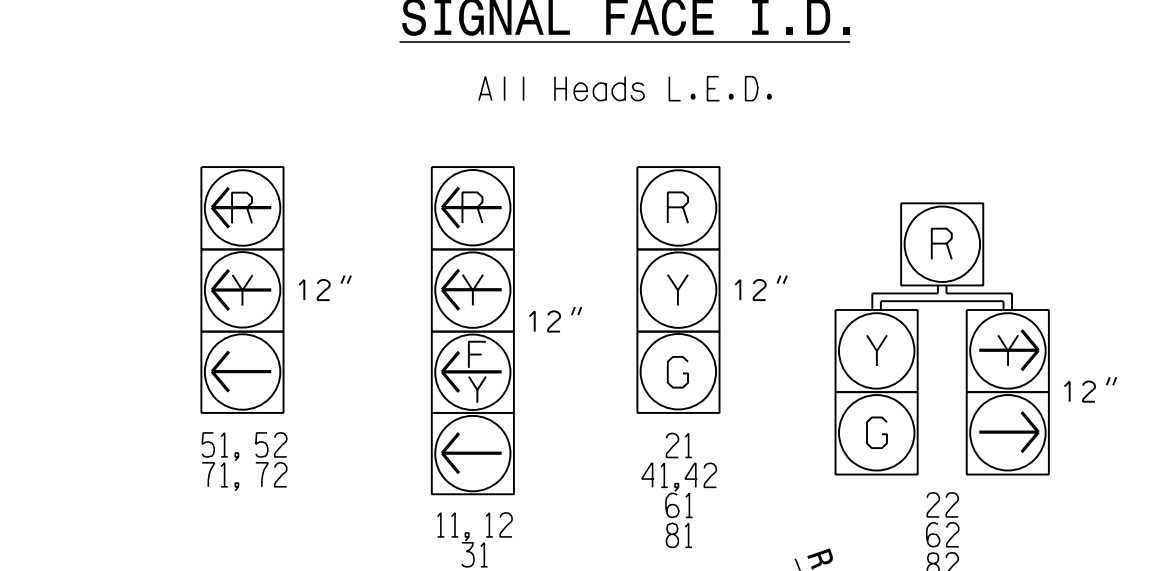
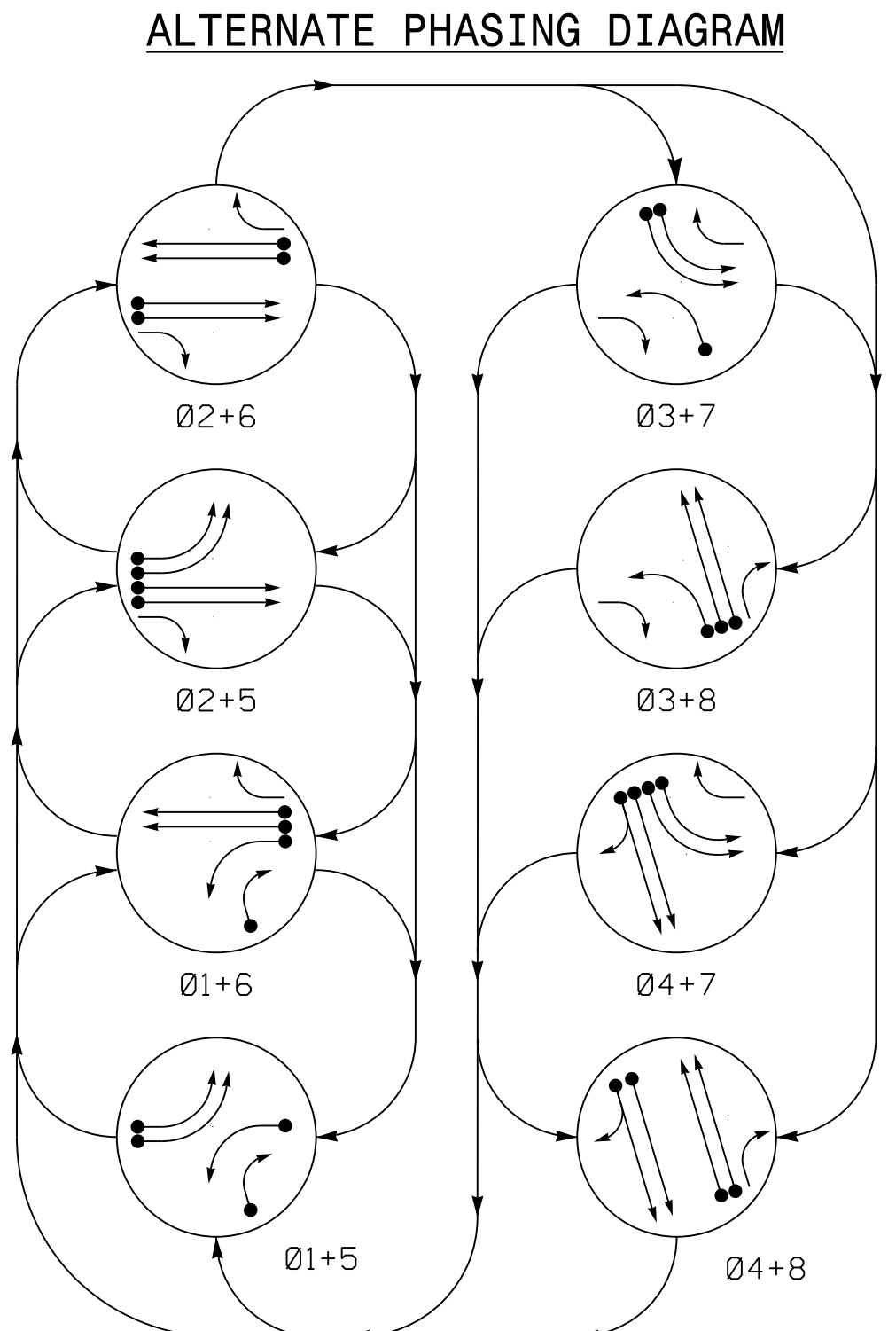
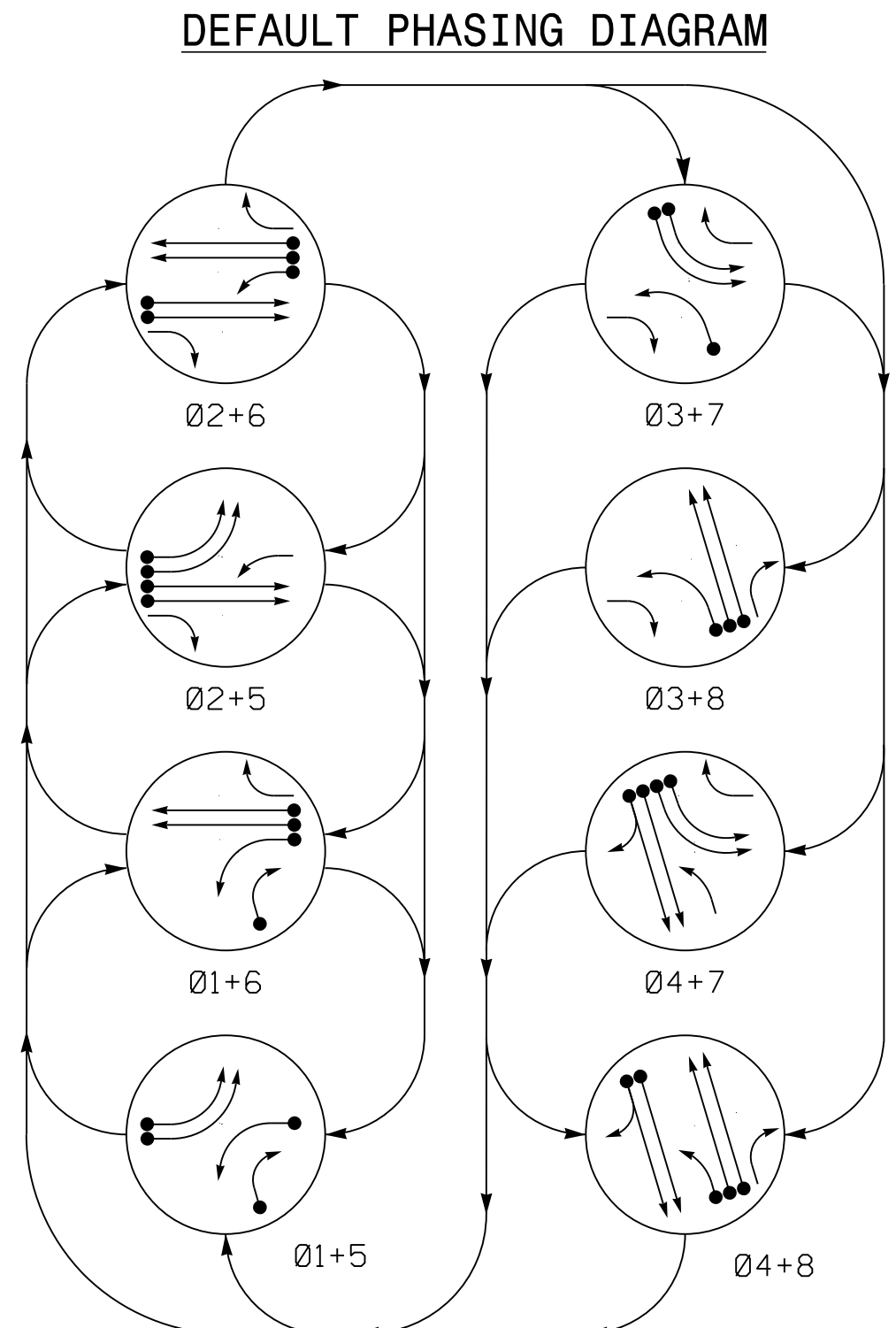
THIS ELECTRICAL DETAIL IS FOR
THE SIGNAL DESIGN: 09-0264T3
DESIGNED: February 2024
SEALED: February 12, 2024
REVISED:

DOCUMENT NOT CONSIDERED
FINAL UNLESS ALL
SIGNATURES COMPLETED

SEAL

DocuSigned by:
Porter Jones 2/12/2024

SIG. INVENTORY NO. 09-0264T3



DEFAULT PHASING TABLE OF OPERATION

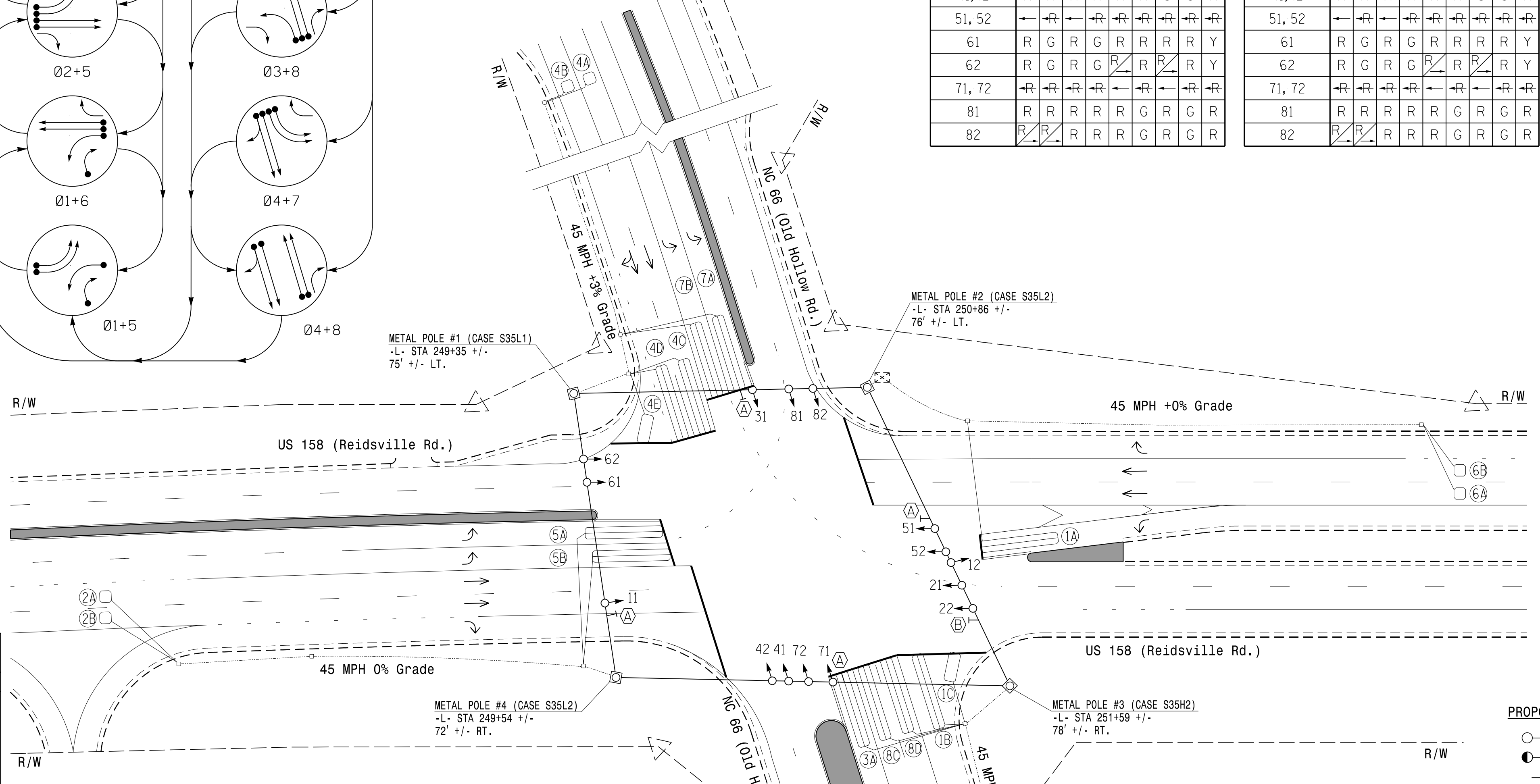
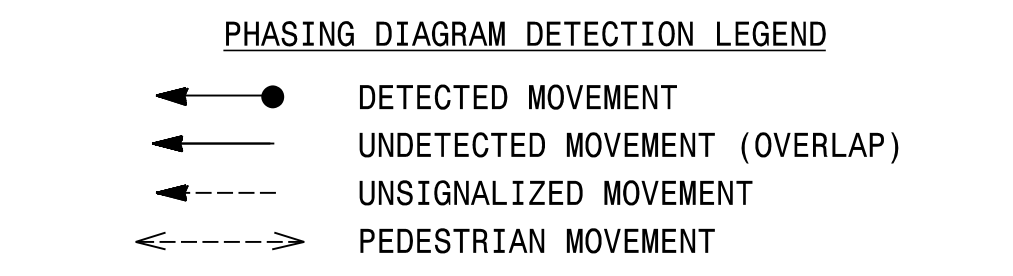
SIGNAL FACE	PHASE							
	01+5	01+6	02+5	02+6	03+7	03+8	04+7	04+8
11, 12	←	←	←	←	←	←	←	←
21	R	R	G	G	R	R	R	Y
22	R	R	G	G	R	R	R	Y
31	R	R	R	R	←	←	←	←
41, 42	R	R	R	R	R	G	G	R
51, 52	←	←	←	←	←	←	←	←
61	R	G	R	G	R	R	R	Y
62	R	G	R	G	R	R	R	Y
71, 72	←	←	←	←	←	←	←	←
81	R	R	R	R	R	G	G	R
82	R	R	R	R	R	G	G	R

ALTERNATE PHASING TABLE OF OPERATION

SIGNAL FACE	PHASE							
	01+5	01+6	02+5	02+6	03+7	03+8	04+7	04+8
11, 12	←	←	←	←	←	←	←	←
21	R	R	G	G	R	R	R	Y
22	R	R	G	G	R	R	R	Y
31	←	←	←	←	←	←	←	←
41, 42	R	R	R	R	R	G	G	R
51, 52	←	←	←	←	←	←	←	←
61	R	G	R	G	R	R	R	Y
62	R	G	R	G	R	R	R	Y
71, 72	←	←	←	←	←	←	←	←
81	R	R	R	R	R	G	G	R
82	R	R	R	R	R	G	G	R

8 Phase Fully Actuated (US 158 Signal System)
Signal System #: D09-11_Winston-Salem
NOTES

- Refer to "Roadway Standard Drawings NCDOT" dated January 2024 and "Standard Specifications for Roads and Structures" dated January 2024.
- Do not program signal for late night flashing operation unless otherwise directed by the Engineer.
- Phase 1 and/or phase 5 may be lagged.
- Phase 3 and/or phase 7 may be lagged.
- Set all detector units to presence mode.
- The Division (City) Traffic Engineer will determine the hours of use for each phasing plan.
- Maximum times shown in timing chart are for free-run operation only. Coordinated signal system timing values supersede these values.

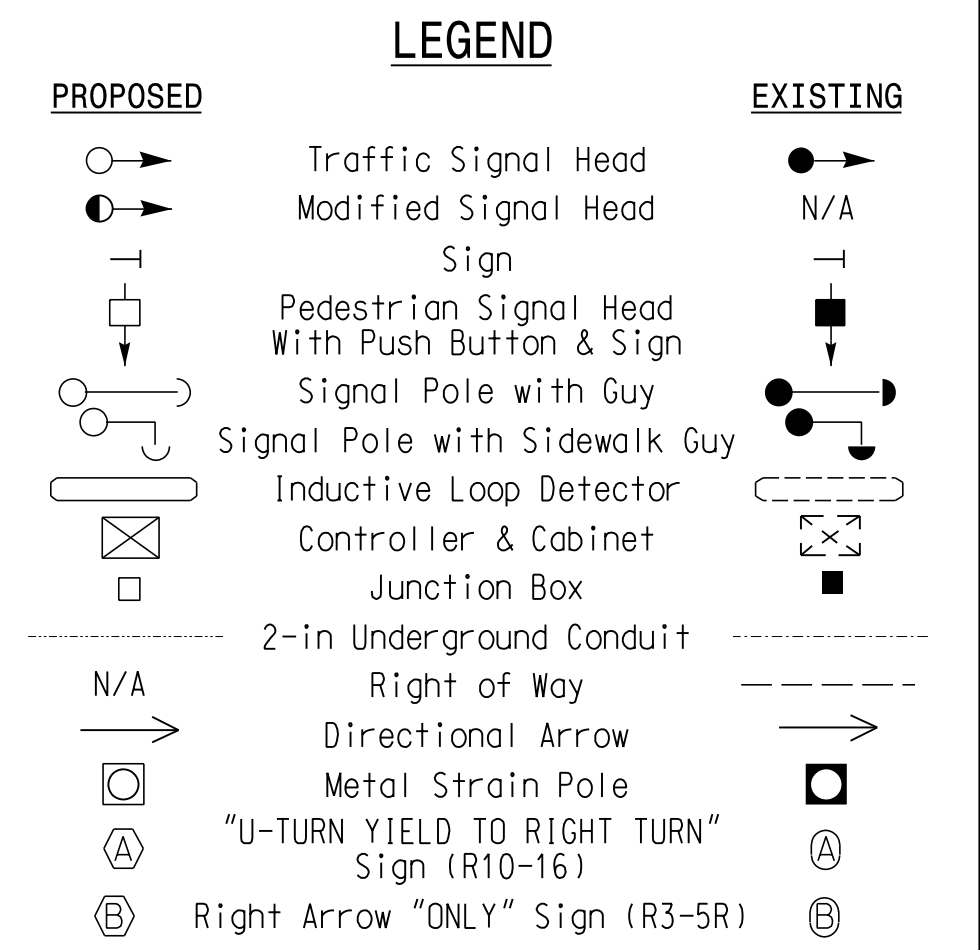


MAXTIME DETECTOR INSTALLATION CHART

DETECTOR		PROGRAMMING												
LOOP	SIZE (FT)	DISTANCE FROM STOPBAR (FT)	TURNS	NEW LOOP	CALL PHASE	DELAY TIME	EXTEND TIME	EXTEND	ADDED INITIAL	CALL	DELAY DURING GREEN	NEW CARD		
1A	6X40	0	2-4-2	X	1	15*	-	X	-	X	-	X	-	X
1B	6X40	0	2-4-2	X	1	15	-	X	-	X	-	X	-	X
1C	6X15	0	4	X	1	15	-	X	-	X	-	X	-	X
2A	6X6	300	4	X	2	-	-	X	X	X	-	X	-	X
2B	6X6	300	4	X	2	-	-	X	X	X	-	X	-	X
3A	6X40	0	2-4-2	X	3	15*	-	X	-	X	-	X	-	X
4A	6X6	300	6	X	4	-	-	X	X	X	-	X	-	X
4B	6X6	300	6	X	4	-	-	X	X	X	-	X	-	X
4C	6X40	0	2-4-2	X	4	5	2.0	X	-	X	X	X	-	X
4D	6X40	0	2-4-2	X	4	5	2.0	X	-	X	X	X	-	X
4E	6X15	0	4	X	4	15	-	X	-	X	-	X	-	X
5A	6X40	0	2-4-2	X	5	-	-	X	-	X	-	X	-	X
5B	6X40	0	2-4-2	X	5	-	-	X	-	X	-	X	-	X
6A	6X6	300	4	X	6	-	-	X	X	X	-	X	-	X
6B	6X6	300	4	X	6	-	-	X	X	X	-	X	-	X
7A	6X40	0	2-4-2	X	7	-	-	X	-	X	-	X	-	X
7B	6X40	0	2-4-2	X	7	-	-	X	-	X	-	X	-	X
8A	6X6	300	6	X	8	-	-	X	X	X	-	X	-	X
8B	6X6	300	6	X	8	-	-	X	X	X	-	X	-	X
8C	6X40	0	2-4-2	X	8	5	2.0	X	-	X	X	X	-	X
8D	6X40	0	2-4-2	X	8	5	2.0	X	-	X	X	X	-	X

MAXTIME TIMING CHART

FEATURE	PHASE							
	1	2	3	4	5	6	7	8
Walk *	-	-	-	-	-	-	-	-
Ped Clear *	-	-	-	-	-	-	-	-
Min Green	7	12	7	7	7	12	7	7
Passage *	2.0	6.0	2.0	2.0	2.0	6.0	2.0	2.0
Max 1 *	20	90	20	40	20	90	20	40
Yellow Change	3.0	4.5	3.0	4.4	3.0	4.5	3.0	4.4
Red Clear	3.7	2.4	4.2	2.8	3.2	2.4	4.7	2.8
Added Initial *	-	1.5	-	-	-	1.5	-	-
Maximum Initial *	-	34	-	-	-	34	-	-
Time Before Reduction *	-	15	-	5	-	15	-	5
Time To Reduce *	-	30	-	15	-	30	-	15
Minimum Gap	-	3.0	-	3.0	-	3.0	-	3.0
Advance Walk	-	-	-	-	-	-	-	-
Non Lock Detector	X	-	X	X	X	-	X	X
Vehicle Recall	-	MIN RECALL	-	-	-	MIN RECALL	-	-
Dual Entry	-	-	-	X	-	-	-	X



Signal Upgrade - Final Design

Prepared For the Offices of:

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 NC License No. F-4112

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US 158 (Reidsville Rd.) at NC 66 (Old Hollow Rd.)

Division 9 Forsyth County Walkertown

PLAN DATE: February 2024 REVIEWED BY: WP Erickson-Jones

PREPARED BY: H Townsend REVIEWED BY: DTSears

DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED

2/12/2024

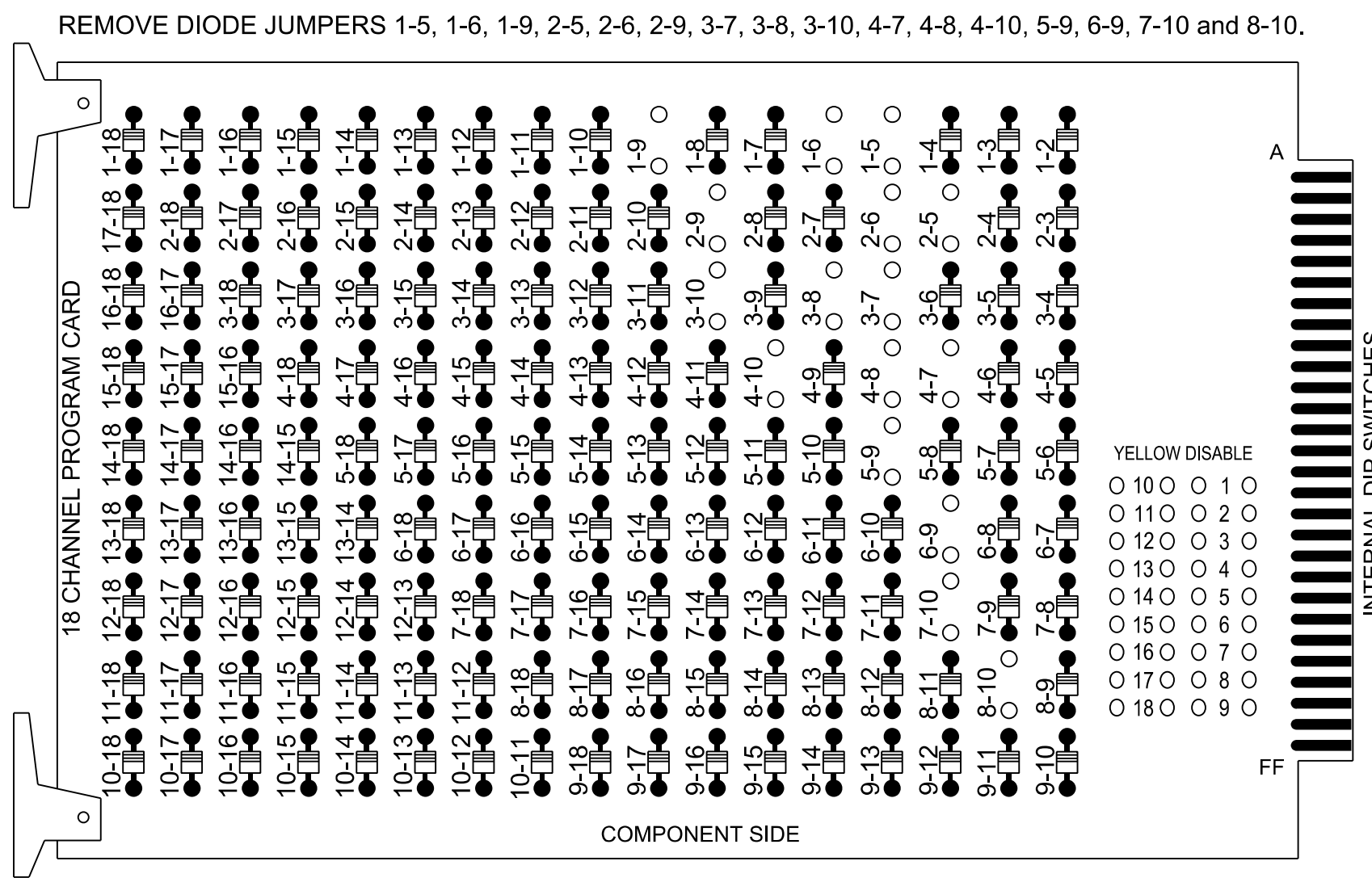
DATE

* Disable Delay During Alternate Phasing Operation.
 # Disable Phase Call For Loop(s) During Alternate Phasing Operation.

* These values may be field adjusted. Do not adjust Min Green and Extension times for phases 2 and 6 lower than what is shown. Min Green for all other phases should not be lower than 4 seconds.

18 CHANNEL IP CONFLICT MONITOR PROGRAMMING DETAIL

(remove jumpers and set switches as shown)



- NOTES:**
- Card is provided with all diode jumpers in place. Removal of any jumper allows its channels to run concurrently.
 - Ensure jumpers SEL2-SEL5 and SEL9 are present on the monitor board.
 - Ensure that the Red Enable is active at all times during normal operation.
 - Integrate monitor with Ethernet network in cabinet.

NOTES

- To prevent "flash-conflict" problems, insert red flash program blocks for all unused vehicle load switches in the output file. The installer shall verify that signal heads flash in accordance with the signal plan.
- Return controller to Factory Defaults before programming per this electrical detail.
- Program phases 4 and 8 for Dual Entry.
- Program controller to start up in phase 2 Green No Walk and 6 Green No Walk.
- If this signal will be managed by an ATMS software, enable controller and detector logging for all detectors used at this location.
- The cabinet and controller are part of the US 158 Signal System, Signal System : D09-11_Winston-Salem System.

EQUIPMENT INFORMATION

Controller.....2070LX
 Cabinet.....332 w/ Aux
 Software.....Q-Free MAXTIME
 Cabinet Mount.....Base
 Output File Positions.....18 With Aux. Output File
 Load Switches Used.....S1, S2, S4, S5, S7, S8, S10, S11, AUX S1, AUX S2
 Phases Used.....1, 2, 3, 4, 5, 6, 7, 8
 Overlap "1".....*
 Overlap "2".....*
 Overlap "3".....NOT USED
 Overlap "4".....NOT USED

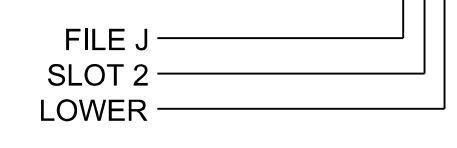
*See overlap programming detail on sheet 2.

INPUT FILE CONNECTION & PROGRAMMING CHART

LOOP NO.	LOOP TERMINAL	INPUT FILE POS.	PIN NO.	INPUT POINT	DETECTOR NO.	CALL PHASE	DELAY TIME	EXTEND TIME	EXTEND	ADDED INITIAL	CALL	DELAY DURING GREEN
1A	TB2-1,2	I1U	56	18	1 ★	1	15		X		X	
				-	29 ★	6			X		X	X
1B	TB2-5,6	I2U	39	1	2	1	15		X		X	
1C	TB2-7,8	I2L	43	5	3	1	15		X		X	
2A	TB2-9,10	I3U	63	29	4	2			X	X	X	
2B	TB2-11,12	I3L	76	42	5	2			X	X	X	
3A	TB4-5,6	I5U	58	20	7 ★	3	15		X		X	
				-	30 ★	8			X		X	
4A	TB4-9,10	I6U	41	3	8	4			X	X		
4B	TB4-11,12	I6L	45	7	9	4			X	X		
4C	TB6-1,2	I7U	65	31	10	4	5	2.0	X		X	X
4D	TB6-3,4	I7L	78	44	11	4	5	2.0	X		X	X
4E	TB6-5,6	I8U	49	11	12	4	15		X		X	
5A	TB3-1,2	J1U	55	17	15	5			X		X	
5B	TB3-1,2	J1U	55	17	15	5			X		X	
6A	TB3-5,6	J2U	40	2	16	6			X	X	X	
6B	TB3-7,8	J2L	44	6	17	6			X	X	X	
7A	TB5-5,6	J5U	57	19	21	7			X		X	
7B	TB5-5,6	J5U	57	19	21	7			X		X	
8A	TB5-9,10	J6U	42	4	22	8			X	X		
8B	TB5-11,12	J6L	46	8	23	8			X	X		
8C	TB7-1,2	J7U	66	32	24	8	5	2.0	X		X	X
8D	TB7-3,4	J7L	79	45	25	8	5	2.0	X		X	X

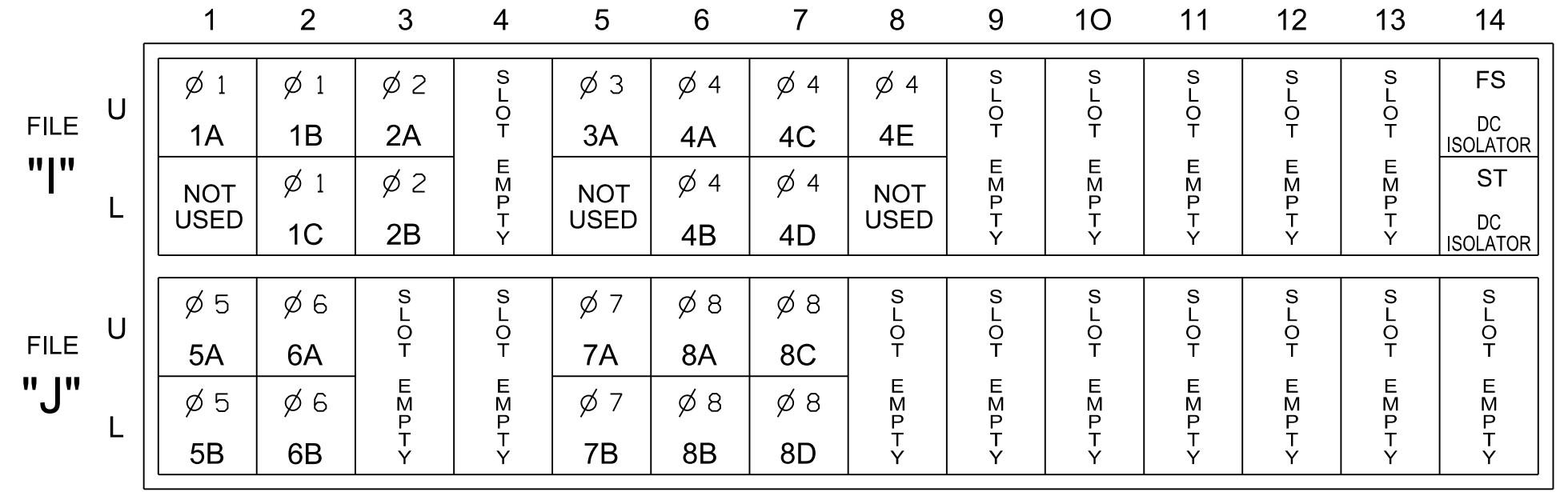
★ For the detector to work as shown on the signal design plan, see the vehicle detector setup programming detail for alternate phasing on sheet 2.

INPUT FILE POSITION LEGEND: J2L



INPUT FILE POSITION LAYOUT

(front view)



EX.: 1A, 2A, ETC. = LOOP NO.'S

FS = FLASH SENSE
 ST = STOP TIME

FLASHER CIRCUIT MODIFICATION DETAIL

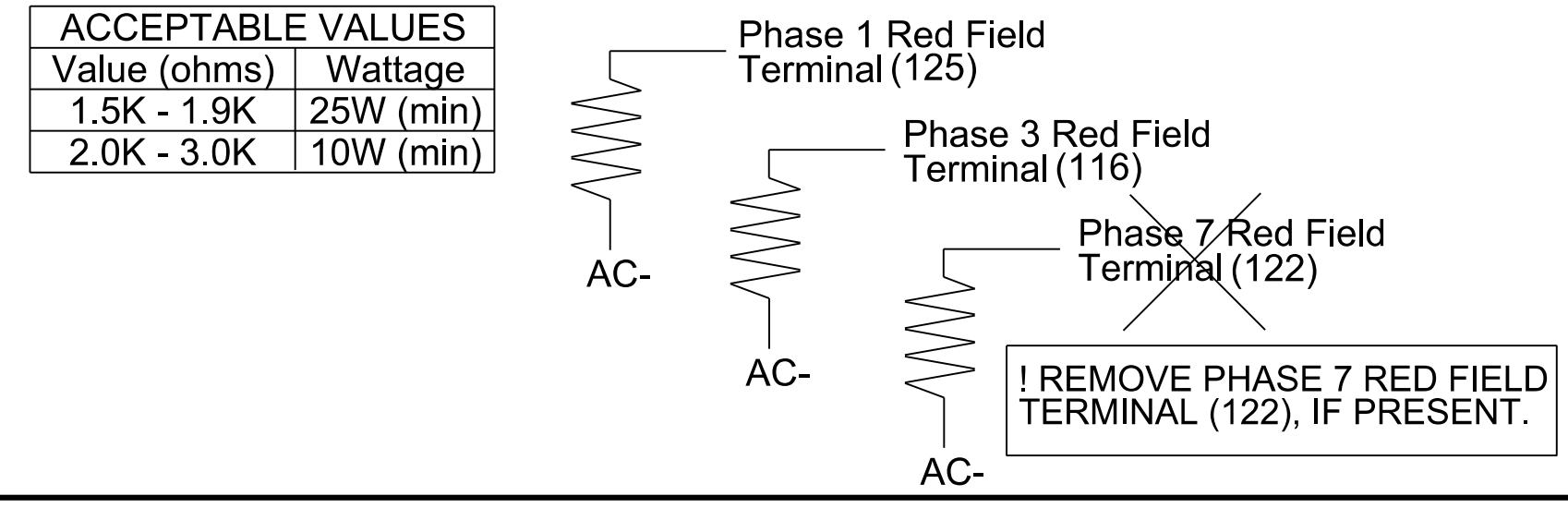
IN ORDER TO INSURE THAT SIGNALS FLASH CONCURRENTLY ON THE SAME APPROACH, MAKE THE FOLLOWING FLASHER CIRCUIT CHANGES:

- ON REAR OF PDA - REMOVE WIRE FROM TERM. T2-4 AND TERMINATE ON T2-2.
- ON REAR OF PDA - REMOVE WIRE FROM TERM. T2-5 AND TERMINATE ON T2-3.
- REMOVE FLASHER UNIT 2.

THE CHANGES LISTED ABOVE TIES ALL PHASES AND OVERLAPS TO FLASHER UNIT 1.

LOAD RESISTOR INSTALLATION DETAIL

(install resistors as shown)



SIGNAL HEAD HOOK-UP CHART

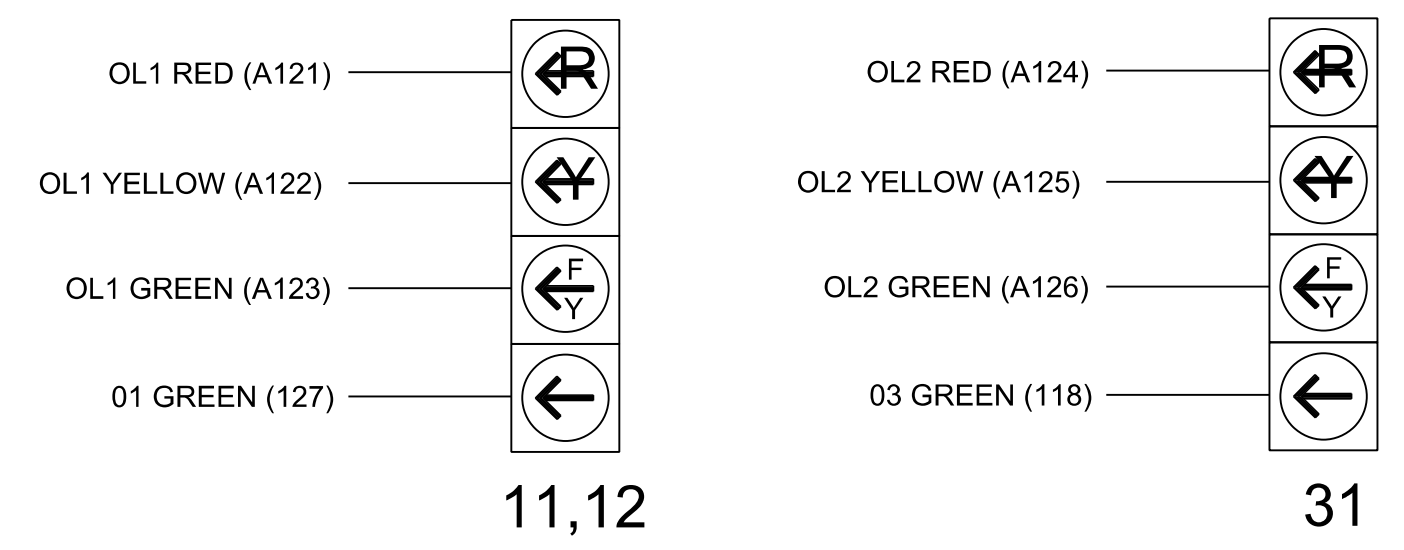
LOAD SWITCH NO.	S1	S2	S3	S4	S5	S6	S7	S8	S9	S10	S11	S12	AUX S1	AUX S2	AUX S3	AUX S4	AUX S5	AUX S6
CMU CHANNEL NO.	1	2	13	3	4	14	5	6	15	7	8	16	9	10	17	11	12	18
PHASE	1	2	2 PED	3	4	4 PED	5	6	6 PED	7	8	8 PED	OL1	OL2	SPARE	OL3	OL4	SPARE
SIGNAL HEAD NO.	11,12	82	21,22	22	31	41,42	51,52	61,62	62	71,72	81,82	82	11,12	31	NU	NU	NU	NU
RED	*	128		*	101		134			107								
YELLOW		129			102		135			108								
GREEN		130			103		136			109								
RED ARROW							131			122			A121	A124				
YELLOW ARROW	126			117			132		123	123			A122	A125				
FLASHING YELLOW ARROW													A123	A126				
GREEN ARROW	127	127		118	118		133		124	124								

NU = Not Used

* Denotes install load resistor. See load resistor installation detail this sheet.
 * See pictorial of head wiring in detail this sheet.

FYA SIGNAL WIRING DETAIL

(wire signal heads as shown)



THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 09-0264
 DESIGNED: February 2024
 SEALED: February 12, 2024
 REVISED:

Signal Upgrade - Final Design - Electrical Detail - Sheet 1 of 2

Electrical and Programming Details For:

Prepared for the Offices of:

US 158 (Reidsville Rd.) at NC 66 (Old Hollow Rd.)

Division 9 Forsyth County Walkertown

PLAN DATE: February 2024 REVIEWED BY: DT Sears

PREPARED BY: WP Erickson-Jones REVIEWED BY:

REVISIONS

INIT. DATE

Seal: NORTH CAROLINA PROFESSIONAL ENGINEER PORTER JONES

DocuSigned by: Porter Jones 2/12/2024

SIG. INVENTORY NO. 09-0264

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MAXTIME ALTERNATE PHASING ACTIVATION DETAIL

To run alternate phasing, select a Pattern that is programmed to run Overlap Plan 2 and Detector Plan 2.
A Pattern can be selected through the scheduler or manually by changing the Operational Mode.

PHASING	OVERLAP PLAN	VEH DET PLAN
ACTIVE PLAN REQUIRED TO RUN DEFAULT PHASING	1	1
ACTIVE PLAN REQUIRED TO RUN ALTERNATE PHASING	2	2

MAXTIME DETECTOR PROGRAMMING DETAIL FOR ALTERNATE PHASING LOOPS 1A & 3A

Front Panel
Main Menu >Controller >Detector >Veh Det Plans

Web Interface
Home >Controller >Detector Configuration >Vehicle Detectors

In the table view of web interface right click on "Detector" in the top left corner of the table. Copy the entire contents of Detector Plan 1. Paste Detector Plan 1 into Detector Plan 2. Modify Detector Plan 2 as shown below and save changes.

1A

Plan 2		
Detector	Call Phase	Delay
1	1	0
29	0	-

3A

Detector	Call Phase	Delay
7	3	0
30	0	-

ALTERNATE PHASING CHANGE SUMMARY

THE FOLLOWING IS A SUMMARY OF WHAT TAKES PLACE WHEN OVERLAP PLAN 2 AND VEHICLE DETECTOR PLAN 2 ACTIVATE TO CALL THE "ALTERNATE PHASING":

OVERLAP PLAN 2: Modifies overlap included phases for heads 11, 12, and 31 to run protected turns only.

VEH DET PLAN 2: Disables phase 6 call on loop 1A and reduces delay time for phase 1 call on loop 1A to 0 seconds.

Disables phase 8 call on loop 3A and reduces delay time for phase 3 call on loop 3A to 0 seconds.

MAXTIME ALTERNATE PHASING PATTERN PROGRAMMING DETAIL

Front Panel
Main Menu >Controller >Coordination >Patterns

Web Interface
Home >Controller >Coordination >Patterns

Pattern Parameters

Pattern	Veh Det Plan	Overlap Plan
*	2	2

* The Pattern number(s) are to be determined by the Division and/or City Traffic Engineer.

MAXTIME OVERLAP PROGRAMMING DETAIL FOR DEFAULT PHASING

Front Panel
Main Menu >Controller >Overlap >Overlap Parameters/Overlap Timings

Web Interface
Home >Controller >Overlap Configuration >Overlaps

Overlap Plan 1

Overlap	1	2
Type	FYA 4 - Section	FYA 4 - Section
Included Phases	2	4
Modifier Phases	1	3
Modifier Overlaps	-	-
Trail Green	0	0
Trail Yellow	0.0	0.0
Trail Red	0.0	0.0

MAXTIME OVERLAP PROGRAMMING DETAIL FOR ALTERNATE PHASING

Front Panel
Main Menu >Controller >Overlap >Overlap Parameters/Overlap Timings

Web Interface
Home >Controller >Overlap Configuration >Overlaps

In the table view of the web interface, right click on "Overlap" in the top left corner of the table. Copy the entire contents of Overlap Plan 1. Paste Overlap Plan 1 into Overlap Plan 2. Modify Overlap Plan 2 as shown below and save changes.

Overlap Plan 2

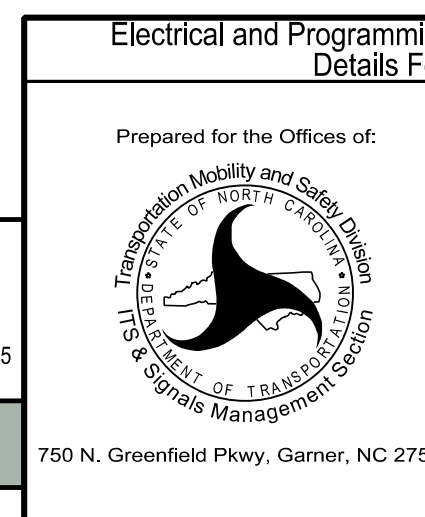
Overlap	1	2
Type	FYA 4 - Section	FYA 4 - Section
Included Phases	-	-
Modifier Phases	1	3
Modifier Overlaps	-	-
Trail Green	0	0
Trail Yellow	0.0	0.0
Trail Red	0.0	0.0

← NOTICE INCLUDED PHASE

THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 09-0264
DESIGNED: February 2024
SEALED: February 12, 2024
REVISED:

Signal Upgrade - Final Design - Electrical Detail - Sheet 2 of 2

DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED



US 158 (Reidsville Rd.) at NC 66 (Old Hollow Rd.)	
Division 9	Forsyth County Walkertown
PLAN DATE: February 2024	REVIEWED BY: DT Sears
PREPARED BY: WP Erickson-Jones	REVIEWED BY:
REVISIONS	INIT. DATE

SEAL NORTH CAROLINA PROFESSIONAL ENGINEER PORTER JONES	2/12/2024
DocuSigned by: Porter Jones	DATE
SIG. INVENTORY NO. 09-0264	

PHASING DIAGRAM

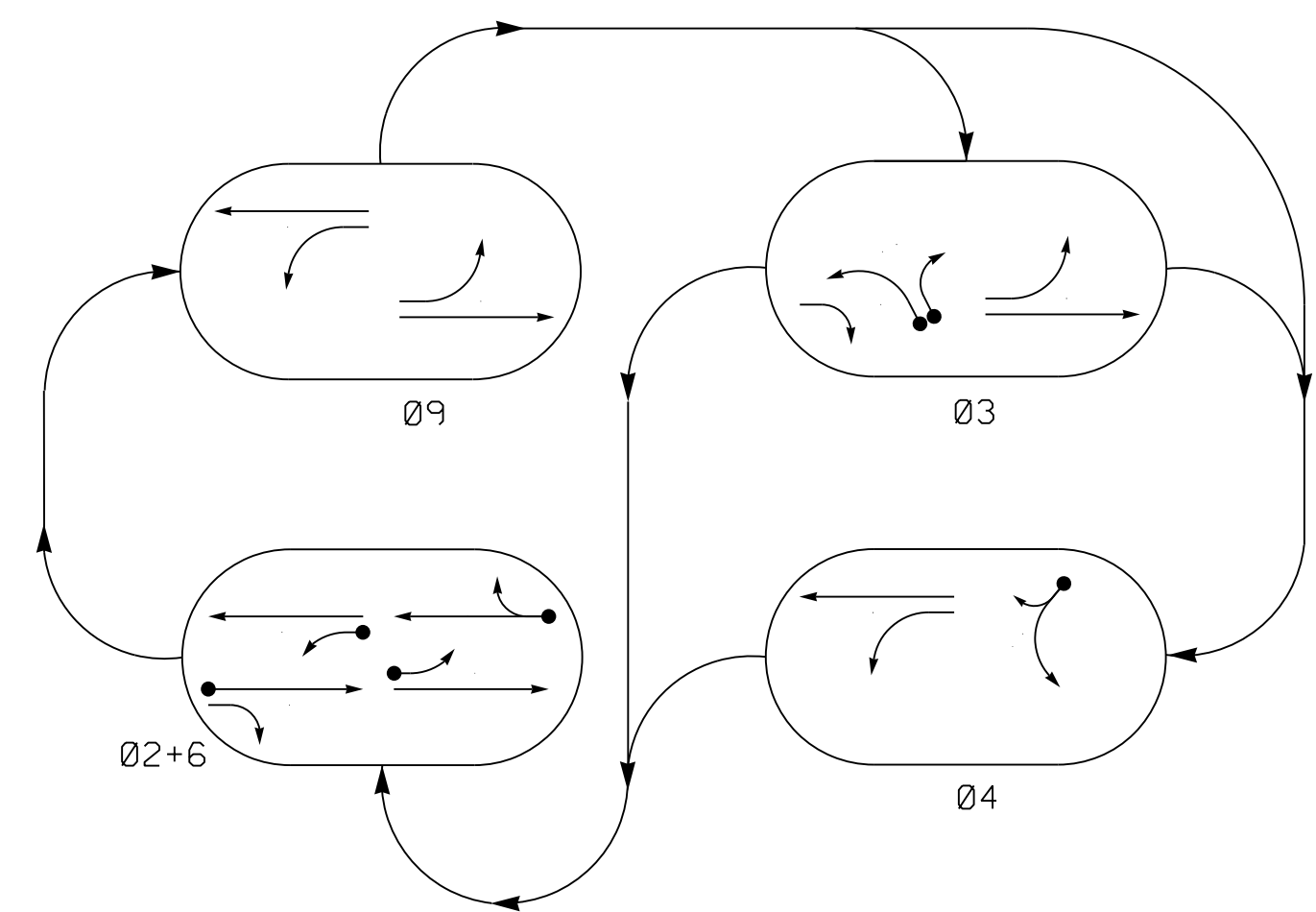


TABLE OF OPERATION

SIGNAL FACE	PHASE				
	02+6	03	04	09	11
11	Y	R	R	R	Y
21	G	R	R	R	Y
22	G	R	R	R	Y
23, 24	G	G	G	R	Y
31	R	R	R	R	Y
32	R	R	R	R	Y
41	R	R	R	R	Y
42	R	R	R	R	Y
51	Y	R	R	R	Y
61, 62	G	R	R	R	Y
63, 64	G	G	R	G	Y

MAXTIME DETECTOR INSTALLATION CHART

ZONE	SIZE (FT)	DISTANCE FROM STOPBAR (FT)	TURNS	NEW LOOP	PROGRAMMING								
					CALL PHASE	ADDITIONAL CALL PHASE	DELAY TIME	EXTEND TIME	EXTEND	INITIAL	CALL	DELAY DURING GREEN	NEW CARD
2A*	6X6	300	*	*	2	-	-	-	-	X	X	-	*
2B*	6X40	0	*	*	2	-	-	-	-	X	X	-	*
3A*	6X40	0	*	*	9	3	10	-	-	X	X	-	*
3B*	6X40	0	*	*	9	3	15	-	-	X	X	-	*
3C*	6X6	0	*	*	9	3	15	-	-	X	X	-	*
4A*	6X40	0	*	*	9	4	10	-	-	X	X	-	*
6A*	6X6	300	*	*	6	-	-	-	-	X	X	-	*
6B*	6X40	0	*	*	6	-	-	-	-	X	X	-	*

* Video Detection Zone

4 Phase Fully Actuated (Isolated) NOTES

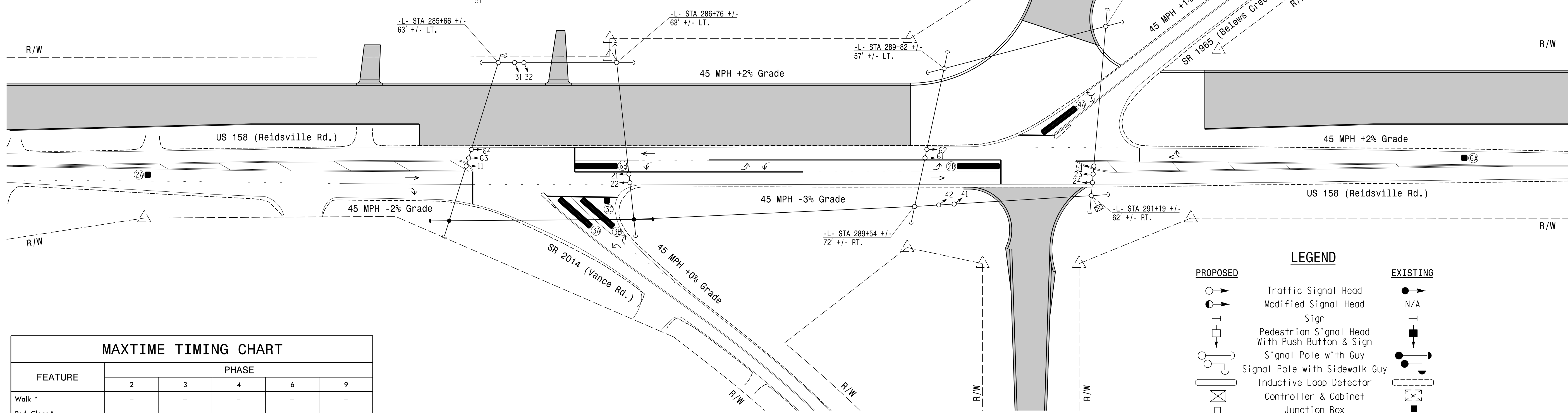
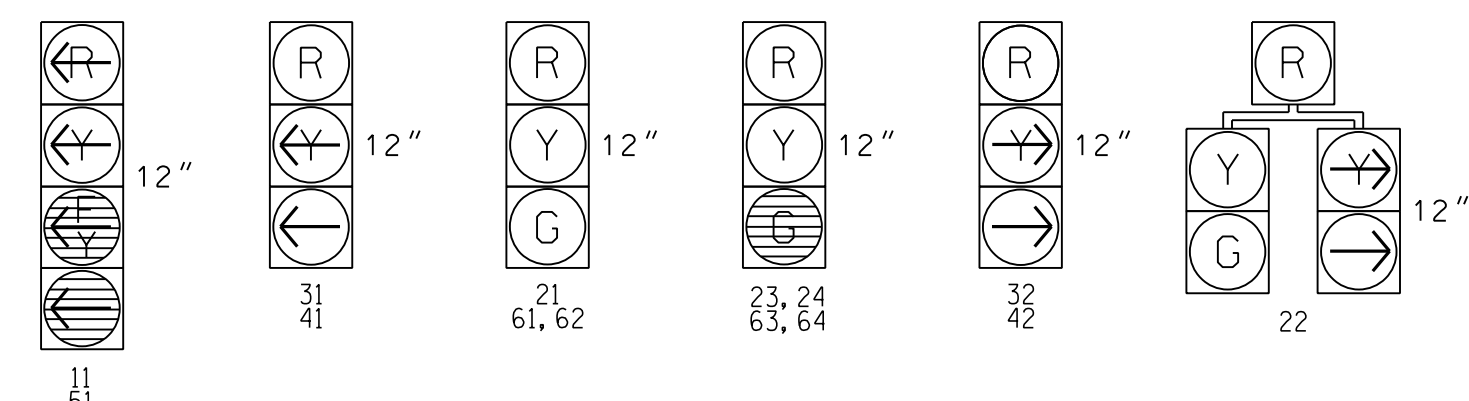
1. Refer to "Roadway Standard Drawings NCDOT" dated January 2024 and "Standard Specifications for Roads and Structures" dated January 2024.
2. Do not program signal for late night flashing unless otherwise directed by the Engineer.
3. Program controller to clear from phase 2+6 to phase 3 or phase 4 by progressing through phase 9 (see Electrical Details).
4. The order of phase 3 and phase 4 may be reversed.
5. Tether signal heads numbered 11, 23, 24, 51, 63, and 64.
6. Set all detector units to presence mode.
7. Locate new cabinet so as not to obstruct sight distance of vehicles turning right on red.
8. Pavement markings are existing.
9. This intersection uses video detection. Install detectors according to the manufacturer's instructions to achieve the desired detection.

PHASING DIAGRAM DETECTION LEGEND

- ←● DETECTED MOVEMENT
- ← UNDETECTED MOVEMENT (OVERLAP)
- UNSIGNALIZED MOVEMENT
- ↔ PEDESTRIAN MOVEMENT

SIGNAL FACE I.D.

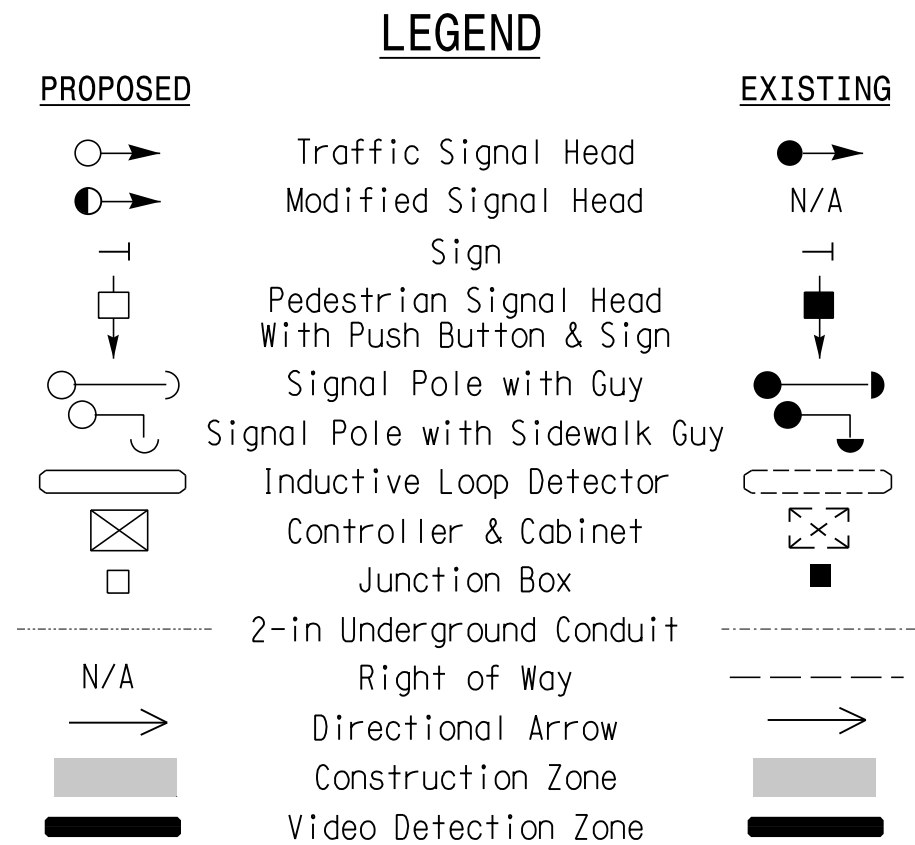
All Heads L.E.D.
 ☉ Denotes Louvers



MAXTIME TIMING CHART

FEATURE	PHASE				
	2	3	4	6	9
Walk *	-	-	-	-	-
Ped Clear *	-	-	-	-	-
Min Green	12	7	7	12	11
Passage *	6.0	2.0	2.0	6.0	2.0
Max I *	90	30	30	90	11
Yellow Change	4.7	4.8	4.3	4.7	4.8
Red Clear	2.2	3.3	1.9	2.2	1.6
Added Initial *	2.5	-	-	2.5	-
Maximum Initial *	34	-	-	34	-
Time Before Reduction *	15	-	-	15	-
Time To Reduce *	30	-	-	30	-
Minimum Gap	3.0	-	-	3.0	-
Advance Walk	-	-	-	-	-
Non Lock Detector	-	X	X	-	-
Vehicle Recall	MIN RECALL	-	-	MIN RECALL	-
Dual Entry	-	-	-	-	-

* These values may be field adjusted. Do not adjust Min Green and Extension times for phases 2 and 6 lower than what is shown. Min Green for all other phases should not be lower than 4 seconds.



Signal Upgrade - Temporary Design 1 (TMP Phase I Step 1)

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 TRANSPORTATION MOBILITY AND SAFETY DIVISION
 DEPARTMENT OF TRANSPORTATION
 SIGNAL DESIGN SECTION
 750 N. Greenfield Pkwy, Garner, NC 27529

US 158 (Reidsville Rd.) at SR 2014 (Vance Rd.) / SR 1965 (Belews Creek Rd.)
 Division 9 Forsyth County Walkertown
 PLAN DATE: February 2024 REVIEWED BY: WP Erickson-Jones
 PREPARED BY: H Townsend REVIEWED BY:

DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED

SEAL
 NORTH CAROLINA PROFESSIONAL ENGINEER
 SEAL 056142
 W. PORTER JONES
 PORTER JONES ENGINEERS
 2/12/2024
 DATE

2/12/2024
 R:\Traffic\c45\Signal\04as\1gn45\1gn45\1gn45\03110011.dwg
 W:\Jones

SEQUENCE DETAIL

Front Panel
Main Menu >Controller >Sequence & Phs Config>Sequences

Web Interface
Home >Controller >Sequence

Sequence 1

Ring	Sequence Data
1	2,a,9,b,3,4,c
2	6,a,b,c

OVERLAP PROGRAMMING

Front Panel
Main Menu >Controller >Overlap >Overlap Parameters/Overlap Timings

Web Interface
Home >Controller >Overlap Configuration >Overlaps

Overlap Plan 1

Overlap	1	2	3	4	7	8
Type	FYA 4 - Section	Normal	FYA 4 - Section	Normal	Normal	Normal
Included Phases	2	4,6,9	6	2,3,9	4,9	3,9
Modifier Phases	-	-	-	-	-	-
Modifier Overlaps	7	-	8	-	-	-
Trail Green	0	0	0	0	0	0
Trail Yellow	0.0	0.0	0.0	0.0	0.0	0.0
Trail Red	0.0	0.0	0.0	0.0	0.0	0.0

FLASHER CIRCUIT MODIFICATION DETAIL

IN ORDER TO INSURE THAT SIGNALS FLASH CONCURRENTLY ON THE SAME APPROACH, MAKE THE FOLLOWING FLASHER CIRCUIT CHANGES:

- ON REAR OF PDA - REMOVE WIRE FROM TERM. T2-4 AND TERMINATE ON T2-2.
- ON REAR OF PDA - REMOVE WIRE FROM TERM. T2-5 AND TERMINATE ON T2-3.
- REMOVE FLASHER UNIT 2.

THE CHANGES LISTED ABOVE TIES ALL PHASES AND OVERLAPS TO FLASHER UNIT 1.

OUTPUT CHANNEL CONFIGURATION

Front Panel
Main Menu >Controller >More>Channels>Channels Config

Web Interface
Home >Controller >Advanced IO>Channels>Channels Configuration

Channel Configuration

Channel	Control Type	Control Source	Flash Yellow	Flash Red	Flash Alt	MMU Channel
1	Overlap	7	X	-	X	1
2	Phase Vehicle	2	X	-	-	2
3	Phase Vehicle	3	-	X	X	3
4	Phase Vehicle	4	-	X	-	4
5	Phase Vehicle	5	-	X	-	5
6	Phase Vehicle	6	X	-	X	6
7	Overlap	8	X	-	-	7
8	Phase Vehicle	8	-	X	X	8
9	Overlap	1	X	-	X	9
10	Overlap	2	-	X	X	10
11	Overlap	3	X	-	-	11
12	Overlap	4	-	X	-	12
13	Phase Ped	2	-	-	-	13
14	Phase Ped	4	-	-	-	14
15	Phase Ped	6	-	-	-	15
16	Phase Ped	8	-	-	-	16
17	Overlap	5	-	X	X	17
18	Overlap	6	-	X	-	18

NOTICE OVERLAP 7 ASSIGNED TO CHANNEL 1. →

NOTICE OVERLAP 8 ASSIGNED TO CHANNEL 7. →

← NOTICE CHANNEL 1 YELLOW FLASH

← NOTICE CHANNEL 7 YELLOW FLASH

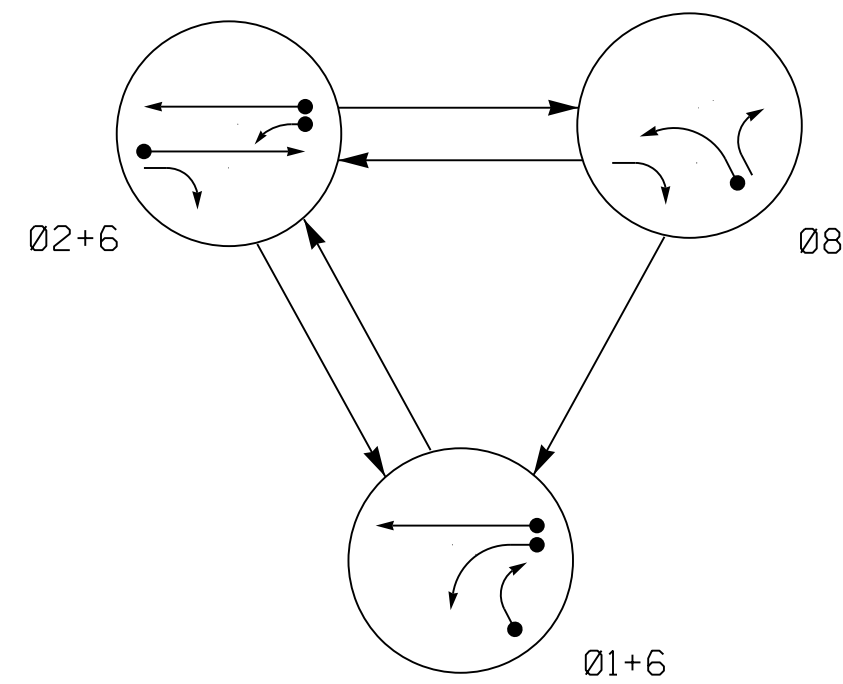
THIS ELECTRICAL DETAIL IS FOR
THE SIGNAL DESIGN: 09-1100T1
DESIGNED: February 2024
SEALED: February 12, 2024
REVISED:



Signal Upgrade - Temporary Design 1
(TMP Phase I Step 1) Electrical Detail - Sheet 2 of 2

Electrical and Programming Details For: Prepared for the Offices of: 750 N. Greenfield Pkwy, Garner, NC 27529	US 158 (Reidsville Rd.) at SR 2014 (Vance Rd.)/ SR 1965 (Belevs Creek Rd.)		DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED SEAL NORTH CAROLINA PROFESSIONAL ENGINEER SEAL 056142 PORTER JONES DocuSigned by: Porter Jones 2/12/2024 DATE
	Division 9 PLAN DATE: February 2024 PREPARED BY: WP Erickson-Jones REVISIONS INIT. DATE	Forsyth County Walkertown REVIEWED BY: DT Sears REVIEWED BY:	

PHASING DIAGRAM



SIGNAL FACE	PHASE			
	01+6	02+6	08	Flooded
11	←	←	←	←
21	R	G	R	Y
22	R	G	R	Y
31	R	R	←	R
32	→	R	→	R
63,64	G	G	R	Y

MAXTIME DETECTOR INSTALLATION CHART												
DETECTOR					PROGRAMMING							
ZONE	SIZE (FT)	DISTANCE FROM STOPBAR (FT)	TURNS	NEW LOOP	CALL PHASE	DELAY TIME	EXTEND TIME	EXTEND	ADDED INITIAL	CALL	DELAY DURING GREEN	NEW CARD
1A *	6X40	0	*	*	1	15	-	X	X	X	-	*
					6	3	-	X	X	X	-	*
1B *	6X40	0	*	*	1	15	-	X	X	X	-	*
1C *	6X6	0	*	*	1	15	-	X	X	X	-	*
2A *	6X6	300	*	*	2	-	-	X	X	X	-	*
6A *	6X6	300	*	*	6	-	-	X	X	X	-	*
8A *	6X40	0	*	*	6	-	-	X	X	X	-	*

* Video Detection Zone

3 Phase Fully Actuated (Isolated)

NOTES

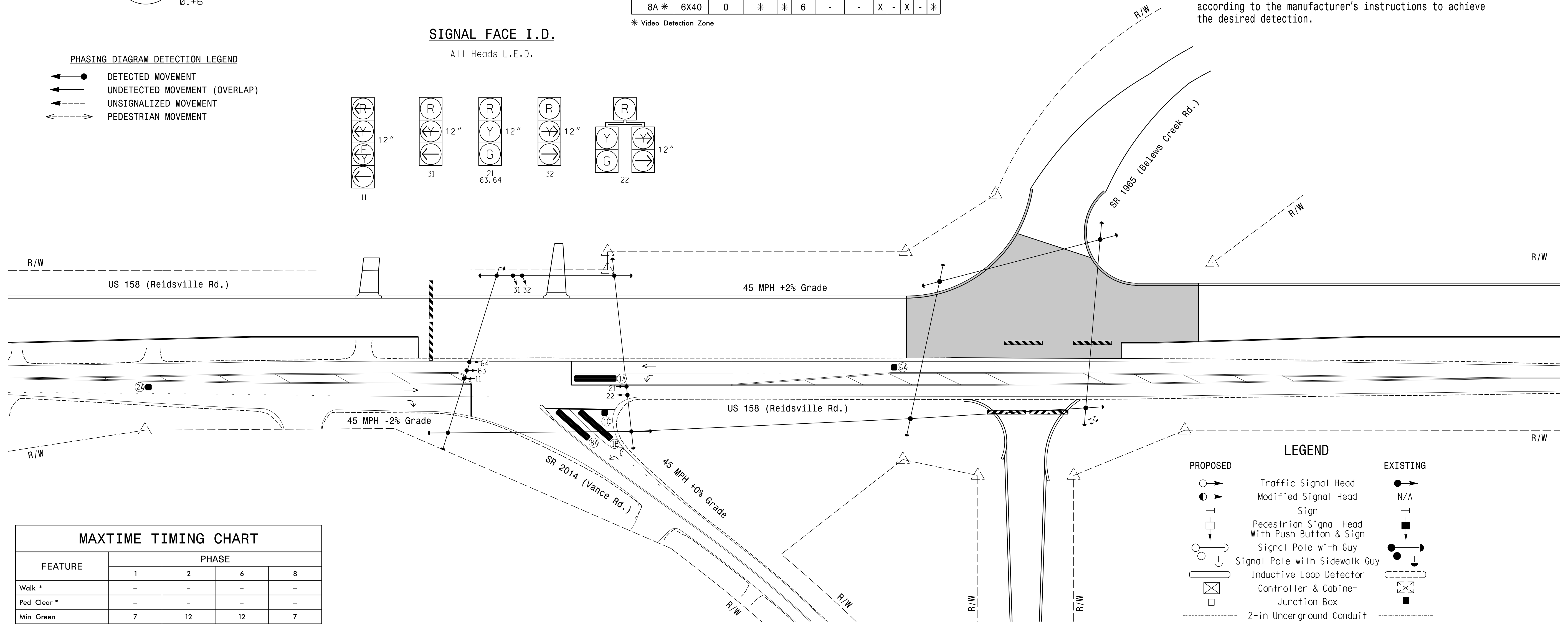
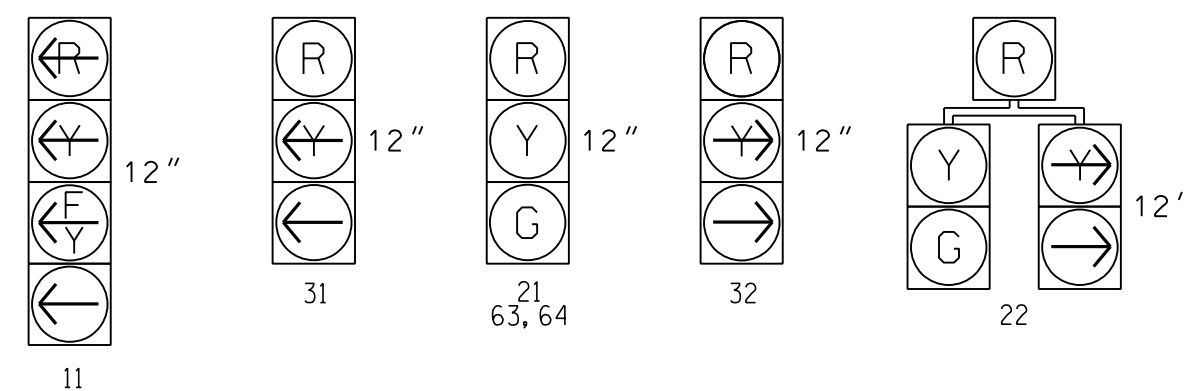
1. Refer to "Roadway Standard Drawings NCDOT" dated January 2024 and "Standard Specifications for Roads and Structures" dated January 2024.
2. Do not program signal for late night flashing unless otherwise directed by the Engineer.
3. Phase 1 may be lagged.
4. Disconnect and bag heads from 09-1100T1 numbered 23, 24, 41, 42, 51, 61, and 62.
5. Remove louvers from signal heads numbered 11, 63, and 64.
6. Set all detector units to presence mode.
7. Pavement markings are existing.
8. This intersection uses video detection. Install detectors according to the manufacturer's instructions to achieve the desired detection.

PHASING DIAGRAM DETECTION LEGEND

- ←● DETECTED MOVEMENT
- ← UNDETECTED MOVEMENT (OVERLAP)
- ← UNSIGNALIZED MOVEMENT
- ← PEDESTRIAN MOVEMENT

SIGNAL FACE I.D.

All Heads L.E.D.



FEATURE	PHASE			
	1	2	6	8
Walk *	-	-	-	-
Ped Clear *	-	-	-	-
Min Green	7	12	12	7
Passage *	2.0	6.0	6.0	2.0
Max 1 *	20	90	90	30
Yellow Change	3.0	4.8	4.8	3.0
Red Clear	2.3	2.2	2.2	3.3
Added Initial *	-	2.5	2.5	-
Maximum Initial *	-	34	34	-
Time Before Reduction *	-	15	15	-
Time To Reduce *	-	30	30	-
Minimum Gap	-	3.0	3.0	-
Advance Walk	-	-	-	-
Non Lock Detector	X	-	-	X
Vehicle Recall	-	MIN RECALL	MIN RECALL	-
Dual Entry	-	-	-	-

* These values may be field adjusted. Do not adjust Min Green and Extension times for phases 2 and 6 lower than what is shown. Min Green for all other phases should not be lower than 4 seconds.

PROPOSED	EXISTING
○ → Traffic Signal Head	● → N/A
○ → Modified Signal Head	○ → N/A
□ → Sign	□ → N/A
□ → Pedestrian Signal Head With Push Button & Sign	□ → N/A
□ → Signal Pole with Guy	□ → N/A
□ → Signal Pole with Sidewalk Guy	□ → N/A
□ → Inductive Loop Detector	□ → N/A
□ → Controller & Cabinet	□ → N/A
□ → Junction Box	□ → N/A
□ → 2-in Underground Conduit	□ → N/A
--- N/A Right of Way	--- N/A Right of Way
→ Directional Arrow	→ Directional Arrow
█ Construction Zone	█ Construction Zone
█ Video Detection Zone	█ Video Detection Zone
█ Barricade	█ Barricade

Signal Upgrade - Temporary Design 2 (TMP Phase I Step 2)

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Prepared for the Offices of:
 Transportation Mobility and Safety Division
 NORTH CAROLINA DEPARTMENT OF TRANSPORTATION
 SIGNAL DESIGN SECTION
 750 N. Greenfield Pkwy, Garner, NC 27529

US 158 (Reidsville Rd.)
 at
 SR 2014 (Vance Rd.)
 Division 9 Forsyth County Walkertown
 PLAN DATE: February 2024 REVIEWED BY: WP Erickson-Jones
 PREPARED BY: H Townsend REVIEWED BY:

REVISIONS	INIT.	DATE

DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED

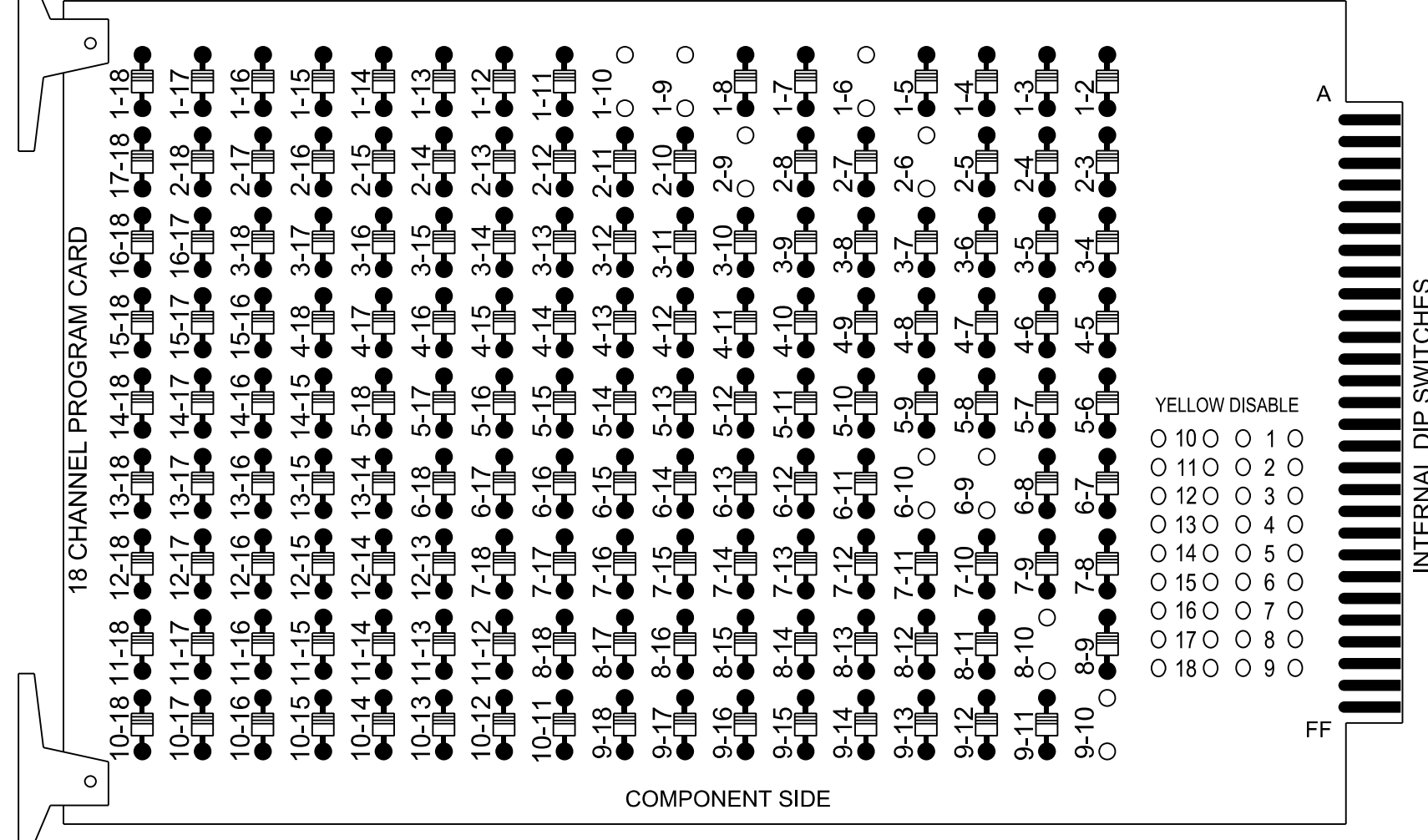
SEAL
 NORTH CAROLINA PROFESSIONAL ENGINEER
 SEAL 056142
 W. PORTER JONES
 PORTER JONES
 2/12/2024
 DATE
 SIG. INVENTORY NO. 09-1100T2

2/12/2024
 R:\Traffic\c4s1\gnal\04081\gnal\04081\0012.dwg, den...XXXXXX.dgn
 wpjones

18 CHANNEL IP CONFLICT MONITOR PROGRAMMING DETAIL

(remove jumpers and set switches as shown)

REMOVE DIODE JUMPERS 1-6, 1-9, 1-10, 2-6, 2-9, 6-9, 6-10, 8-10 and 9-10.



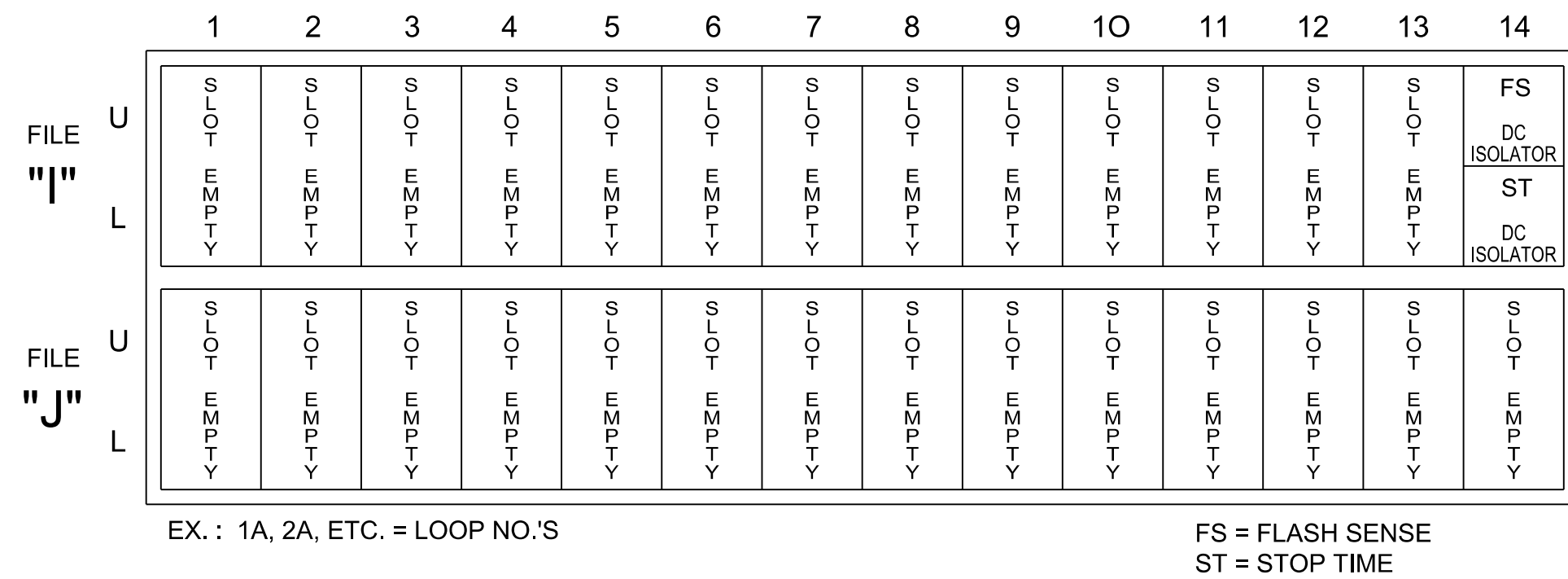
REMOVE JUMPERS AS SHOWN

NOTES:

- Card is provided with all diode jumpers in place. Removal of any jumper allows its channels to run concurrently.
- Ensure jumpers SEL2-SEL5 and SEL9 are present on the monitor board.
- Ensure that the Red Enable is active at all times during normal operation.
- Connect serial cable from conflict monitor to comm. port 1 of 2070 controller. Ensure conflict monitor communicates with 2070.

INPUT FILE POSITION LAYOUT

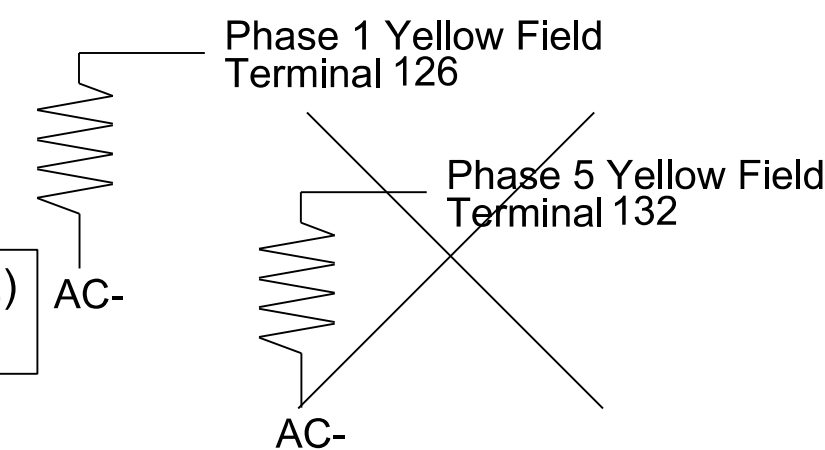
(front view)



LOAD RESISTOR INSTALLATION DETAIL

(install resistors as shown)

ACCEPTABLE VALUES	
Value (ohms)	Wattage
1.5K - 1.9K	25W (min)
2.0K - 3.0K	10W (min)



SPECIAL DETECTOR NOTE

Install a video detection system for vehicle detection for zones. 1A, 1B, 1C, 2A, 6A and 8A. Perform installation according to manufacturer's directions and NCDOT engineer-approved mounting locations to accomplish the detection schemes shown on the Signal Design Plans.

NOTES

- To prevent "flash-conflict" problems, insert red flash program blocks for all unused vehicle load switches in the output file. The installer shall verify that signal heads flash in accordance with the signal plan.
- Return controller to Factory Defaults before programming per this electrical detail.
- Program controller to start up in phase 2 Green No Walk and 6 Green No Walk.
- If this signal will be managed by an ATMS software, enable controller and detector logging for all detectors used at this location.

EQUIPMENT INFORMATION

Controller.....2070LX
 Cabinet.....332 w/ Aux
 Software.....Q-Free MAXTIME
 Cabinet Mount.....Base
 Output File Positions.....18 With Aux. Output File
 Load Switches Used.....S1, S2, S8, S11, AUX S1, AUX S2
 Phases Used.....1, 2, 6, 8
 Overlap "1".....*
 Overlap "2".....*
 Overlap "3".....NOT USED
 Overlap "4".....NOT USED

*See overlap programming detail on this sheet.

OVERLAP PROGRAMMING

Front Panel
 Main Menu >Controller >Overlap >Overlap Parameters/Overlap Timings

Web Interface
 Home >Controller >Overlap Configuration >Overlaps

Overlap Plan 1

Overlap	1	2
Type	FYA 4 - Section	Normal
Included Phases	2	1,8
Modifier Phases	1	-
Modifier Overlaps	-	-
Trail Green	0	0
Trail Yellow	0.0	0.0
Trail Red	0.0	0.0

FLASHER CIRCUIT MODIFICATION DETAIL

IN ORDER TO INSURE THAT SIGNALS FLASH CONCURRENTLY ON THE SAME APPROACH, MAKE THE FOLLOWING FLASHER CIRCUIT CHANGES:

- ON REAR OF PDA - REMOVE WIRE FROM TERM. T2-4 AND TERMINATE ON T2-2.
- ON REAR OF PDA - REMOVE WIRE FROM TERM. T2-5 AND TERMINATE ON T2-3.
- REMOVE FLASHER UNIT 2.

THE CHANGES LISTED ABOVE TIES ALL PHASES AND OVERLAPS TO FLASHER UNIT 1.

SIGNAL HEAD HOOK-UP CHART

LOAD SWITCH NO.	S1	S2	S3	S4	S5	S6	S7	S8	S9	S10	S11	S12	AUX S1	AUX S2	AUX S3	AUX S4	AUX S5	AUX S6
CMU CHANNEL NO.	1	2	13	3	4	14	5	6	15	7	8	16	9	10	17	11	12	18
PHASE	1	2	2 PED	3	4	4 PED	5	6	6 PED	7	8	8 PED	OL1	OL2	SPARE	OL3	OL4	SPARE
SIGNAL HEAD NO.	11*	21,22	NU	NU	NU	NU	NU	63,64	NU	NU	22	31	NU	11*	32	NU	NU	NU
RED		128						134										
YELLOW	*	129						135										
GREEN		130						136										
RED ARROW											107		A121	A124				
YELLOW ARROW											108	108	A122	A125				
FLASHING YELLOW ARROW													A123					
GREEN ARROW	127										109	109						A126

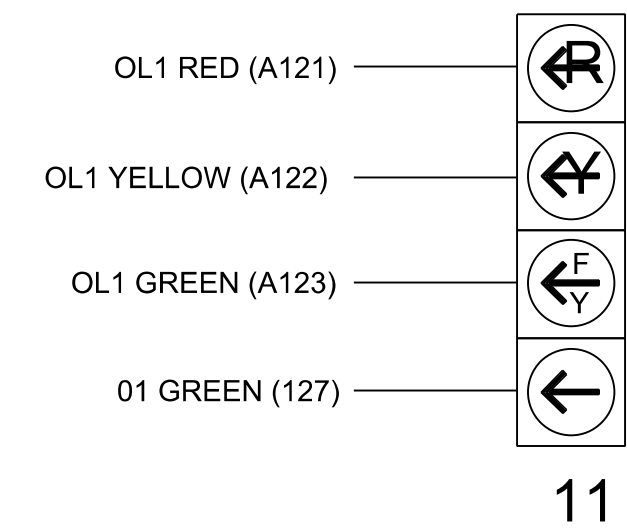
NU = Not Used

*Denotes install load resistor. See load resistor installation detail this sheet.

*See pictorial of head wiring in detail this sheet.

FYA SIGNAL WIRING DETAIL

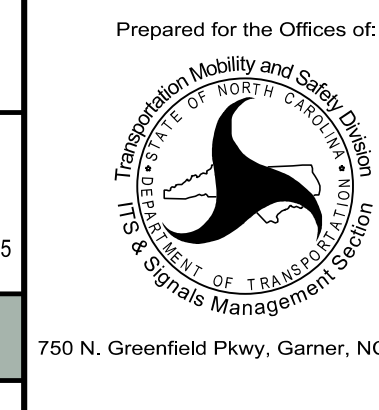
(wire signal heads as shown)



THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 09-1100T2
 DESIGNED: February 2024
 SEALED: February 12, 2024
 REVISED:

Signal Upgrade - Temporary Design 2 (TMP Phase I Step 2) Electrical Detail

Electrical and Programming Details For:



US 158 (Reidsville Rd.) at SR 2014 (Vance Rd.)

REVISIONS	INIT.	DATE

DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED

Prepared for the Offices of:
 Division 9 Forsyth County Walkertown
 PLAN DATE: February 2024 REVIEWED BY: DT Sears
 PREPARED BY: WP Erickson-Jones REVIEWED BY:
 750 N. Greenfield Pkwy, Garner, NC 27529
 Date: 2/12/2024
 Signature: Porter Jones
 Date: 2/12/2024
 Sig. Inventory No. 09-1100T2

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6 Phase Fully Actuated (Isolated)

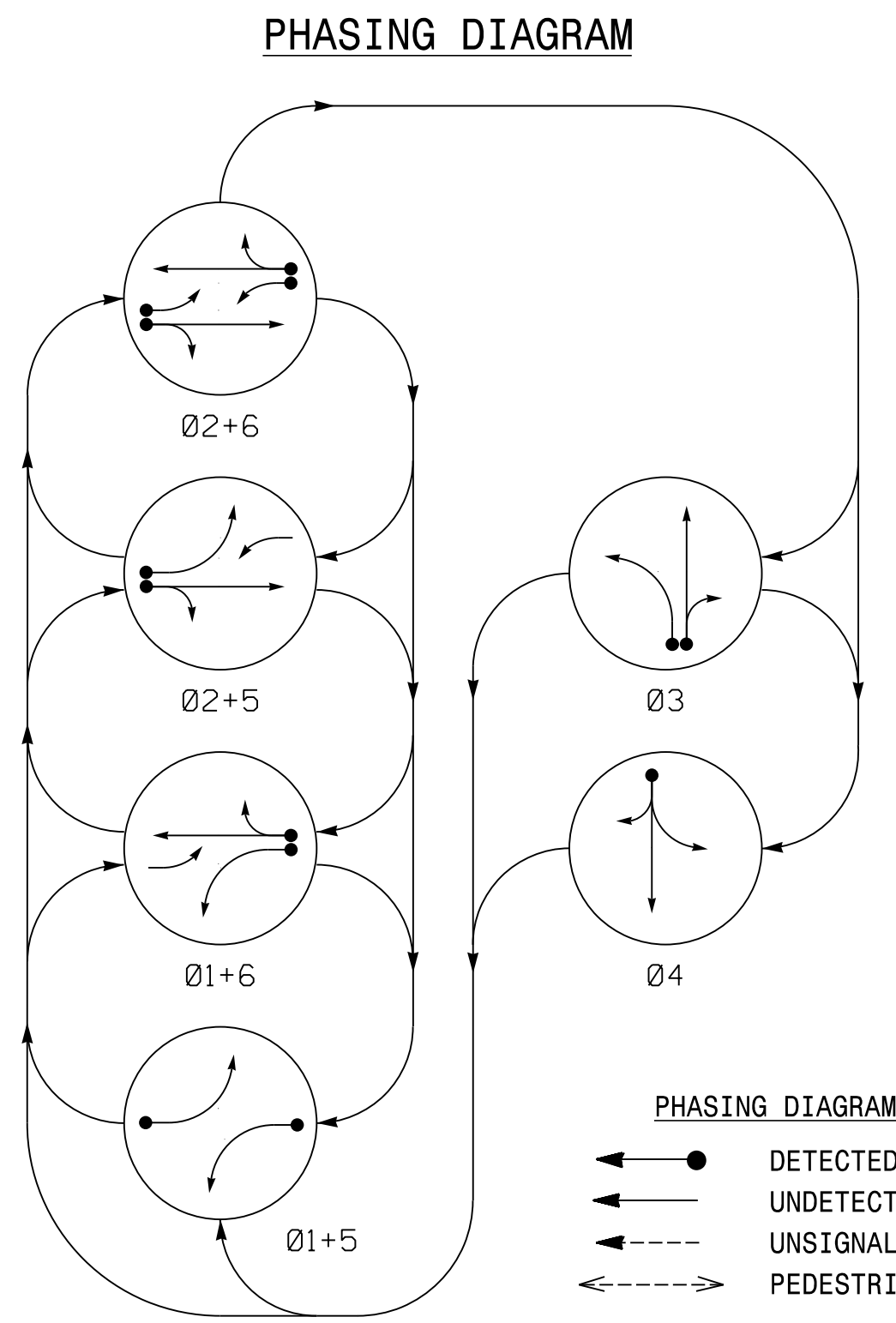
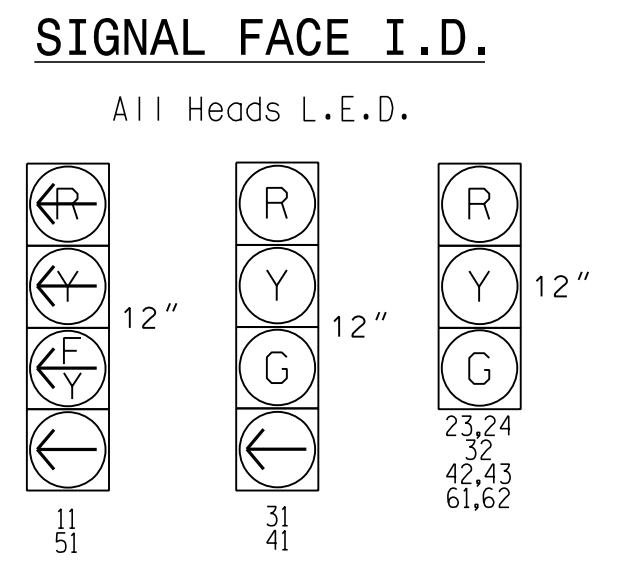


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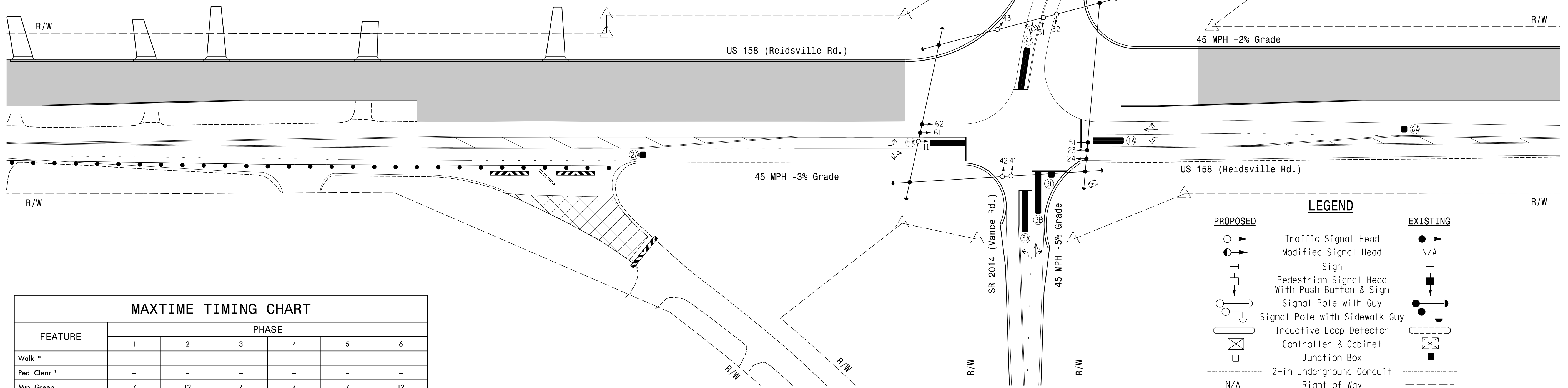
SIGNAL FACE	PHASE					
	01+5	01+6	02+5	02+6	03	04
11	←	←	←	←	←	←
23,24	R	R	G	G	R	R
31	R	R	R	R	G	R
41	R	R	R	R	R	C
42,43	R	R	R	R	R	G
51	←	←	←	←	←	←
61,62	R	G	R	G	R	R

MAXTIME DETECTOR INSTALLATION CHART

ZONE	SIZE (FT)	DISTANCE FROM STOPBAR (FT)	TURNS	NEW LOOP	PROGRAMMING							
					CALL PHASE	DELAY TIME	EXTEND TIME	EXTEND	ADDED INITIAL	CALL	DELAY DURING GREEN	NEW CARD
1A*	6X40	0	*	*	1	15	-	X	-	X	-	*
					6	3	-	X	-	X	X	*
2A*	6X6	300	*	*	2	-	-	X	X	X	-	*
3A*	6X40	0	*	*	3	3	-	X	-	X	-	*
3B*	6X40	0	*	*	3	10	-	X	-	X	-	*
3C*	6X6	0	*	*	3	10	-	X	-	X	-	*
4A*	6X40	0	*	*	4	10	-	X	-	X	-	*
5A*	6X40	0	*	*	5	15	-	X	-	X	-	*
					2	3	-	X	-	X	X	*
6A*	6X6	300	*	*	6	-	-	X	X	X	-	*



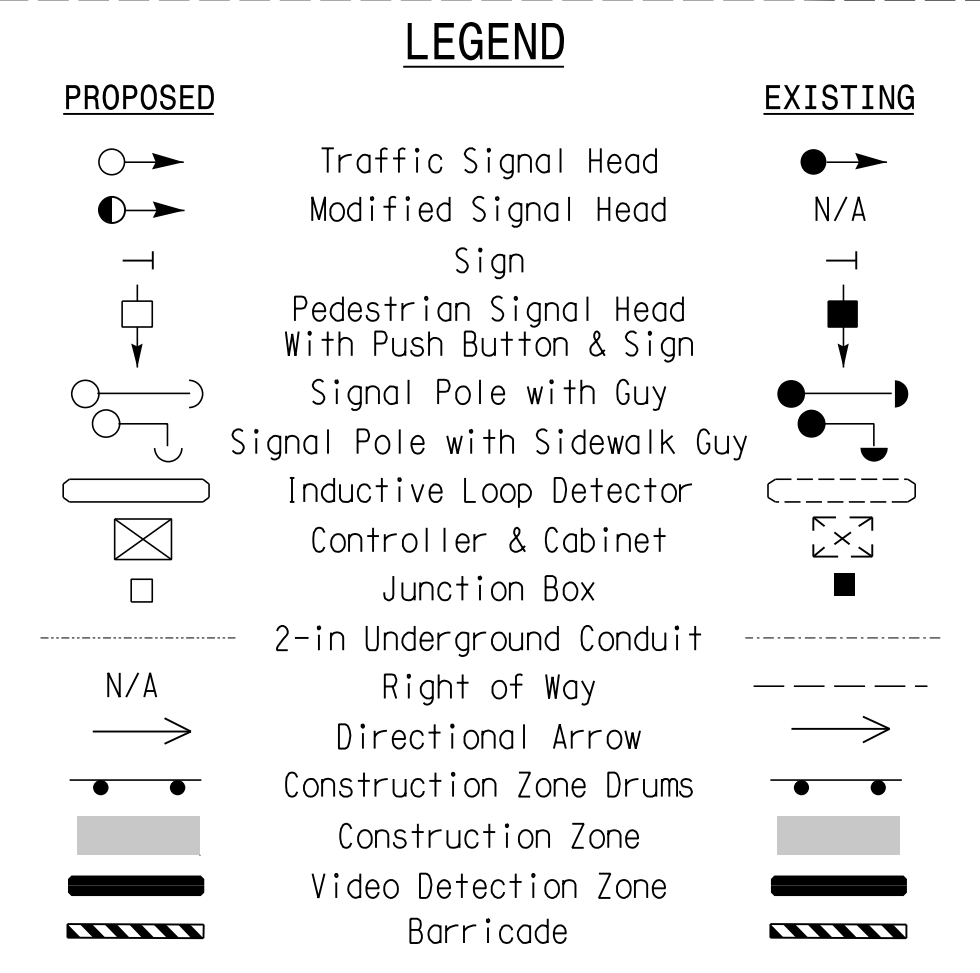
- NOTES**
- Refer to "Roadway Standard Drawings NCDOT" dated January 2024 and "Standard Specifications for Roads and Structures" dated January 2024.
 - Do not program signal for late night flashing unless otherwise directed by the Engineer.
 - Phase 1 and/or phase 5 may be lagged.
 - The order of phase 3 and 4 may be reversed.
 - Remove louvers from heads 23, 24, and 51.
 - Set all detector units to presence mode.
 - This intersection uses video detection. Install detectors according to the manufacturer's instructions to achieve the desired detection.
 - Remove signal heads 11,21,22,31,32,63, and 64 and the corresponding span wires.



MAXTIME TIMING CHART

FEATURE	PHASE					
	1	2	3	4	5	6
Walk *	-	-	-	-	-	-
Ped Clear *	-	-	-	-	-	-
Min Green	7	12	7	7	7	12
Passage *	2.0	6.0	2.0	2.0	2.0	6.0
Max 1 *	20	90	30	30	20	90
Yellow Change	3.0	4.8	5.0	5.1	3.0	4.8
Red Clear	2.3	1.5	1.0	1.0	2.1	1.5
Added Initial *	-	2.5	-	-	-	2.5
Maximum Initial *	-	34	-	-	-	34
Time Before Reduction *	-	15	-	-	-	15
Time To Reduce *	-	30	-	-	-	30
Minimum Gap	-	3.0	-	-	-	3.0
Advance Walk	-	-	-	-	-	-
Non Lock Detector	X	-	X	X	X	-
Vehicle Recall	-	MIN RECALL	-	-	-	MIN RECALL
Dual Entry	-	-	-	-	-	-

* These values may be field adjusted. Do not adjust Min Green and Extension times for phases 2 and 6 lower than what is shown. Min Green for all other phases should not be lower than 4 seconds.



Signal Upgrade - Temporary Design 3 (TMP Phase I Step 3)

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Prepared for the Offices of:
Transportation Mobility and Safety Division
NORTH CAROLINA DEPARTMENT OF TRANSPORTATION
SIGNAL DESIGN SECTION
750 N. Greenfield Pkwy, Garner, NC 27529

US 158 (Reidsville Rd.)
at
SR 2014 (Vance Rd.) /
SR 1965 (Belews Creek Rd.)
Division 9 Forsyth County Walkertown
PLAN DATE: February 2024 REVIEWED BY: WP Erickson-Jones
PREPARED BY: H Townsend REVIEWED BY:

DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED

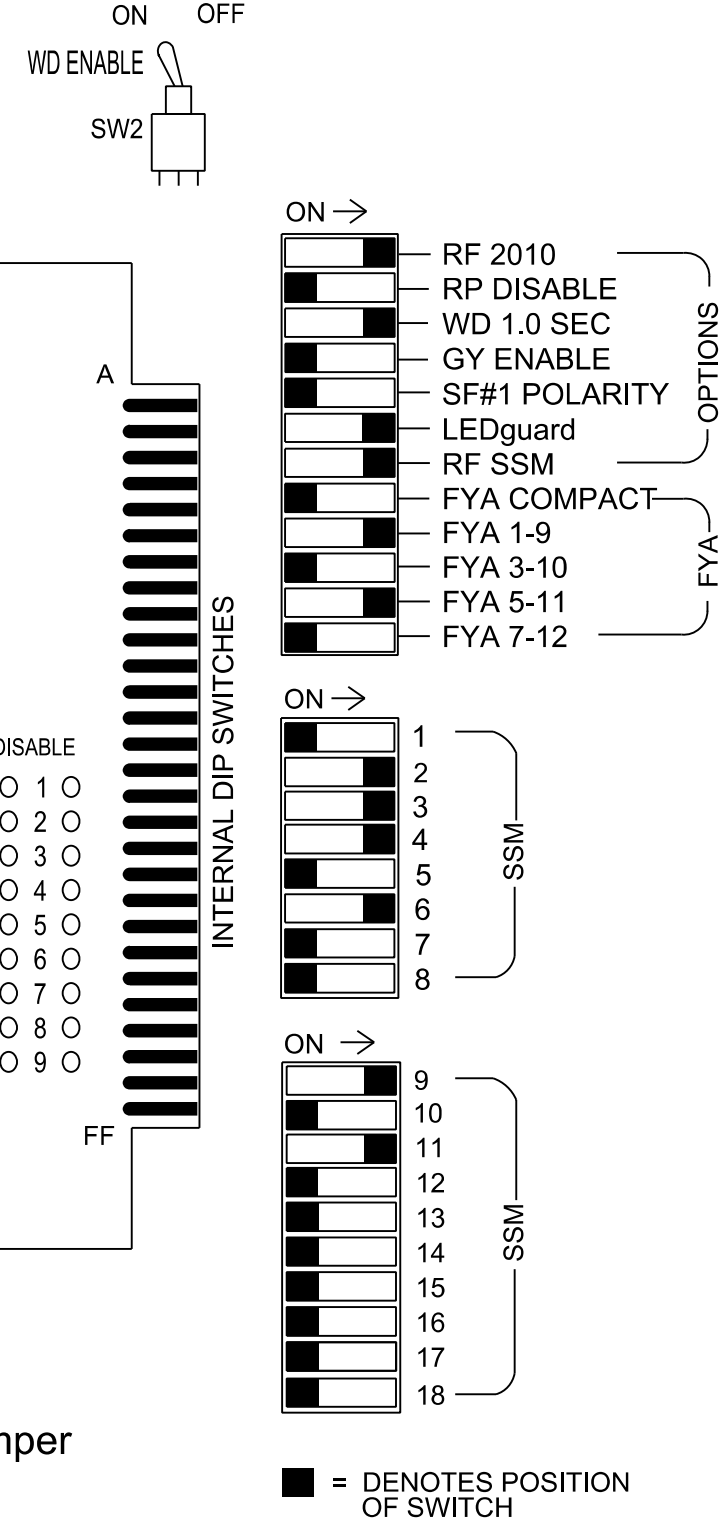
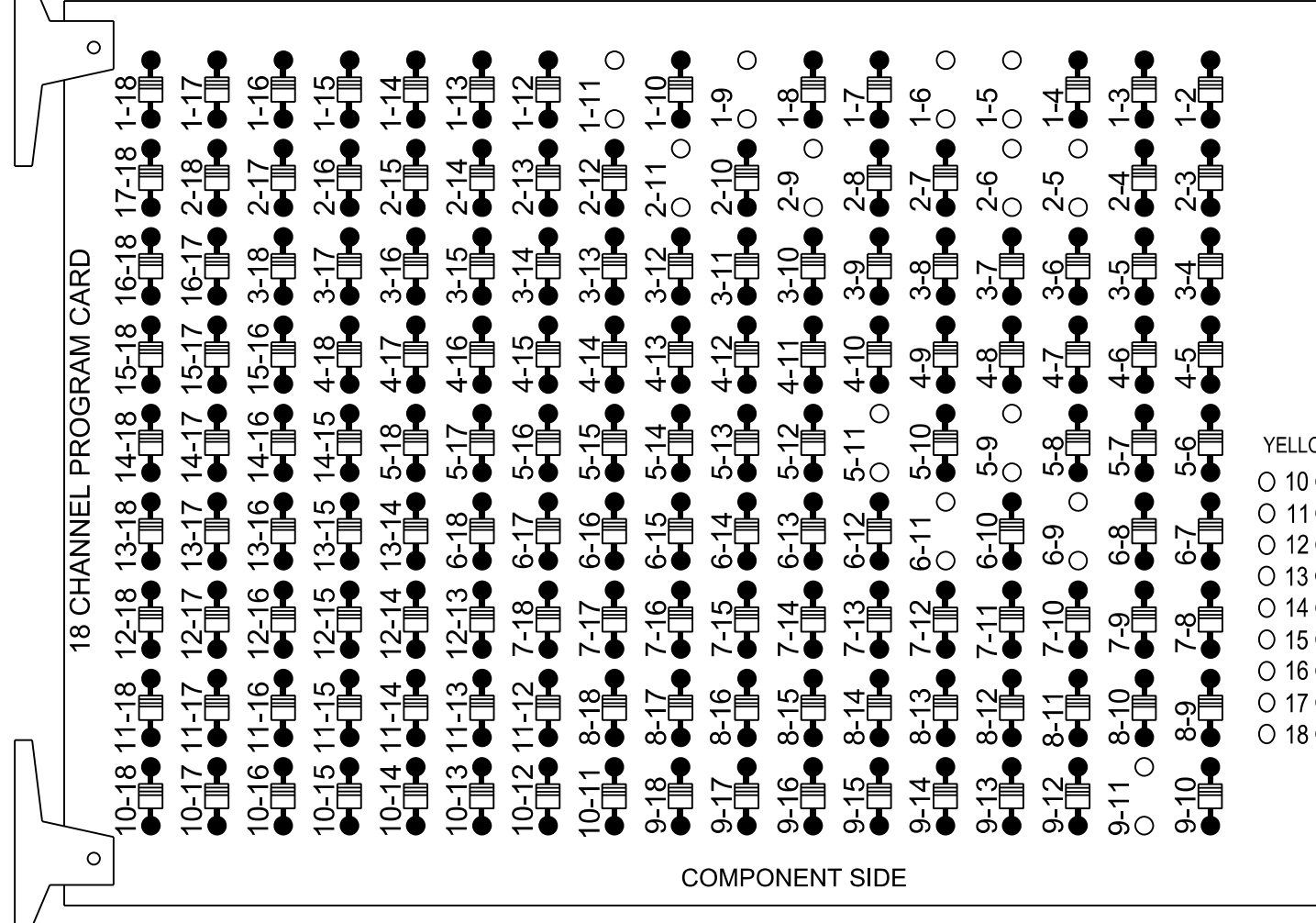
SEAL
NORTH CAROLINA PROFESSIONAL ENGINEER
SEAL 056142
W. PORTER JONES
Porter Jones
2/12/2024
DATE
SIG. INVENTORY NO. 09-1100T3

2/12/2024 R:\Traffic\c4s1\gn41\gn41.dgn 409110013.dwg den_XXXXXXX.dgn wplones

18 CHANNEL IP CONFLICT MONITOR PROGRAMMING DETAIL

(remove jumpers and set switches as shown)

REMOVE DIODE JUMPERS 1-5, 1-6, 1-9, 1-11, 2-5, 2-6, 2-9, 2-11, 5-9, 5-11, 6-9, 6-11 and 9-11.



REMOVE JUMPERS AS SHOWN

NOTES:

1. Card is provided with all diode jumpers in place. Removal of any jumper allows its channels to run concurrently.
2. Ensure jumpers SEL2-SEL5 and SEL9 are present on the monitor board.
3. Ensure that the Red Enable is active at all times during normal operation.
4. Connect serial cable from conflict monitor to comm. port 1 of 2070 controller. Ensure conflict monitor communicates with 2070.

NOTES

1. To prevent "flash-conflict" problems, insert red flash program blocks for all unused vehicle load switches in the output file. The installer shall verify that signal heads flash in accordance with the signal plan.
2. Return controller to Factory Defaults before programming per this electrical detail.
3. Program controller to start up in phase 2 Green No Walk and 6 Green No Walk.
4. If this signal will be managed by an ATMS software, enable controller and detector logging for all detectors used at this location.

EQUIPMENT INFORMATION

Controller.....2070LX
Cabinet.....332 w/ Aux
Software.....Q-Free MAXTIME
Cabinet Mount.....Base
Output File Positions.....18 With Aux. Output File
Load Switches Used.....S1, S2, S4, S5, S7, S8, AUX S1, AUX S4

Phases Used.....1, 2, 3, 4, 5, 6
Overlap "1".....*
Overlap "2".....NOT USED
Overlap "3".....*
Overlap "4".....NOT USED

*See overlap programming detail on this sheet.

SIGNAL HEAD HOOK-UP CHART

LOAD SWITCH NO.	S1	S2	S3	S4	S5	S6	S7	S8	S9	S10	S11	S12	AUX S1	AUX S2	AUX S3	AUX S4	AUX S5	AUX S6
CMU CHANNEL NO.	1	2	13	3	4	14	5	6	15	7	8	16	9	10	17	11	12	18
PHASE	1	2	2 PED	3	4	4 PED	5	6	6 PED	7	8	8 PED	OL1	OL2	SPARE	OL3	OL4	SPARE
SIGNAL HEAD NO.	11*	23,24	NU	31	32	41	42,43	51*	61,62	NU	NU	NU	11*	NU	NU	51*	NU	NU
RED	128			116	116	101	101		134									
YELLOW	*	129		117	117	102	102	*	135									
GREEN		130		118	118	103	103		136									
RED ARROW													A121			A114		
YELLOW ARROW													A122			A115		
FLASHING YELLOW ARROW													A123			A116		
GREEN ARROW	127			118		103		133										

NU = Not Used
*Denotes install load resistor. See load resistor installation detail this sheet.
*See pictorial of head wiring in detail this sheet.

OVERLAP PROGRAMMING

Front Panel
Main Menu >Controller >Overlap >Overlap Parameters/Overlap Timings

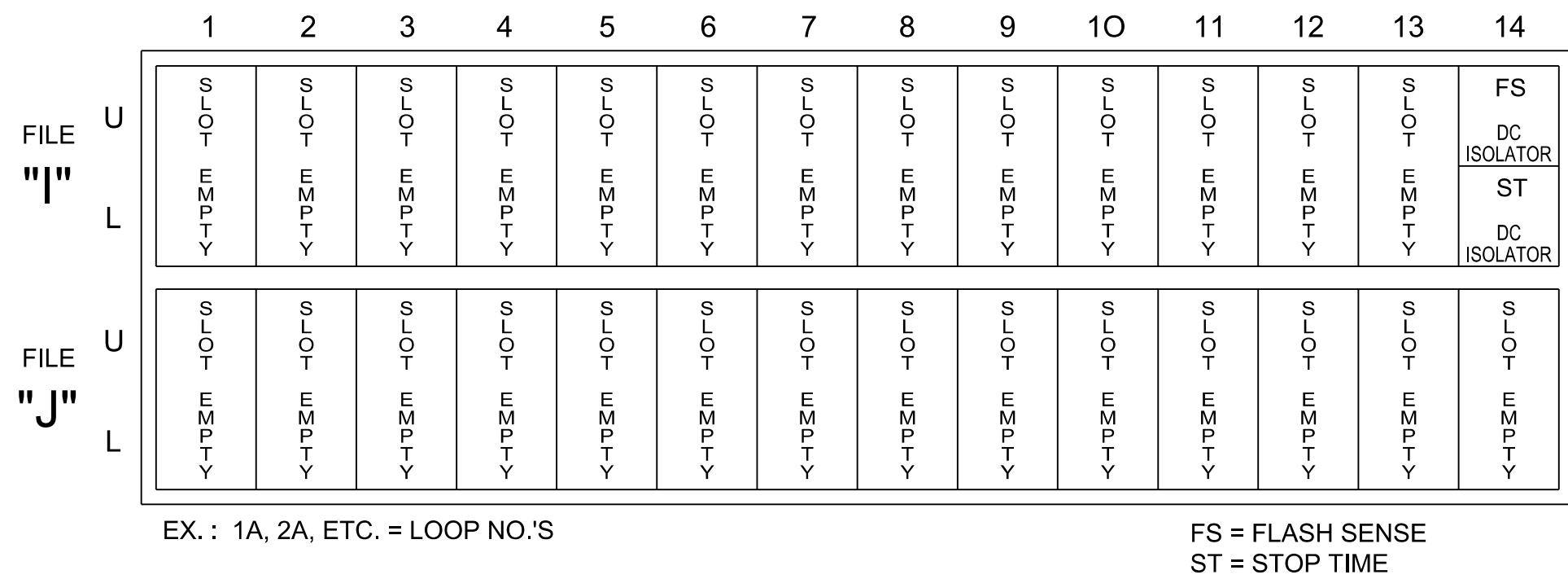
Web Interface
Home >Controller >Overlap Configuration >Overlaps

Overlap Plan 1

Overlap	1	3
Type	FYA 4 - Section	FYA 4 - Section
Included Phases	2	6
Modifier Phases	1	5
Modifier Overlaps	-	-
Trail Green	0	0
Trail Yellow	0.0	0.0
Trail Red	0.0	0.0

INPUT FILE POSITION LAYOUT

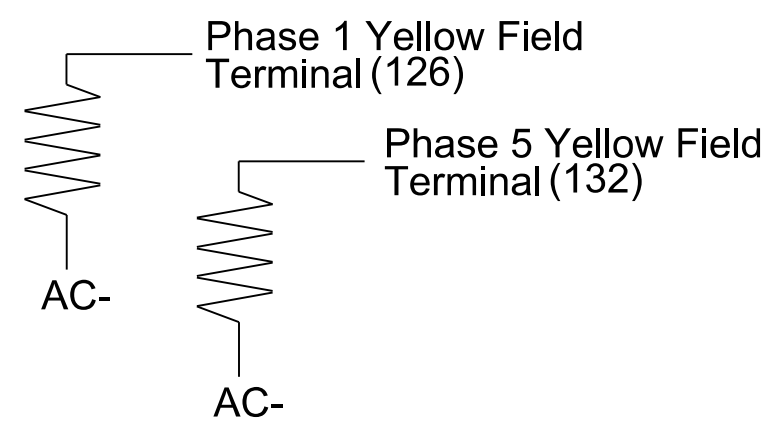
(front view)



LOAD RESISTOR INSTALLATION DETAIL

(install resistors as shown)

Value (ohms)	Wattage
1.5K - 1.9K	25W (min)
2.0K - 3.0K	10W (min)

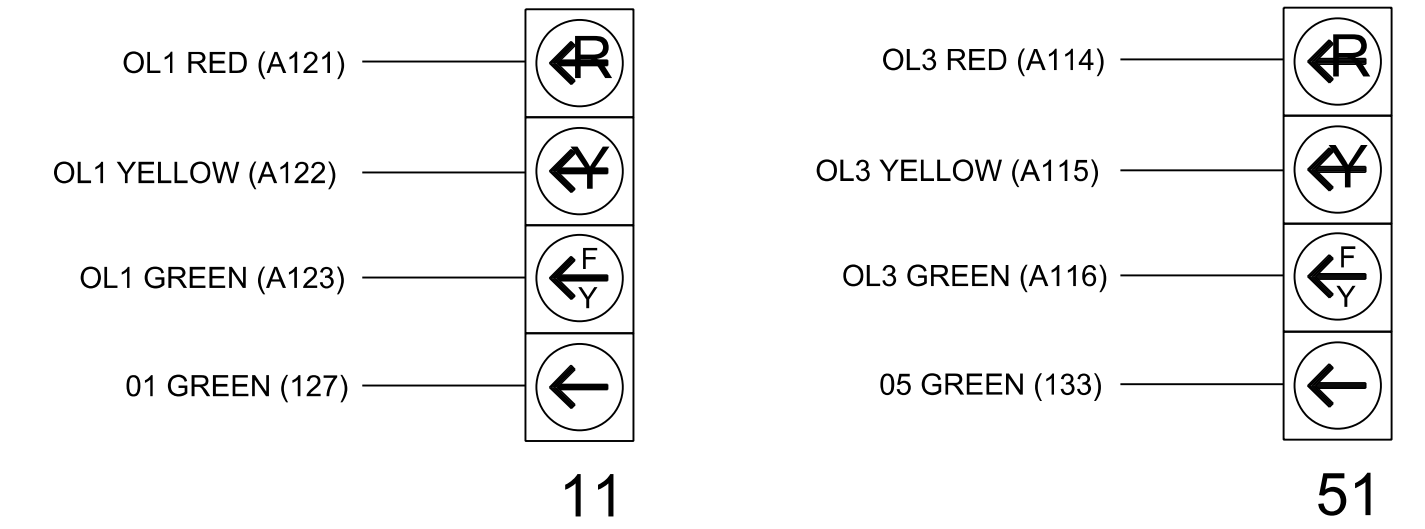


SPECIAL DETECTOR NOTE

Install a video detection system for vehicle detection for zones. 1A, 2A, 3A, 3B, 3C, 4A, 5A and 6A. Perform installation according to manufacturer's directions and NCDOT engineer-approved mounting locations to accomplish the detection schemes shown on the Signal Design Plans.

FYA SIGNAL WIRING DETAIL

(wire signal heads as shown)



THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 09-1100T3
DESIGNED: February 2024
SEALED: February 12, 2024
REVISED:

Signal Upgrade - Temporary Design 3 (TMP Phase I Step 3) Electrical Detail

Electrical and Programming Details For: US 158 (Reidsville Rd.) at SR 2014 (Vance Rd.)/ SR 1965 (Belevs Creek Rd.)

Division 9 Forsyth County Walkertown

Prepared for the Offices of: Porter Jones

PLANNING ENGINEER

PLAN DATE: February 2024 REVIEWED BY: DT Sears

PREPARED BY: WP Erickson-Jones REVIEWED BY:

REVISIONS INIT. DATE

750 N. Greenfield Pkwy, Garner, NC 27529

DocuSigned by: Porter Jones 2/12/2024

SIG. INVENTORY NO. 09-1100T3

PHASING DIAGRAM

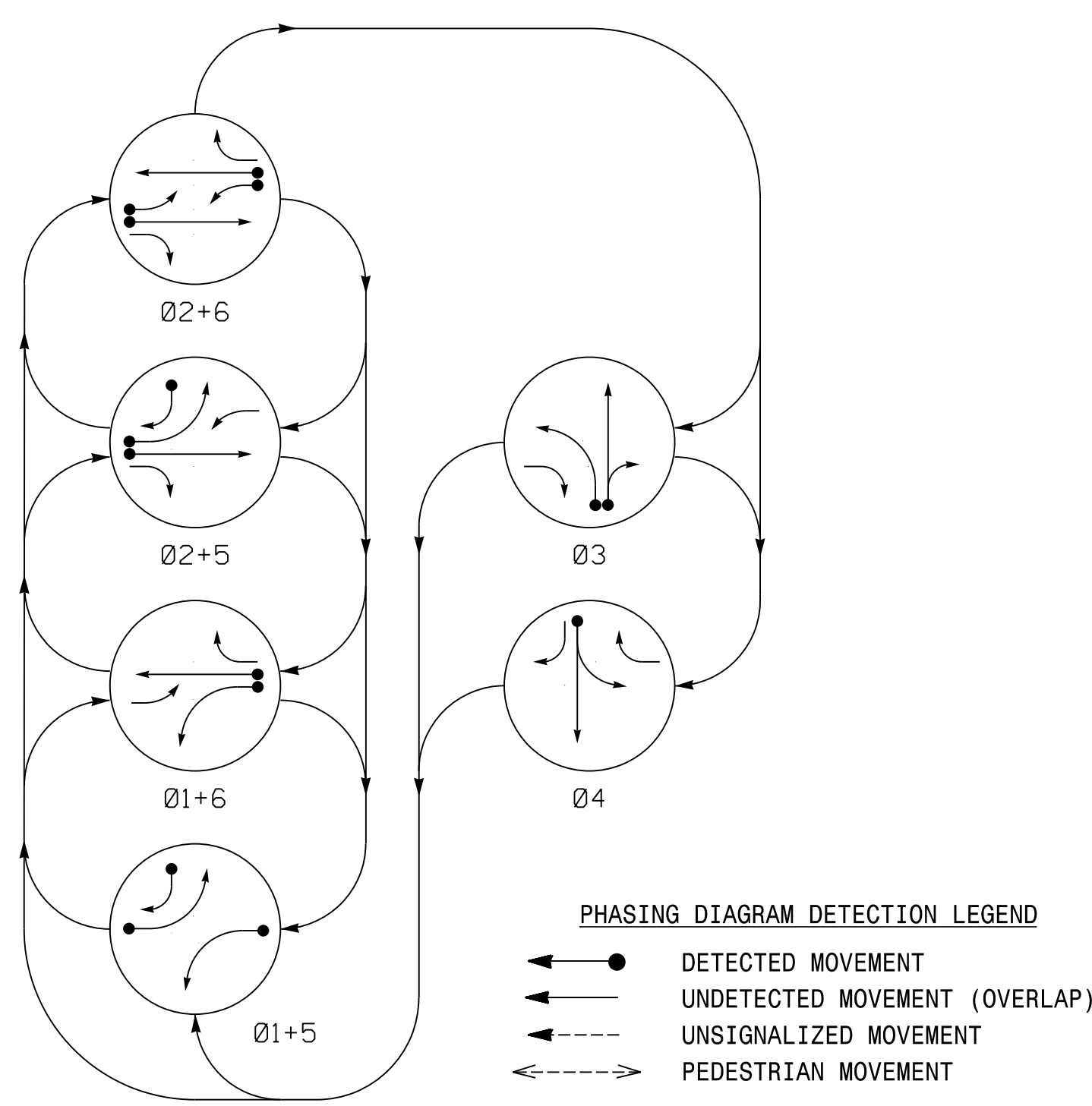


TABLE OF OPERATION

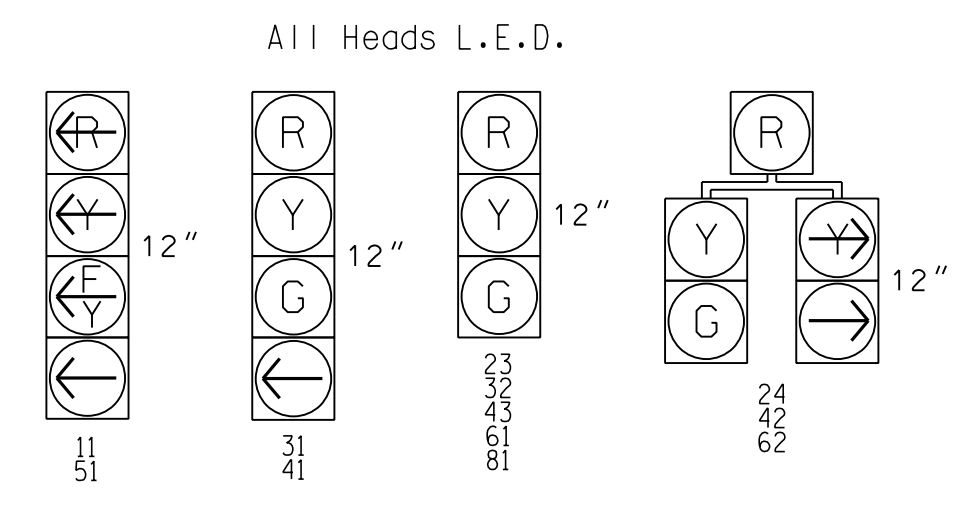
SIGNAL FACE	PHASE						FLASH
	01+5	01+6	02+5	02+6	03	04	
11	←	←	←	←	←	←	Y
23	R	R	G	G	R	R	Y
24	R	R	G	G	R	R	Y
31	R	R	R	R	R	R	R
32	R	R	R	R	G	R	R
41	R	R	R	R	R	C	R
42	R	R	R	R	R	G	R
43	R	R	R	R	R	G	R
51	←	←	←	←	←	←	Y
61	R	G	R	G	R	R	Y
62	R	G	R	G	R	R	Y

MAXTIME DETECTOR INSTALLATION CHART

ZONE	SIZE (FT)	DISTANCE FROM STOPBAR (FT)	TURNS	NEW LOOP	PROGRAMMING							
					CALL PHASE	DELAY TIME	EXTEND TIME	EXTEND	ADDED INITIAL CALL	DELAY DURING GREEN	NEW CARD	
1A *	6X40	0	*	*	1	15	-	X	-	X	-	*
2A *	6X6	300	*	*	2	-	-	X	X	X	-	*
3A *	6X40	0	*	*	3	3	-	X	-	X	-	*
3B *	6X40	0	*	*	3	10	-	X	-	X	-	*
3C *	6X6	0	*	*	3	10	-	X	-	X	-	*
4A *	6X40	0	*	*	4	3	-	X	-	X	-	*
5A *	6X40	0	*	*	5	15	-	X	-	X	-	*
5B *	6X40	0	*	*	5	15	-	X	-	X	-	*
6A *	6X6	300	*	*	6	-	-	X	X	X	-	*

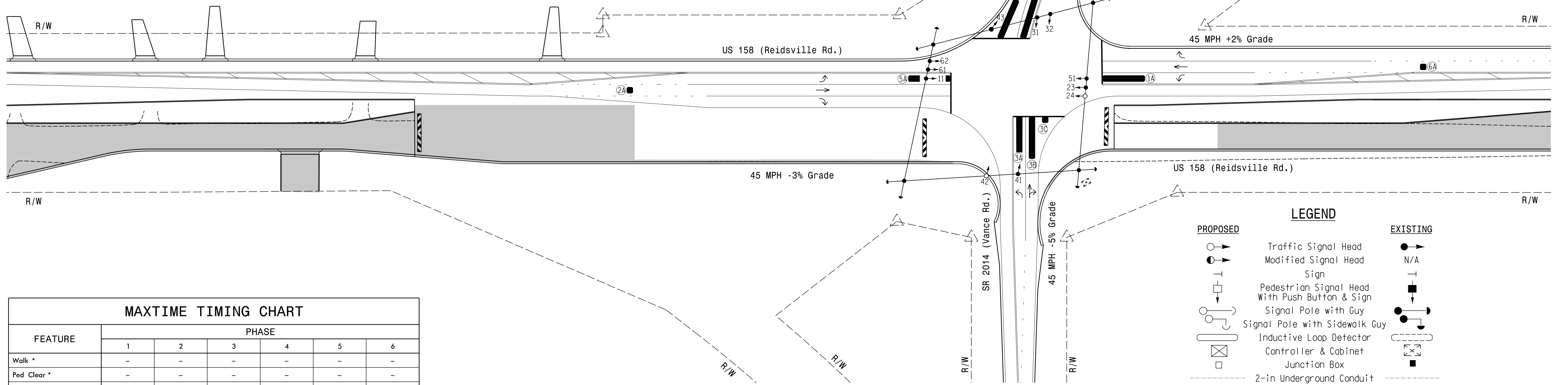
* Video Detection Zone

SIGNAL FACE I.D.



6 Phase Fully Actuated (Isolated) NOTES

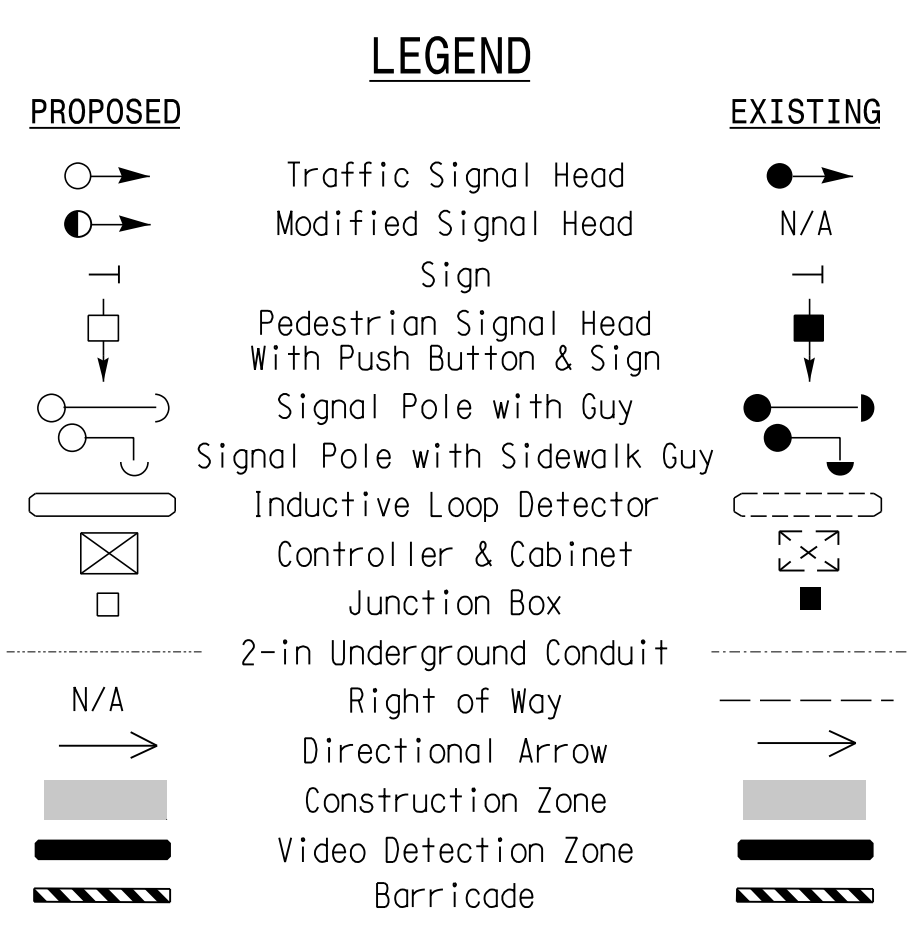
- Refer to "Roadway Standard Drawings NCDOT" dated January 2024 and "Standard Specifications for Roads and Structures" dated January 2024.
- Do not program signal for late night flashing unless otherwise directed by the Engineer.
- Phase 1 and/or phase 5 may be lagged.
- The order of phase 3 and 4 may be reversed.
- Reposition existing signal heads numbered 11, 23, 41, 51, 61, and 62.
- Set all detector units to presence mode.
- This intersection uses video detection. Install detectors according to the manufacturer's instructions to achieve the desired detection.



MAXTIME TIMING CHART

FEATURE	PHASE					
	1	2	3	4	5	6
Walk *	-	-	-	-	-	-
Ped Clear *	-	-	-	-	-	-
Min Green	7	12	7	7	7	12
Passage *	2.0	6.0	2.0	2.0	2.0	6.0
Max I *	20	90	30	30	20	90
Yellow Change	3.0	4.8	5.0	5.1	3.0	4.8
Red Clear	3.2	1.9	1.1	1.0	2.8	1.9
Added Initial *	-	2.5	-	-	-	2.5
Maximum Initial *	-	34	-	-	-	34
Time Before Reduction *	-	15	-	-	-	15
Time To Reduce *	-	30	-	-	-	30
Minimum Gap	-	3.0	-	-	-	3.0
Advance Walk	-	-	-	-	-	-
Non Lock Detector	X	-	X	X	X	-
Vehicle Recall	-	MIN RECALL	-	-	-	MIN RECALL
Dual Entry	-	-	-	-	-	-

* These values may be field adjusted. Do not adjust Min Green and Extension times for phases 2 and 6 lower than what is shown. Min Green for all other phases should not be lower than 4 seconds.



Signal Upgrade - Temporary Design 4 (TMP Phase II)

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Prepared for the Offices of:
 Transportation Mobility and Safety Division
 DEPARTMENT OF TRANSPORTATION
 STATE OF NORTH CAROLINA
 SIGNAL DESIGN SECTION

US 158 (Reidsville Rd.) at SR 2014 (Vance Rd.) / SR 1965 (Belews Creek Rd.)
 Division 9 Forsyth County Walkertown
 PLAN DATE: February 2024 REVIEWED BY: WP Erickson-Jones
 PREPARED BY: H Townsend REVIEWED BY:

REVISIONS	INIT.	DATE

DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED

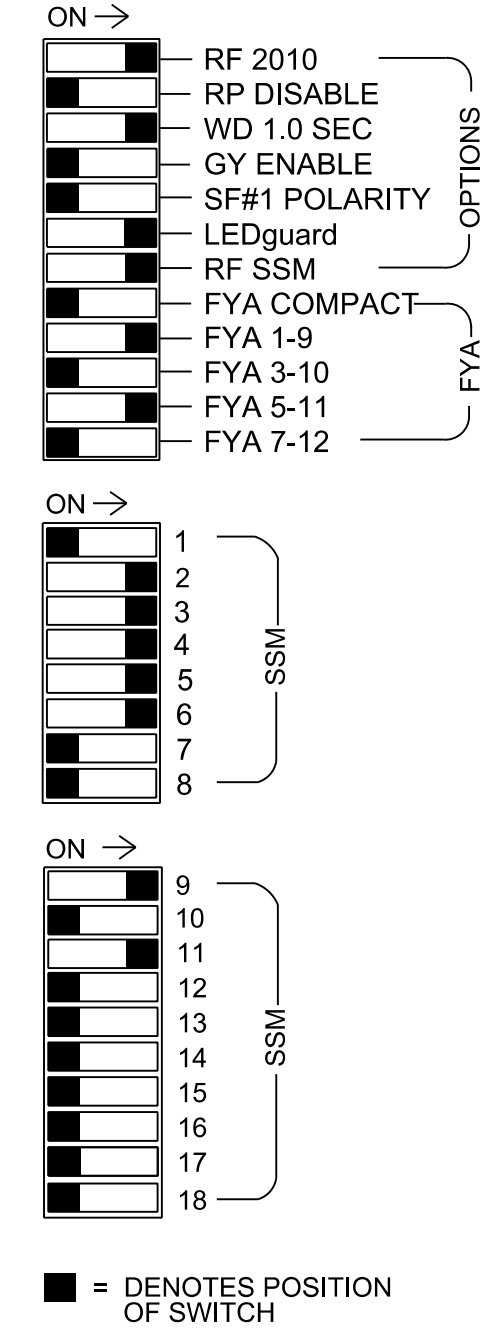
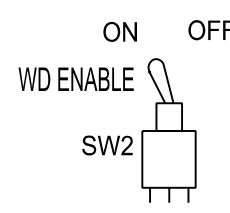
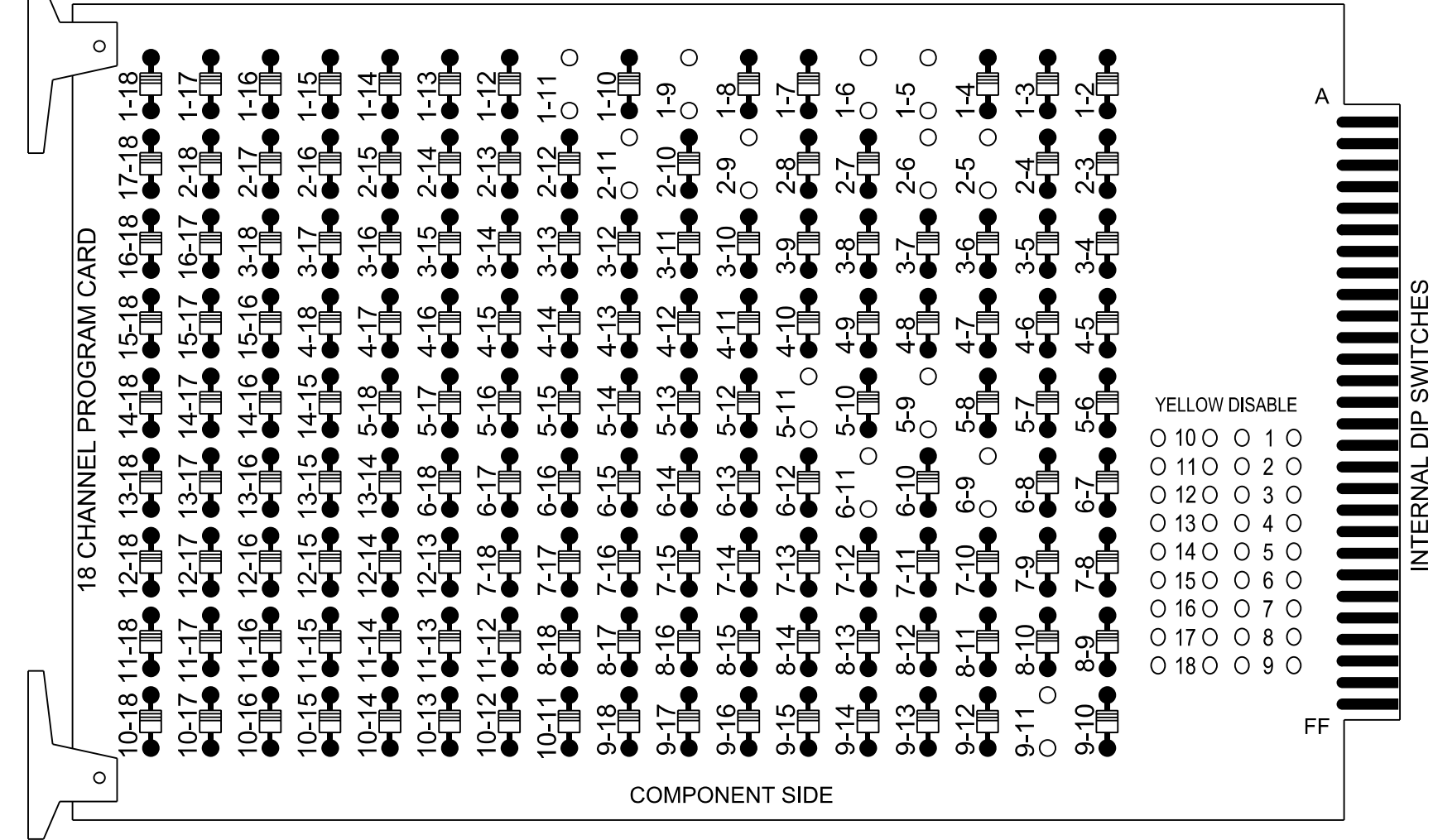
SEAL
 NORTH CAROLINA PROFESSIONAL ENGINEER
 SEAL 056142
 W. PORTER JONES
 PORTER JONES
 2/12/2024
 DATE
 SIG. INVENTORY NO. 09-110074

2/12/2024
 R:\Traffic\c4s1\gnal\sh40as1\gnal\sh40110014_s1\g_den_XXXXXXX.dgn
 wplones

18 CHANNEL IP CONFLICT MONITOR PROGRAMMING DETAIL

(remove jumpers and set switches as shown)

REMOVE DIODE JUMPERS 1-5, 1-6, 1-9, 1-11, 2-5, 2-6, 2-9, 2-11, 5-9, 5-11, 6-9, 6-11 and 9-11.



REMOVE JUMPERS AS SHOWN

NOTES:

- Card is provided with all diode jumpers in place. Removal of any jumper allows its channels to run concurrently.
- Ensure jumpers SEL2-SEL5 and SEL9 are present on the monitor board.
- Ensure that the Red Enable is active at all times during normal operation.
- Connect serial cable from conflict monitor to comm. port 1 of 2070 controller. Ensure conflict monitor communicates with 2070.

NOTES

- To prevent "flash-conflict" problems, insert red flash program blocks for all unused vehicle load switches in the output file. The installer shall verify that signal heads flash in accordance with the signal plan.
- Program controller to start up in phase 2 Green No Walk and 6 Green No Walk.
- If this signal will be managed by an ATMS software, enable controller and detector logging for all detectors used at this location.

EQUIPMENT INFORMATION

Controller.....2070LX
 Cabinet.....332 w/ Aux
 Software.....Q-Free MAXTIME
 Cabinet Mount.....Base
 Output File Positions.....18 With Aux. Output File
 Load Switches Used.....S1, S2, S4, S5, S7, S8, AUX S1, AUX S4
 Phases Used.....1, 2, 3, 4, 5, 6
 Overlap "1".....*
 Overlap "2".....NOT USED
 Overlap "3".....*
 Overlap "4".....NOT USED
 *See overlap programming detail on this sheet.

SIGNAL HEAD HOOK-UP CHART

LOAD SWITCH NO.	S1	S2	S3	S4	S5	S6	S7	S8	S9	S10	S11	S12	AUX S1	AUX S2	AUX S3	AUX S4	AUX S5	AUX S6							
CMU CHANNEL NO.	1	2	13	3	4	14	5	6	15	7	8	16	9	10	17	11	12	18							
PHASE	1	2	2 PED	3	4	4 PED	5	6	6 PED	7	8	8 PED	OL1	OL2	SPARE	OL3	OL4	SPARE							
SIGNAL HEAD NO.	11*	23,24	NU	31	32	24	41	42,43	62	NU	51*	42	61,62	NU	NU	NU	NU	11*	NU	NU	51*	NU	NU		
RED		128		116	116		101	101				*	134												
YELLOW	*	129		117	117		102	102					135												
GREEN		130		118	118		103	103					136												
RED ARROW																							A121		A114
YELLOW ARROW							117		102				132										A122		A115
FLASHING YELLOW ARROW																							A123		A116
GREEN ARROW	127			118	118	103		103		133	133														

NU = Not Used
 * Denotes install load resistor. See load resistor installation detail this sheet.
 * See pictorial of head wiring in detail this sheet.

OVERLAP PROGRAMMING

Front Panel
 Main Menu > Controller > Overlap > Overlap Parameters/Overlap Timings

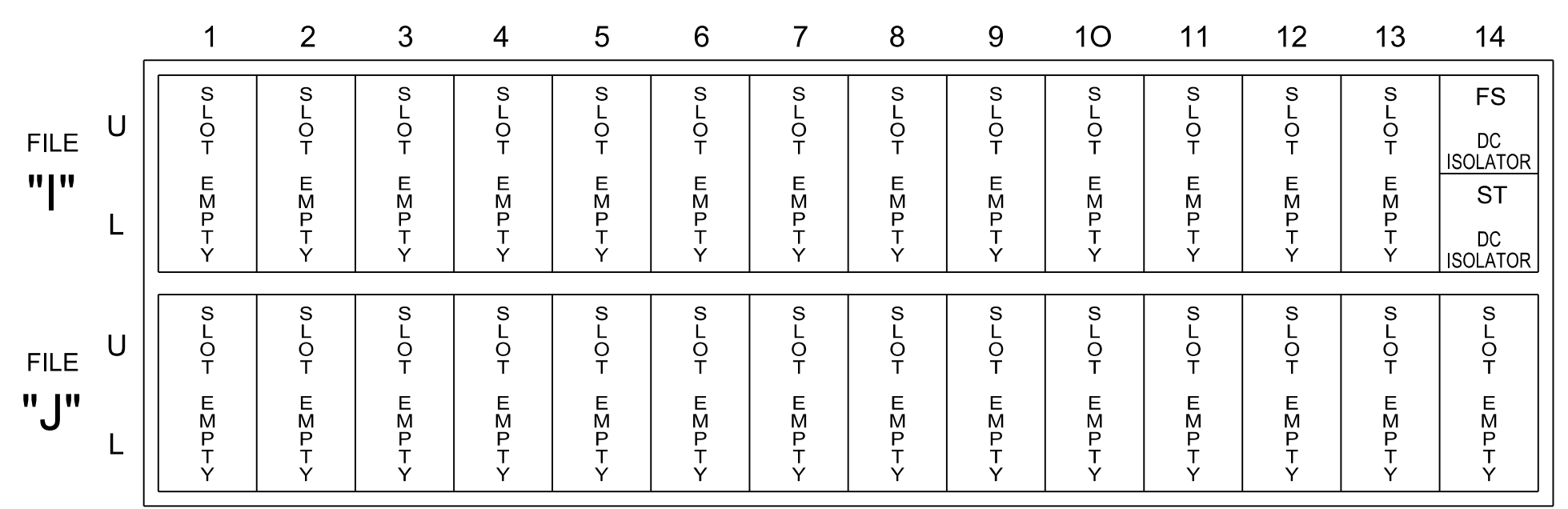
Web Interface
 Home > Controller > Overlap Configuration > Overlaps

Overlap Plan 1

Overlap	1	3
Type	FYA 4 - Section	FYA 4 - Section
Included Phases	2	6
Modifier Phases	1	5
Modifier Overlaps	-	-
Trail Green	0	0
Trail Yellow	0.0	0.0
Trail Red	0.0	0.0

INPUT FILE POSITION LAYOUT

(front view)

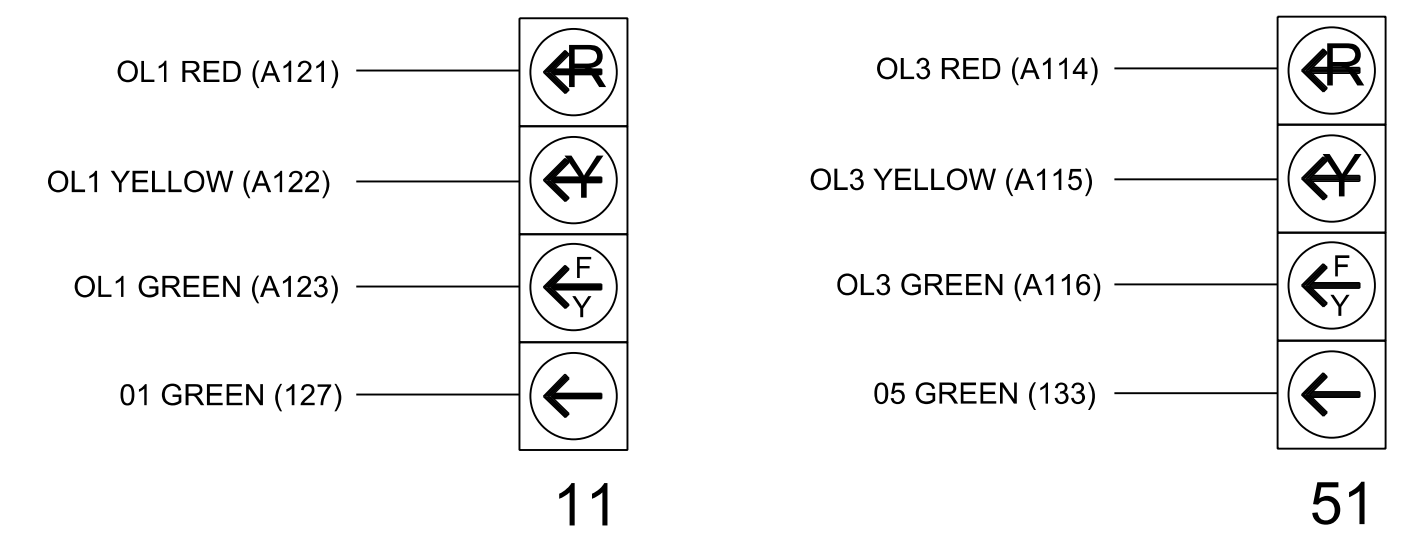


EX.: 1A, 2A, ETC. = LOOP NO.'S

FS = FLASH SENSE
 ST = STOP TIME

FYA SIGNAL WIRING DETAIL

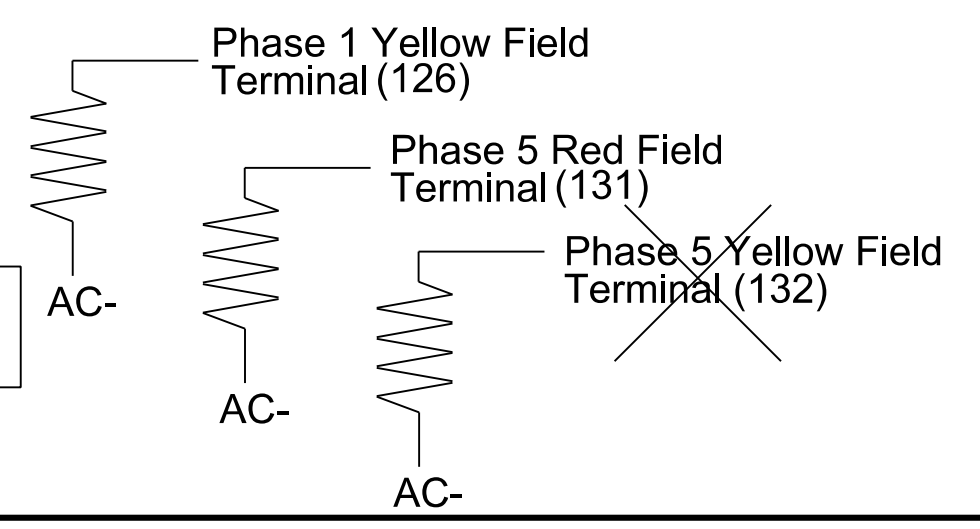
(wire signal heads as shown)



LOAD RESISTOR INSTALLATION DETAIL

(install resistors as shown)

ACCEPTABLE VALUES	
Value (ohms)	Wattage
1.5K - 1.9K	25W (min)
2.0K - 3.0K	10W (min)



Remove Phase 5 Yellow Field Terminal (132), if present.

SPECIAL DETECTOR NOTE

Install a video detection system for vehicle detection for zones 1A, 2A, 3A, 3B, 3C, 4A, 5A, 5B and 6A. Perform installation according to manufacturer's directions and NCDOT engineer-approved mounting locations to accomplish the detection schemes shown on the Signal Design Plans.

Signal Upgrade - Temporary Design 4 (TMP Phase II) Electrical Detail

Electrical and Programming Details For: US 158 (Reidsville Rd.) at SR 2014 (Vance Rd.)/ SR 1965 (Belews Creek Rd.)

Division 9 Forsyth County Walkertown

Prepared for the Offices of: [Seal of North Carolina Professional Engineer Porter Jones]

PLAN DATE: February 2024 REVIEWED BY: DT Sears

PREPARED BY: WP Erickson-Jones REVIEWED BY:

REVISIONS: [Table with columns for REVISIONS, INIT., DATE]

750 N. Greenfield Pkwy, Garner, NC 27529

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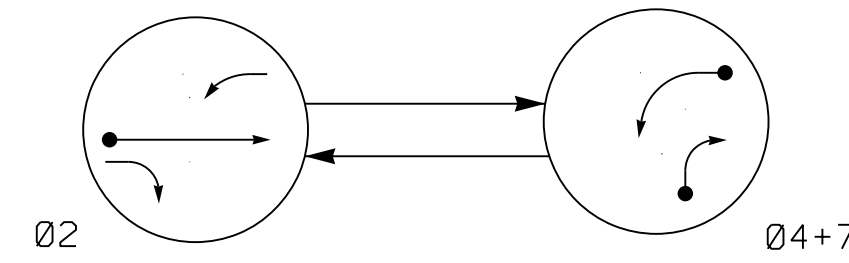
Seal: PORTER JONES, PROFESSIONAL ENGINEER, SEAL 056142

2/12/2024

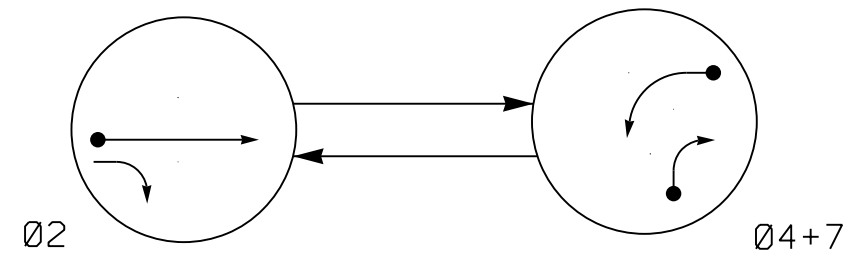
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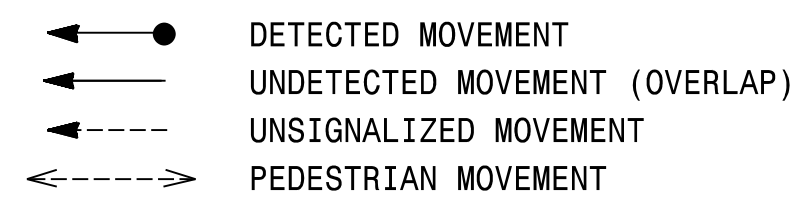
DEFAULT PHASING DIAGRAM



ALTERNATE PHASING DIAGRAM



PHASING DIAGRAM DETECTION LEGEND



DEFAULT PHASING TABLE OF OPERATION

SIGNAL FACE	PHASE		
	0 2	0 4 + 7	FLASH
21	↑	R Y	Y
22	G	R	Y
23	←	R	Y
41, 42, 43	→	R	Y
71,72	←	→	Y

ALTERNATE PHASING TABLE OF OPERATION

SIGNAL FACE	PHASE		
	0 2	0 4 + 7	FLASH
21	↑	R Y	Y
22	G	R	Y
23	←	R	Y
41, 42, 43	→	R	Y
71,72	←	→	Y

MAXTIME DETECTOR INSTALLATION CHART

DETECTOR				PROGRAMMING							
ZONE	SIZE (FT)	DISTANCE FROM STOPBAR (FT)	TURNS	NEW LOOP	CALL PHASE	DELAY TIME	EXTEND TIME	EXTEND	ADDED INITIAL CALL	DELAY DURING GREEN	NEW CARD
2A *	6X6	300	*	*	2	-	-	X	X	X	*
4A *	6X40	0	*	*	4	15	-	X	-	X	*
7A *	6X40	0	*	*	7	15#	-	X	-	X	*

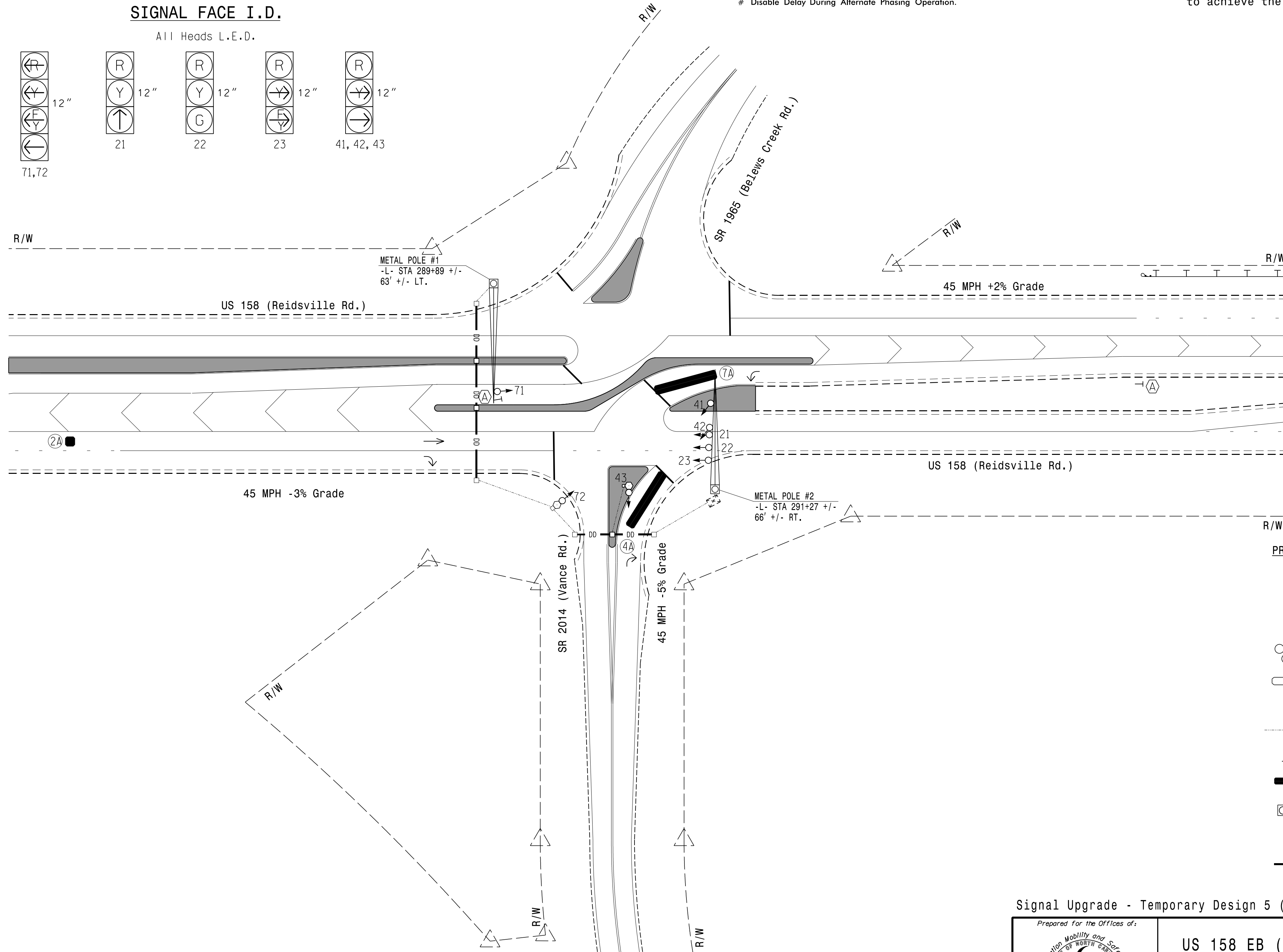
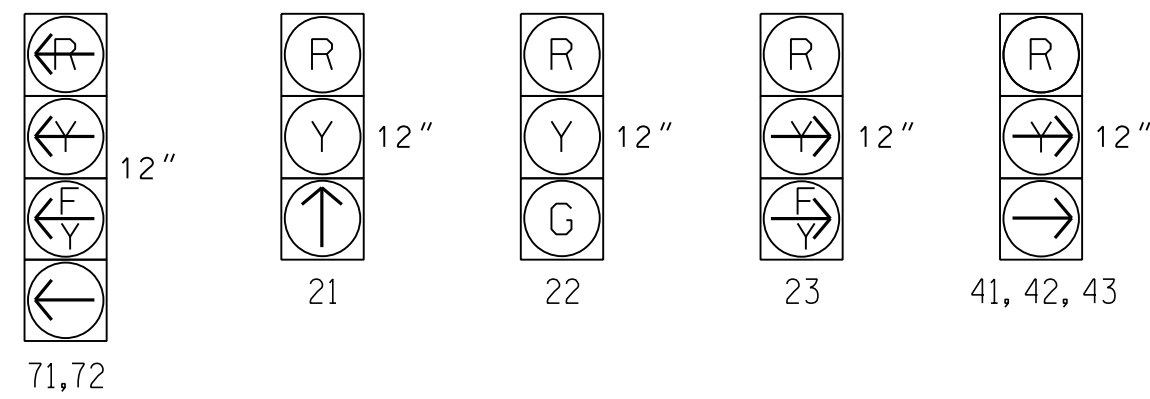
* Video Detection Zone
Disable Delay During Alternate Phasing Operation.

2 Phase Fully Actuated (Isolated)
NOTES

1. Refer to "Roadway Standard Drawings NCDOT" dated January 2024 and "Standard Specifications for Roads and Structures" dated January 2024.
2. Do not program signal for late night flashing operation unless otherwise directed by the Engineer.
3. Set all detector units to presence mode.
4. This intersection uses video detection. Install detectors according to the manufacturer's instructions to achieve the desired detection.

SIGNAL FACE I.D.

All Heads L.E.D.

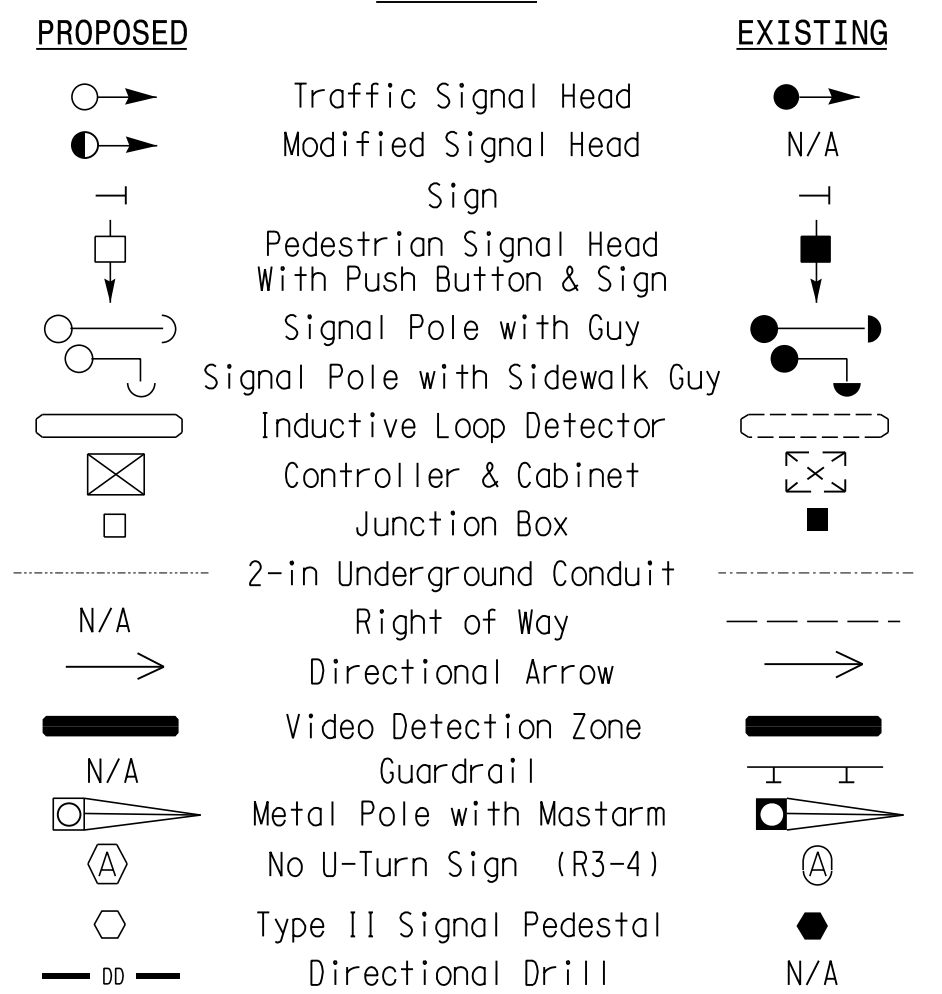


MAXTIME TIMING CHART

FEATURE	PHASE		
	2	4	7
Walk *	-	-	-
Ped Clear *	-	-	-
Min Green	12	7	7
Passage *	6.0	2.0	2.0
Max I *	90	30	30
Yellow Change	4.8	3.0	3.0
Red Clear	1.3	2.3	2.3
Added Initial *	2.5	-	-
Maximum Initial *	34	-	-
Time Before Reduction *	15	-	-
Time To Reduce *	30	-	-
Minimum Gap	3.0	-	-
Advance Walk	-	-	-
Non Lock Detector	-	X	X
Vehicle Recall	MIN RECALL	-	-
Dual Entry	-	X	X

* These values may be field adjusted. Do not adjust Min Green and Extension times for phase 2 lower than what is shown. Min Green for all other phases should not be lower than 4 seconds.

LEGEND



Signal Upgrade - Temporary Design 5 (TMP Phase III Step 3)

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Prepared for the Offices of:

 STATE OF NORTH CAROLINA
 DEPARTMENT OF TRANSPORTATION
 Signal Design Section

750 N. Greenfield Pkwy, Garner, NC 27529
 SCALE: 0" = 40'
 1" = 40'

**US 158 EB (Reidsville Rd.)
at
SR 2014 (Vance Rd.)**
 Division 9 Forsyth County Walkertown
 PLAN DATE: February 2024 REVIEWED BY: WP Erickson-Jones
 PREPARED BY: H Townsend REVIEWED BY: [Signature]

REVISIONS	INIT.	DATE

DocuSigned by:
 Porter Jones
 056142

2/12/2024
 DATE

SIG. INVENTORY NO. 09-110075

SEAL

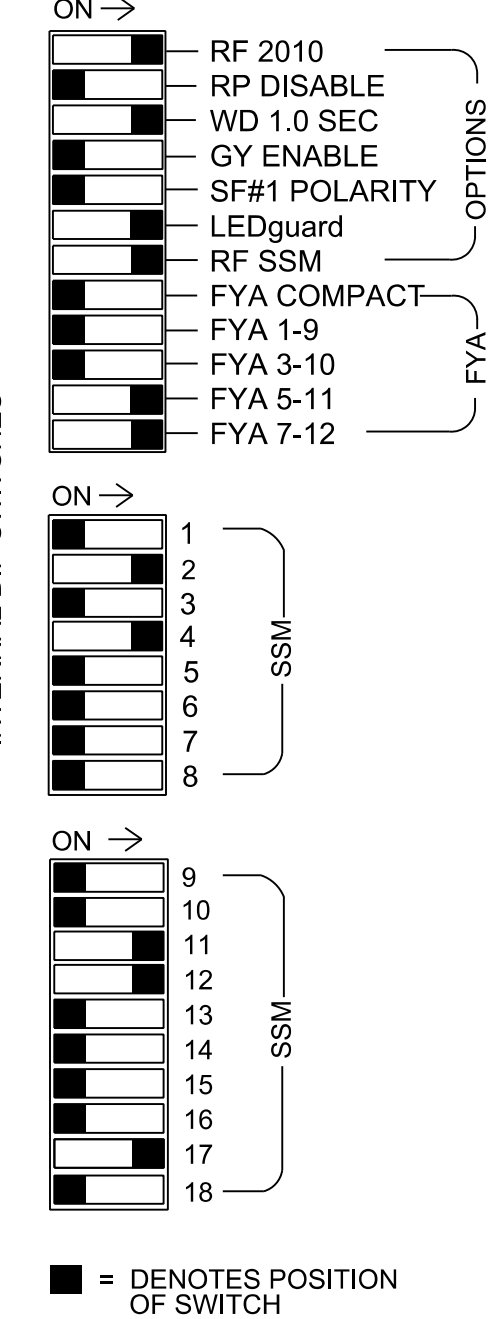
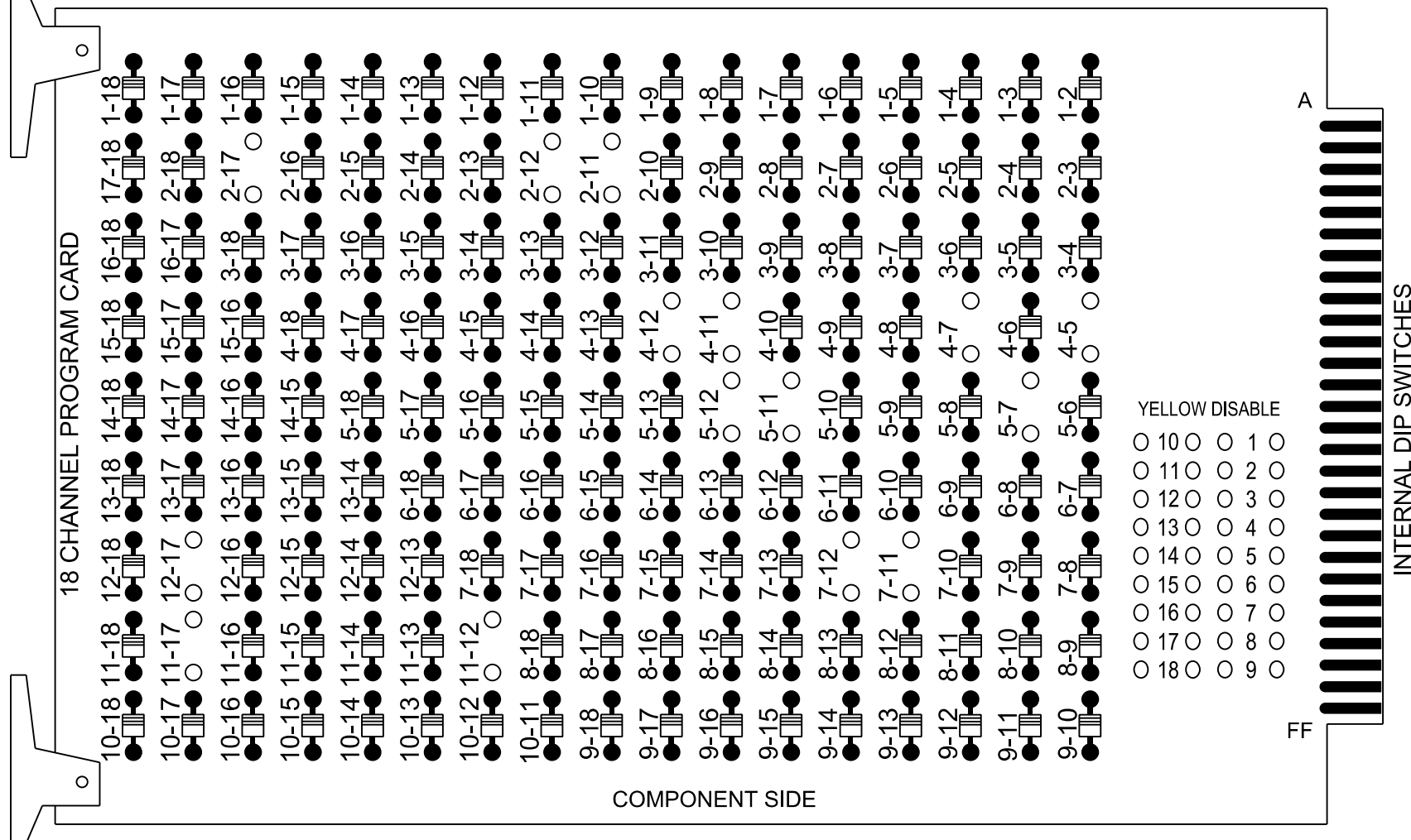
 PORTER JONES
 ENGINEER

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18 CHANNEL IP CONFLICT MONITOR PROGRAMMING DETAIL

(remove jumpers and set switches as shown)

REMOVE DIODE JUMPERS 2-11, 2-12, 2-17, 4-5, 4-7, 4-11, 4-12, 5-7, 5-11, 5-12, 7-11, 7-12, 11-12, 11-17 AND 12-17.



REMOVE JUMPERS AS SHOWN

NOTES:

- Card is provided with all diode jumpers in place. Removal of any jumper allows its channels to run concurrently.
- Ensure jumpers SEL2-SEL5 and SEL9 are present on the monitor board.
- Ensure that the Red Enable is active at all times during normal operation.
- Connect serial cable from conflict monitor to comm. port 1 of 2070 controller. Ensure conflict monitor communicates with 2070.

NOTES

- To prevent "flash-conflict" problems, insert red flash program blocks for all unused vehicle load switches in the output file. The installer shall verify that signal heads flash in accordance with the signal plan.
- Return controller to Factory Defaults before programming per this electrical detail.
- Program controller to start up in phase 2 Green No Walk.
- If this signal will be managed by an ATMS software, enable controller and detector logging for all detectors used at this location.

EQUIPMENT INFORMATION

Controller.....2070LX
Cabinet.....332 w/ Aux
Software.....Q-Free MAXTIME
Cabinet Mount.....Base
Output File Positions.....18 With Aux. Output File
Load Switches Used.....S2, S5, S7, S10, AUX S3, AUX S4, AUX S5
Phases Used.....2, 4, 7
Overlap "1".....NOT USED
Overlap "2".....NOT USED
Overlap "3".....*
Overlap "4".....*
Overlap "5".....*
Overlap "7".....*

*See overlap programming detail on sheet 2.

SIGNAL HEAD HOOK-UP CHART

LOAD SWITCH NO.	S1	S2	S3	S4	S5	S6	S7	S8	S9	S10	S11	S12	AUX S1	AUX S2	AUX S3	AUX S4	AUX S5	AUX S6
CMU CHANNEL NO.	1	2	13	3	4	14	5	6	15	7	8	16	9	10	17	11	12	18
PHASE	1	2	2 PED	3	4	4 PED	OL7	6	6 PED	7	8	8 PED	OL1	OL2	OL5	OL3	OL4	SPARE
SIGNAL HEAD NO.	NU	21	22	NU	41, 42, 43	NU	72*	NU	NU	71*	NU	NU	NU	NU	23*	72*	71*	NU
RED		128	128		101										A111			
YELLOW		129	129				*		*									
GREEN		130																
RED ARROW															A114	A101		
YELLOW ARROW					102										A112	A115	A102	
FLASHING YELLOW ARROW															A113	A116	A103	
GREEN ARROW	130				103		133			124								

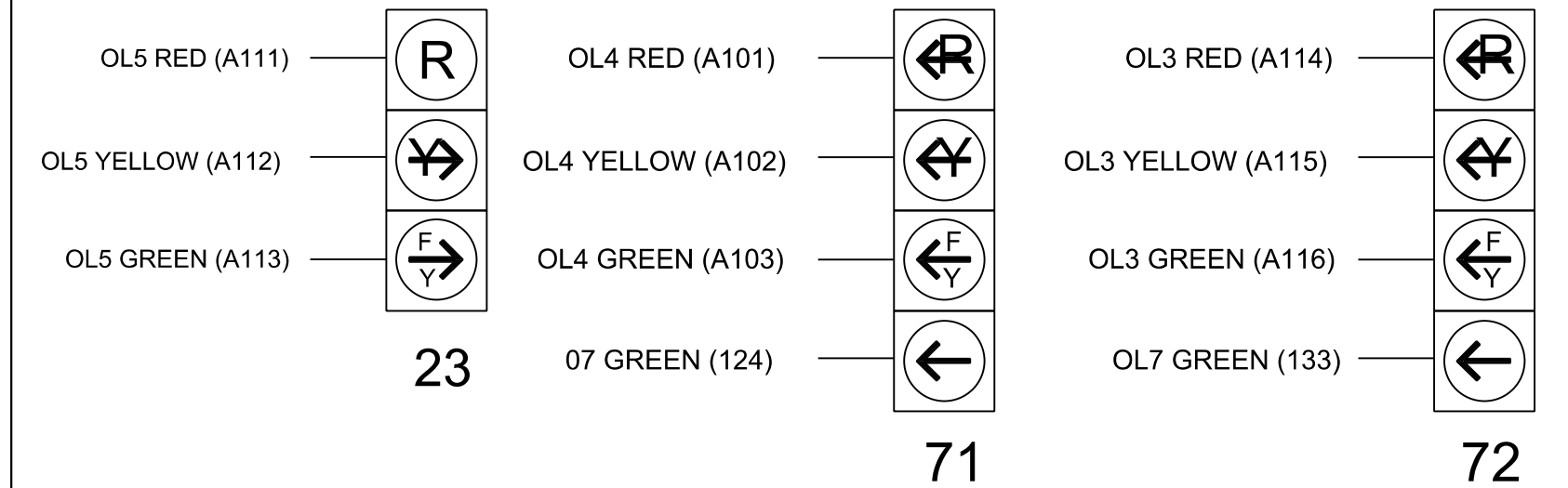
NU = Not Used

* Denotes install load resistor. See load resistor installation detail this sheet.

* See pictorial of head wiring in detail this sheet.

FYA SIGNAL WIRING DETAIL

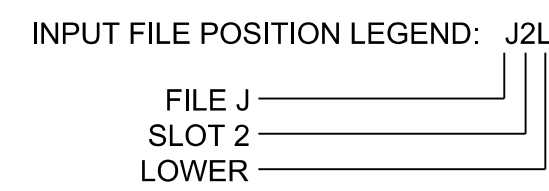
(wire signal heads as shown)



INPUT FILE CONNECTION & PROGRAMMING CHART

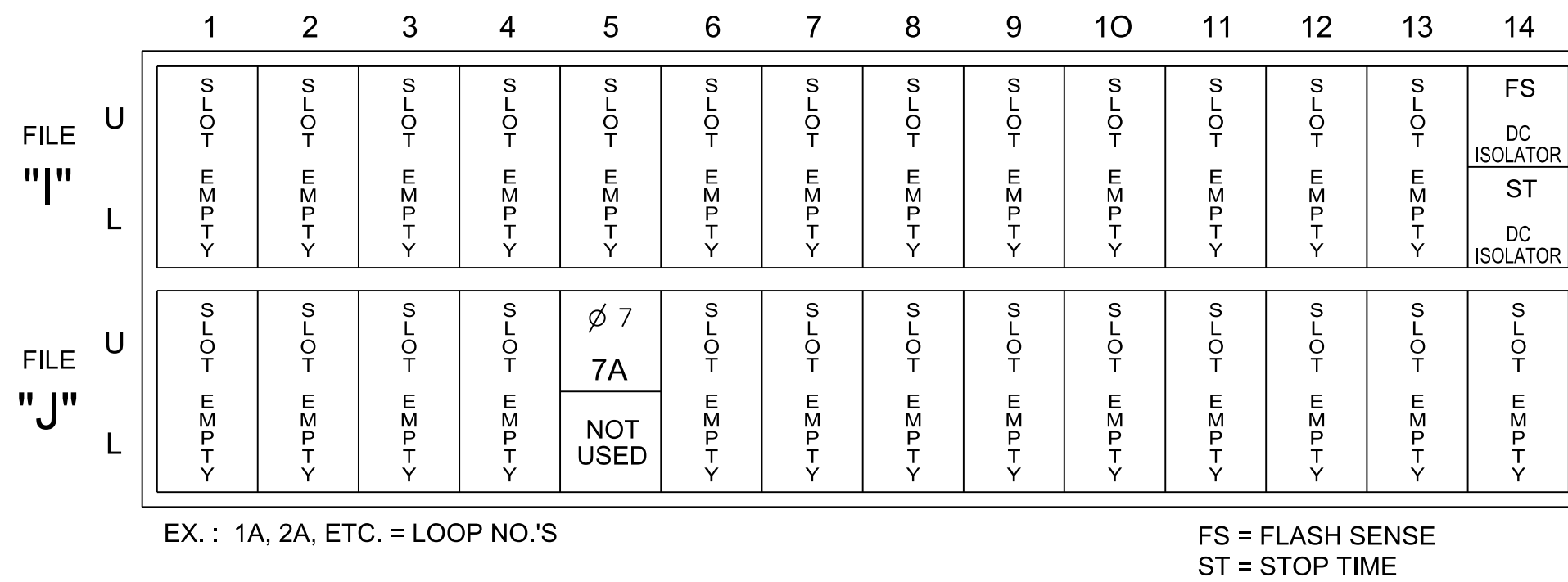
LOOP NO.	LOOP TERMINAL	INPUT FILE POS.	PIN NO.	INPUT POINT	DETECTOR NO.	CALL PHASE	DELAY TIME	EXTEND TIME	EXTEND	ADDED INITIAL	CALL	DELAY DURING GREEN
7A	TB5-5,6	J5U	57	19	21★	7	15		X		X	

★ For the detector to work as shown on the signal design plan, see the vehicle detector setup programming detail for alternate phasing on sheet 2.



INPUT FILE POSITION LAYOUT

(front view)

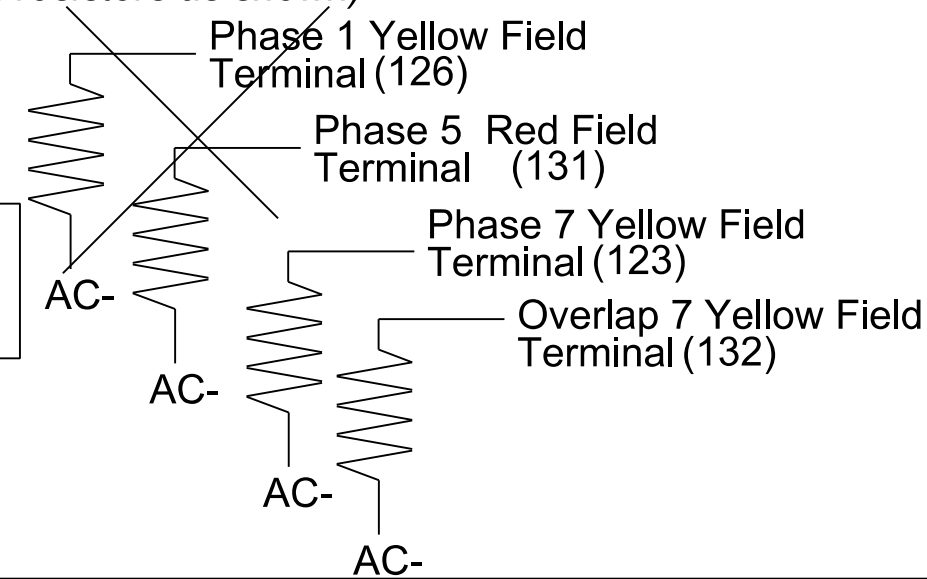


LOAD RESISTOR INSTALLATION DETAIL

ACCEPTABLE VALUES	
Value (ohms)	Wattage
1.5K - 1.9K	25W (min)
2.0K - 3.0K	10W (min)

!Remove Phase 1 Yellow Field Terminal (126) and Phase 5 Red Field Terminal (131), if present.

(install resistors as shown)



SPECIAL DETECTOR NOTE

Install a video detection system for vehicle detection for zones. 2A, 4A and 7A. Perform installation according to manufacturer's directions and NCDOT engineer-approved mounting locations to accomplish the detection schemes shown on the Signal Design Plans.

For Detection Zone 7A, the equipment placement and slots are typical for a NCDOT installation. Inputs associated with these slots are compatible with time of day instructions located on sheet 2 of this electrical detail.



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THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 09-1100T5
DESIGNED: February 2024
SEALED: February 12, 2024
REVISED:

Signal Upgrade - Temporary Design 5
(TMP Phase III Step 3) - Electrical Detail - Sheet 1 of 2

Electrical and Programming Details For:		US 158 EB (Reidsville Rd.) at SR 2014 (Vance Rd.)	
Prepared for the Offices of:	Forsyth County Walkertown		
Division 9		PLAN DATE: February 2024	REVIEWED BY: DT Sears
Prepared by: WP Erickson-Jones		REVIEWED BY:	
REVISIONS	INIT.	DATE	
		2/12/2024	
750 N. Greenfield Pkwy, Garner, NC 27529		SIG. INVENTORY NO. 09-1100T5	

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SEAL

DocuSigned by:
Porter Jones
2/12/2024

MAXTIME OVERLAP PROGRAMMING DETAIL FOR DEFAULT PHASING

Front Panel
Main Menu >Controller >Overlap >Overlap Parameters/Overlap Timings

Web Interface
Home >Controller >Overlap Configuration >Overlaps

Overlap Plan 1

Overlap	3	4	5	7
Type	FYA 4 - Section	FYA 4 - Section	FYA 4 - Section	Normal
Included Phases	2	2	2	7
Modifier Phases	-	7	-	-
Modifier Overlaps	7	-	-	-
Trail Green	0	0	0	0
Trail Yellow	0.0	0.0	0.0	0.0
Trail Red	0.0	0.0	0.0	0.0

OUTPUT CHANNEL CONFIGURATION

Front Panel
Main Menu >Controller >More>Channels>Channels Config

Web Interface
Home >Controller >Advanced IO>Channels>Channels Configuration

Channel Configuration

Channel	Control Type	Control Source	Flash Yellow	Flash Red	Flash Alt	MMU Channel
1	Phase Vehicle	1	-	X	X	1
2	Phase Vehicle	2	X	-	-	2
3	Phase Vehicle	3	-	X	X	3
4	Phase Vehicle	4	-	X	X	4
5	Overlap	7	X	-	-	5
6	Phase Vehicle	6	X	-	X	6
7	Phase Vehicle	7	X	-	-	7
8	Phase Vehicle	8	-	X	X	8
9	Overlap	1	X	-	X	9
10	Overlap	2	-	X	X	10
11	Overlap	3	X	-	X	11
12	Overlap	4	X	-	X	12
13	Phase Ped	2	-	-	-	13
14	Phase Ped	4	-	-	-	14
15	Phase Ped	6	-	-	-	15
16	Phase Ped	8	-	-	-	16
17	Overlap	5	X	-	-	17
18	Overlap	6	-	X	-	18

NOTICE OVERLAP 7 ASSIGNED TO CHANNEL 5 →

← NOTICE CHANNEL 5 YELLOW FLASH

← NOTICE CHANNEL 7 YELLOW FLASH

← NOTICE CHANNEL 12 YELLOW FLASH

← NOTICE CHANNEL 17 YELLOW FLASH

MAXTIME ALTERNATE PHASING ACTIVATION DETAIL

To run alternate phasing, select a Pattern that is programmed to run Overlap Plan 2 and Detector Plan 2. A Pattern can be selected through the scheduler or manually by changing the Operational Mode.

PHASING	OVERLAP PLAN	VEH DET PLAN
ACTIVE PLAN REQUIRED TO RUN DEFAULT PHASING	1	1
ACTIVE PLAN REQUIRED TO RUN ALTERNATE PHASING	2	2

ALTERNATE PHASING CHANGE SUMMARY

THE FOLLOWING IS A SUMMARY OF WHAT TAKES PLACE WHEN OVERLAP PLAN 2 AND VEHICLE DETECTOR PLAN 2 ACTIVATE TO CALL THE "ALTERNATE PHASING":

OVERLAP PLAN 2: Modifies overlap included phases for heads 71 and 72 to run protected turns only.

VEH DET PLAN 2: Reduces delay time for phase 7 call on loop 7A to 0 seconds.

MAXTIME OVERLAP PROGRAMMING DETAIL FOR ALTERNATE PHASING

Front Panel
Main Menu >Controller >Overlap >Overlap Parameters/Overlap Timings

Web Interface
Home >Controller >Overlap Configuration >Overlaps

In the table view of the web interface, right click on "Overlap" in the top left corner of the table. Copy the entire contents of Overlap Plan 1. Paste Overlap Plan 1 into Overlap Plan 2. Modify Overlap Plan 2 as shown below and save changes.

Overlap Plan 2

Overlap	3	4	5	7
Type	FYA 4 - Section	FYA 4 - Section	FYA 4 - Section	Normal
Included Phases	-	-	2	7
Modifier Phases	-	7	-	-
Modifier Overlaps	7	-	-	-
Trail Green	0	0	0	0
Trail Yellow	0.0	0.0	0.0	0.0
Trail Red	0.0	0.0	0.0	0.0

← NOTICE INCLUDED PHASE

MAXTIME DETECTOR PROGRAMMING DETAIL FOR ALTERNATE PHASING LOOP 7A

Front Panel
Main Menu >Controller >Detector >Veh Det Plans

Web Interface
Home >Controller >Detector Configuration >Vehicle Detectors

In the table view of web interface right click on "Detector" in the top left corner of the table. Copy the entire contents of Detector Plan 1. Paste Detector Plan 1 into Detector Plan 2. Modify Detector Plan 2 as shown below and save changes.

7A

Plan 2	Detector	Call Phase	Delay
	21	7	0

FLASHER CIRCUIT MODIFICATION DETAIL

IN ORDER TO INSURE THAT SIGNALS FLASH CONCURRENTLY ON THE SAME APPROACH, MAKE THE FOLLOWING FLASHER CIRCUIT CHANGES:

- ON REAR OF PDA - REMOVE WIRE FROM TERM. T2-4 AND TERMINATE ON T2-2.
- ON REAR OF PDA - REMOVE WIRE FROM TERM. T2-5 AND TERMINATE ON T2-3.
- REMOVE FLASHER UNIT 2.

THE CHANGES LISTED ABOVE TIES ALL PHASES AND OVERLAPS TO FLASHER UNIT 1.

THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 09-1100T5
DESIGNED: February 2024
SEALED: February 12, 2024
REVISED:

MAXTIME ALTERNATE PHASING PATTERN PROGRAMMING DETAIL

Front Panel
Main Menu >Controller >Coordination >Patterns

Web Interface
Home >Controller >Coordination >Patterns

Pattern Parameters

Pattern	Veh Det Plan	Overlap Plan
*	2	2

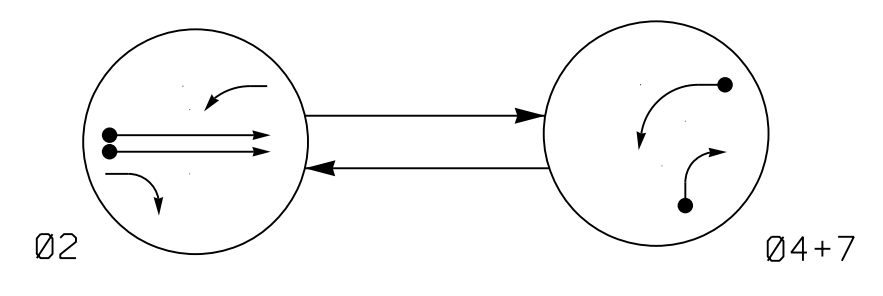
* The Pattern number(s) are to be determined by the Division and/or City Traffic Engineer.

Signal Upgrade - Temporary Design 5
(TMP Phase III Step 3) - Electrical Detail - Sheet 2 of 2

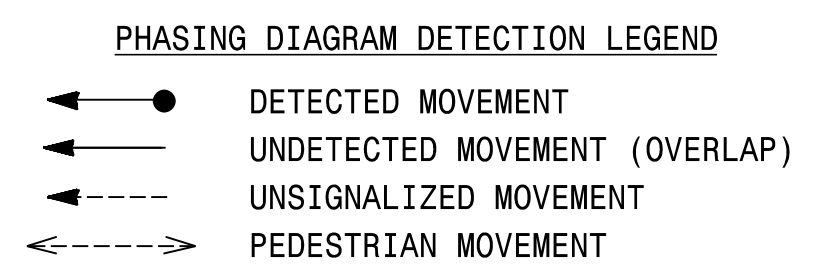
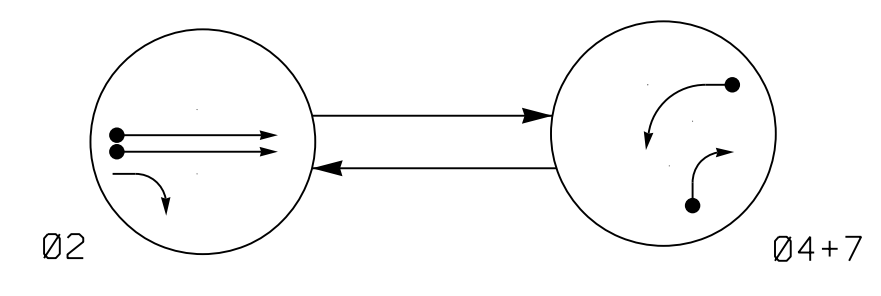


Electrical and Programming Details For:	US 158 EB (Reidsville Rd.) at SR 2014 (Vance Rd.)	DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED
Prepared for the Offices of:	Division 9 Forsyth County Walkertown	SEAL NORTH CAROLINA PROFESSIONAL ENGINEER PORTER JONES SEAL 056142
PLAN DATE: February 2024	REVIEWED BY: DT Sears	DocuSigned by Porter Jones 2/12/2024
PREPARED BY: WP Erickson-Jones	REVIEWED BY:	DATE
REVISIONS	INIT.	DATE
750 N. Greenfield Pkwy, Garner, NC 27529		SIG. INVENTORY NO. 09-1100T5

DEFAULT PHASING DIAGRAM



ALTERNATE PHASING DIAGRAM



DEFAULT PHASING TABLE OF OPERATION

SIGNAL FACE	PHASE		
	02	04+7	FLASH
21	↑	R	Y
22	G	R	Y
23	←	R	Y
41, 42, 43	R	→	R
71, 72	←	→	Y

ALTERNATE PHASING TABLE OF OPERATION

SIGNAL FACE	PHASE		
	02	04+7	FLASH
21	↑	R	Y
22	G	R	Y
23	←	R	Y
41, 42, 43	R	→	R
71, 72	←	→	Y

MAXTIME DETECTOR INSTALLATION CHART

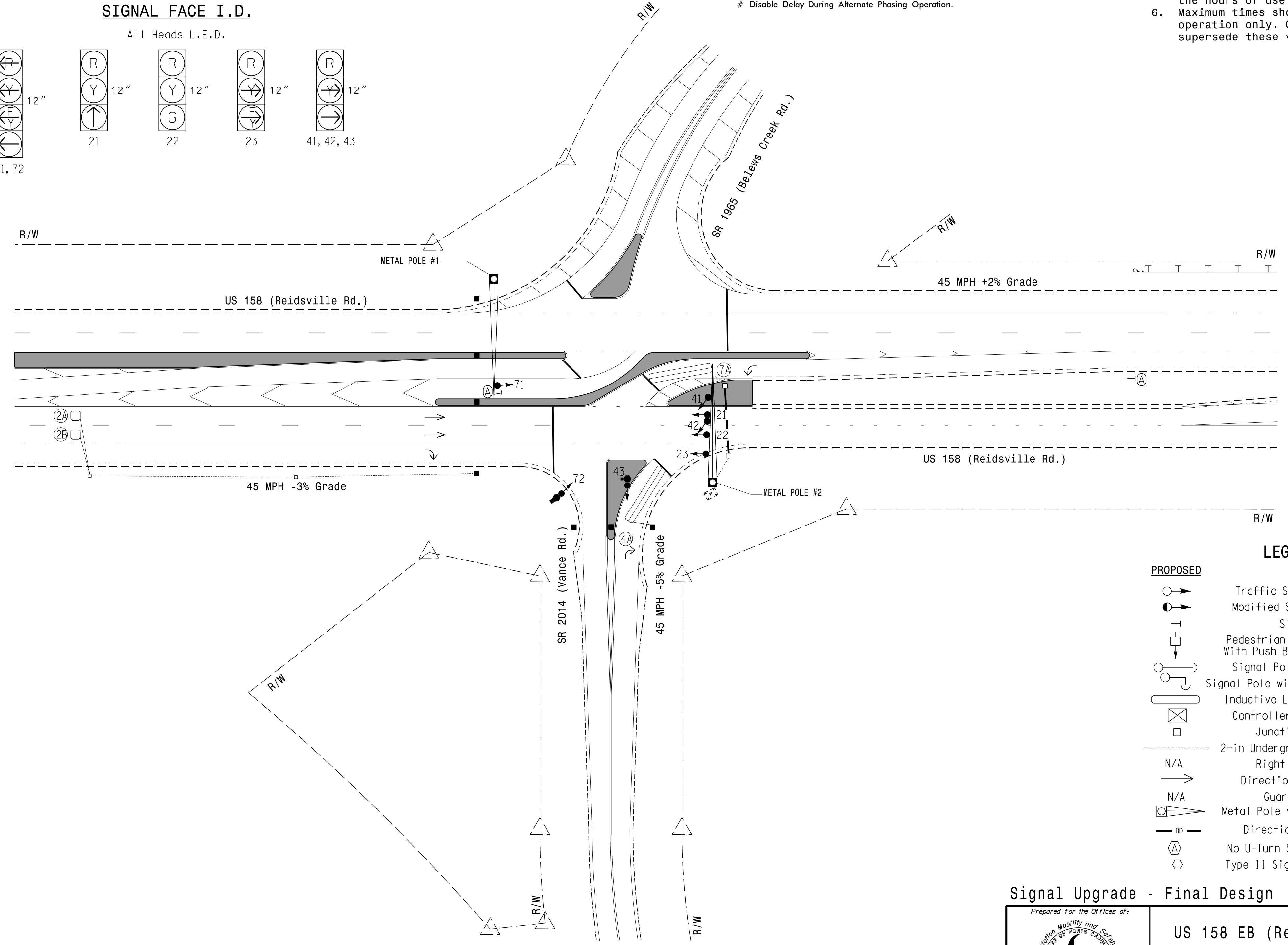
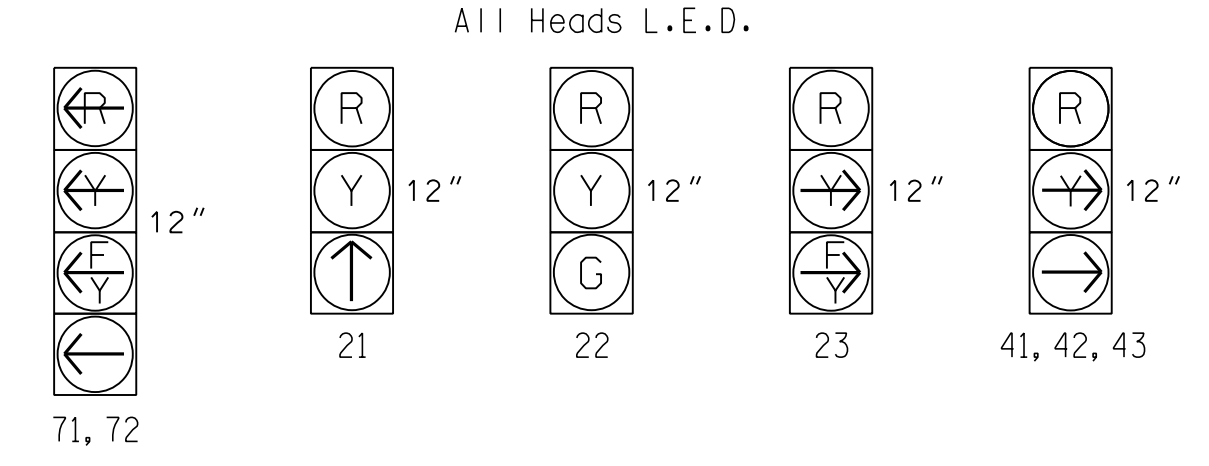
LOOP	SIZE (FT)	DISTANCE FROM STOPBAR (FT)	TURNS	NEW LOOP	PROGRAMMING							
					CALL PHASE	DELAY TIME	EXTEND TIME	EXTEND	ADDED INITIAL CALL	DELAY DURING GREEN	NEW CARD	
2A	6X6	300	5	X	2	-	-	X	X	X	-	X
2B	6X6	300	5	X	2	-	-	X	X	X	-	X
4A	6X40	0	2-4-2	X	4	15	-	X	-	X	-	X
7A	6X40	0	2-4-2	X	7	15#	-	X	-	X	-	X

Disable Delay During Alternate Phasing Operation.

2 Phase Fully Actuated (US 158 Signal System) Signal System #: D09-11_Winston-Salem NOTES

- Refer to "Roadway Standard Drawings NCDOT" dated January 2024 and "Standard Specifications for Roads and Structures" dated January 2024.
- Do not program signal for late night flashing operation unless otherwise directed by the Engineer.
- Reposition existing signal heads numbered 21 and 22.
- Set all detector units to presence mode.
- The Division Traffic Engineer will determine the hours of use for each phasing plan.
- Maximum times shown in timing chart are for free-run operation only. Coordinated signal system timing values supersede these values.

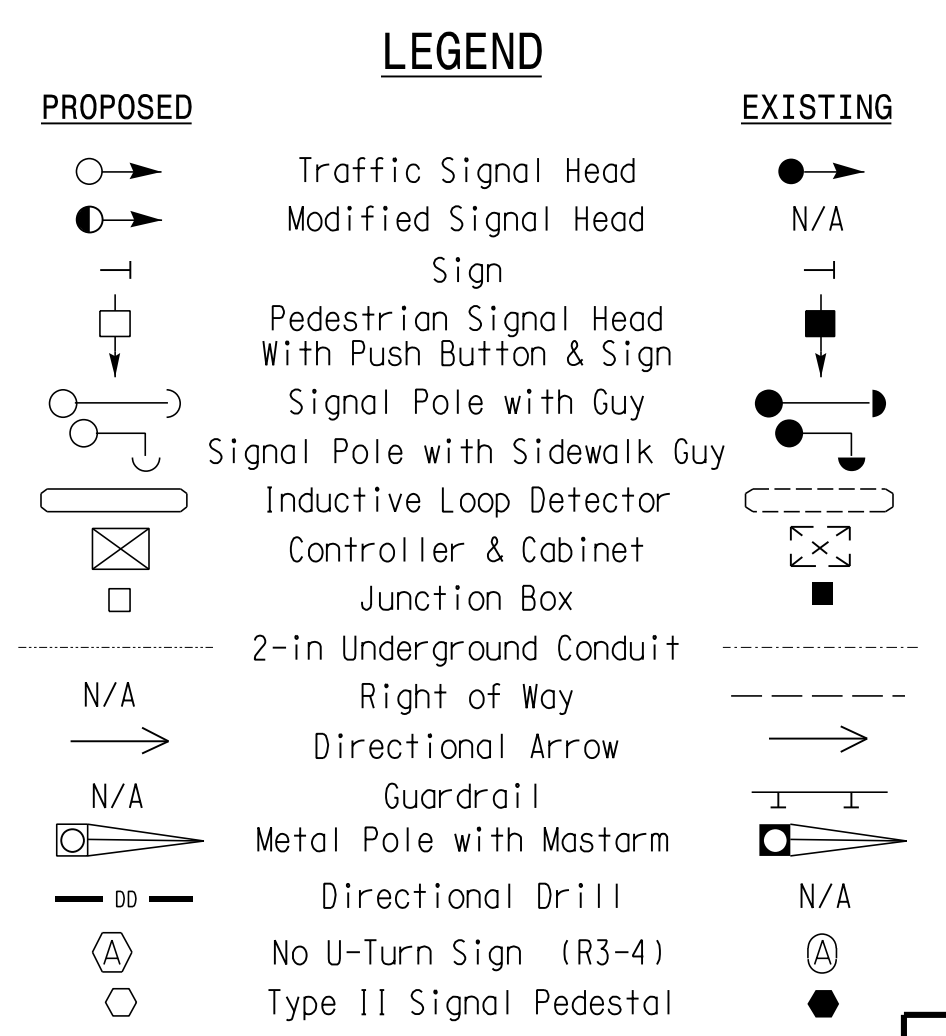
SIGNAL FACE I.D.



MAXTIME TIMING CHART

FEATURE	PHASE		
	2	4	7
Walk *	-	-	-
Ped Clear *	-	-	-
Min Green	12	7	7
Passage *	6.0	2.0	2.0
Max I *	90	30	30
Yellow Change	4.8	3.0	3.0
Red Clear	1.3	2.3	2.3
Added Initial *	1.5	-	-
Maximum Initial *	34	-	-
Time Before Reduction *	15	-	-
Time To Reduce *	30	-	-
Minimum Gap	3.0	-	-
Advance Walk	-	-	-
Non Lock Detector	-	X	X
Vehicle Recall	MIN RECALL	-	-
Dual Entry	-	X	X

* These values may be field adjusted. Do not adjust Min Green and Extension times for phase 2 lower than what is shown. Min Green for all other phases should not be lower than 4 seconds.



Signal Upgrade - Final Design

RK&K
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 8801 Six Forks Road Suite 700 | Raleigh, North Carolina 27615-2965
 NC License No. F-0112
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Prepared for the Offices of:
 Transportation Mobility and Safety Division
 NORTH CAROLINA DEPARTMENT OF TRANSPORTATION
 Signal Design Section
 750 N. Greenfield Pkwy, Garner, NC 27529

US 158 EB (Reidsville Rd.) at SR 2014 (Vance Rd.)
 Division 9 Forsyth County Walkertown
 PLAN DATE: February 2024 REVIEWED BY: WP Erickson-Jones
 PREPARED BY: H Townsend REVIEWED BY:

DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED

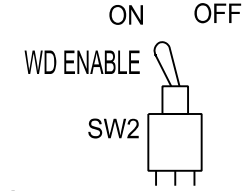
SEAL
 NORTH CAROLINA PROFESSIONAL ENGINEER
 SEAL 056142
 W. PORTER JONES
 PORTER JONES
 2/12/2024
 DATE
 SIG. INVENTORY NO. 09-1100

REVISIONS	INIT.	DATE

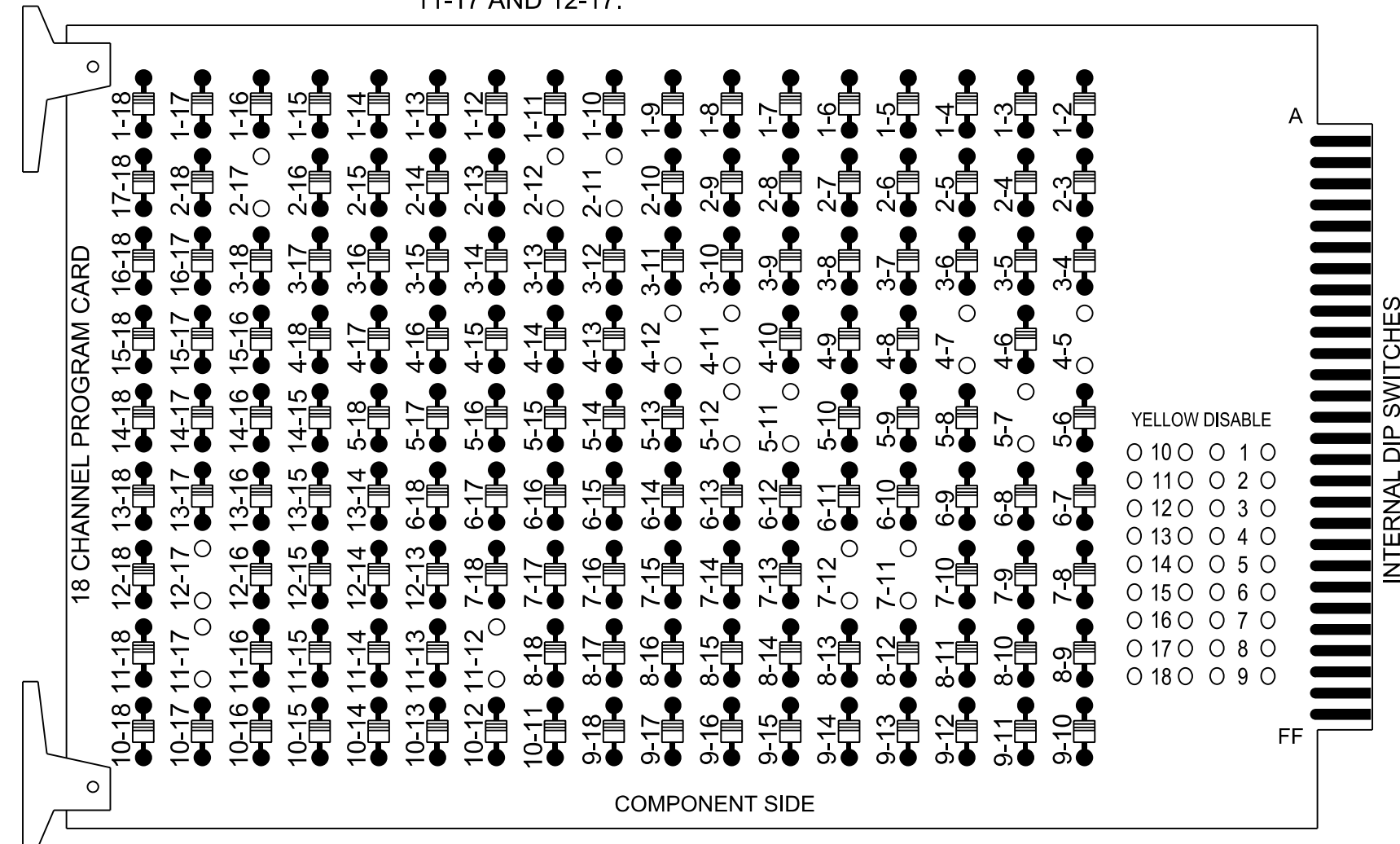
2/12/2024
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 wpJones

18 CHANNEL IP CONFLICT MONITOR PROGRAMMING DETAIL

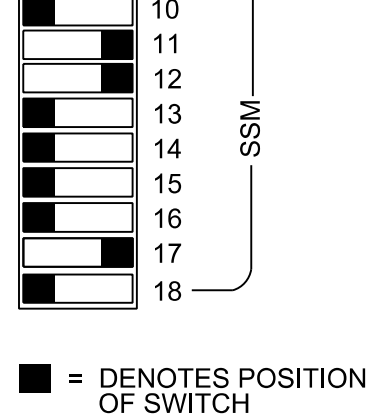
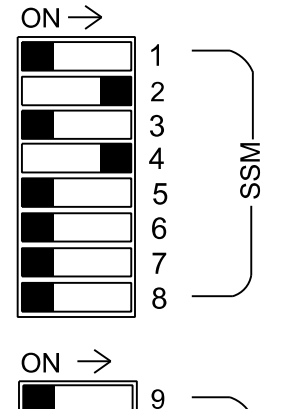
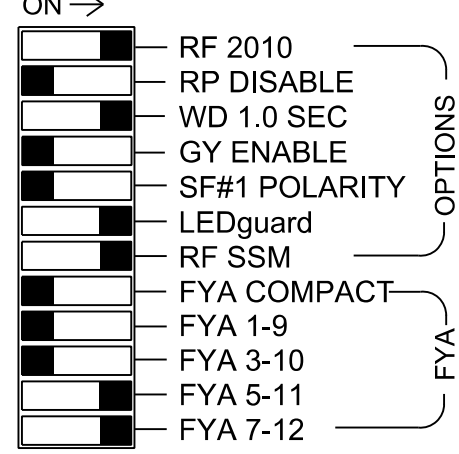
(remove jumpers and set switches as shown)



REMOVE DIODE JUMPERS 2-11, 2-12, 2-17, 4-5, 4-7, 4-11, 4-12, 5-7, 5-11, 5-12, 7-11, 7-12, 11-12, 11-17 AND 12-17.



REMOVE JUMPERS AS SHOWN



NOTES:

- Card is provided with all diode jumpers in place. Removal of any jumper allows its channels to run concurrently.
- Ensure jumpers SEL2-SEL5 and SEL9 are present on the monitor board.
- Ensure that the Red Enable is active at all times during normal operation.
- Integrate monitor with Ethernet network in cabinet.

NOTES

- To prevent "flash-conflict" problems, insert red flash program blocks for all unused vehicle load switches in the output file. The installer shall verify that signal heads flash in accordance with the signal plan.
- Program controller to start up in phase 2 Green No Walk.
- If this signal will be managed by an ATMS software, enable controller and detector logging for all detectors used at this location.
- The cabinet and controller are part of the US 158 Signal System, Signal System : D09-11_Winston-Salem System.

EQUIPMENT INFORMATION

Controller.....2070LX
 Cabinet.....332 w/ Aux
 Software.....Q-Free MAXTIME
 Cabinet Mount.....Base
 Output File Positions.....18 With Aux. Output File
 Load Switches Used.....S2, S5, S7, S10, AUX S3, AUX S4, AUX S5
 Phases Used.....2, 4, 7
 Overlap "1".....NOT USED
 Overlap "2".....NOT USED
 Overlap "3".....
 Overlap "4".....
 Overlap "5".....
 Overlap "7".....

*See overlap programming detail on sheet 2.

SIGNAL HEAD HOOK-UP CHART

LOAD SWITCH NO.	S1	S2	S3	S4	S5	S6	S7	S8	S9	S10	S11	S12	AUX S1	AUX S2	AUX S3	AUX S4	AUX S5	AUX S6
CMU CHANNEL NO.	1	2	13	3	4	14	5	6	15	7	8	16	9	10	17	11	12	18
PHASE	1	2	2 PED	3	4	4 PED	OL7	6	6 PED	7	8	8 PED	OL1	OL2	OL5	OL3	OL4	SPARE
SIGNAL HEAD NO.	NU	21	22	NU	41,42,43	NU	72	NU	NU	71	NU	NU	NU	NU	23	72	71	NU
RED	128	128			101										A111			
YELLOW		129	129				*			*								
GREEN			130															
RED ARROW																A114	A101	
YELLOW ARROW						102										A112	A115	A102
FLASHING YELLOW ARROW																A113	A116	A103
GREEN ARROW	130				103		133			124								

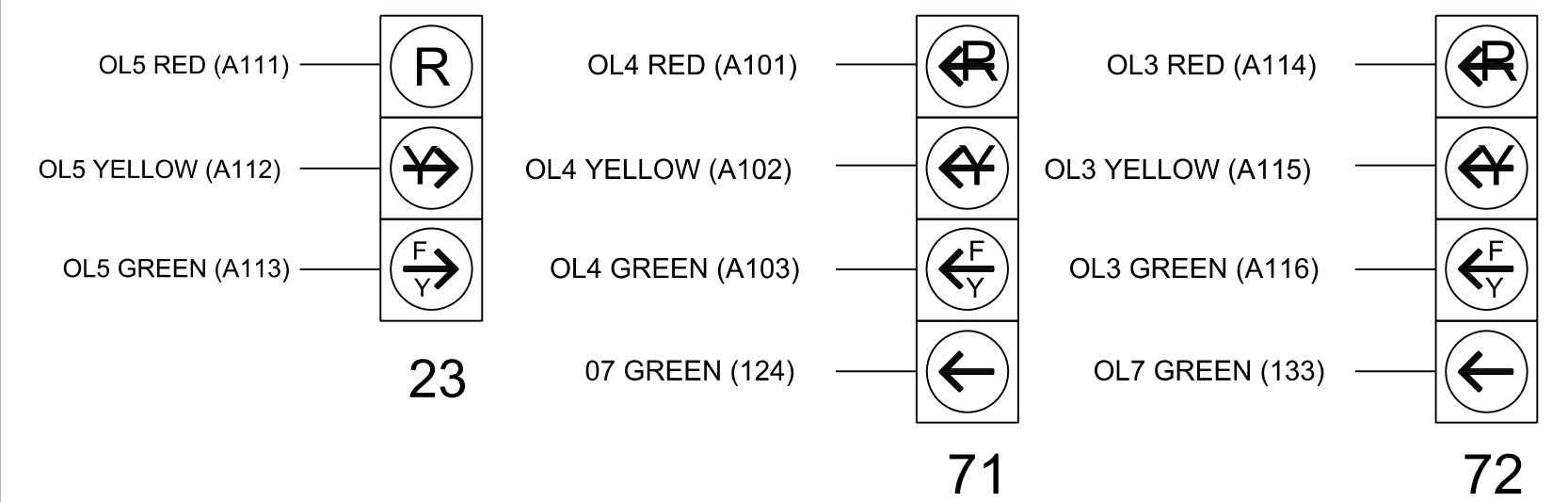
NU = Not Used

* Denotes install load resistor. See load resistor installation detail this sheet.

* See pictorial of head wiring in detail this sheet.

FYA SIGNAL WIRING DETAIL

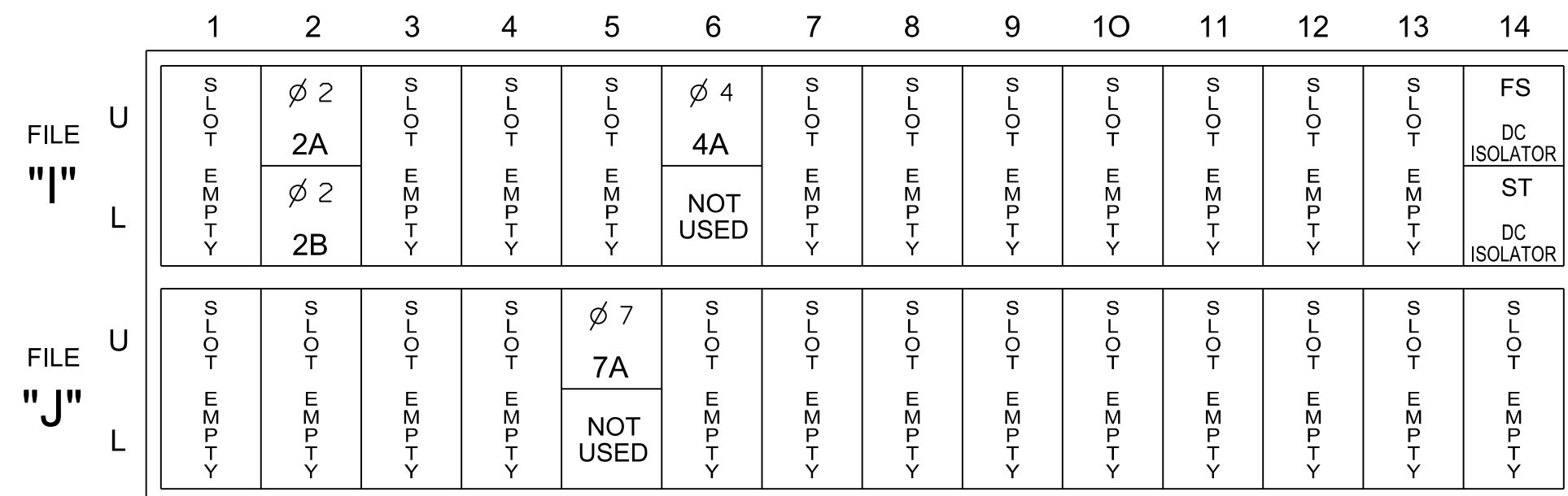
(wire signal heads as shown)



INPUT FILE CONNECTION & PROGRAMMING CHART

INPUT FILE POSITION LAYOUT

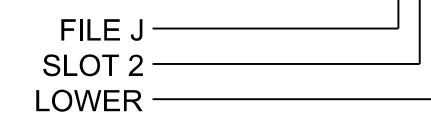
(front view)



LOOP NO.	LOOP TERMINAL	INPUT FILE POS.	PIN NO.	INPUT POINT	DETECTOR NO.	CALL PHASE	DELAY TIME	EXTEND TIME	EXTEND	ADDED INITIAL	CALL	DELAY DURING GREEN
2A	TB2-5.6	I2U	39	1	2	2				X	X	X
2B	TB2-7.8	I2L	43	5	3	2				X	X	X
4A	TB4-9.10	I6U	41	3	8	4	15			X		X
7A	TB5-5.6	J5U	57	19	21	7	15			X		X

* For the detector to work as shown on the signal design plan, see the vehicle detector setup programming detail for alternate phasing on sheet 2.

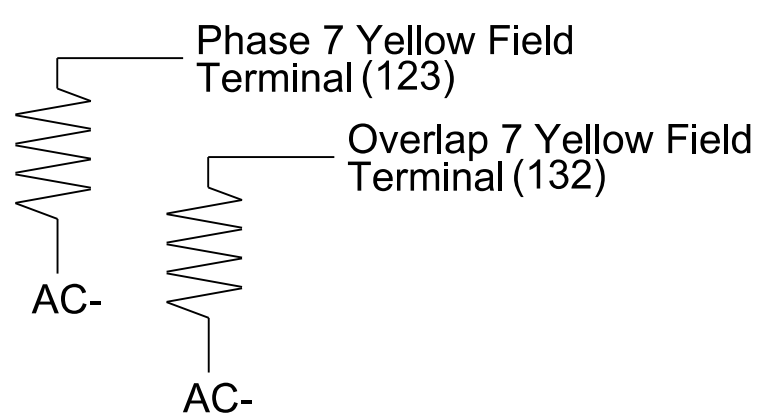
INPUT FILE POSITION LEGEND: J2L



LOAD RESISTOR INSTALLATION DETAIL

(install resistors as shown)

Value (ohms)	Wattage
1.5K - 1.9K	25W (min)
2.0K - 3.0K	10W (min)



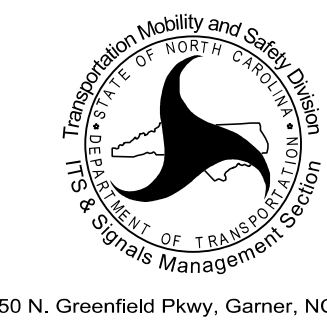
THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 09-1100
 DESIGNED: February 2024
 SEALED: February 12, 2024
 REVISED:

Signal Upgrade - Final Design - Electrical Detail - Sheet 1 of 2

DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED

Electrical and Programming Details For:

Prepared for the Offices of:



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 Engineers | Construction Managers | Planners | Scientists
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750 N. Greenfield Pkwy, Garner, NC 27529

US 158 EB (Reidsville Rd.)
 at
 SR 2014 (Vance Rd.)

Division 9 Forsyth County Walkertown

PLAN DATE: February 2024 REVIEWED BY: DT Sears

PREPARED BY: WP Erickson-Jones REVIEWED BY:

REVISIONS INIT. DATE

SEAL
 NORTH CAROLINA PROFESSIONAL ENGINEER
 SEAL 056142
 PORTER JONES
 Documented by: Porter Jones
 DATE: 2/12/2024
 DATE: _____
 DATE: _____

SIG. INVENTORY NO. 09-1100

MAXTIME OVERLAP PROGRAMMING DETAIL FOR DEFAULT PHASING

Front Panel
Main Menu >Controller >Overlap >Overlap Parameters/Overlap Timings

Web Interface
Home >Controller >Overlap Configuration >Overlaps

Overlap Plan 1

Overlap	3	4	5	7
Type	FYA 4 - Section	FYA 4 - Section	FYA 4 - Section	Normal
Included Phases	2	2	2	7
Modifier Phases	-	7	-	-
Modifier Overlaps	7	-	-	-
Trail Green	0	0	0	0
Trail Yellow	0.0	0.0	0.0	0.0
Trail Red	0.0	0.0	0.0	0.0

MAXTIME OVERLAP PROGRAMMING DETAIL FOR ALTERNATE PHASING

Front Panel
Main Menu >Controller >Overlap >Overlap Parameters/Overlap Timings

Web Interface
Home >Controller >Overlap Configuration >Overlaps

In the table view of the web interface, right click on "Overlap" in the top left corner of the table. Copy the entire contents of Overlap Plan 1. Paste Overlap Plan 1 into Overlap Plan 2. Modify Overlap Plan 2 as shown below and save changes.

Overlap Plan 2

Overlap	3	4	5	7
Type	FYA 4 - Section	FYA 4 - Section	FYA 4 - Section	Normal
Included Phases	-	-	2	7
Modifier Phases	-	7	-	-
Modifier Overlaps	7	-	-	-
Trail Green	0	0	0	0
Trail Yellow	0.0	0.0	0.0	0.0
Trail Red	0.0	0.0	0.0	0.0

MAXTIME ALTERNATE PHASING PATTERN PROGRAMMING DETAIL

Front Panel
Main Menu >Controller >Coordination >Patterns

Web Interface
Home >Controller >Coordination >Patterns

Pattern Parameters

Pattern	Veh Det Plan	Overlap Plan
*	2	2

* The Pattern number(s) are to be determined by the Division and/or City Traffic Engineer.

OUTPUT CHANNEL CONFIGURATION

Front Panel
Main Menu >Controller >More>Channels>Channels Config

Web Interface
Home >Controller >Advanced IO>Channels>Channels Configuration

Channel Configuration

Channel	Control Type	Control Source	Flash Yellow	Flash Red	Flash Alt	MMU Channel
1	Phase Vehicle	1	-	X	X	1
2	Phase Vehicle	2	X	-	-	2
3	Phase Vehicle	3	-	X	X	3
4	Phase Vehicle	4	-	X	X	4
5	Overlap	7	X	-	-	5
6	Phase Vehicle	6	X	-	X	6
7	Phase Vehicle	7	X	-	-	7
8	Phase Vehicle	8	-	X	X	8
9	Overlap	1	X	-	X	9
10	Overlap	2	-	X	X	10
11	Overlap	3	X	-	X	11
12	Overlap	4	X	-	X	12
13	Phase Ped	2	-	-	-	13
14	Phase Ped	4	-	-	-	14
15	Phase Ped	6	-	-	-	15
16	Phase Ped	8	-	-	-	16
17	Overlap	5	X	-	-	17
18	Overlap	6	-	X	-	18

NOTICE
OVERLAP 7
ASSIGNED TO
CHANNEL 5

NOTICE CHANNEL 5
YELLOW FLASH

NOTICE CHANNEL 7
YELLOW FLASH

NOTICE CHANNEL 12
YELLOW FLASH

NOTICE CHANNEL 17
YELLOW FLASH

MAXTIME ALTERNATE PHASING ACTIVATION DETAIL

To run alternate phasing, select a Pattern that is programmed to run Overlap Plan 2 and Detector Plan 2. A Pattern can be selected through the scheduler or manually by changing the Operational Mode.

PHASING	OVERLAP PLAN	VEH DET PLAN
ACTIVE PLAN REQUIRED TO RUN DEFAULT PHASING	1	1
ACTIVE PLAN REQUIRED TO RUN ALTERNATE PHASING	2	2

ALTERNATE PHASING CHANGE SUMMARY

THE FOLLOWING IS A SUMMARY OF WHAT TAKES PLACE WHEN OVERLAP PLAN 2 AND VEHICLE DETECTOR PLAN 2 ACTIVATE TO CALL THE "ALTERNATE PHASING":

OVERLAP PLAN 2: Modifies overlap included phases for heads 71 and 72 to run protected turns only.

VEH DET PLAN 2: Reduces delay time for phase 7 call on loop 7A to 0 seconds.

FLASHER CIRCUIT MODIFICATION DETAIL

IN ORDER TO INSURE THAT SIGNALS FLASH CONCURRENTLY ON THE SAME APPROACH, MAKE THE FOLLOWING FLASHER CIRCUIT CHANGES:

- ON REAR OF PDA - REMOVE WIRE FROM TERM. T2-4 AND TERMINATE ON T2-2.
- ON REAR OF PDA - REMOVE WIRE FROM TERM. T2-5 AND TERMINATE ON T2-3.
- REMOVE FLASHER UNIT 2.

THE CHANGES LISTED ABOVE TIES ALL PHASES AND OVERLAPS TO FLASHER UNIT 1.

THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 09-1100
DESIGNED: February 2024
SEALED: February 12, 2024
REVISED:

MAXTIME DETECTOR PROGRAMMING DETAIL FOR ALTERNATE PHASING LOOP 7A

Front Panel
Main Menu >Controller >Detector >Veh Det Plans

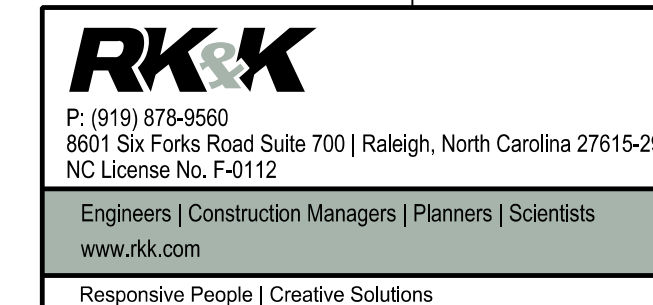
Web Interface
Home >Controller >Detector Configuration >Vehicle Detectors

In the table view of web interface right click on "Detector" in the top left corner of the table. Copy the entire contents of Detector Plan 1. Paste Detector Plan 1 into Detector Plan 2. Modify Detector Plan 2 as shown below and save changes.

Plan 2

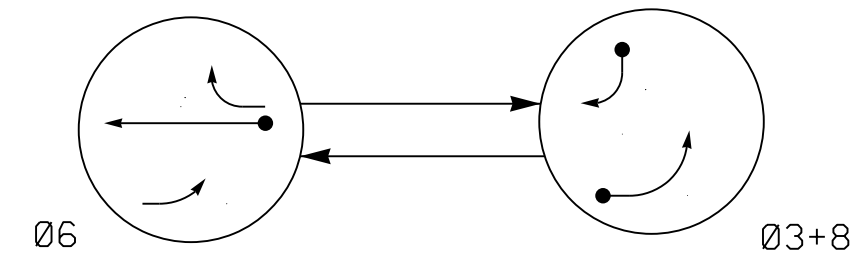
Detector	Call Phase	Delay
7A	21	7
		0

Signal Upgrade - Final Design - Electrical Detail - Sheet 2 of 2

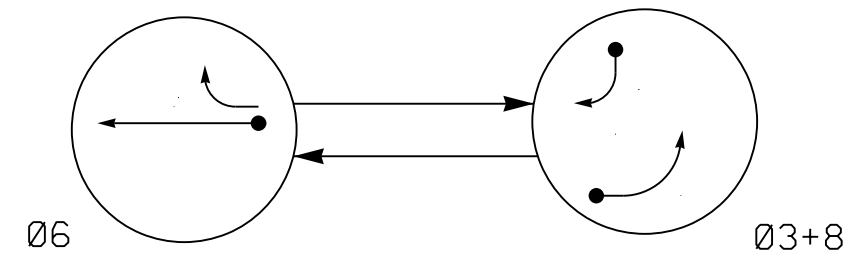


<p>Electrical and Programming Details For:</p> <p>Prepared for the Offices of:</p>	<p>US 158 EB (Reidsville Rd.) at SR 2014 (Vance Rd.)</p> <p>Division 9 Forsyth County Walkertown</p> <p>PLAN DATE: February 2024 REVIEWED BY: DT Sears</p> <p>PREPARED BY: WP Erickson-Jones REVIEWED BY:</p> <table border="1"> <thead> <tr> <th>REVISIONS</th> <th>INIT.</th> <th>DATE</th> </tr> </thead> <tbody> <tr> <td> </td> <td> </td> <td> </td> </tr> </tbody> </table> <p>DocuSigned by: Porter Jones 2/12/2024</p>	REVISIONS	INIT.	DATE				<p>DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED</p> <p>SEAL</p> <p>SIG. INVENTORY NO. 09-1100</p>
REVISIONS	INIT.	DATE						

DEFAULT PHASING DIAGRAM



ALTERNATE PHASING DIAGRAM



PHASING DIAGRAM DETECTION LEGEND

- ←●← DETECTED MOVEMENT
- ← UNDETECTED MOVEMENT (OVERLAP)
- UNSIGNALIZED MOVEMENT
- ←--- PEDESTRIAN MOVEMENT

DEFAULT PHASING TABLE OF OPERATION

SIGNAL FACE	PHASE		
	06	03+8	F
31,32	F	←	←
61	↑	R	Y
62	G	R	Y
63	F	R	←
81, 82, 83	R	←	R

ALTERNATE PHASING TABLE OF OPERATION

SIGNAL FACE	PHASE		
	06	03+8	F
31,32	←	←	←
61	↑	R	Y
62	G	R	Y
63	F	R	←
81, 82, 83	R	←	R

MAXTIME DETECTOR INSTALLATION CHART

ZONE	SIZE (FT)	DISTANCE FROM STOPBAR (FT)	TURNS	NEW LOOP	PROGRAMMING							
					CALL PHASE	DELAY TIME	EXTEND TIME	EXTEND	ADDED INITIAL CALL	DELAY DURING GREEN	NEW CARD	
3A *	6X40	0	*	*	3	15#	-	X	-	X	-	*
6A *	6X6	300	*	*	6	-	-	X	X	X	-	*
8A *	6X40	0	*	*	8	15	-	X	-	X	-	*

* Video Detection Zone
Disable Delay During Alternate Phasing Operation.

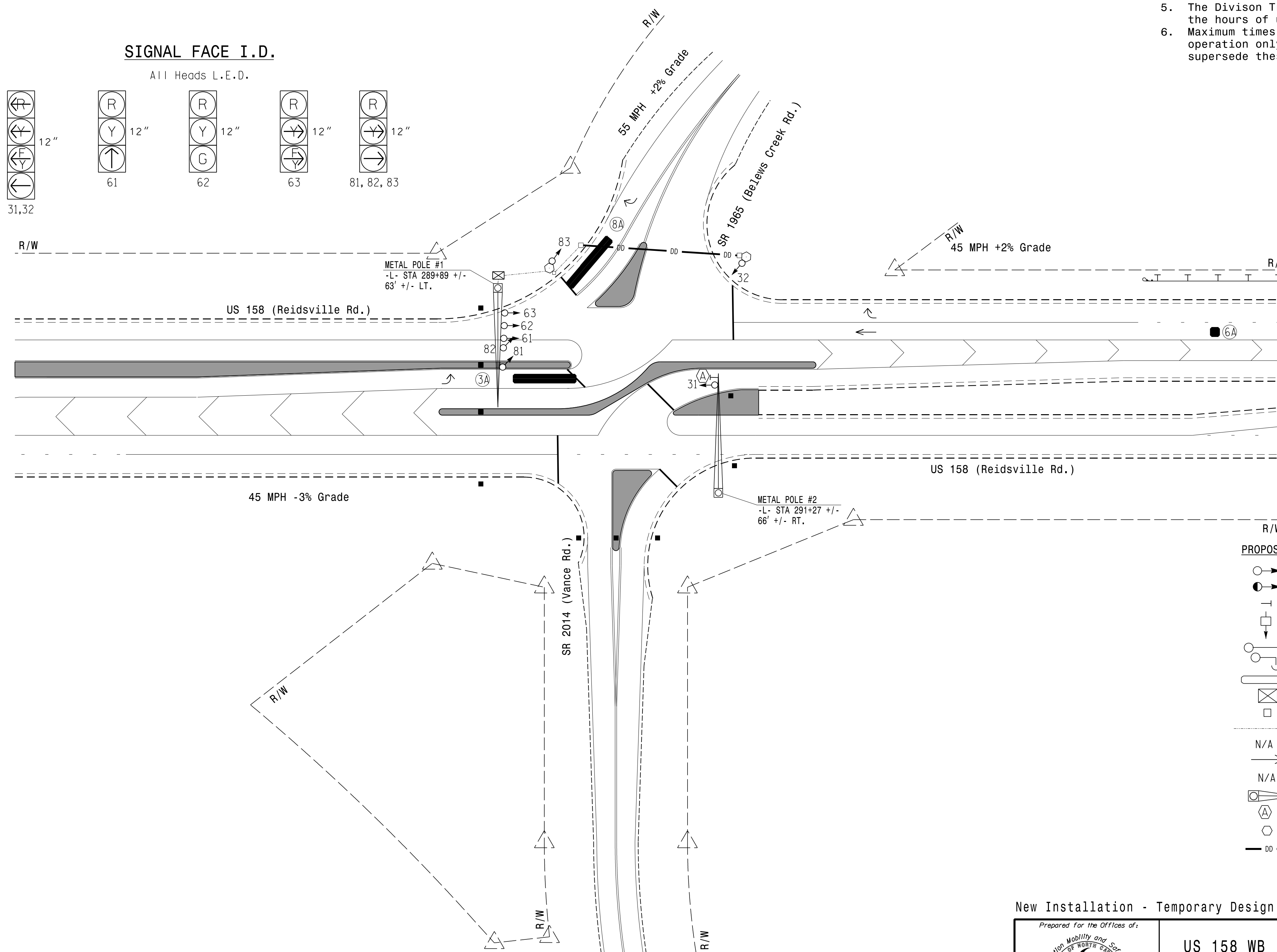
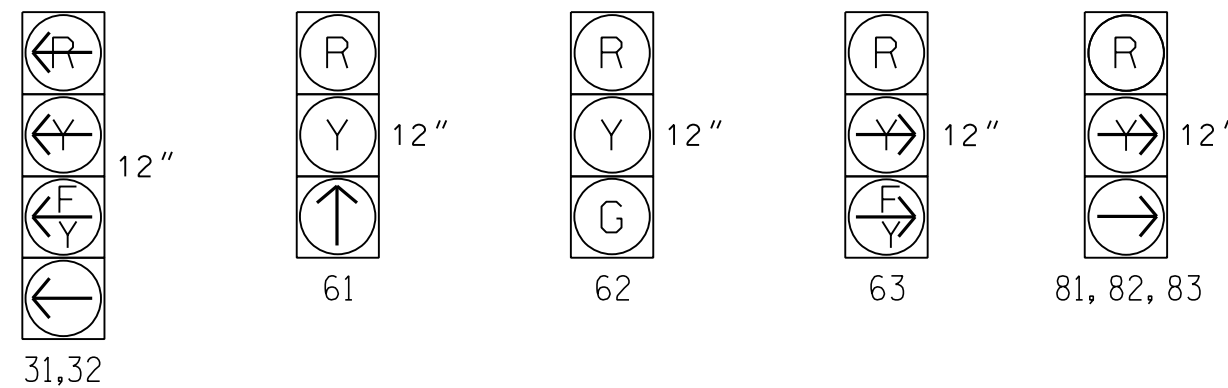
2 Phase Fully Actuated (Isolated)

NOTES

1. Refer to "Roadway Standard Drawings NCDOT" dated January 2024 and "Standard Specifications for Roads and Structures" dated January 2024.
2. Do not program signal for late night flashing operation unless otherwise directed by the Engineer.
3. Set all detector units to presence mode.
4. Locate new cabinet so as not to obstruct sight distance of vehicles turning right on red.
5. The Division Traffic Engineer will determine the hours of use for each phasing plan.
6. Maximum times shown in timing chart are for free-run operation only. Coordinated signal system timing values supersede these values.

SIGNAL FACE I.D.

All Heads L.E.D.



MAXTIME TIMING CHART

FEATURE	PHASE		
	3	6	8
Walk *	-	-	-
Ped Clear *	-	-	-
Min Green	7	12	7
Passage *	2.0	6.0	2.0
Max I *	30	90	30
Yellow Change	3.0	4.3	3.0
Red Clear	2.3	1.6	2.3
Added Initial *	-	2.5	-
Maximum Initial *	-	34	-
Time Before Reduction *	-	15	-
Time To Reduce *	-	30	-
Minimum Gap	-	3.0	-
Advance Walk	-	-	-
Non Lock Detector	X	-	X
Vehicle Recall	-	MIN RECALL	-
Dual Entry	X	-	X

* These values may be field adjusted. Do not adjust Min Green and Extension times for phase 6 lower than what is shown. Min Green for all other phases should not be lower than 4 seconds.

LEGEND

- | | | |
|-----|--|----------|
| R/W | PROPOSED | EXISTING |
| ○→ | Traffic Signal Head | ●→ |
| ○→ | Modified Signal Head | N/A |
| ↓ | Sign | ↓ |
| □→ | Pedestrian Signal Head With Push Button & Sign | □→ |
| ○→ | Signal Pole with Guy | ○→ |
| ○→ | Signal Pole with Sidewalk Guy | ○→ |
| ⊗ | Inductive Loop Detector | ⊗ |
| ⊗ | Controller & Cabinet | ⊗ |
| □ | Junction Box | □ |
| --- | 2-in Underground Conduit | --- |
| N/A | Right of Way | --- |
| → | Directional Arrow | → |
| N/A | Guardrail | --- |
| ○→ | Metal Pole with Mastarm | ○→ |
| ○→ | No U-Turn Sign (R3-4) | ○→ |
| ○ | Type II Signal Pedestal | ○ |
| --- | Directional Drill | N/A |

New Installation - Temporary Design (TMP Phase III Step 3)

RK&K
P: (919) 878-9550
8001 Six Forks Road Suite 700 | Raleigh, North Carolina 27615-2965
NC License No. F-0112
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Prepared for the Offices of:
TRANSPORTATION MOBILITY AND SAFETY DIVISION
DEPARTMENT OF TRANSPORTATION
SIGNAL DESIGN SECTION

US 158 WB (Reidsville Rd.) at SR 1965 (Belews Creek Rd.)
Division 9 Forsyth County Walkertown
PLAN DATE: February 2024 REVIEWED BY: WP Erickson-Jones
PREPARED BY: H Townsend REVIEWED BY:

REVISIONS	INIT.	DATE

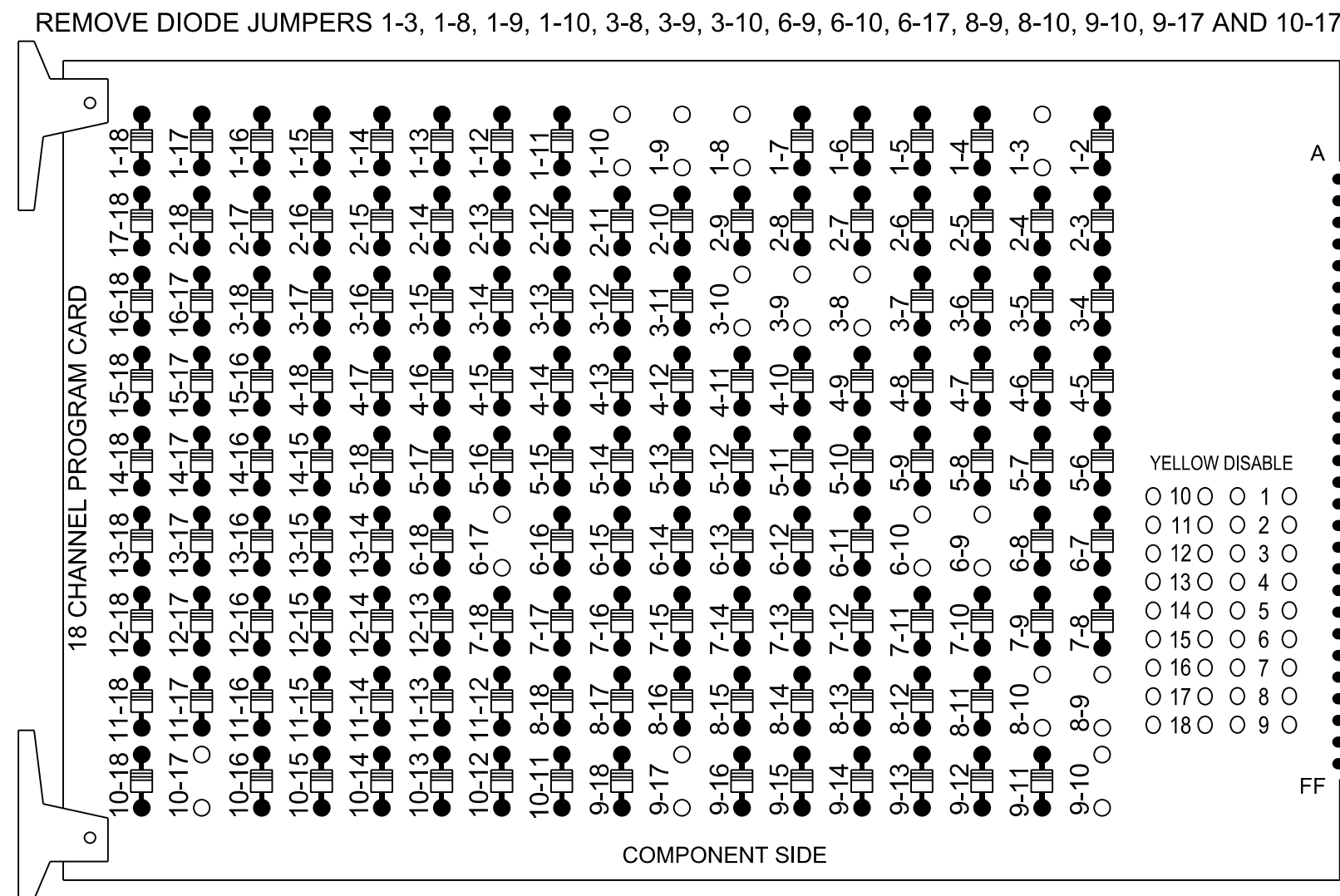
DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED

SEAL
NORTH CAROLINA PROFESSIONAL ENGINEER
SEAL 056142
W. PORTER JONES
Porter Jones
2/12/2024
SIG. INVENTORY NO. 09-0985T

2/12/2024 R:\Traffic\c451\gna1\40as1\gna1\40as1\sig_dgn_XXXXXX.dgn wp1.dwg

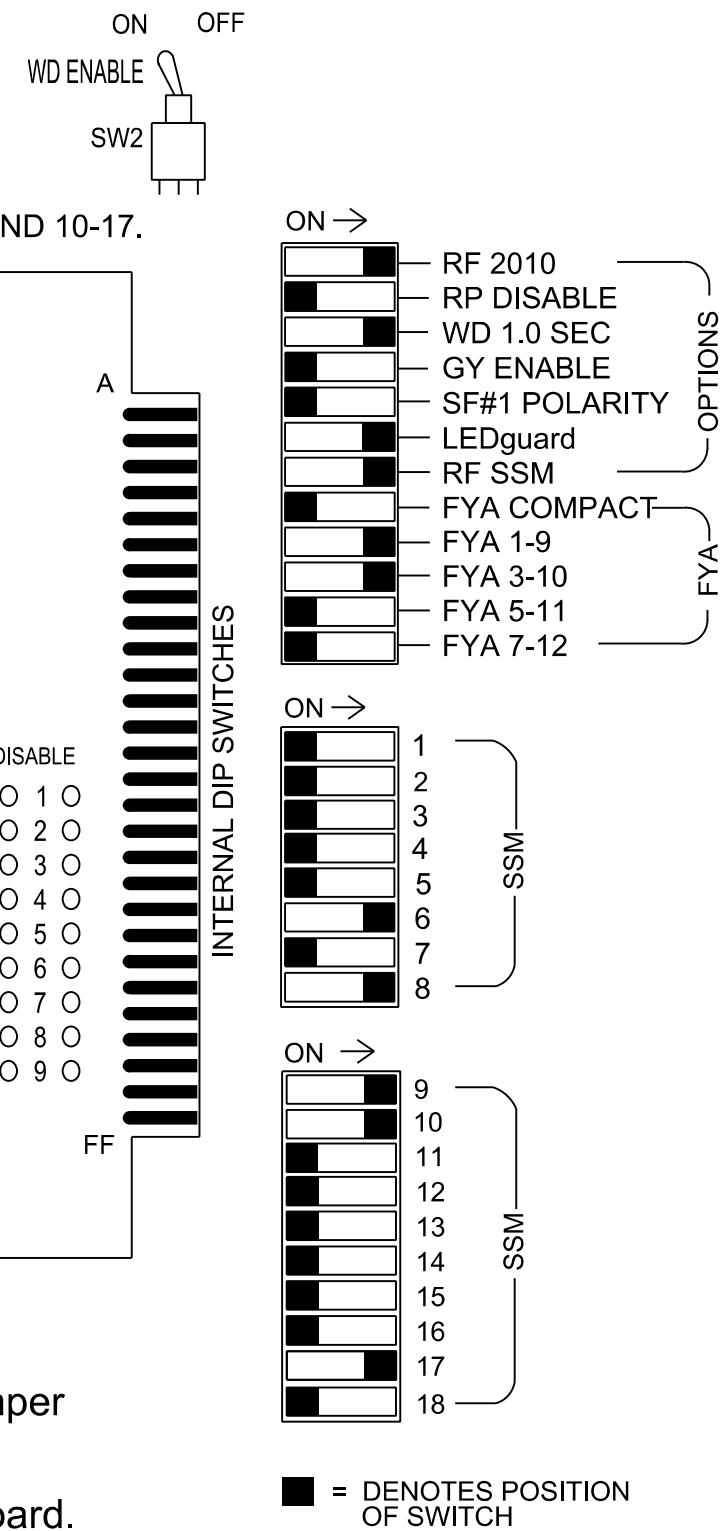
18 CHANNEL IP CONFLICT MONITOR PROGRAMMING DETAIL

(remove jumpers and set switches as shown)



NOTES:

1. Card is provided with all diode jumpers in place. Removal of any jumper allows its channels to run concurrently.
2. Ensure jumpers SEL2-SEL5 and SEL9 are present on the monitor board.
3. Ensure that the Red Enable is active at all times during normal operation.
4. Connect serial cable from conflict monitor to comm. port 1 of 2070 controller. Ensure conflict monitor communicates with 2070.



NOTES

1. To prevent "flash-conflict" problems, insert red flash program blocks for all unused vehicle load switches in the output file. The installer shall verify that signal heads flash in accordance with the signal plan.
2. Program controller to start up in phase 6 Green.
3. If this signal will be managed by an ATMS software, enable controller and detector logging for all detectors used at this location.

EQUIPMENT INFORMATION

Controller.....2070LX
 Cabinet.....332 w/ Aux
 Software.....Q-Free MAXTIME
 Cabinet Mount.....Base
 Output File Positions.....18 With Aux. Output File
 Load Switches Used.....S1, S4, S8, S11, AUX S1, AUX S2, AUX S3
 Phases Used.....3, 6, 8
 Overlap "1".....*
 Overlap "2".....*
 Overlap "3".....NOT USED
 Overlap "4".....NOT USED
 Overlap "5".....*
 Overlap "7".....*
 *See overlap programming detail on sheet 2.

SIGNAL HEAD HOOK-UP CHART

LOAD SWITCH NO.	S1	S2	S3	S4	S5	S6	S7	S8	S9	S10	S11	S12	AUX S1	AUX S2	AUX S3	AUX S4	AUX S5	AUX S6
CMU CHANNEL NO.	1	2	13	3	4	14	5	6	15	7	8	16	9	10	17	11	12	18
PHASE	OL7	2	2 PED	3	4	4 PED	5	6	6 PED	7	8	8 PED	OL1	OL2	OL5	OL3	OL4	SPARE
SIGNAL HEAD NO.	32	NU	NU	31	NU	NU	NU	61	62	NU	NU	81, 82, 83	NU	32	31	63	NU	NU
RED								134	134			107				A111		
YELLOW	*			*				135	135			108						
GREEN									136			109						
RED ARROW													A121	A124				
YELLOW ARROW													A122	A125	A112			
FLASHING YELLOW ARROW													A123	A126	A113			
GREEN ARROW	127																	

NU = Not Used
 * Denotes install load resistor. See load resistor installation detail this sheet.
 * See pictorial of head wiring in detail this sheet.

INPUT FILE POSITION LAYOUT

(front view)

FILE	1	2	3	4	5	6	7	8	9	10	11	12	13	14
U	S	S	S	S	3	S	S	S	S	S	S	S	S	S
L	S	S	S	S	3A	S	S	S	S	S	S	S	S	S
U	S	S	S	S	NOT USED	S	S	S	S	S	S	S	S	S
L	S	S	S	S	NOT USED	S	S	S	S	S	S	S	S	S

EX.: 1A, 2A, ETC. = LOOP NO.'S FS = FLASH SENSE ST = STOP TIME

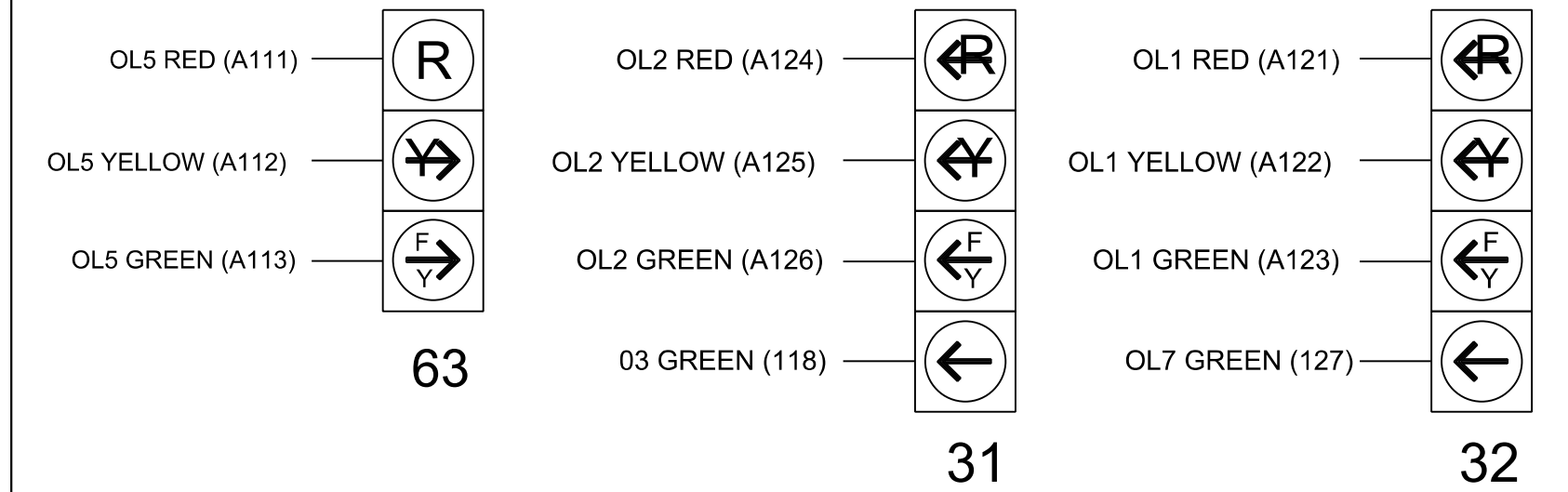
INPUT FILE CONNECTION & PROGRAMMING CHART

LOOP NO.	LOOP TERMINAL	INPUT FILE POS.	PIN NO.	INPUT POINT	DETECTOR NO.	CALL PHASE	DELAY TIME	EXTEND TIME	EXTEND	ADDED INITIAL	CALL	DELAY DURING GREEN
3A	TB4-5,6	I5U	58	20	7	*	3	15		X	X	

* For the detector to work as shown on the signal design plan, see the vehicle detector setup programming detail for alternate phasing on sheet 2.

FYA SIGNAL WIRING DETAIL

(wire signal heads as shown)

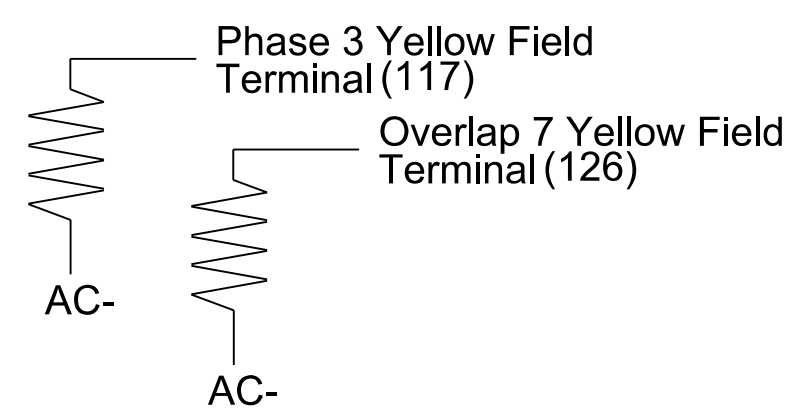


THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 09-0985T
 DESIGNED: February 2024
 SEALED: February 12, 2024
 REVISED:

LOAD RESISTOR INSTALLATION DETAIL

(install resistors as shown)

ACCEPTABLE VALUES	
Value (ohms)	Wattage
1.5K - 1.9K	25W (min)
2.0K - 3.0K	10W (min)



SPECIAL DETECTOR NOTE

Install a video detection system for vehicle detection for zones. 3A, 6A and 8A. Perform installation according to manufacturer's directions and NCDOT engineer -approved mounting locations to accomplish the detection schemes shown on the Signal Design Plans.

For Detection Zone 3A, the equipment placement and slots are typical for a NCDOT installation. Inputs associated with these slots are compatible with time of day instructions located on sheet 2 of this electrical detail.

New Installation - Temporary Design
 (TMP Phase III Step 3) Electrical Detail - Sheet 1 of 2

Electrical and Programming Details For: Prepared for the Offices of: 750 N. Greenfield Pkwy, Garner, NC 27529 Responsive People Creative Solutions	US 158 WB (Reidsville Rd.) at SR 1965 (Belews Creek Rd.)		DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED DocuSigned by Porter Jones 2/12/2024
	Division 9 PLAN DATE: February 2024 PREPARED BY: WP Erickson-Jones	Forsyth County near Winston-Salem REVIEWED BY: DT Sears REVIEWED BY:	

MAXTIME OVERLAP PROGRAMMING DETAIL FOR DEFAULT PHASING

Front Panel
Main Menu >Controller >Overlap >Overlap Parameters/Overlap Timings

Web Interface
Home >Controller >Overlap Configuration >Overlaps

Overlap Plan 1

Overlap	1	2	5	7
Type	FYA 4 - Section	FYA 4 - Section	FYA 4 - Section	Normal
Included Phases	6	6	6	3
Modifier Phases	-	3	-	-
Modifier Overlaps	7	-	-	-
Trail Green	0	0	0	0
Trail Yellow	0.0	0.0	0.0	0.0
Trail Red	0.0	0.0	0.0	0.0

OUTPUT CHANNEL CONFIGURATION

Front Panel
Main Menu >Controller >More>Channels>Channels Config

Web Interface
Home >Controller >Advanced IO>Channels>Channels Configuration

Channel Configuration

Channel	Control Type	Control Source	Flash Yellow	Flash Red	Flash Alt	MMU Channel
1	Overlap	7	X	-	X	1
2	Phase Vehicle	2	X	-	-	2
3	Phase Vehicle	3	X	-	X	3
4	Phase Vehicle	4	-	X	-	4
5	Phase Vehicle	5	-	X	-	5
6	Phase Vehicle	6	X	-	X	6
7	Phase Vehicle	7	-	X	-	7
8	Phase Vehicle	8	-	X	-	8
9	Overlap	1	X	-	-	9
10	Overlap	2	X	-	-	10
11	Overlap	3	X	-	-	11
12	Overlap	4	-	X	-	12
13	Phase Ped	2	-	-	-	13
14	Phase Ped	4	-	-	-	14
15	Phase Ped	6	-	-	-	15
16	Phase Ped	8	-	-	-	16
17	Overlap	5	X	-	X	17
18	Overlap	6	-	X	-	18

NOTICE OVERLAP 7 ASSIGNED TO CHANNEL 1.

NOTICE CHANNEL 1 YELLOW FLASH

NOTICE CHANNEL 3 YELLOW FLASH

NOTICE CHANNEL 10 YELLOW FLASH

NOTICE CHANNEL 17 YELLOW FLASH

MAXTIME ALTERNATE PHASING ACTIVATION DETAIL

To run alternate phasing, select a Pattern that is programmed to run Overlap Plan 2 and Detector Plan 2. A Pattern can be selected through the scheduler or manually by changing the Operational Mode.

PHASING	OVERLAP PLAN	VEH DET PLAN
ACTIVE PLAN REQUIRED TO RUN DEFAULT PHASING	1	1
ACTIVE PLAN REQUIRED TO RUN ALTERNATE PHASING	2	2

ALTERNATE PHASING CHANGE SUMMARY

THE FOLLOWING IS A SUMMARY OF WHAT TAKES PLACE WHEN OVERLAP PLAN 2 AND VEHICLE DETECTOR PLAN 2 ACTIVATE TO CALL THE "ALTERNATE PHASING":

OVERLAP PLAN 2: Modifies overlap included phases for heads 31 and 32 to run protected turns only.

VEH DET PLAN 2: Reduces delay time for phase 3 call on loop 3A to 0 seconds.

MAXTIME OVERLAP PROGRAMMING DETAIL FOR ALTERNATE PHASING

Front Panel
Main Menu >Controller >Overlap >Overlap Parameters/Overlap Timings

Web Interface
Home >Controller >Overlap Configuration >Overlaps

In the table view of the web interface, right click on "Overlap" in the top left corner of the table. Copy the entire contents of Overlap Plan 1. Paste Overlap Plan 1 into Overlap Plan 2. Modify Overlap Plan 2 as shown below and save changes.

Overlap Plan 2

Overlap	1	2	5	7
Type	FYA 4 - Section	FYA 4 - Section	FYA 4 - Section	Normal
Included Phases	-	-	6	3
Modifier Phases	-	3	-	-
Modifier Overlaps	7	-	-	-
Trail Green	0	0	0	0
Trail Yellow	0.0	0.0	0.0	0.0
Trail Red	0.0	0.0	0.0	0.0

NOTICE INCLUDED PHASE

MAXTIME DETECTOR PROGRAMMING DETAIL FOR ALTERNATE PHASING LOOP 3A

Front Panel
Main Menu >Controller >Detector >Veh Det Plans

Web Interface
Home >Controller >Detector Configuration >Vehicle Detectors

In the table view of web interface right click on "Detector" in the top left corner of the table. Copy the entire contents of Detector Plan 1. Paste Detector Plan 1 into Detector Plan 2. Modify Detector Plan 2 as shown below and save changes.

Plan 2

Detector	Call Phase	Delay
7	3	0

FLASHER CIRCUIT MODIFICATION DETAIL

IN ORDER TO INSURE THAT SIGNALS FLASH CONCURRENTLY ON THE SAME APPROACH, MAKE THE FOLLOWING FLASHER CIRCUIT CHANGES:

- ON REAR OF PDA - REMOVE WIRE FROM TERM. T2-4 AND TERMINATE ON T2-2.
- ON REAR OF PDA - REMOVE WIRE FROM TERM. T2-5 AND TERMINATE ON T2-3.
- REMOVE FLASHER UNIT 2.

THE CHANGES LISTED ABOVE TIES ALL PHASES AND OVERLAPS TO FLASHER UNIT 1.

MAXTIME ALTERNATE PHASING PATTERN PROGRAMMING DETAIL

Front Panel
Main Menu >Controller >Coordination >Patterns

Web Interface
Home >Controller >Coordination >Patterns

Pattern Parameters

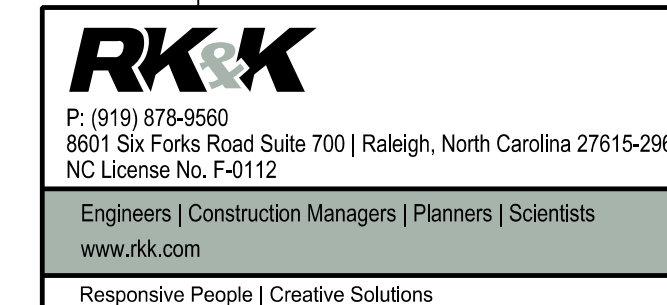
Pattern	Veh Det Plan	Overlap Plan
*	2	2

* The Pattern number(s) are to be determined by the Division and/or City Traffic Engineer.

THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 09-0985T
DESIGNED: February 2024
SEALED: February 12, 2024
REVISED:

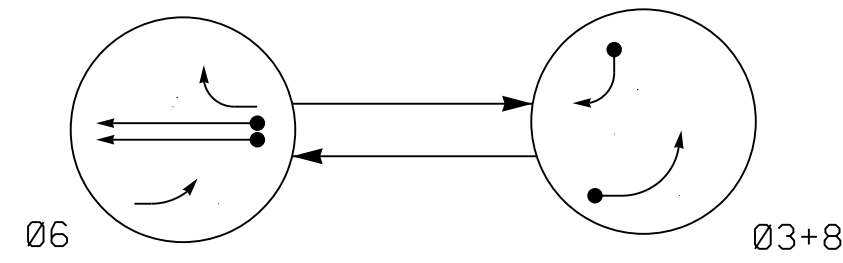
New Installation - Temporary Design
(TMP Phase III Step 3) Electrical Detail - Sheet 2 of 2

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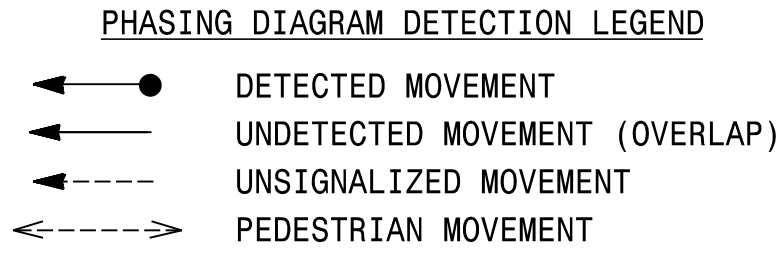
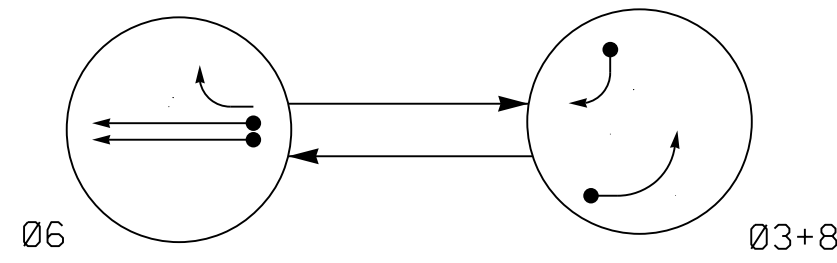


<p>Electrical and Programming Details For:</p> <p>Prepared for the Offices of:</p>	<p>US 158 WB (Reidsville Rd.) at SR 1965 (Belews Creek Rd.)</p> <p>Division 9 Forsyth County Walkertown</p> <p>PLAN DATE: February 2024 REVIEWED BY: DT Sears</p> <p>PREPARED BY: WP Erickson-Jones REVIEWED BY:</p> <table border="1"> <thead> <tr> <th>REVISIONS</th> <th>INIT.</th> <th>DATE</th> </tr> </thead> <tbody> <tr> <td> </td> <td> </td> <td> </td> </tr> </tbody> </table> <p>DocuSigned by: Porter Jones 2/12/2024</p> <p>SIG. INVENTORY NO. 09-0985T</p>	REVISIONS	INIT.	DATE			
REVISIONS	INIT.	DATE					

DEFAULT PHASING DIAGRAM



ALTERNATE PHASING DIAGRAM



DEFAULT PHASING TABLE OF OPERATION

SIGNAL FACE	PHASE		
	Ø	3+8	F
31,32	E Y	R Y	Y
61	↑	R	Y
62	G	R	Y
63	E Y	R	Y
81, 82, 83	R	-	R

ALTERNATE PHASING TABLE OF OPERATION

SIGNAL FACE	PHASE		
	Ø	3+8	F
31,32	R	-	Y
61	↑	R	Y
62	G	R	Y
63	E Y	R	Y
81, 82, 83	R	-	R

MAXTIME DETECTOR INSTALLATION CHART

LOOP	SIZE (FT)	DISTANCE FROM STOPBAR (FT)	TURNS	NEW LOOP	PROGRAMMING								
					CALL PHASE	DELAY TIME	EXTEND TIME	EXTEND INITIAL	ADDED INITIAL CALL	DELAY DURING GREEN	NEW CARD		
3A	6X40	0	2-4-2	X	3	15#	-	-	X	-	X	-	X
6A	6X6	300	5	X	6	-	-	-	X	X	X	-	X
6B	6X6	300	5	X	6	-	-	-	X	X	X	-	X
8A	6X40	0	2-4-2	X	8	15	-	-	X	-	X	-	X

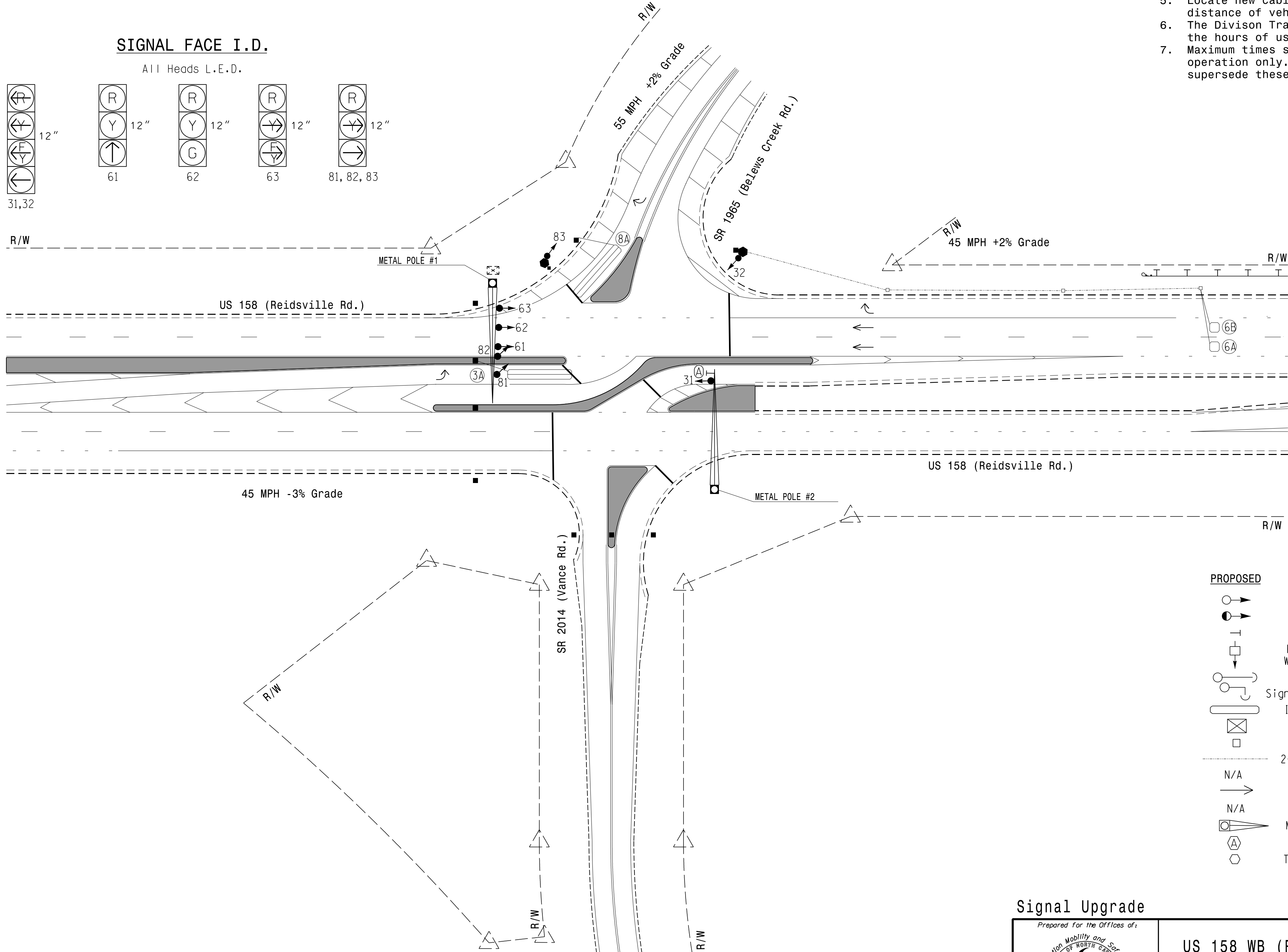
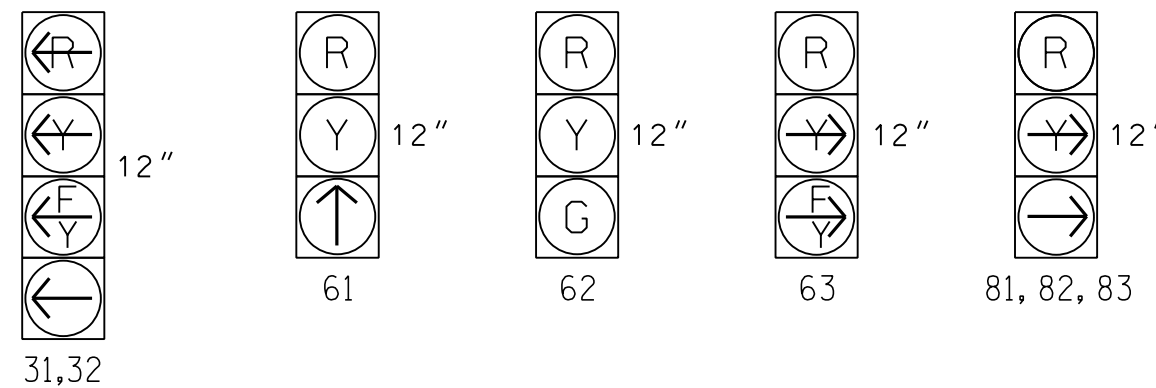
Disable Delay During Alternate Phasing Operation.

2 Phase Fully Actuated (US 158 Signal System)
Signal System #: D09-11_Winston-Salem

- NOTES**
1. Refer to "Roadway Standard Drawings NCDOT" dated January 2024 and "Standard Specifications for Roads and Structures" dated January 2024.
 2. Do not program signal for late night flashing operation unless otherwise directed by the Engineer.
 3. Reposition existing signal heads numbered 61, 62, 63, 81 and 82.
 4. Set all detector units to presence mode.
 5. Locate new cabinet so as not to obstruct sight distance of vehicles turning right on red.
 6. The Division Traffic Engineer will determine the hours of use for each phasing plan.
 7. Maximum times shown in timing chart are for free-run operation only. Coordinated signal system timing values supersede these values.

SIGNAL FACE I.D.

All Heads L.E.D.

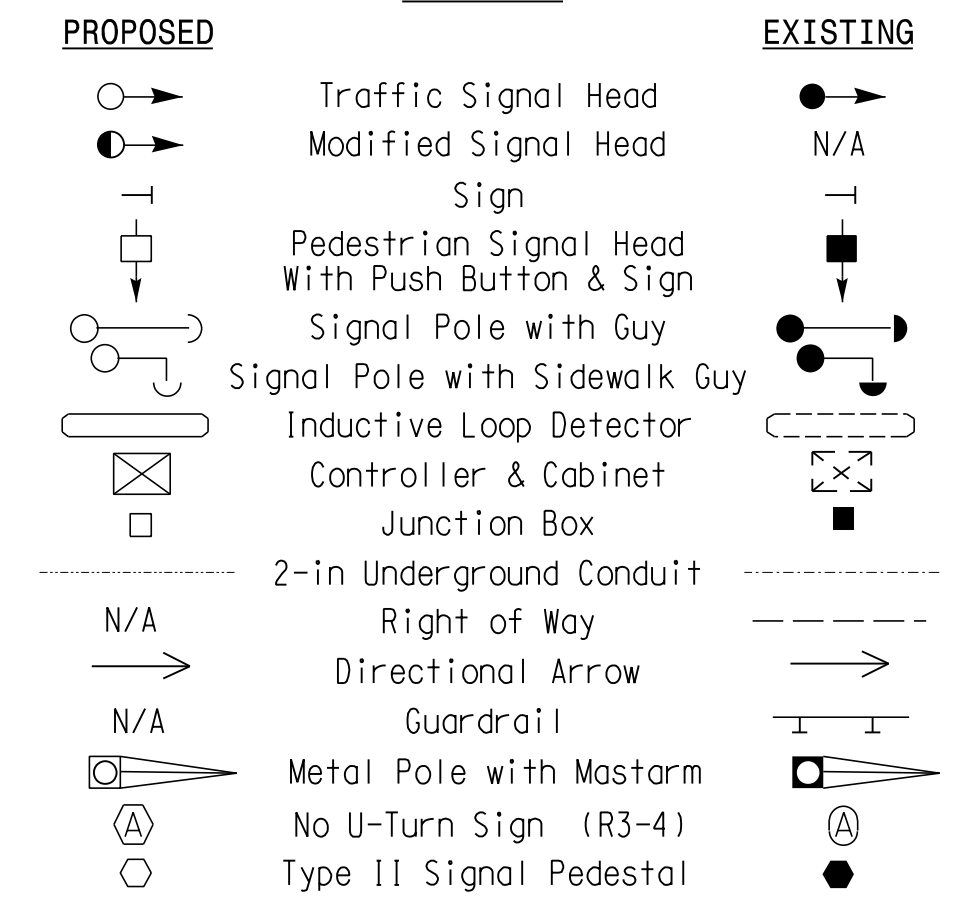


MAXTIME TIMING CHART

FEATURE	PHASE		
	3	6	8
Walk *	-	-	-
Ped Clear *	-	-	-
Min Green	7	12	7
Passage *	2.0	6.0	2.0
Max I *	30	90	30
Yellow Change	3.0	4.3	3.0
Red Clear	2.3	1.6	2.3
Added Initial *	-	1.5	-
Maximum Initial *	-	34	-
Time Before Reduction *	-	15	-
Time To Reduce *	-	30	-
Minimum Gap	-	3.0	-
Advance Walk	-	-	-
Non Lock Detector	X	-	X
Vehicle Recall	-	MIN RECALL	-
Dual Entry	X	-	X

* These values may be field adjusted. Do not adjust Min Green and Extension times for phase 6 lower than what is shown. Min Green for all other phases should not be lower than 4 seconds.

LEGEND



Signal Upgrade

Prepared for the Offices of:

Transportation Mobility and Safety Solutions
SCHOOL OF TRANSPORTATION ENGINEERING
UNIVERSITY OF NORTH CAROLINA AT RALEIGH
SIGNAL DESIGN SECTION

US 158 WB (Reidsville Rd.) at SR 1965 (Belews Creek Rd.)
Division 9 Forsyth County Walkertown
PLAN DATE: February 2024 REVIEWED BY: WP Erickson-Jones
PREPARED BY: H Townsend REVIEWED BY: [Signature]

REVISIONS: [Table with columns for REVISIONS, INIT., DATE]

SCALE: 1" = 40'

DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED

SEAL: W. PORTER JONES, ENGINEER, No. 056142, State of North Carolina

DocuSigned by: Porter Jones, DATE: 2/12/2024
SIGNATURE: [Signature], DATE: [Date]
SIG. INVENTORY NO. 09-0985

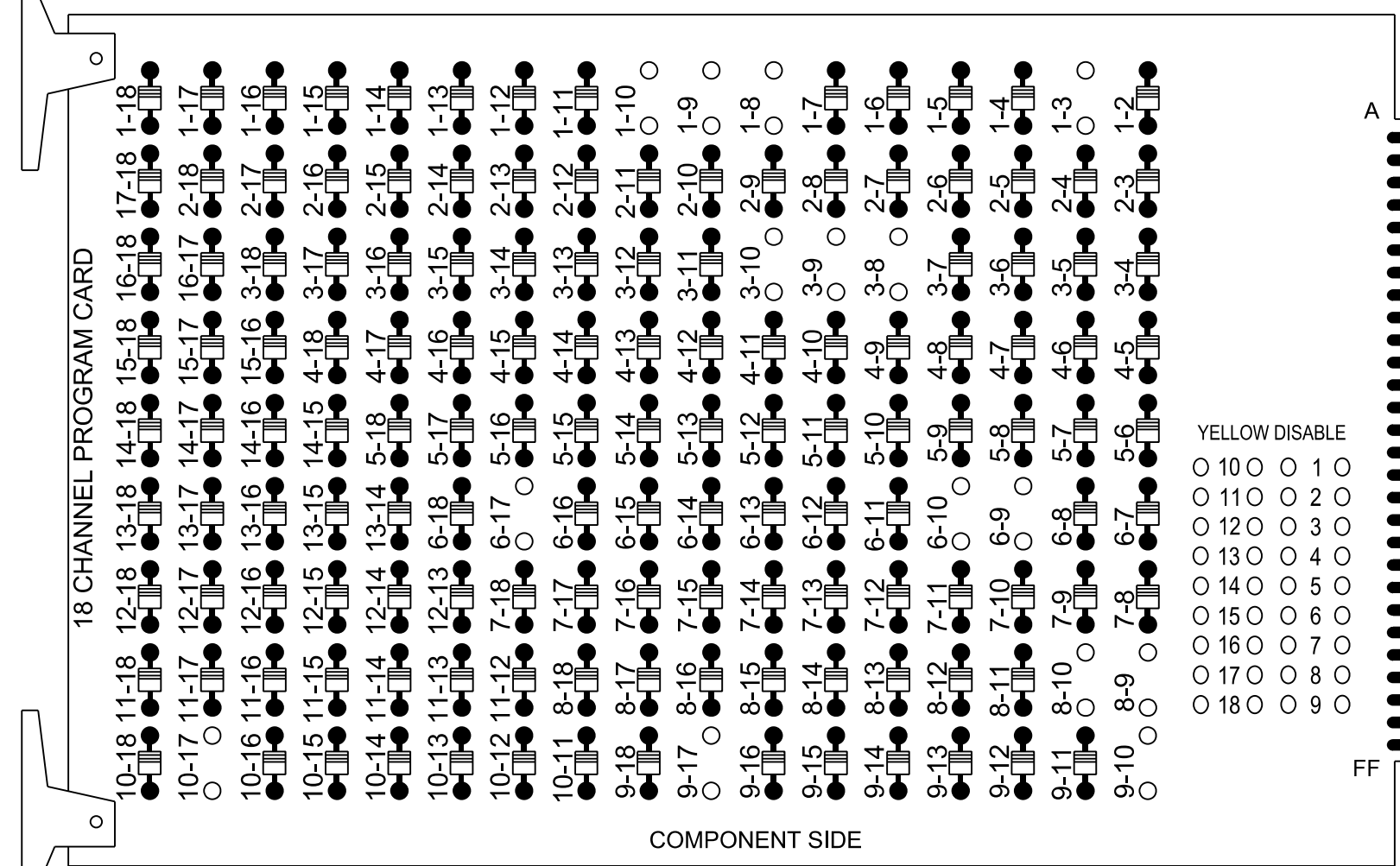
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2/12/2024 R:\Traffic\c481\gna1\04as1\gna4\1\gna1\04090985_1.s1g.dgn...XXXXXX.dgn wp1ones

18 CHANNEL IP CONFLICT MONITOR PROGRAMMING DETAIL

(remove jumpers and set switches as shown)

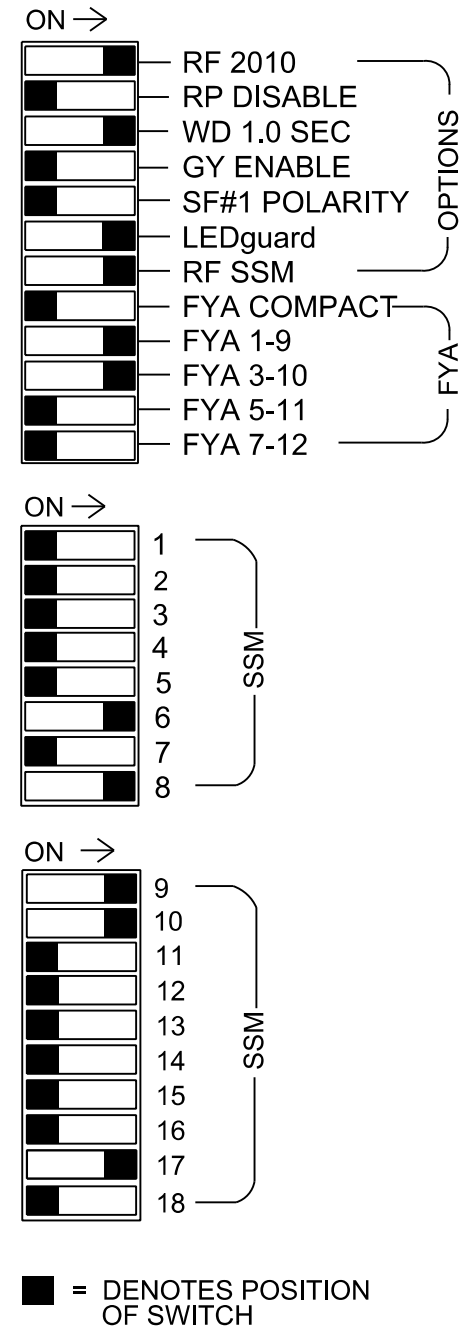
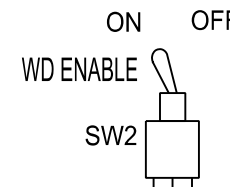
REMOVE DIODE JUMPERS 1-3, 1-8, 1-9, 1-10, 3-8, 3-9, 3-10, 6-9, 6-10, 6-17, 8-9, 8-10, 9-10, 9-17 AND 10-17.



REMOVE JUMPERS AS SHOWN

NOTES:

1. Card is provided with all diode jumpers in place. Removal of any jumper allows its channels to run concurrently.
2. Ensure jumpers SEL2-SEL5 and SEL9 are present on the monitor board.
3. Ensure that the Red Enable is active at all times during normal operation.
4. Connect serial cable from conflict monitor to comm. port 1 of 2070 controller. Ensure conflict monitor communicates with 2070.



NOTES

1. To prevent "flash-conflict" problems, insert red flash program blocks for all unused vehicle load switches in the output file. The installer shall verify that signal heads flash in accordance with the signal plan.
2. Program controller to start up in phase 6 Green No Walk.
3. If this signal will be managed by an ATMS software, enable controller and detector logging for all detectors used at this location.
4. The cabinet and controller are part of the US 158 Signal System, Signal System: D09-11_Winston-Salem System.

EQUIPMENT INFORMATION

Controller.....2070LX
 Cabinet.....332 w/ Aux
 Software.....Q-Free MAXTIME
 Cabinet Mount.....Base
 Output File Positions.....18 With Aux. Output File
 Load Switches Used.....S1, S4, S8, S11, AUX S1, AUX S2, AUX S3
 Phases Used.....3, 6, 8
 Overlap "1".....*
 Overlap "2".....*
 Overlap "3".....NOT USED
 Overlap "4".....NOT USED
 Overlap "5".....*
 Overlap "7".....*

*See overlap programming detail on sheet 2.

SIGNAL HEAD HOOK-UP CHART

LOAD SWITCH NO.	S1	S2	S3	S4	S5	S6	S7	S8	S9	S10	S11	S12	AUX S1	AUX S2	AUX S3	AUX S4	AUX S5	AUX S6
CMU CHANNEL NO.	1	2	13	3	4	14	5	6	15	7	8	16	9	10	17	11	12	18
PHASE	OL7	2	2 PED	3	4	4 PED	5	6	6 PED	7	8	8 PED	OL1	OL2	OL5	OL3	OL4	SPARE
SIGNAL HEAD NO.	32	NU	NU	31	NU	NU	NU	61	62	NU	NU	81, 82, 83	NU	32	31	63	NU	NU
RED								134	134			107				A111		
YELLOW	*			*				135	135			108						
GREEN									136			109						
RED ARROW													A121	A124				
YELLOW ARROW													A122	A125	A112			
FLASHING YELLOW ARROW													A123	A126	A113			
GREEN ARROW	127																	

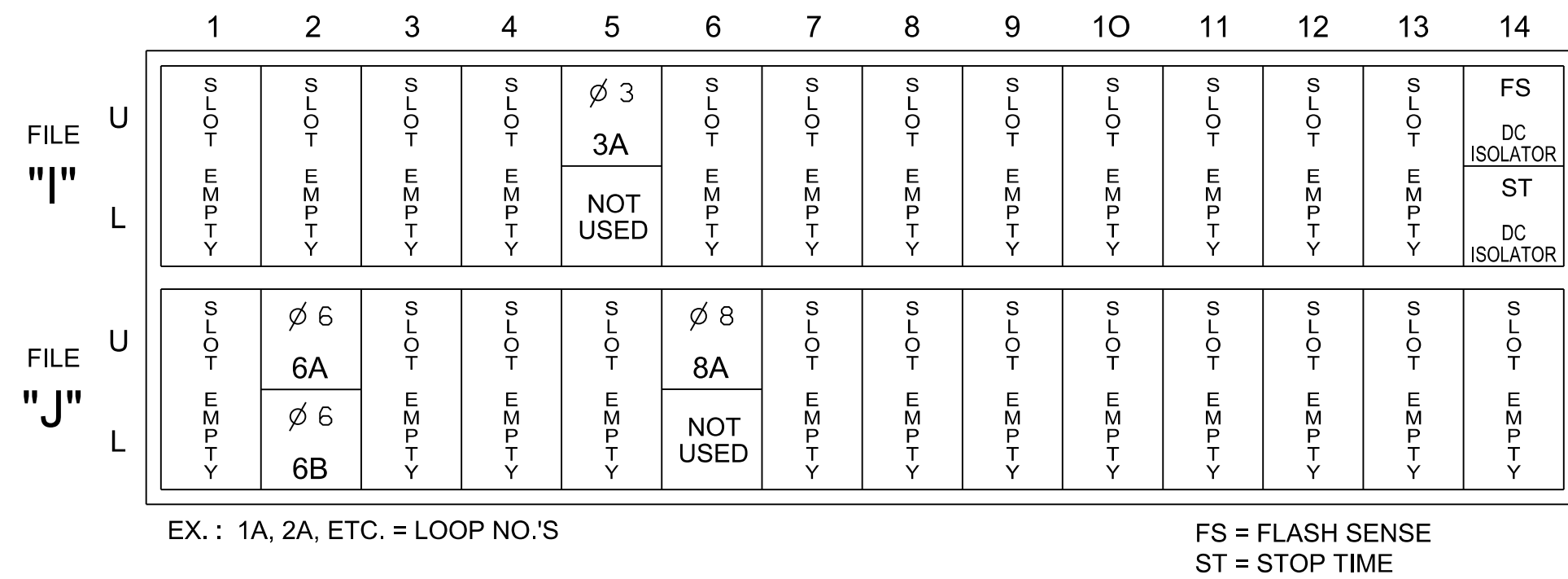
NU = Not Used

*Denotes Install load resistor. See load resistor installation detail this sheet.

*See pictorial of head wiring in detail this sheet.

INPUT FILE POSITION LAYOUT

(front view)



EX.: 1A, 2A, ETC. = LOOP NO.'S

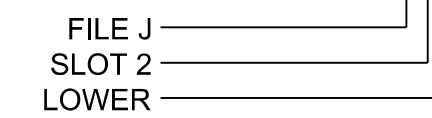
FS = FLASH SENSE
ST = STOP TIME

INPUT FILE CONNECTION & PROGRAMMING CHART

LOOP NO.	LOOP TERMINAL	INPUT FILE POS.	PIN NO.	INPUT POINT	DETECTOR NO.	CALL PHASE	DELAY TIME	EXTEND TIME	EXTEND	ADDED INITIAL	CALL	DELAY DURING GREEN
3A	TB4-5,6	I5U	58	20	7	3	15		X		X	
6A	TB3-5,6	J2U	40	2	16	6			X	X	X	
6B	TB3-7,8	J2L	44	6	17	6			X	X	X	
8A	TB5-9,10	J6U	42	4	22	8	15		X		X	

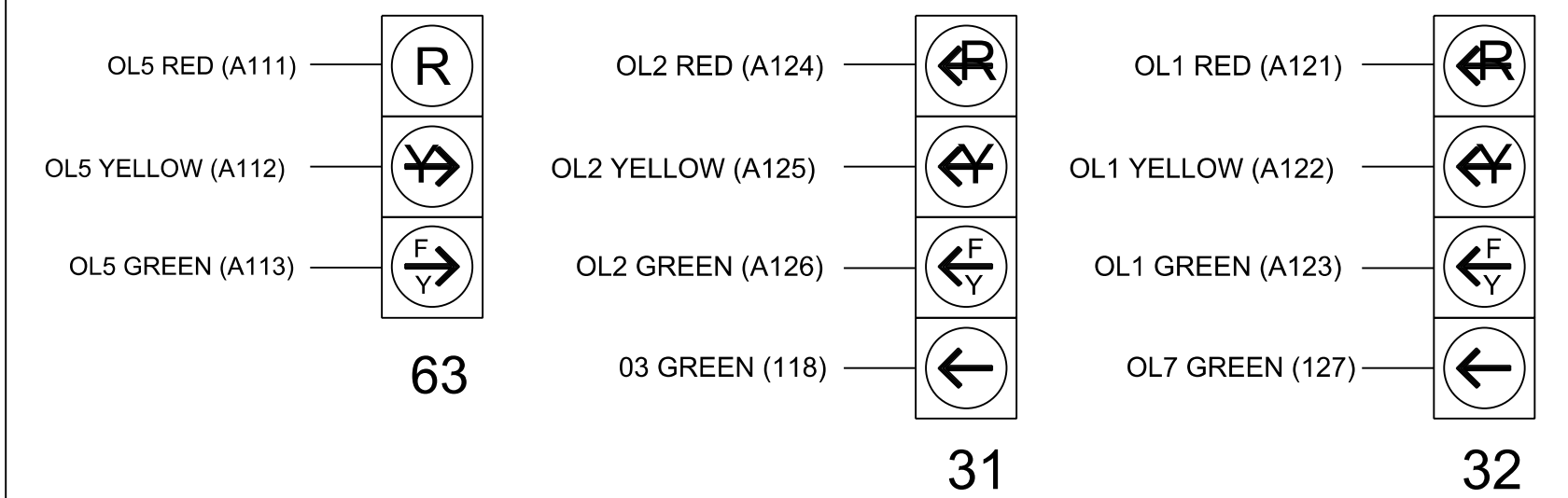
* For the detector to work as shown on the signal design plan, see the vehicle detector setup programming detail for alternate phasing on sheet 2.

INPUT FILE POSITION LEGEND: J2L



FYA SIGNAL WIRING DETAIL

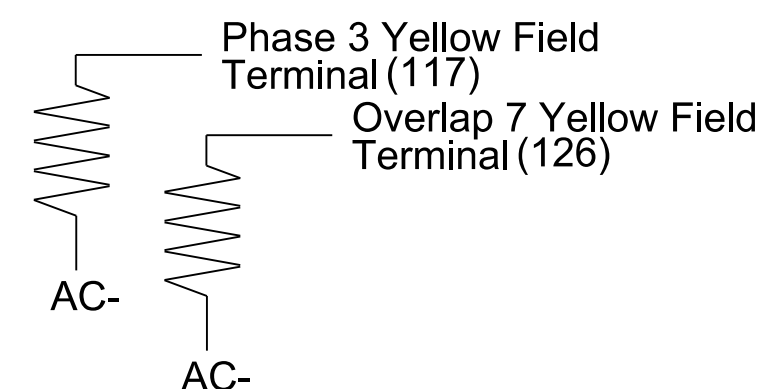
(wire signal heads as shown)



LOAD RESISTOR INSTALLATION DETAIL

(install resistors as shown)

ACCEPTABLE VALUES	
Value (ohms)	Wattage
1.5K - 1.9K	25W (min)
2.0K - 3.0K	10W (min)



Signal Upgrade - Final Design - Electrical Detail - Sheet 1 of 2

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Electrical and Programming Details For:

Prepared for the Offices of:

US 158 WB (Reidsville Rd.) at SR 1965 (Belews Creek Rd.)

Division 9 Forsyth County Walkertown

PLAN DATE: February 2024 REVIEWED BY: DT Sears

PREPARED BY: WP Erickson-Jones REVIEWED BY:

REVISIONS: INIT. DATE

750 N. Greenfield Pkwy, Garner, NC 27529

SEAL: PORTER JONES, ENGINEER, No. 056142

DocuSigned by: Porter Jones, 2/12/2024

SIG. INVENTORY NO. 09-0985

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MAXTIME OVERLAP PROGRAMMING DETAIL FOR DEFAULT PHASING

Front Panel
Main Menu >Controller >Overlap >Overlap Parameters/Overlap Timings

Web Interface
Home >Controller >Overlap Configuration >Overlaps

Overlap Plan 1

Overlap	1	2	5	7
Type	FYA 4 - Section	FYA 4 - Section	FYA 4 - Section	Normal
Included Phases	6	6	6	3
Modifier Phases	-	3	-	-
Modifier Overlaps	7	-	-	-
Trail Green	0	0	0	0
Trail Yellow	0.0	0.0	0.0	0.0
Trail Red	0.0	0.0	0.0	0.0

OUTPUT CHANNEL CONFIGURATION

Front Panel
Main Menu >Controller >More>Channels>Channels Config

Web Interface
Home >Controller >Advanced IO>Channels>Channels Configuration

Channel Configuration

Channel	Control Type	Control Source	Flash Yellow	Flash Red	Flash Alt	MMU Channel
1	Overlap	7	X	-	X	1
2	Phase Vehicle	2	X	-	-	2
3	Phase Vehicle	3	X	-	X	3
4	Phase Vehicle	4	-	X	-	4
5	Phase Vehicle	5	-	X	-	5
6	Phase Vehicle	6	X	-	X	6
7	Phase Vehicle	7	-	X	-	7
8	Phase Vehicle	8	-	X	-	8
9	Overlap	1	X	-	-	9
10	Overlap	2	X	-	-	10
11	Overlap	3	X	-	-	11
12	Overlap	4	-	X	-	12
13	Phase Ped	2	-	-	-	13
14	Phase Ped	4	-	-	-	14
15	Phase Ped	6	-	-	-	15
16	Phase Ped	8	-	-	-	16
17	Overlap	5	X	-	X	17
18	Overlap	6	-	X	-	18

NOTICE OVERLAP 7
ASSIGNED TO
CHANNEL 1.

NOTICE CHANNEL 1
YELLOW FLASH
NOTICE CHANNEL 3
YELLOW FLASH

NOTICE CHANNEL 10
YELLOW FLASH

NOTICE CHANNEL 17
YELLOW FLASH

MAXTIME ALTERNATE PHASING ACTIVATION DETAIL

To run alternate phasing, select a Pattern that is programmed to run Overlap Plan 2 and Detector Plan 2. A Pattern can be selected through the scheduler or manually by changing the Operational Mode.

PHASING	OVERLAP PLAN	VEH DET PLAN
ACTIVE PLAN REQUIRED TO RUN DEFAULT PHASING	1	1
ACTIVE PLAN REQUIRED TO RUN ALTERNATE PHASING	2	2

ALTERNATE PHASING CHANGE SUMMARY

THE FOLLOWING IS A SUMMARY OF WHAT TAKES PLACE WHEN OVERLAP PLAN 2 AND VEHICLE DETECTOR PLAN 2 ACTIVATE TO CALL THE "ALTERNATE PHASING":

OVERLAP PLAN 2: Modifies overlap included phases for heads 31 and 32 to run protected turns only.

VEH DET PLAN 2: Reduces delay time for phase 3 call on loop 3A to 0 seconds.

MAXTIME OVERLAP PROGRAMMING DETAIL FOR ALTERNATE PHASING

Front Panel
Main Menu >Controller >Overlap >Overlap Parameters/Overlap Timings

Web Interface
Home >Controller >Overlap Configuration >Overlaps

In the table view of the web interface, right click on "Overlap" in the top left corner of the table. Copy the entire contents of Overlap Plan 1. Paste Overlap Plan 1 into Overlap Plan 2. Modify Overlap Plan 2 as shown below and save changes.

Overlap Plan 2

Overlap	1	2	5	7
Type	FYA 4 - Section	FYA 4 - Section	FYA 4 - Section	Normal
Included Phases	-	-	6	3
Modifier Phases	-	3	-	-
Modifier Overlaps	7	-	-	-
Trail Green	0	0	0	0
Trail Yellow	0.0	0.0	0.0	0.0
Trail Red	0.0	0.0	0.0	0.0

NOTICE INCLUDED PHASE

MAXTIME DETECTOR PROGRAMMING DETAIL FOR ALTERNATE PHASING LOOP 3A

Front Panel
Main Menu >Controller >Detector >Veh Det Plans

Web Interface
Home >Controller >Detector Configuration >Vehicle Detectors

In the table view of web interface right click on "Detector" in the top left corner of the table. Copy the entire contents of Detector Plan 1. Paste Detector Plan 1 into Detector Plan 2. Modify Detector Plan 2 as shown below and save changes.

Plan 2

Detector	Call Phase	Delay
3A	7	3
		0

FLASHER CIRCUIT MODIFICATION DETAIL

IN ORDER TO INSURE THAT SIGNALS FLASH CONCURRENTLY ON THE SAME APPROACH, MAKE THE FOLLOWING FLASHER CIRCUIT CHANGES:

- ON REAR OF PDA - REMOVE WIRE FROM TERM. T2-4 AND TERMINATE ON T2-2.
- ON REAR OF PDA - REMOVE WIRE FROM TERM. T2-5 AND TERMINATE ON T2-3.
- REMOVE FLASHER UNIT 2.

THE CHANGES LISTED ABOVE TIES ALL PHASES AND OVERLAPS TO FLASHER UNIT 1.

MAXTIME ALTERNATE PHASING PATTERN PROGRAMMING DETAIL

Front Panel
Main Menu >Controller >Coordination >Patterns

Web Interface
Home >Controller >Coordination >Patterns

Pattern Parameters

Pattern	Veh Det Plan	Overlap Plan
*	2	2

* The Pattern number(s) are to be determined by the Division and/or City Traffic Engineer.

THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 09-0985
DESIGNED: February 2024
SEALED: February 12, 2024
REVISED:

Signal Upgrade - Final Design - Electrical Detail - Sheet 2 of 2

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Seal of Porter Jones, Professional Engineer, No. 056142

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