PROJECT REFERENCE NO.	SHEET NO.
R-2577A	Sig. 32.2

MAXTIME DETECTOR PROGRAMMING DETAIL FOR ALTERNATE PHASING LOOPS 3A & 7A

Front Panel
Main Menu >Controller >Detector >Veh Det Plans

Web Interface
Home >Controller >Detector Configuration >Vehicle Detectors

In the table view of web interface right click on "Detector" in the top left corner of the table. Copy the entire contents of Detector Plan 1. Paste Detector Plan 1 into Detector Plan 2. Modify Detector Plan 2 as shown below and save changes.

Plan 2

3A

Detector	Call Phase	Delay
7	3	3
30	0	3

7A

Detector	Call Phase	Delay
21	7	3
32	0	3

MAXTIME ALTERNATE PHASING ACTIVATION DETAIL

To run alternate phasing, select a Pattern that is programmed to run Overlap Plan 2 and Detector Plan 2.
A Pattern can be selected through the scheduler or manually by changing the Operational Mode.

PHASING	OVERLAP PLAN	VEH DET PLAN
ACTIVE PLAN REQUIRED TO RUN DEFAULT PHASING	1	1
ACTIVE PLAN REQUIRED TO RUN ALTERNATE PHASING	2	2

ALTERNATE PHASING CHANGE SUMMARY

THE FOLLOWING IS A SUMMARY OF WHAT TAKES PLACE WHEN OVERLAP PLAN 2 AND VEHICLE DETECTOR PLAN 2 ACTIVATE TO CALL THE "ALTERNATE PHASING":

OVERLAP PLAN 2: Modifies overlap included phases for heads 31 and 71 to run protected turns only.

VEH DET PLAN 2: Disables phase 8 call on loop 3A and reduces delay time for phase 3 call on loop 3A to 3 seconds.

Disables phase 4 call on loop 7A and reduces delay time for phase 7 call on loop 7A to 3 seconds.

FLASHER CIRCUIT MODIFICATION DETAIL

IN ORDER TO INSURE THAT SIGNALS FLASH CONCURRENTLY ON THE SAME APPROACH, MAKE THE FOLLOWING FLASHER CIRCUIT CHANGES:

1. ON REAR OF PDA - REMOVE WIRE FROM TERM. T2-4 AND TERMINATE ON T2-2.
2. ON REAR OF PDA - REMOVE WIRE FROM TERM. T2-5 AND TERMINATE ON T2-3.
3. REMOVE FLASHER UNIT 2.

THE CHANGES LISTED ABOVE TIES ALL PHASES AND OVERLAPS TO FLASHER UNIT 1.

MAXTIME OVERLAP PROGRAMMING DETAIL FOR DEFAULT PHASING

Front Panel
Main Menu >Controller >Overlap >Overlap Parameters/Overlap Timings

Web Interface
Home >Controller >Overlap Configuration >Overlaps

Overlap Plan 1

Overlap	2	4
Type	FYA 4 - Section	FYA 4 - Section
Included Phases	4	8
Modifier Phases	3	7
Modifier Overlaps	-	-
Trail Green	0	0
Trail Yellow	0.0	0.0
Trail Red	0.0	0.0

MAXTIME OVERLAP PROGRAMMING DETAIL FOR ALTERNATE PHASING

Front Panel
Main Menu >Controller >Overlap >Overlap Parameters/Overlap Timings

Web Interface
Home >Controller >Overlap Configuration >Overlaps

In the table view of the web interface, right click on "Overlap" in the top left corner of the table. Copy the entire contents of Overlap Plan 1. Paste Overlap Plan 1 into Overlap Plan 2. Modify Overlap Plan 2 as shown below and save changes.

Overlap Plan 2

Overlap	2	4	
Type	FYA 4 - Section	FYA 4 - Section	
Included Phases	-	-	← NOTICE INCLUDED PHASE
Modifier Phases	3	7	
Modifier Overlaps	-	-	
Trail Green	0	0	
Trail Yellow	0.0	0.0	
Trail Red	0.0	0.0	

MAXTIME ALTERNATE PHASING PATTERN PROGRAMMING DETAIL

Front Panel
Main Menu >Controller >Coordination >Patterns

Web Interface
Home >Controller >Coordination >Patterns

Pattern Parameters

Pattern	Veh Det Plan	Overlap Plan
*	2	2

* The Pattern number(s) are to be determined by the Division and/or City Traffic Engineer.

THIS ELECTRICAL DETAIL IS FOR
THE SIGNAL DESIGN: 09-0264T3
DESIGNED: February 2024
SEALED: February 12, 2024
REVISED:

Signal Upgrade - Temporary Design 3
(TMP Phase III) - Electrical Detail - Sheet 2 of 2

DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED

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State of North Carolina Department of Transportation
Traffic Engineering & Signals Management Section
750 N. Greenfield Pkwy, Garner, NC 27529

US 158 (Reidsville Rd.)
at
NC 66 (Old Hollow Rd.)

Division 9 | Forsyth County | Walkertown

PLAN DATE: February 2024	REVIEWED BY: DT Sears
PREPARED BY: WP Erickson-Jones	REVIEWED BY:

REVISIONS	INIT.	DATE

SEAL
NORTH CAROLINA PROFESSIONAL SEAL 056142 ENGINEER W. PORTER JONES
DocuSigned by: Porter Jones 2/12/2024
DATE

SIG. INVENTORY NO. 09-0264T3