OUTPUT CHANNEL CONFIGURATION

Front Panel

Main Menu >Controller >More>Channels>Channels Config

Web Interface

Home >Controller >Advanced IO>Channels>Channels Configuration

Channel Configuration



FLASHER CIRCUIT MODIFICATION DETAIL

IN ORDER TO INSURE THAT SIGNALS FLASH CONCURRENTLY ON THE SAME APPROACH, MAKE THE FOLLOWING FLASHER CIRCUIT CHANGES:

- 1. ON REAR OF PDA REMOVE WIRE FROM TERM. T2-4 AND TERMINATE ON T2-2.
- 2. ON REAR OF PDA REMOVE WIRE FROM TERM. T2-5 AND TERMINATE ON T2-3.
- 3. REMOVE FLASHER UNIT 2.

THE CHANGES LISTED ABOVE TIES ALL PHASES AND OVERLAPS TO FLASHER UNIT 1.

MAXTIME ALTERNATE PHASING ACTIVATION DETAIL

To run alternate phasing, select a Pattern that is programmed to run Overlap Plan 2 and Detector Plan 2. A Pattern can be selected through the scheduler or manually by changing the Operational Mode.

PHASING	OVERLAP PLAN	VEH DET PLAN
ACTIVE PLAN REQUIRED TO RUN DEFAULT PHASING	1	1
ACTIVE PLAN REQUIRED TO RUN ALTERNATE PHASING	2	2

ALTERNATE PHASING CHANGE SUMMARY

THE FOLLOWING IS A SUMMARY OF WHAT TAKES PLACE WHEN OVERLAP 2 AND VEHICLE DETECTOR PLAN 2 ACTIVATE TO CALL THE "ALTERNATE PHASING":

TO CALL THE ALTERNATE PHASING

OVERLAP PLAN 2: Modifies overlap included phases

for head 51 to run protected turns only.

VEH DET PLAN 2: Disables phase 2 call on loop 5A

and reduces delay time for phase 5 call on loop 5A to 3 seconds.

THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 11-0944

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DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED

Electrical Detail-Sheet 3 of 3

ELECTRICAL AND PROGRAMMING

Mobility and Science Mobility

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PLAN DATE: May 2023 REVIEWED BY: M.L. Stygles
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REVISIONS INIT. DATE

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