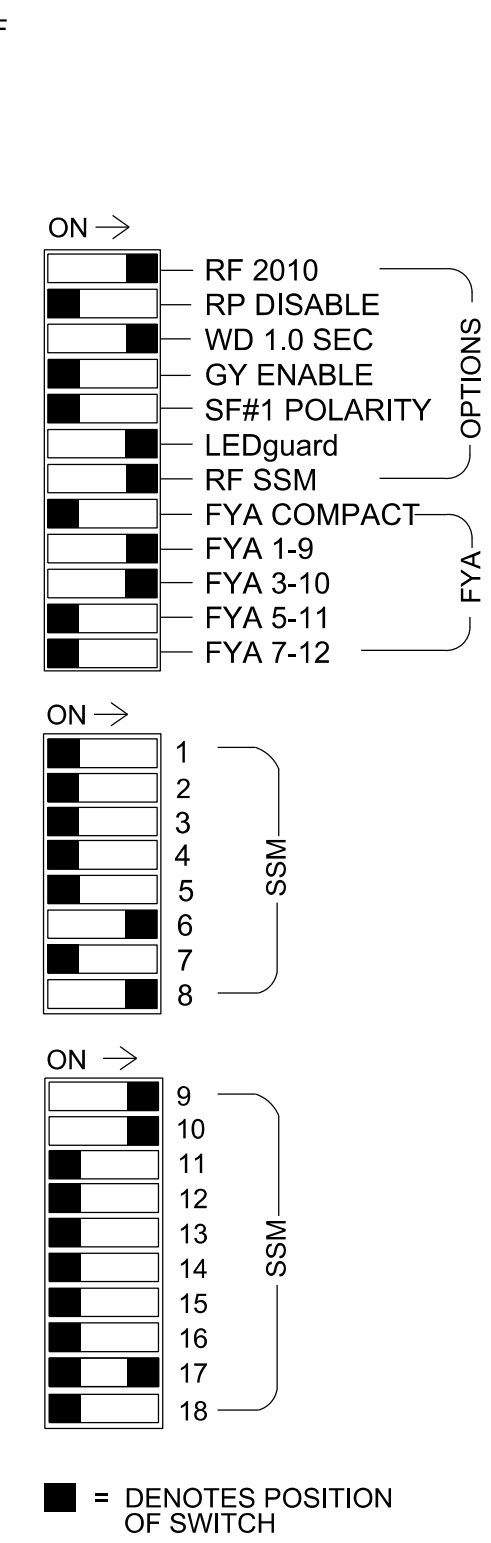
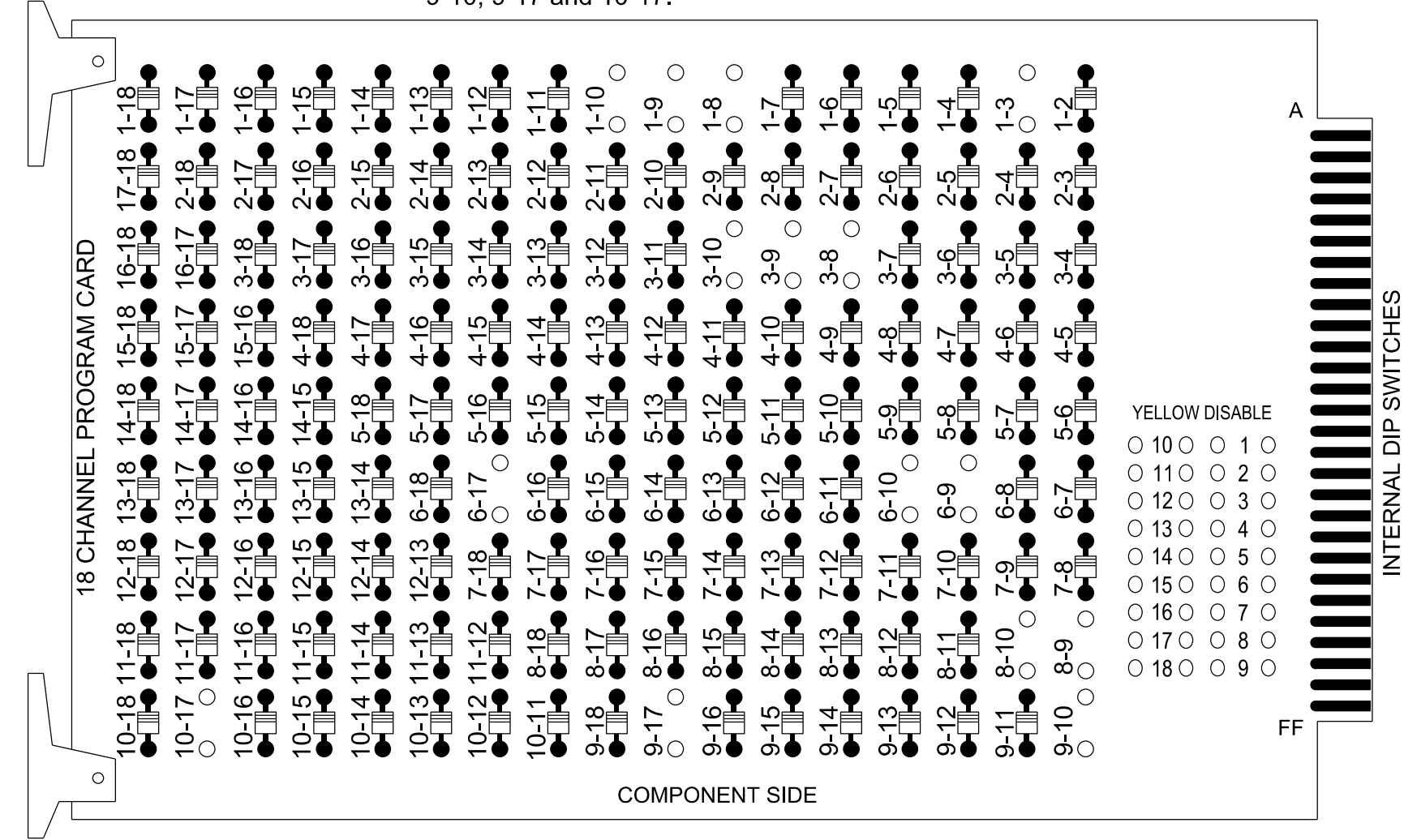


18 CHANNEL CONFLICT MONITOR PROGRAMMING DETAIL

(remove jumpers and set switches as shown)

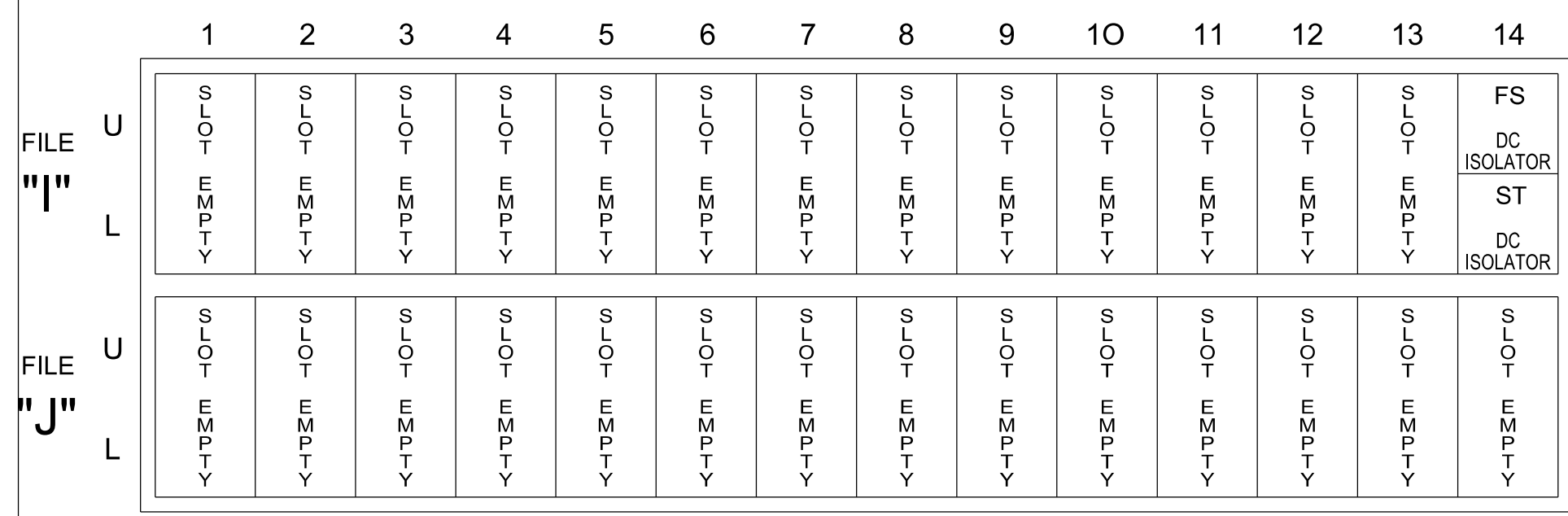
REMOVE DIODE JUMPERS 1-3, 1-8, 1-9, 1-10, 3-8, 3-9, 3-10, 6-9, 6-10, 6-17, 8-9, 8-10, 9-10, 9-17 and 10-17.



- NOTES:**
1. Card is provided with all diode jumpers in place. Removal of any jumper allows its channels to run concurrently.
 2. Ensure jumpers SEL2-SEL5 and SEL9 are present on the monitor board.
 3. Ensure that the Red Enable is active at all times during normal operation.
 4. Connect serial cable from conflict monitor to comm. port 1 of 2070 controller. Ensure conflict monitor communicates with 2070.

INPUT FILE POSITION LAYOUT

(front view)



EX.: 1A, 2A, ETC. = LOOP NO.'S
FS = FLASH SENSE
ST = STOP TIME

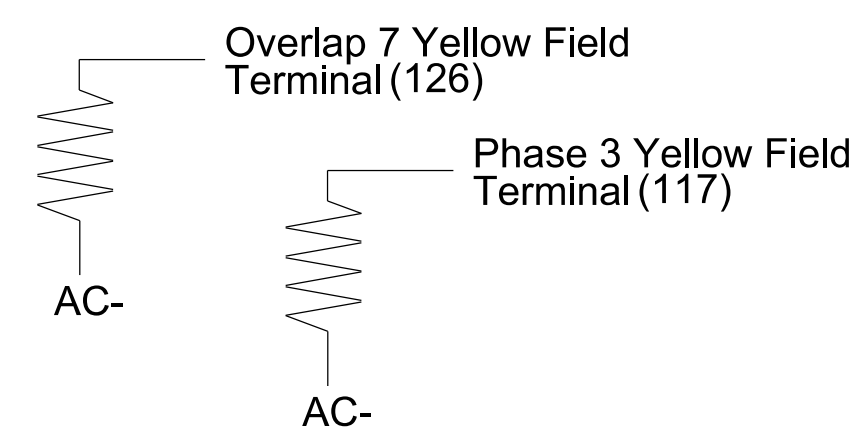
SPECIAL DETECTOR NOTE

Install a video detection system for vehicle detection. Perform installation according to manufacturer's directions and NCDOT engineer-approved mounting locations to accomplish the detection schemes shown on the Signal Design Plans.

LOAD RESISTOR INSTALLATION DETAIL

(install resistors as shown)

ACCEPTABLE VALUES	
Value (ohms)	Wattage
1.5K - 1.9K	25W (min)
2.0K - 3.0K	10W (min)



NOTES

1. To prevent "flash-conflict" problems, insert red flash program blocks for all unused vehicle load switches in the output file. The installer shall verify that signal heads flash in accordance with the signal plan.
2. Program controller to start up in phase 6 Green No Walk.
3. If this signal will be managed by an ATMS software, enable controller and detector logging for all detectors used at this location.

EQUIPMENT INFORMATION

Controller.....2070LX
Cabinet.....332 w/ Aux
Software.....Q-Free MAXTIME
Cabinet Mount.....Base
Output File Positions.....18 With Aux. Output File
Load Switches Used.....S1,S4,S8,S11,AUX S1,AUX S2,AUX S3
Phases Used.....3, 6, 8
Overlap "1".....*
Overlap "2".....*
Overlap "3".....NOT USED
Overlap "4".....NOT USED
Overlap "5".....*

*See overlap programming detail on sheet 2.

OUTPUT CHANNEL CONFIGURATION

Front Panel
Main Menu >Controller >More>Channels>Channels Config

Web Interface
Home >Controller >Advanced IO>Channels>Channels Configuration

Channel Configuration

Channel	Control Type	Control Source	Flash Yellow	Flash Red	Flash Alt	MMU Channel
1	Overlap	7		X	X	1
2	Phase Vehicle	2	X			2
3	Phase Vehicle	3		X	X	3
4	Phase Vehicle	4		X		4
5	Phase Vehicle	5		X		5
6	Phase Vehicle	6	X		X	6
7	Phase Vehicle	7		X		7
8	Phase Vehicle	8		X	X	8
9	Overlap	1	X		X	9
10	Overlap	2	X		X	10
11	Overlap	3	X			11
12	Overlap	4		X		12
13	Phase Ped	2				13
14	Phase Ped	4				14
15	Phase Ped	6				15
16	Phase Ped	8				16
17	Overlap	5	X		X	17
18	Overlap	6		X		18

ASSIGN CHANNEL 1 TO OVERLAP 7

OVERLAP PROGRAMMING

Front Panel
Main Menu >Controller >Overlap >Overlap Parameters/Overlap Timings

Web Interface
Home >Controller >Overlap Configuration >Overlaps

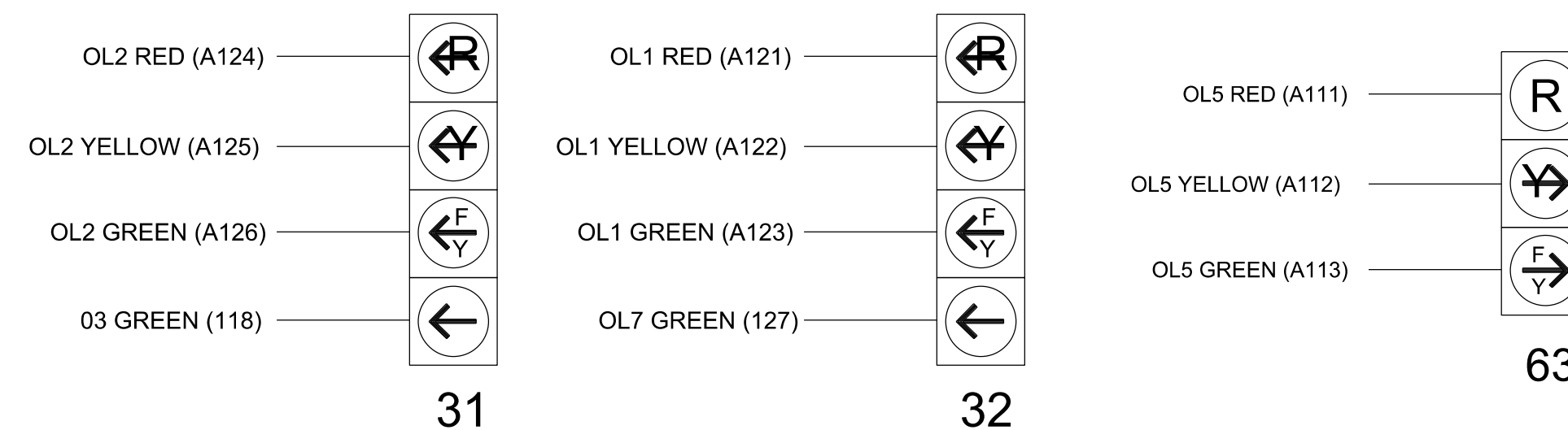
Overlap Plan 1

Overlap	1	2	5	7
Type	FYA 4 - Section	FYA 4 - Section	FYA 4 - Section	Normal
Included Phases	6	6	6	3
Modifier Phases	3	3	-	-
Trail Green	0	0	0	0
Trail Yellow	0.0	0.0	0.0	0.0
Trail Red	0.0	0.0	0.0	0.0

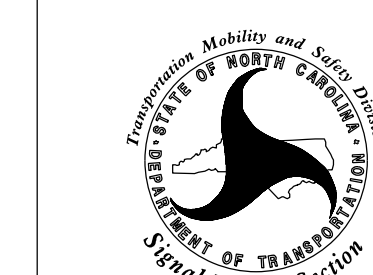
THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 11-1332T2
DESIGNED: May 2023
SEALED: 5/24/2023
REVISED: N/A

FYA SIGNAL WIRING DETAIL

(wire signal heads as shown)



ELECTRICAL AND PROGRAMMING DETAILS FOR:



750 N. Greenfield Pkwy, Garner, NC 27529

US 421 - NC 16 at SR 1323 (Dancy Rd) /

Division 11 Wilkes County Wilkesboro
PLAN DATE: May 2023 REVIEWED BY: M.L.Stygles
PREPARED BY: S.R.Chiluka REVIEWED BY: J.Ma

REVISIONS	INIT.	DATE



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DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED

SEAL
NORTH CAROLINA PROFESSIONAL ENGINEER
MATTHEW L. STYGLES
046057
5/24/2023
DATE
SIG. INVENTORY NO. 11-1332T2