ALTERNATE PHASING ACTIVATION DETAIL

TO RUN ALT, PHASING DURING FREE RUN - PROGRAM CHANGES (SHOWN BELOW) IN A TIME BASED ACTION PLAN. SCHEDULE A DAY PLAN THAT INCLUDES THE ACTION PLAN PROGRAMMED TO SELECT VEH DET PLAN 2 AND ENABLE SF BITS 1 AND 5.

TO RUN ALT. PHASING DURING COORDINATION - SELECT THE TIME BASED ACTION PLAN THAT IS PROGRAMMED TO SELECT VEH DET PLAN 2 AND ENABLE SF BITS 1 AND 5.

NONE
1,5

IMPORTANT: IF ALT. PHASING IS USED DURING FREE RUN AND COORDINATION, DO NOT OPERATE TIME OF DAY EVENTS CONCURRENTLY WITH COORDINATION PLAN EVENTS IN THE EVENT SCHEDULER. (EX. FREE RUN EVENT SHOULD END BEFORE COORDINATION PLAN EVENT STARTS AND VICE-VERSA).

ALTERNATE PHASING CHANGE SUMMARY

THE FOLLOWING IS A SUMMARY OF WHAT TAKES PLACE WHEN SF BITS 1 AND 5 AND VEH DET PLAN 2 ACTIVATE TO CALL THE "ALTERNATE PHASING":

SF BITS 1,5: Modifies overlap parent phases

> for heads 11 and 51 to run protected turns only.

VEH DET PLAN 2:

Disables phase 6 call on loop 1A and reduces delay time for phase 1 call on loop 1A to 0 seconds.

Disables phase 2 call on loop 5A and reduces delay time for phase 5 call on loop 5A to 0 seconds.

FLASHER CIRCUIT MODIFICATION DETAIL

IN ORDER TO INSURE THAT SIGNALS FLASH CONCURRENTLY ON THE SAME APPROACH, MAKE THE FOLLOWING FLASHER CIRCUIT CHANGES:

1. ON REAR OF PDA - REMOVE WIRE FROM TERM. T2-4 AND TERMINATE ON T2-2.

2. ON REAR OF PDA - REMOVE WIRE FROM TERM. T2-5 AND TERMINATE ON T2-3.

3. REMOVE FLASHER UNIT 2.

THE CHANGES LISTED ABOVE TIES ALL PHASES AND OVERLAPS TO FLASHER UNIT 1.

PROJECT REFERENCE NO. SIG-6.3 R-3833C

ECONOLITE ASC/3-2070 ACTION PLAN PROGRAMMING DETAIL

1. From Main Menu select | 5. TIME BASE

2. From TIME BASE Submenu select | 2. ACTION PLAN

ACTION PL	AN.	[*	-]												
PATTERN		SYS	OV	ERR	IDE		. N	10								
TIMING PLAN 0						SEQ	UEN	CE.			•	0				
VEH DETECTOR PLAN 2						DET	LC	G			NON	ΙE				
FLASH		RED	RE	ST.			. N	10								
VEH DET D		PED	DE	T D	IAG	PL	Ν	0								
DIMMING ENABLE NO						PRI	OR I	ΤΥ	RET	URN	. N	10				
PED PR RE	TUR	Ν		NO		QUE	UE	DEL	ΑΥ.		. N	10				
PMT COND	DEL	ΑΥ		NO												
PHASE	1	2	3	4	5	6	7	8	9	0	1	2	3	4	5	
PED RCL	•	•	•	•	•	•	•	•	•	•	•	•	•	•	•	
WALK 2	•	•	•	•	•	•	•	•	•	•	•	•	•	•	•	
VEX 2	•	•	•	•	•	•	•	•	•	•	•	•	•	•	•	
VEH RCL	•	•	•	•	•	•	•	•	•	•	•	•	•	•	•	
MAX RCL	•	•	•	•	•	•	•	•	•	•	•	•	•	•	•	
MAX 2	•	•	•	•	•	•	•	•	•	•	•	•	•	•	•	
PHASE	1	2	3	4	5	6	7	8	9	0	1	2	3	4	5	
MAX 3	•	•	•	•	•	•	•	•	•	•	•	•	•	•	•	
CS INH	•	•	•	•	•	•	•	•	•	•	•	•	•	•	•	
OMIT	•	•	•	•	•	•	•	•	•	•	•	•	•	•	•	
SPC FCT	Χ	•	•	•	Χ	•	•	•	(1	-8)						
AUX FCT	•	•	•	(1	− 3 :)										
	1	2	3	4	5	6	7	8	9	0	1	2	3	4	5	
LP 1-15	•	•	•	•	•	•	•	•	•	•	•	•	•	•	•	
LP 16-30	•	•	•	•	•	•	•	•	•	•	•	•	•	•	•	
LP 31-45	•	•	•	•	•	•	•	•	•	•	•	•	•	•	•	
LP 46-60	•	•	•	•	•	•	•	•	•	•	•	•	•	•	•	
LP 61-75	•	•	•	•	•	•	•	•	•	•	•	•	•	•	•	
LP 76-90	•	•	•	•	•	•	•	•	•	•	•	•	•	•	•	
LP 91-100	•	•	•	•	•	•	•	•	•	•	•	•	•	•	•	

* The Action Plan number(s) are to be determined by the Division and/or City Traffic Engineer.

> THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 12-1897 DESIGNED: MAY 2022 SEALED: 3/22/2023 REVISED: N/A

Final Design Electrical Detail - Sheet 3 of 4



SR 1100 (Brawley School Road)

Balmy Lane Division 12 Iredell County Mooresville REVIEWED BY: E D Harris PLAN DATE: May 2022 PREPARED BY: D A Waller REVIEWED BY: R M Muncev REVISIONS INIT. DATE

042678

DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED

3/22/202 Derrick Waller SIG. INVENTORY NO.

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