

DETAIL	
SEQUENCE	_

OVERLAP PROGRAMMING DETAIL FOR DEFAULT PHASING

(program controller as shown below)

FROM MAIN MENU PRESS '8' (OVERLAPS), THEN '1' (VEHICLE OVERLAP SETTINGS).

PAGE 1: VEHICLE OVERLAP 'A' SETTINGS 12345678910111213141516 PHASE: VEH OVL PARENTS: ¦ X VEH OVL NOT VEH: VEH OVL NOT PED: | VEH OVL GRN EXT: STARTUP COLOR: \_ RED \_ YELLOW \_ GREEN NOTICE GREEN FLASH FLASH COLORS: \_ RED \_ YELLOW X GREEN SELECT VEHICLE OVERLAP OPTIONS: (Y/N) FLASH YELLOW IN CONTROLLER FLASH?...Y GREEN EXTENSION (0-255 SEC)..... YELLOW CLEAR (O=PARENT, 3-25.5 SEC)..O.O RED CLEAR (0=PARENT,0.1-25.5 SEC)...0.0 OUTPUT AS PHASE # (O=NONE, 1-16)....0 PRESS '+' TWICE PAGE 1: VEHICLE OVERLAP 'C' SETTINGS ¦12345678910111213141516 PHASE: VEH OVL PARENTS: | XX VEH OVL NOT VEH: VEH OVL NOT PED: VEH OVL GRN EXT: ¦ STARTUP COLOR: \_ RED \_ YELLOW \_ GREEN FLASH COLORS: \_ RED \_ YELLOW X GREEN NOTICE GREEN FLASH SELECT VEHICLE OVERLAP OPTIONS: (Y/N) FLASH YELLOW IN CONTROLLER FLASH?...Y GREEN EXTENSION (0-255 SEC)..... YELLOW CLEAR (O=PARENT, 3-25.5 SEC)..0.0 RED CLEAR (0=PARENT,0.1-25.5 SEC)...0.0 OUTPUT AS PHASE # (O=NONE, 1-16)....0 PRESS '+'PAGE 1: VEHICLE OVERLAP 'D' SETTINGS PHASE: 12345678910111213141516 VEH OVL PARENTS: VEH OVL NOT VEH: VEH OVL NOT PED: VEH OVL GRN EXT: ¦ STARTUP COLOR: \_ RED \_ YELLOW \_ GREEN FLASH COLORS: \_ RED \_ YELLOW X GREEN MOTICE GREEN FLASH SELECT VEHICLE OVERLAP OPTIONS: (Y/N) FLASH YELLOW IN CONTROLLER FLASH?...N GREEN EXTENSION (0-255 SEC)..... YELLOW CLEAR (O=PARENT, 3-25.5 SEC)..0.0 RED CLEAR (O=PARENT, 0.1-25.5 SEC)...0.0 OUTPUT AS PHASE # (O=NONE, 1-16)....O PRESS '+' TWICE PAGE 1: VEHICLE OVERLAP 'F' SETTINGS 12345678910111213141516 PHASE: VEH OVL PARENTS: ¦ X VEH OVL NOT VEH: | VEH OVL NOT PED: | VEH OVL GRN EXT: STARTUP COLOR: \_ RED \_ YELLOW \_ GREEN FLASH COLORS: \_ RED \_ YELLOW X GREEN NOTICE GREEN FLASH SELECT VEHICLE OVERLAP OPTIONS: (Y/N) FLASH YELLOW IN CONTROLLER FLASH?...Y GREEN EXTENSION (0-255 SEC).....0 YELLOW CLEAR (O=PARENT, 3-25.5 SEC)..0.0 RED CLEAR (0=PARENT,0.1-25.5 SEC)...0.0 OUTPUT AS PHASE # (O=NONE, 1-16)....O OVERLAP PROGRAMMING COMPLETE Μ THIS ELECTRICAL DETAIL IS FOR

THE SIGNAL DESIGN: 09-1105T2 DESIGNED: March 2023 SEALED: April 25, 2023 REVISED:



