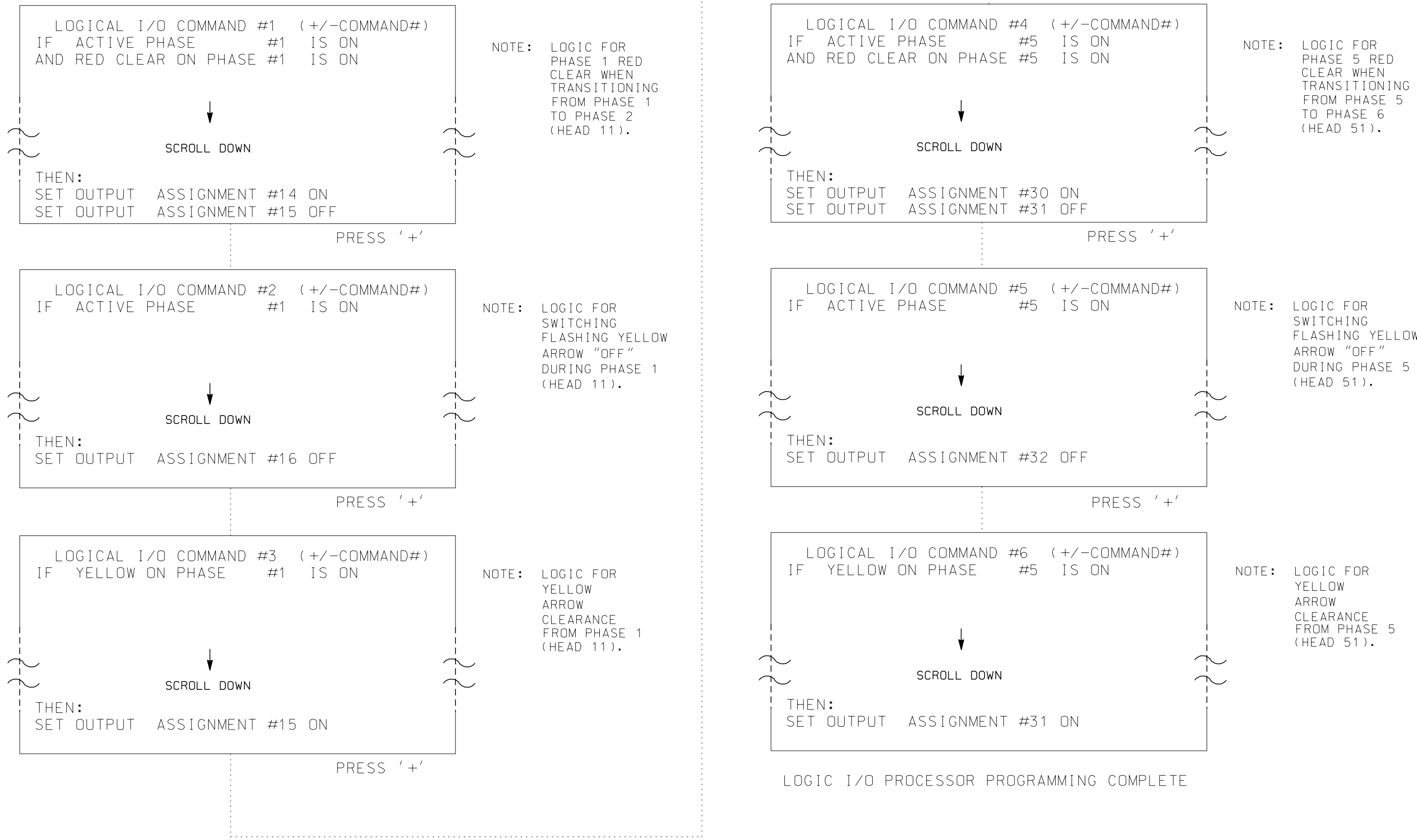


LOGICAL I/O PROCESSOR PROGRAMMING DETAIL TO PRODUCE SPECIAL FYA-PPLT SIGNAL SEQUENCE

(program controller as shown below)

1. FROM MAIN MENU PRESS '2' (PHASE CONTROL), THEN '1' (PHASE CONTROL FUNCTIONS). SCROLL TO THE BOTTOM OF THE MENU AND ENABLE ACT LOGIC COMMANDS 1, 2, 3, 4, 5 AND 6.
2. FROM MAIN MENU PRESS '6' (OUTPUTS), THEN '3' (LOGICAL I/O PROCESSOR).



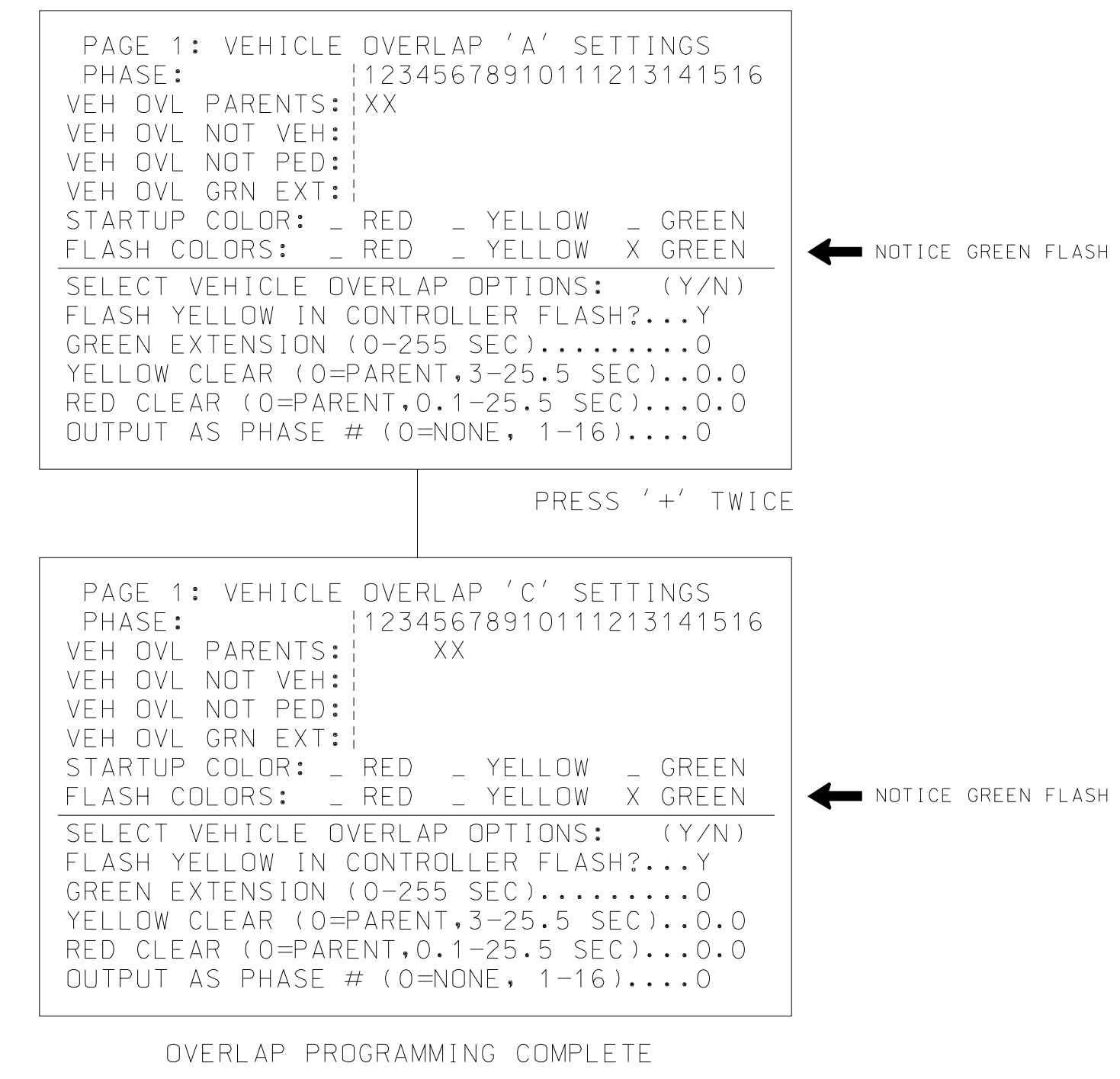
OUTPUT REFERENCE SCHEDULE	
OUTPUT 14 =	Overlap A Red
OUTPUT 15 =	Overlap A Yellow
OUTPUT 16 =	Overlap A Green
OUTPUT 30 =	Overlap C Red
OUTPUT 31 =	Overlap C Yellow
OUTPUT 32 =	Overlap C Green
OUTPUT 33 =	Phase 1 Green
OUTPUT 34 =	Phase 5 Green

Note: All outputs shown above have been remapped. See sheets 3 and 4 of this electrical detail.

OVERLAP PROGRAMMING DETAIL

(program controller as shown below)

FROM MAIN MENU PRESS '8' (OVERLAPS), THEN '1' (VEHICLE OVERLAP SETTINGS).



THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 09-0699T2
DESIGNED: March 2023
SEALED: April 25, 2023
REVISED:

Signal Upgrade - Temporary Design 2 - Electrical Detail - Sheet 2 of 4

	ELECTRICAL AND PROGRAMMING DETAILS FOR:	<p style="font-size: 1.2em;">SR 1672 (Hanes Mill Road) at Museum Drive</p>	<p>SEAL</p>												
		<p>Division 9 Forsyth County Winston-Salem</p>	<p>SEAL 032711</p> <p>Russell W. Thompson</p>												
<p>7621 Pur Foy Road Suite 115 Fuquay-Varina, NC 27526 www.mottmac.com License No. F-0669</p>	<p>750 N. Greenfield Pkwy, Garner, NC 27529</p>	<p>PLAN DATE: March 2023 REVIEWED BY: RW Thompson</p> <p>PREPARED BY: LD Stouchko REVIEWED BY:</p>	<p>REVISIONS</p> <table border="1" style="width: 100%; border-collapse: collapse;"> <thead> <tr> <th>NO.</th> <th>DESCRIPTION</th> <th>INIT.</th> <th>DATE</th> </tr> </thead> <tbody> <tr><td> </td><td> </td><td> </td><td> </td></tr> <tr><td> </td><td> </td><td> </td><td> </td></tr> </tbody> </table>	NO.	DESCRIPTION	INIT.	DATE								
NO.	DESCRIPTION	INIT.	DATE												

DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED

A1985863988404 DATE SIG. INVENTORY NO. 09-0699T2

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