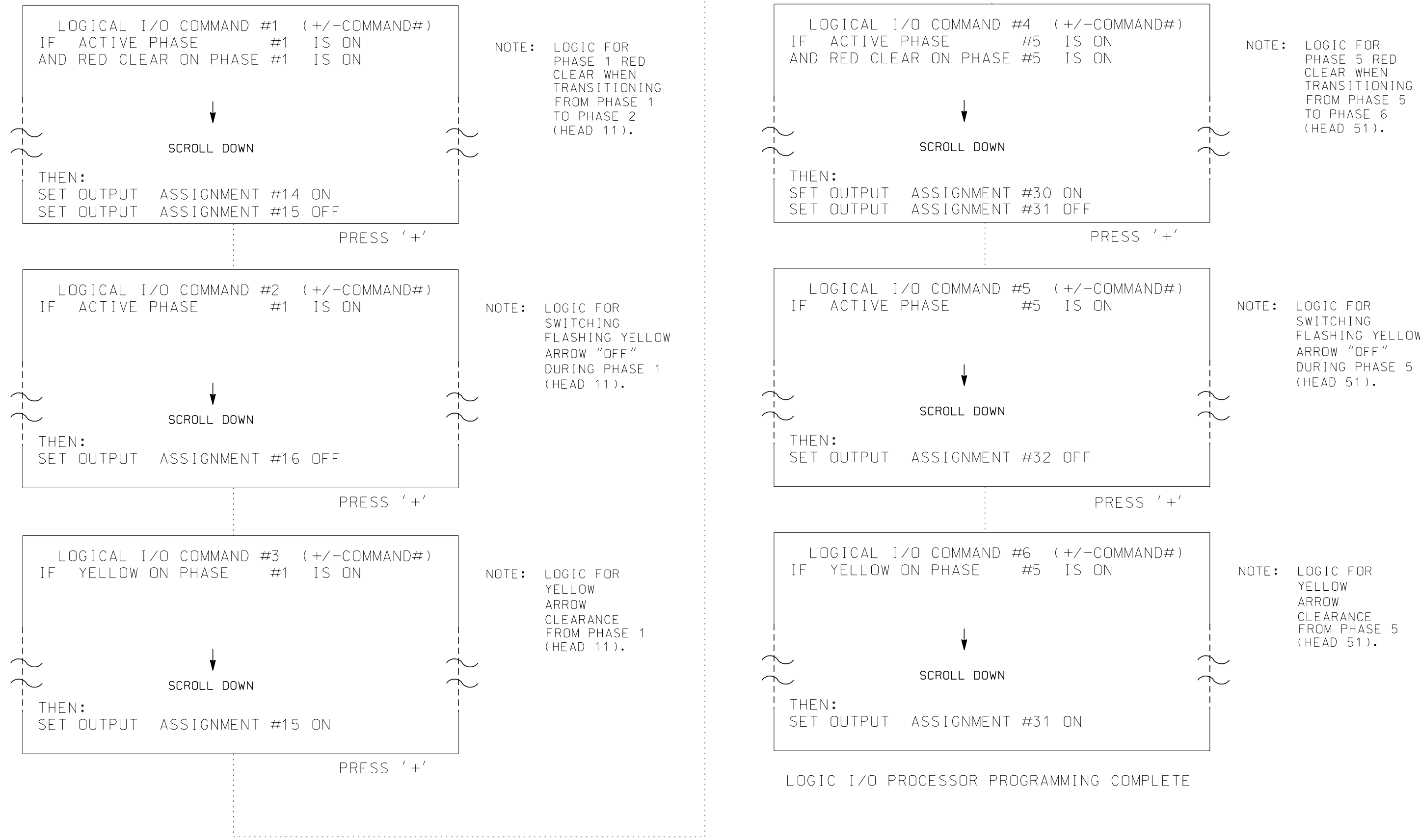


LOGICAL I/O PROCESSOR PROGRAMMING DETAIL TO PRODUCE SPECIAL FYA-PPLT SIGNAL SEQUENCE

(program controller as shown below)

- FROM MAIN MENU PRESS '2' (PHASE CONTROL), THEN '1' (PHASE CONTROL FUNCTIONS). SCROLL TO THE BOTTOM OF THE MENU AND ENABLE ACT LOGIC COMMANDS 1, 2, 3, 4, 5 AND 6.
- FROM MAIN MENU PRESS '6' (OUTPUTS), THEN '3' (LOGICAL I/O PROCESSOR).



LOGIC I/O PROCESSOR PROGRAMMING COMPLETE

OUTPUT REFERENCE SCHEDULE	
OUTPUT 14	= Overlap A Red
OUTPUT 15	= Overlap A Yellow
OUTPUT 16	= Overlap A Green
OUTPUT 30	= Overlap C Red
OUTPUT 31	= Overlap C Yellow
OUTPUT 32	= Overlap C Green
OUTPUT 33	= Phase 1 Green
OUTPUT 34	= Phase 5 Green

Note: All outputs shown above have been remapped. See sheets 3 and 4 of this electrical detail.

OVERLAP PROGRAMMING DETAIL

(program controller as shown below)

FROM MAIN MENU PRESS '8' (OVERLAPS), THEN '1' (VEHICLE OVERLAP SETTINGS).

```

PAGE 1: VEHICLE OVERLAP 'A' SETTINGS
PHASE:          |12345678910111213141516
VEH OVL PARENTS: |XX
VEH OVL NOT VEH: |
VEH OVL NOT PED: |
VEH OVL GRN EXT: |
STARTUP COLOR:  - RED  - YELLOW  - GREEN
FLASH COLORS:   - RED  - YELLOW X GREEN
SELECT VEHICLE OVERLAP OPTIONS: (Y/N)
FLASH YELLOW IN CONTROLLER FLASH?...Y
GREEN EXTENSION (0-255 SEC).....0
YELLOW CLEAR (0=PARENT,3-25.5 SEC)..0.0
RED CLEAR (0=PARENT,0.1-25.5 SEC)...0.0
OUTPUT AS PHASE # (0=NONE, 1-16)....0
    
```

← NOTICE GREEN FLASH

PRESS '+' TWICE

```

PAGE 1: VEHICLE OVERLAP 'C' SETTINGS
PHASE:          |12345678910111213141516
VEH OVL PARENTS: |  XX
VEH OVL NOT VEH: |
VEH OVL NOT PED: |
VEH OVL GRN EXT: |
STARTUP COLOR:  - RED  - YELLOW  - GREEN
FLASH COLORS:   - RED  - YELLOW X GREEN
SELECT VEHICLE OVERLAP OPTIONS: (Y/N)
FLASH YELLOW IN CONTROLLER FLASH?...Y
GREEN EXTENSION (0-255 SEC).....0
YELLOW CLEAR (0=PARENT,3-25.5 SEC)..0.0
RED CLEAR (0=PARENT,0.1-25.5 SEC)...0.0
OUTPUT AS PHASE # (0=NONE, 1-16)....0
    
```

← NOTICE GREEN FLASH

PRESS '+' TWICE

```

PAGE 1: VEHICLE OVERLAP 'E' SETTINGS
PHASE:          |12345678910111213141516
VEH OVL PARENTS: |  X
VEH OVL NOT VEH: |
VEH OVL NOT PED: |
VEH OVL GRN EXT: |
STARTUP COLOR:  - RED  - YELLOW  - GREEN
FLASH COLORS:   - RED  - YELLOW  - GREEN
SELECT VEHICLE OVERLAP OPTIONS: (Y/N)
FLASH YELLOW IN CONTROLLER FLASH?...N
GREEN EXTENSION (0-255 SEC).....0
YELLOW CLEAR (0=PARENT,3-25.5 SEC)..0.0
RED CLEAR (0=PARENT,0.1-25.5 SEC)...0.0
OUTPUT AS PHASE # (0=NONE, 1-16)....0
    
```

OVERLAP PROGRAMMING COMPLETE

THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 09-0699T1
DESIGNED: March 2023
SEALED: April 25, 2023
REVISED:

Signal Upgrade - Temporary Design 1 - Electrical Detail - Sheet 2 of 5

DOCUMENT NOT CONSIDERED
FINAL UNLESS ALL
SIGNATURES COMPLETED

	ELECTRICAL AND PROGRAMMING DETAILS FOR:	SR 1672 (Hanes Mill Road) at Museum Drive	SEAL
		Division 9 Forsyth County Winston-Salem	SEAL 032711 ENGINEER RUSSELL W. THOMPSON
7621 Purfoy Road Suite 115 Fuquay-Varina, NC 27526 www.mottmac.com License No. F-0669	750 N. Greenfield Pkwy, Garner, NC 27529	PLAN DATE: March 2023 REVIEWED BY: RW Thompson PREPARED BY: LD Stouchko REVIEWED BY:	REVISIONS INIT. DATE _____ _____ _____

4/25/2023 0:30:35.0.DOC - 1246C_U-2729-Traffic.k5t.ignals*09-0699*260_025_0500699-20230425e2-11.cop User: ST086227