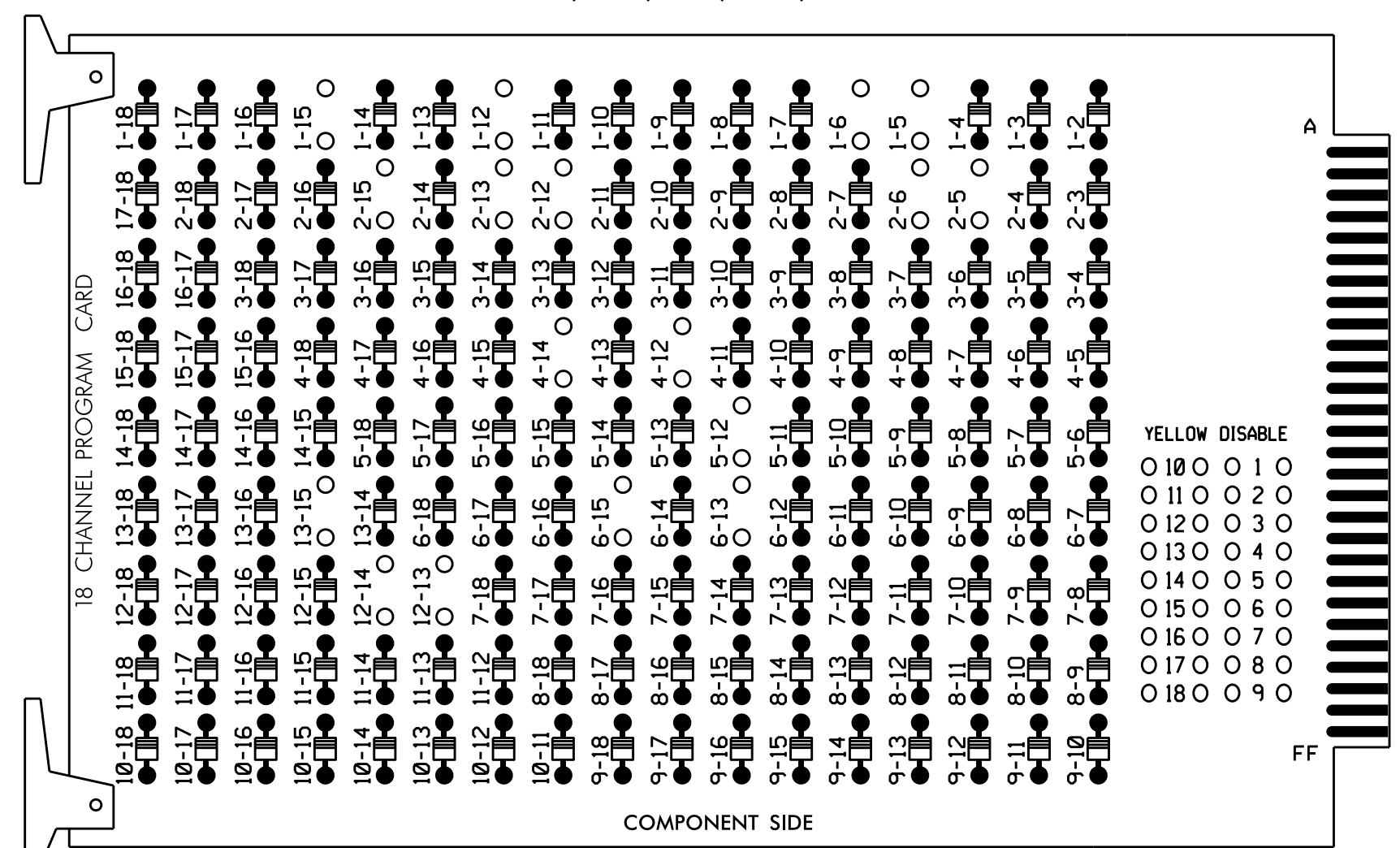


18 CHANNEL CONFLICT MONITOR PROGRAMMING DETAIL

(remove jumpers and set switches as shown)

REMOVE DIODE JUMPERS 1-5, 1-6, 1-12, 1-15, 2-5, 2-6, 2-12, 2-13, 2-15, 4-12, 4-14, 5-12, 5-13, 6-13, 6-15, 12-13, 12-14 and 13-15.



REMOVE JUMPERS AS SHOWN

NOTES:

- Card is provided with all diode jumpers in place. Removal of any jumper allows its channels to run concurrently.
- Ensure jumpers SEL2-SEL5 and SEL9 are present on the monitor board.
- Ensure that Red Enable is active at all times during normal operation.
- Connect serial cable from conflict monitor to comm. port 1 of 2070 controller. Ensure conflict monitor communicates with 2070.

■ = DENOTES POSITION OF SWITCH

SIGNAL HEAD HOOK-UP CHART

LOAD SWITCH NO.	S1	S2	S3	S4	S5	S6	S7	S8	S9	S10	S11	S12	AUX S1	AUX S2	AUX S3	AUX S4	AUX S5	AUX S6				
CMU CHANNEL NO.	1	2	13	3	4	14	5	6	15	7	8	16	9	10	17	11	12	18				
PHASE	1	2	2 PED	3	4	4 PED	5	6	6 PED	7	8	8 PED	OLA	OLB	SPARE	OLC	OLD	SPARE				
SIGNAL HEAD NO.	11	32	21,22, 23,24	P21, P22	31	32	41,42	43	44	62	P41, P42	51,52	44	61,62, 63	P61, P62	NU	NU	NU	45	NU		
RED		128		116	116		101	101						134								
YELLOW		129		117	117		102	102						135								
GREEN		130		118	118		103	103						136								
RED ARROW	125						101				131										A101	
YELLOW ARROW	126	126					102			102	132	132										A102
GREEN ARROW	127	127					103	103	103		133	133										A103
Hand				113						104			119									
Walking				115						106												

NU = Not Used

NOTES

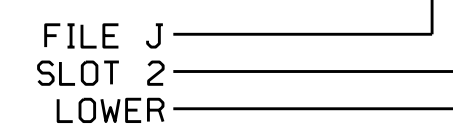
- To prevent "flash-conflict" problems, insert red flash program blocks for all unused vehicle load switches in the output file. The installer shall verify that signal heads flash in accordance with the Signal Plans.
- Program controller to start up in phase 2 Green/Don't Walk and phase 6 Green.
- Enable simultaneous gap-out feature for all phases.
- Program phase 4 for Dynamic Max.
- The cabinet and controller are part of the Raleigh Signal System.

INPUT FILE CONNECTION & PROGRAMMING CHART

LOOP NO.	LOOP TERMINAL	INPUT FILE POS.	PIN NO.	DETECTOR NO.	NEMA PHASE	DELAY TIME	EXTEND (STRETCH) TIME
1A	TB2-1,2	I1U	56	1	1		
1B	TB2-5,6	I2U	39	3	1	15	
2A	TB2-9,10	I3U	63	5	2		
2B	TB2-11,12	I3L	76	6	2		
3A	TB4-5,6	I5U	58	9	3		
4A	TB4-9,10	I6U	41	11	4		
4B	TB4-11,12	I6L	45	12	4		
4C	TB6-1,2	I7U	65	13	4		
5A	TB3-1,2	J1U	55	19	5		
5B	TB3-5,6	J2U	40	21	5		
5C	TB3-7,8	J2L	44	22	5	15	
6A	TB3-9,10	J3U	64	23	6		
6B	TB3-11,12	J3L	77	24	6		
PED PUSH BUTTONS							
P21,P22	TB8-4,6	I12U	67	PED 2	2 PED		
P41,P42	TB8-5,6	I12L	69	PED 4	4 PED		
P61,P62	TB8-7,9	I13U	68	PED 6	6 PED		

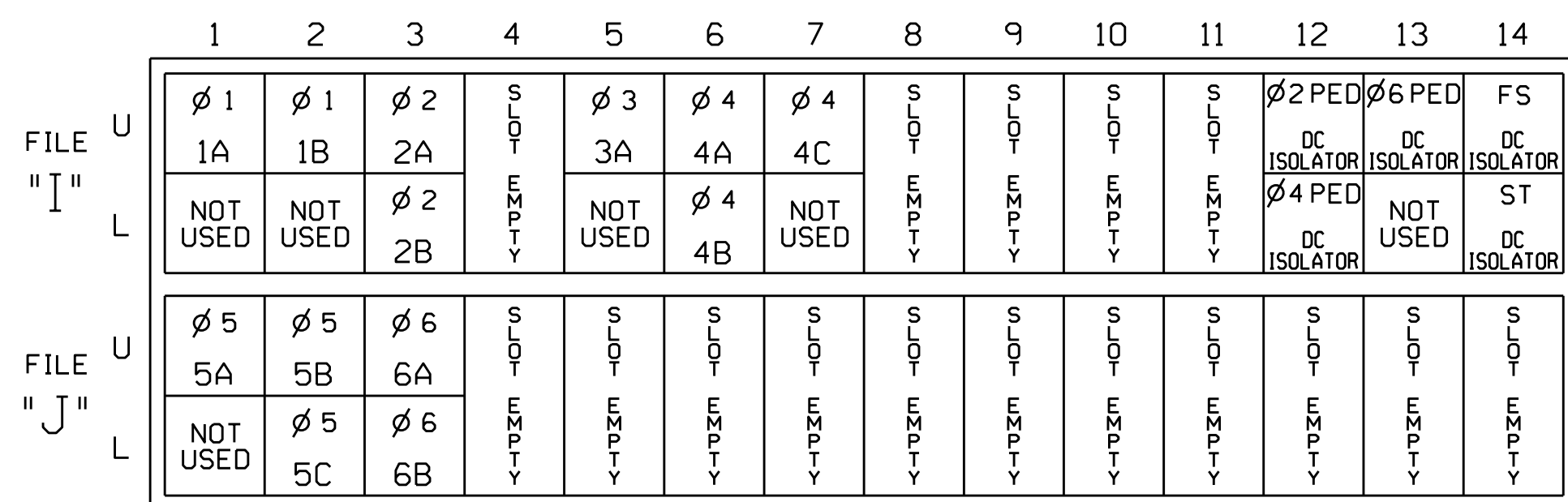
NOTE: INSTALL DC ISOLATORS IN INPUT FILE SLOTS 112 AND 113.

INPUT FILE POSITION LEGEND: J2L



INPUT FILE POSITION LAYOUT

(front view)



EX.: 1A, 2A, ETC. = LOOP NO.'S

FS = FLASH SENSE
ST = STOP TIME

EQUIPMENT INFORMATION

CONTROLLER.....2070LX
 CABINET.....332 W/ AUX
 SOFTWARE.....SE-PAC2070
 CABINET MOUNT.....BASE
 OUTPUT FILE POSITIONS...18 WITH AUX. OUTPUT FILE
 LOAD SWITCHES USED.....S1,S2,S3,S4,S5,S6,S7,S8,S9,AUX S5
 PHASES USED.....1,2,2PED,3,4,4PED,5,6,6PED
 OVERLAP "A".....NOT USED
 OVERLAP "B".....NOT USED
 OVERLAP "C".....NOT USED
 OVERLAP "D".....*

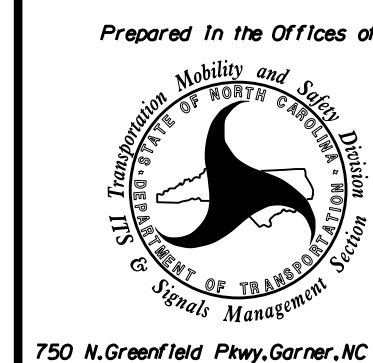
* See sheet 2 for Overlap Programming Detail

Electrical Detail - Sheet 1 of 2

DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED

ELECTRICAL AND PROGRAMMING DETAILS FOR:

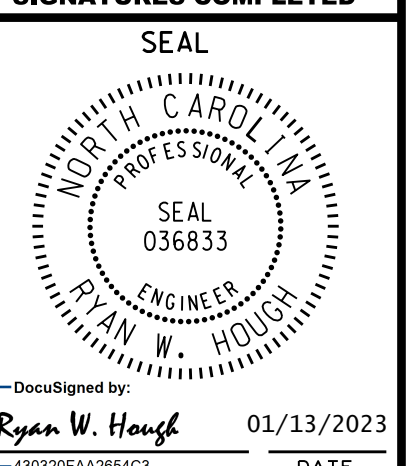
SR 2000 (Falls of Neuse Rd.)



at
I-540 EB Ramps and
Thorpshire Dr.

Division 5 Wake County Raleigh
 PLAN DATE: January 2023 REVIEWED BY:
 PREPARED BY: S.Kirkpatrick REVIEWED BY:

REVISIONS	INIT.	DATE



DocuSigned by:
Ryan W. Hough 01/13/2023
430320FAA2854C3 DATE

SIG. INVENTORY NO. 05-2035

THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 05-2035
 DESIGNED: January 2023
 SEALED: 01/03/2023
 REVISED: N/A