

### RAILROAD PREEMPTION PROGRAMMING DETAIL

(program controller as shown below)

From Main Menu press 'A' (Preemption), then '1' (Standard Preemptions).

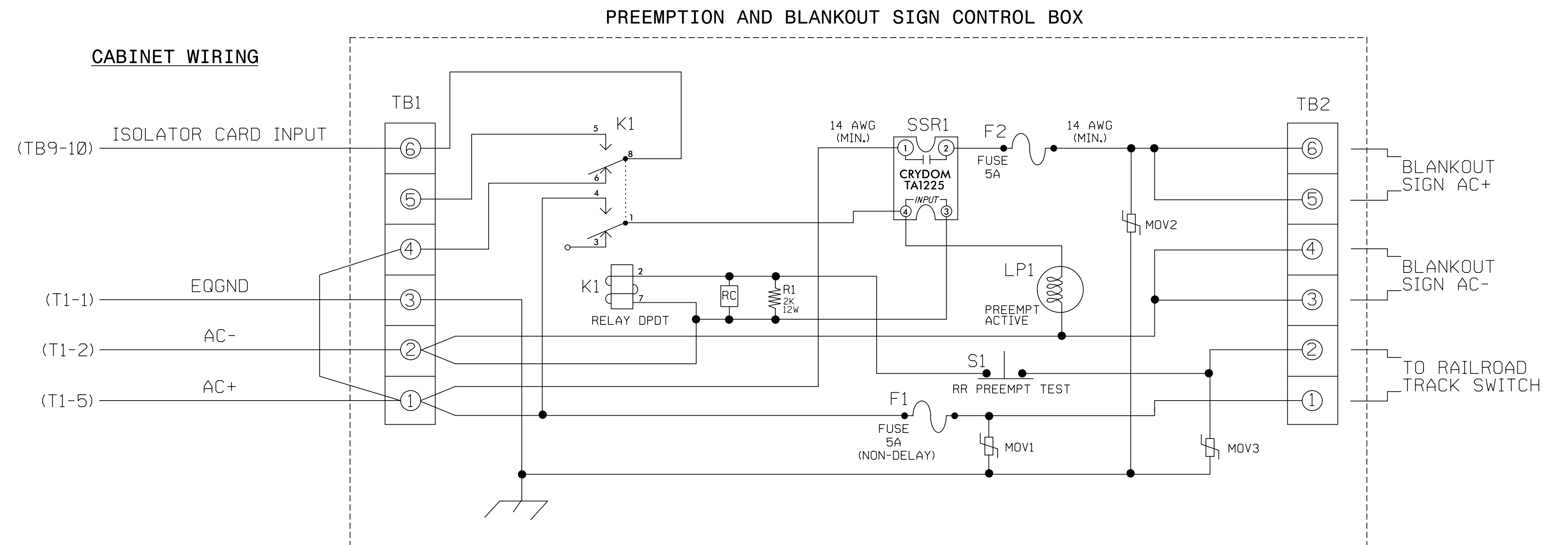
PREEMPTION #1	SETTINGS (NEXT:1-10)
INTERVAL/TIMING	CLEAR/DWELL PHASES
GRN YEL RED	12345678910111213141516
1 0 0.0 0.0	
2 255 0.0 0.0	X
3 0 0.0 0.0	
4 0 0.0 0.0	
5 1 0.0 0.0	X X

EXIT CALLS	OPTIONS
PRIORITY (Y/N TO SELECT)	.....HIGH
DELAY TIMER (0-255 SEC)	.....0
MIN GREEN BEFORE PRE (0= DEFAULT)	....1
PED CLEAR BEFORE PRE (0= DEFAULT)	....0
YELLOW CLEAR BEFORE PRE (0= DEFAULT)	3.8
RED CLEAR BEFORE PRE (0= DEFAULT)	4.1
DWELL MIN TIMER (0-255 SEC)	.....7
DWELL MAX TIMER (0=OFF,1-255MIN)	....0
DWELL HOLD-OVER TIMER (0-255)	.....0
LATCH CALL?	.....N
LINK TO NEXT PREEMPT?	.....N
ENABLE BACKUP PROTECTION?	.....N
HOLD CLEAR 1 PHASES DURING DELAY?	...N
FAST GREEN FLASH DWELL PHASES?	.....N
PED CLEARANCE THROUGH YELLOW?	.....N
INHIBIT OVERLAP GREEN EXTENSION?	...N
SERVICE DURING SOFTWARE FLASH?	.....N
REST IN RED DURING DWELL INTERVAL?	..N
FLASH DWELL INTERVAL?	.....N
ALLOW PEDS IN DWELL INTERVAL?	.....N
RE-TIME DWELL INTERVAL?	.....N
OVERLAPS:	ABCDEFGHIJKLMNOP
DWELL INT FLASH YELLOW	
OMIT OVERLAPS:	X

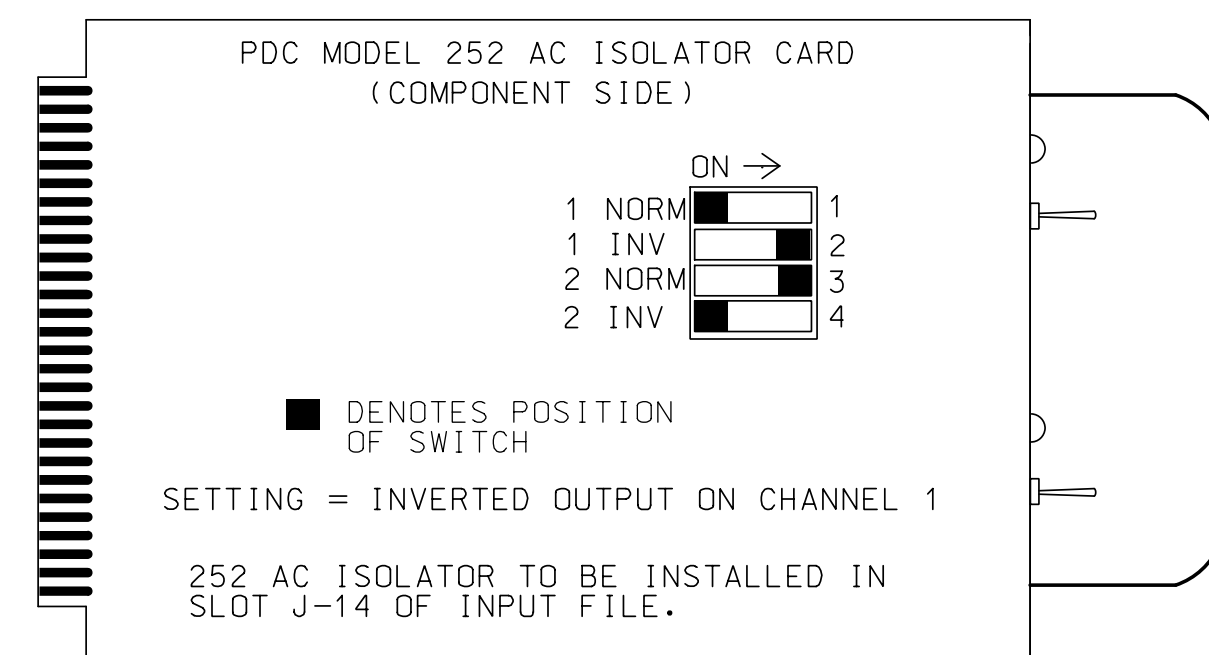
### RAILROAD PREEMPTION WIRING DETAIL

(wire as shown below)



### PREEMPT 1 AC ISOLATOR (MODEL 252) OUTPUT PROGRAMMING DETAIL

(set DIP switches as shown below)

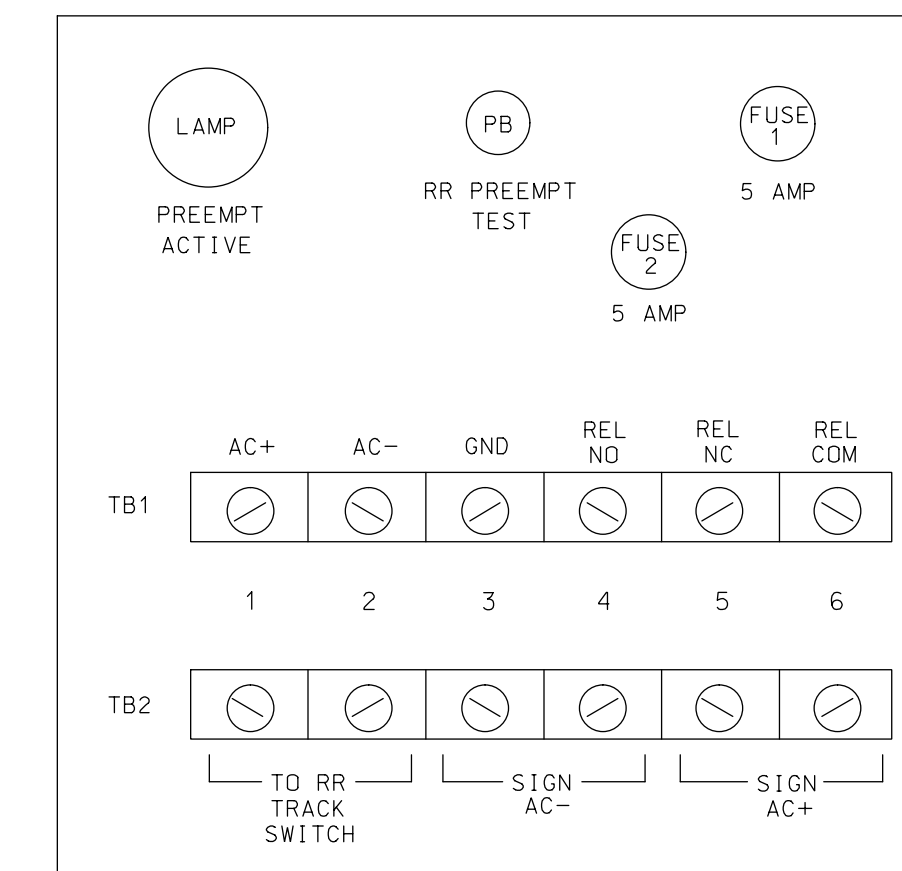


NOTE: IF ANOTHER MANUFACTURER TYPE OF AC ISOLATOR IS USED, OUTPUT PROGRAMMING IS LIKELY NOT TO EQUATE TO THAT SHOWN ABOVE.

### NOTES

- Relay K1 is shown in the energized (Preempt not active) normal operation state.
- Relay 'K1' is an enclosed DPDT general purpose relay with a 120VAC coil, 10A contacts, and octal-style plug.
- Relay SSR1 is a SPST (normally open) Solid State Relay with AC input and AC (25 amp) output.
- AC Isolator Card shall activate preemption upon removal of AC+ from the input (as shown above). To accomplish this, set invert dip switch on AC Isolator Card.
- IMPORTANT!! Terminal TB9-12 (on input panel) shall be connected to AC neutral (jumper may have to be added).

### FRONT VIEW



THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 04-0556T1  
 DESIGNED: December 2018  
 SEALED: 6/30/20  
 REVISED: N/A

### PHASE SEQUENCE PROGRAMMING DETAIL

(program controller as shown below)

FROM OASIS LOCAL CONTROLLER MAIN MENU  
 SELECT: 4 PHASE SEQUENCE

PHASE SEQUENCE: PAGE 1	NEXT: PAGES
RNG;LEAD BARRIER 1 X-LAG;LEAD BARRIER 2 X-LAG;LEAD BARRIER 3 X-LAG	
1   1   2   0   0   3   4   0   0   7   0   0   0	
2   0   6   0   5   0   0   0   0   0   0   0   0	
3   0   0   0   0   0   0   0   0   0   0   0   0	
4   0   0   0   0   0   0   0   0   0   0   0   0	

Temporary Design 1  
 Electrical Detail - Sheet 2 of 2

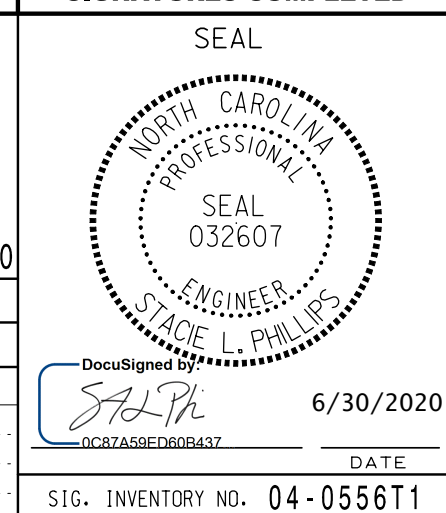
ELECTRICAL AND PROGRAMMING DETAILS FOR:



PLANS PREPARED IN THE OFFICE OF:  
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US 13 (BERKELEY BLVD.)		AT	
SR 1560 (ROYALL AVE.) & SR 1709 (CENTRAL HTS. RD.)		WAYNE COUNTY	
DIVISION 4	DECEMBER 2018	REVIEWED BY: SL PHILLIPS	GOLDSBORO
PREPARED BY: SP PENNINGTON	REVIEWED BY:		
REVISIONS	INIT.	DATE	

DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED



6/29/2020 3:42:47 PM susan.pennington K:\RAL\_TPTDK-SIGNALS\NOT1036333 U5724#54 - Signal Design\1.2 04-0556-2018T1.ezdgn